

ISLAMIC UNIVERSITY OF TECHNOLOGY (IUT)
ORGANISATION OF ISLAMIC COOPERATION (OIC)

DEPARTMENT OF ELECTRICAL AND ELECTRONIC ENGINEERING

Mid-Semester Examination

Course No.: EEE 4383

Course Title: Electronic Devices and Circuits

Winter Semester, A.Y. 2019-2020

Time: 90 Minutes

Full Marks: 75

There are 4 (four) questions. Answer **any 3 (three)** questions. All questions carry equal marks. Marks in the margin indicate full marks. Programmable calculators are not allowed. Do not write on this question paper. Assume suitable value for any missing data.

1. a) Draw the detailed transfer characteristics of a pn junction diode composed of Ge, Si and GaAs in the same graph and indicate different regions in the diagram. 07
- b) Write down a comparative analysis on the characteristics of BJT and FET. 07
- c) Determine I_D and V_D for the circuit in Fig. 1(c) with $V_{DD} = 5\text{ V}$ and $R = 1.1\text{ k}\Omega$ using the iterative analysis method. Assume the diode current is 1.5 mA at a voltage of 0.7 V . 07

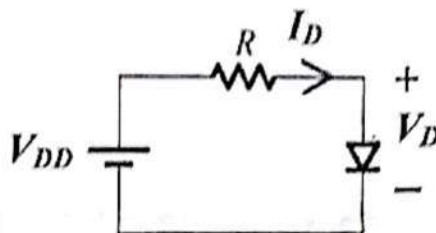


Fig. 1(c)

- d) What is the purpose of DC biasing in BJT? 04
2. a) Design a full wave rectifier and draw the input and output waveshapes for this circuit. 05
- b) Draw the majority and minority carrier concentrations profile of an npn transistor in the forward active mode. Explain the different currents generated due to the flow of these carriers and their relations. 07
- c) Draw the $V_{CE}-I_C$ characteristics of an npn transistor fed with a constant emitter current I_E . Explain the conditions for reaching the saturation for this transistor. What happens to I_C in saturation? 07
- d) What is Q-point? Describe the significance of biasing in case of fixing a Q-point and its effect on amplification in a common-emitter connection for small signals. 06
3. a) What is Shockley's equation? Draw the transfer characteristics of an n-channel JFET. 06
- b) Define the static and dynamic resistance of a diode. 05
- c) What is a virtual ground? What are the characteristics of an ideal op-amp? 06

- d) For the circuit in Fig. 3(d), $I_{DSS} = 6 \text{ mA}$ and $V_P = -3 \text{ V}$, calculate the values of I_{DQ} , V_{GSQ} , V_D and V_S . Also draw the graph for Q-point. 08

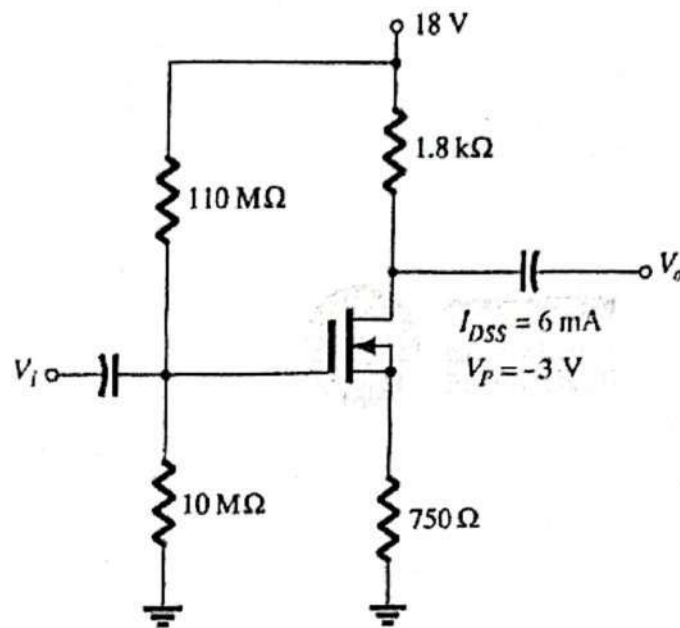


Fig. 3(d)

4. a) For the circuit in Fig. 4(a), calculate the values of I_E , I_B , V_E , V_B , I_C and V_C . 08

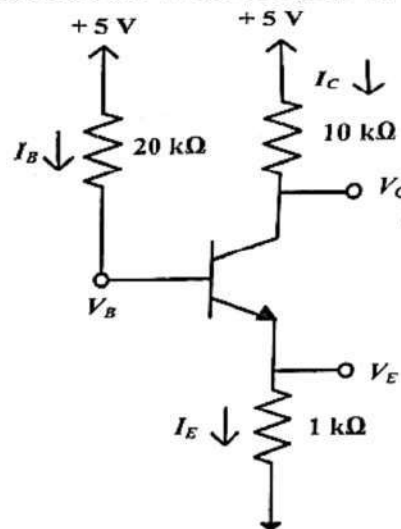


Fig. 4(a)

- b) Implement the expression $V_o = -5u + 1.5v + 3.3x + 7y - 11z$ using op-amps. 06
- c) Explain the mode of operation of a PN junction diode under the forward and reverse bias conditions. 06
- d) What are α and β for a BJT. Derive the expression by which α and β are related to each other. 05