Yousin-Maëlle 1x1

# Baseline model

Current – show 2x diagrams and top level

Simplifications

* Address = client id
* “go” signal to indicate incoming order

Questions

* Where do cancels come from ? should it be from exchange or ok from CPU ? extra cycle to be closer to reality
* Arbitrer on memory – what, when, how

# Testing

3 types testing

* Unit tests (pre defined sequences from sample order/cancels)
* Constrained random tests in system verilog
* Formal verification with Jasper gold

Good because can see signals, time, values and usage

Questions

* Does usage matter
* Jasper gold advice
* Where can get data for good example – sample market data

# Caching

Protocol ?

* 1x cache 2x processor vs 1x processor 2x cache
* Recommended cache models
* Tools for comparison