Computer Architecture Lab

Lab11 – Week #11



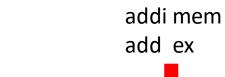
Table 2 – Signals for M_TEXT_FWD.txt

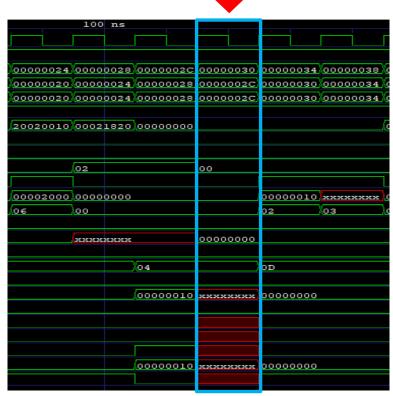
Signal Names	Descriptions	
FWD_ALU_Ai	00: From Register file of ID stage to EX stage	
	01: From ALU output of MM stage	
	10: From Writeback data of WB stage	
FWD_ALU_Bi	00: From Register file of ID stage to EX stage	
	01: From ALU output of MM stage	
	10: From Writeback data of WB stage	



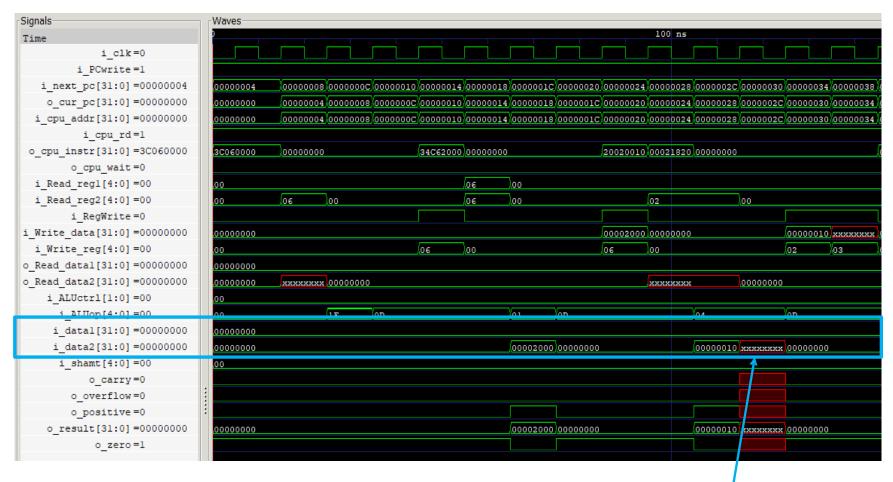
✓ example - Data hazard

```
main:
        Tui
               $6.0x0000
        nop
        nop
        nop
               $6.$6.0x2000
       ori
        nop
        nop
        nop
                $2,$0,0x10
        addi
        add
               $3,$0,$2
        nop
        nop
        nop
        nop
```









B port forwarding 필요!



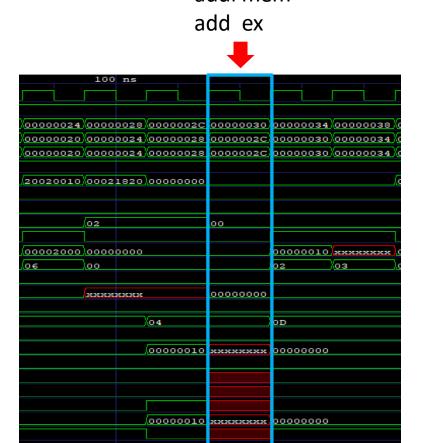
M_TEXT_SEG.txt

lui	1	0011110000000110000000000000000000
	2	000000000000000000000000000000000000000
	3	000000000000000000000000000000000000000
	4	000000000000000000000000000000000000000
ori	5	001101001100011000100000000000000
	6	000000000000000000000000000000000000000
	7	000000000000000000000000000000000000000
addi	8	000000000000000000000000000000000000000
auui	9	001000000000001000000000000010000
add	10	00000000000000100001100000100000
	11	000000000000000000000000000000000000000
	12	000000000000000000000000000000000000000
	13	000000000000000000000000000000000000000
	14	000000000000000000000000000000000000000
	15	0000000010000110010000000100010
	16	000000000000000000000000000000000000000
	17	000000000000000000000000000000000000000
	18	000000000000000000000000000000000000000
	19	000000000000000000000000000000000000000

```
M_TEXT_FWD.txt - Windows
파일(F) 편집(E) 서식(O) 보기
00_00 // 0x000 1
00_00 // 0x004 2
00_00 // 0x008 3
00_00 // 0x00C 4
00_00 // 0x010 5
00_00 // 0x014 6
00_00 // 0x018 7
00 00 // 0x01C 8
00_00 // 0x020 9
00_00 // 0x024 10
00_00 // 0x028 11
00_00 // 0x02C 12
00_00 // 0x030 13
00_00 // 0x034 14
00_00 // 0x038 15
00_00 // 0x03C 16
00 00 // 0x040 17
00_00 // 0x044 18
00_00 // 0x048 19
00_00 // 0x04C 20
```

- <u>line number or address</u> check!
- add인 10번 라인, 0x024의 B port에 forwarding signal 설정



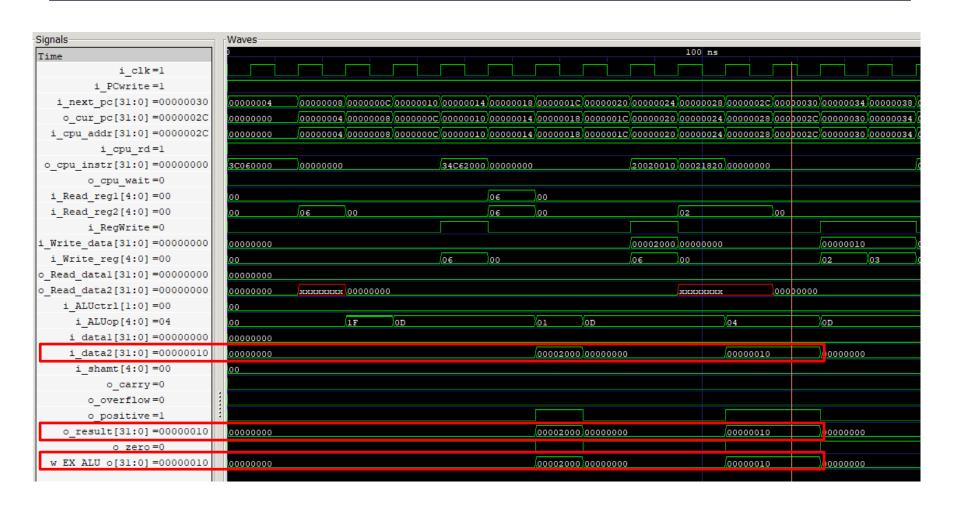


addi mem

 add의 ex stage에서 addi는 mem stage이므로 B port에 01 설정

```
🤳 M_TEXT_FWD.txt - Windows 메모장
파일(F) 편집(E) 서식(O) 보기(V) 도{
00_00 // 0x000 1
00_00 // 0x004 2
00_00 // 0x008 3
00 00 // 0x00C 4
00_00 // 0x010 5
00_00 // 0x014 6
00_00 // 0x018 7
00 00 // 0x01C 8
00 00 // 0x020 9
00_01 // 0x024 10
00_00 // 0x028 11
00_00 // 0x02C 12
00_00 // 0x030 13
00_00 // 0x034 14
00_00 // 0x038 15
00 00 // 0x03C 16
00_00 // 0x040 17
00_00 // 0x044 18
00 00 // 0x048 19
00_00 // 0x04C 20
```







Thank You