

## Christopher R. Aberger

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EDUCATION	<b>Stanford University</b> , Stanford, California <i>Doctor of Philosophy</i> in Computer Science	<i>Expected 2018</i>
	<b>Stanford University</b> , Stanford, California <i>Master of Science</i> in Computer Science	<i>Summer 2016</i>
	<b>Stanford University</b> , Stanford, California <i>Master of Science</i> in Electrical Engineering	<i>Spring 2015</i>
	<b>University of Wisconsin</b> , Madison, Wisconsin <i>Bachelor of Science</i> in Computer Science <i>Bachelor of Science</i> in Computer Engineering <i>Minor</i> in Mathematics Graduated with Highest Distinction	<i>May 2013</i>
	<b>Zhejiang University</b> , Hangzhou, China Technical communication and Mandarin course	<i>Summer 2009</i>
PUBLICATIONS	<b>EmptyHeaded: A Relational Engine for Graph Processing</b> <i>Christopher R. Aberger, Susan Tu, Kunle Olukotun and Christopher Ré</i> SIGMOD, <b>Best of</b>	2016
	<b>Old Techniques for New Join Algorithms: A Case Study in RDF Processing</b> <i>Christopher R. Aberger, Susan Tu, Kunle Olukotun and Christopher Ré</i> ICDE Workshop	2016
	<b>Have Abstraction and Eat Performance, Too: Optimized Heterogeneous Computing with Parallel Patterns</b> <i>Kevin J. Brown, HyoukJoong Lee, Tiark Rompf, Arvind K. Sujeeth, Christopher De Sa, Christopher Aberger, and Kunle Olukotun</i> CGO	2016
EXPERIENCE	<b>Stanford University</b> , Palo Alto, California <i>Research Assistant</i> under Christopher Ré and Kunle Olukotun	<i>Fall 2013-Present</i>
	<b>Apple Inc.</b> , Austin, TX <i>Design Performance Intern</i> Machine learning applied to performance analysis for A7 chip design.	<i>Summer 2013</i>
	<b>IBM</b> , Austin, TX <i>Hardware Engineering Co-op</i> Functional verification and lab bring-up procedures for Power8 chip.	<i>Summer 2012</i>
	<b>Epic Systems</b> , Madison, WI Finance Intern	<i>Summer 2010, 2011</i>
LANGUAGES	C++, Python, Scala, Java, C	

## SELECTED COURSES

### **University of Wisconsin-Madison**

Advanced Computer Architecture I (Superscalar design) (ECE 752)  
Advanced Computer Architecture II (Multi-core design) (ECE 757)  
Digital Engineering Laboratory (ECE 554)  
Digital System Design and Synthesis (ECE 555)  
Digital Signal Processing (ECE 431)  
Operating Systems (CS 537)  
Computer Graphics (CS 559)  
Algorithms (CS 577)

### **Stanford University**

Databases (CS 145)  
Automata and Complexity Theory (CS 154)  
Logic (CS 157)  
Programming Languages (CS 242)  
Topics in Database Management Systems (CS 345)  
Program Analysis and Optimizations (CS 243)  
Advanced Topics in Operating Systems (CS 240)  
Machine Learning (CS 229)

## AWARDS

*2008*, La Crosse Community Foundation Engineering Scholarship  
*2008-2012*, Wisconsin Academic Excellence Scholarship  
*2009, 2010*, Claude and Dora Richardson Engineering Scholarship  
*2010*, Polygon Excellence in Engineering Scholarship  
*2010-2011*, International Engineering Consortium Everitt Award Winner  
*2011-2012*, Tau Beta Pi National Scholar  
*2012*, Fred W. and Josephine H. Colbeck Scholarship Award