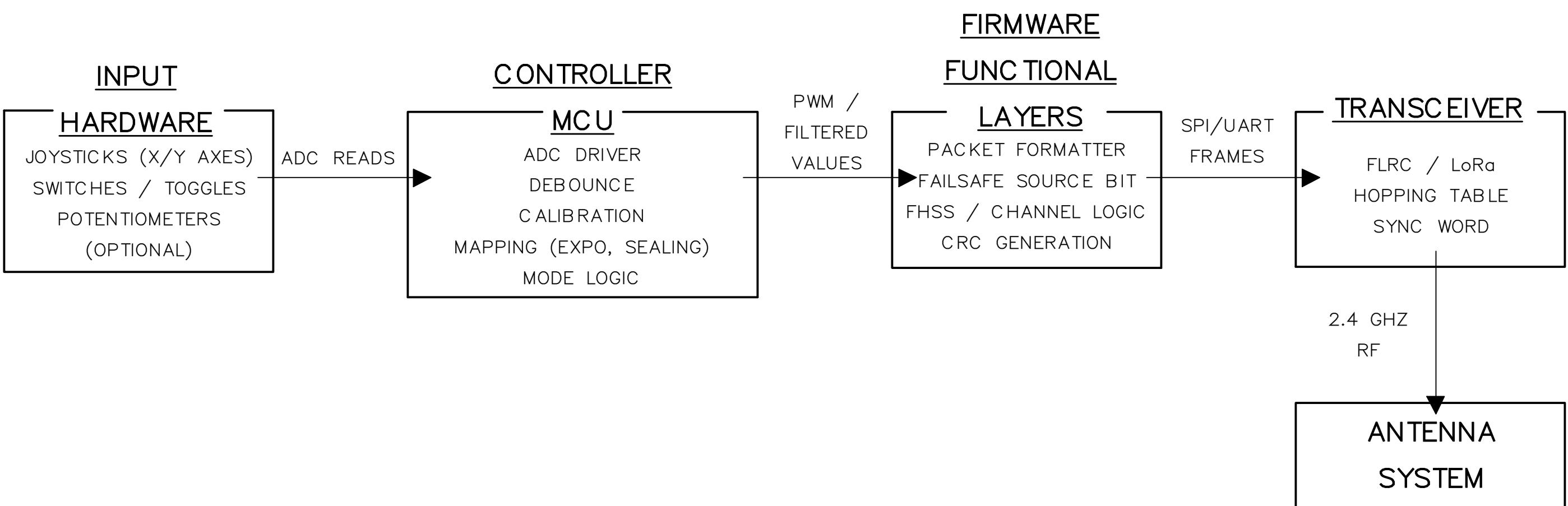


DOCUMENT REVISION HISTORY			
REV.	DATE	DESCRIPTION	AUTHORIZATION
0	11/21/25	DOCUMENT CREATION	CHRIS DILLOW



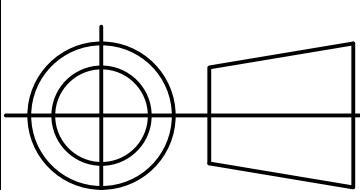
NOTES	
N1	All signal flows shown as logical (not electrical)
N2	MCU shown generically; final pinout defined in PCB schematic.
N3	Diagram is not to scale.
N4	RF module shown in Tx mode only.
N5	
N6	
N7	

TOLERANCE TABLE

USC SI ANGLE

X.X=±.016" X=±0.5mm
X.XX=±.015" X.0=±0.1mm
X.XXX=±.003" X.X=±0.05mm
X.XXX=±.001" X.XXX=±.0005"

ANGLE OF PROJECTION



DOCUMENT PREPARATION

ENGINEER CHRIS DILLOW

DRAFTER CHRIS DILLOW

CHECKER

APPROVAL

PM N/A

PROJECT ASSEMBLY

TROPOCHIEF DIAGRAMS

 CAD BY CHRIS

CONTROLLER

ARCHITECTURE

SHEET: C

SIZE: B

PART NUMBER	MATERIAL	MASS	UNIT	SCALE
N/A	N/A	N/A	N/A	N/A