



DOCUMENT REVISION HISTORY			
REV.	DATE	DESCRIPTION	AUTHORIZATION
0	11/21/25	DOCUMENT CREATION	CHRIS DILLOW

NOTES		TOLERANCE TABLE			ANGLE OF PROJECTION	DOCUMENT PREPARATION	
N1	All signal flows shown as logical (not electrical).	USC	SI	ANGLE		ENGINEER	CHRIS DILLOW
N2	MCU shown generically; final pinout defined in PCB schematic.	X.X=±1/16"	X=±0.5mm	X=±1.0"		DRAFTER	CHRIS DILLOW
N3	Diagram is not to scale.	X.XX=±.015"	X.X=±0.1mm	X.X=±0.5"		CHECKER	
N4	RF module shown in Tx mode only.	X.XXX=±.003"	X.XX=±0.05mm	X.XX=±0.1"		APPROVAL	
N5		X.XXXX=±.001"				PM	N/A
N6		X.XXXXX=±.0005"				PROJECT	ASSEMBLY
N7						TROPOCHIEF	DIAGRAMS



CONTROLLER ARCHITECTURE				SHEET: C	
PART NUMBER		MATERIAL	MASS	UNIT	SCALE
N/A		N/A	N/A	N/A	N/A