

Jumper Settings

DB-88E6218-MNG Development Board

Jumper Setting Configuration

Set the jumpers on the board as follows:

JP	Default	Functionality
JP1	1-2	ARM TMS V
JP2	Out	GPIO XX
JP3	Out	GPIO
JP4	Out	PLD software control
JP5	Out	PLD software control
JP6	Out	PLD software control
JP7	Out	PLD GPIO output

Set the resistors on the board as follows:

Resistor	Default	Functionality
R32	In *	SMI address space
R[38, 35, 36]	In, in, out	Clock frequency select reset configuration
R40	In O	Switch ports mode
R42	Out	P5 duplex mode
R44	Out	P5 rate
R45	ln .	P5 mode
R57	1n	P6 enable/disable mode
R[61, 60, 59, 58]	In, in, in, in	P6 mode reset configuration

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Revision History

Document Type	Rev#	Date	Comments
Initial Release	Α	July 24, 2003	9 1/v
	В	Sept. 30, 2003	The following changes were made in this revision:
			JP7 = Out

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