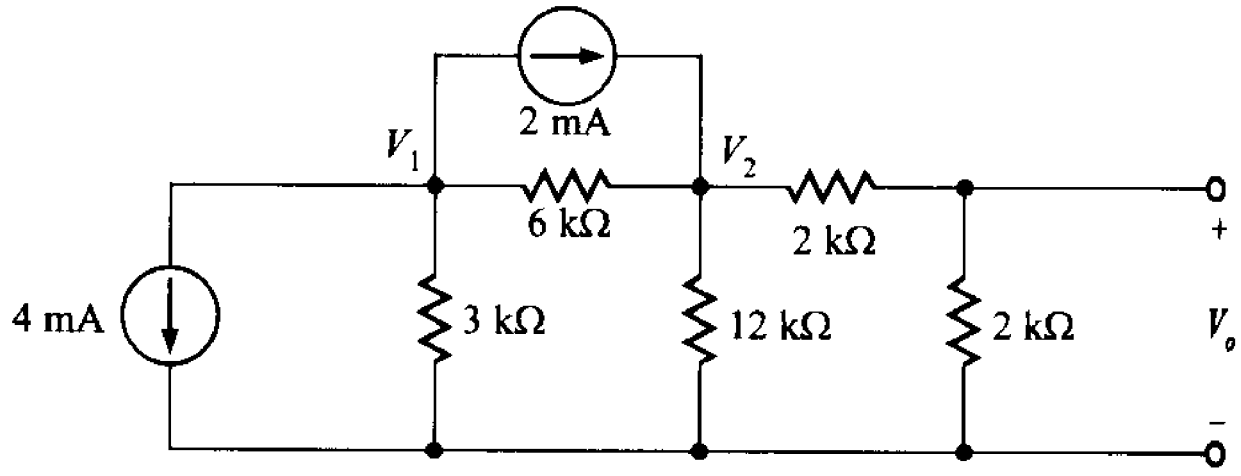
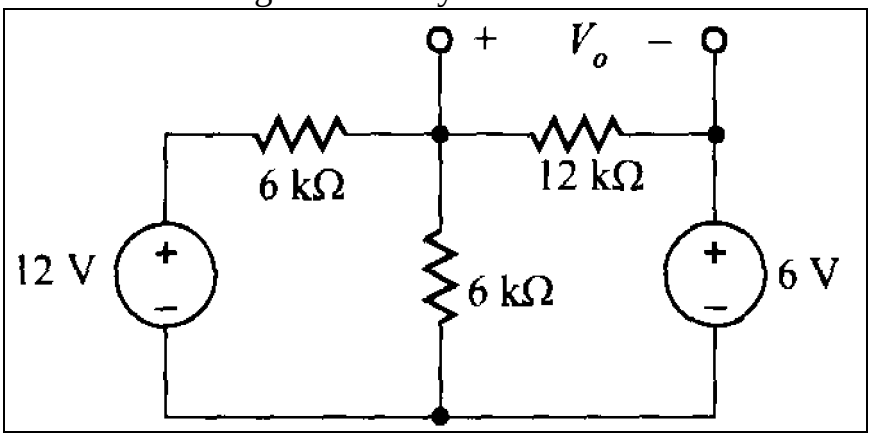
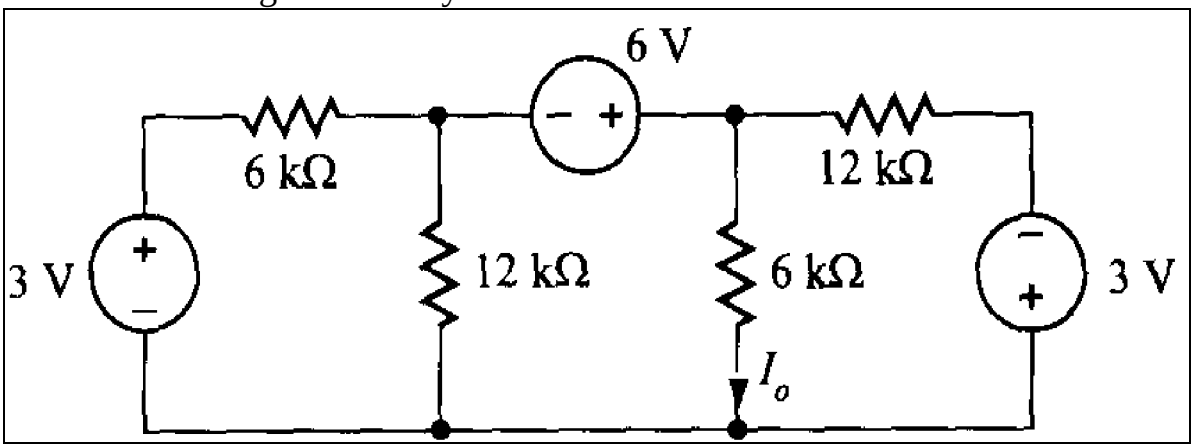
3.3. Use nodal analysis to find both V_1 and V_o in the circuit in Fig. P3.3.



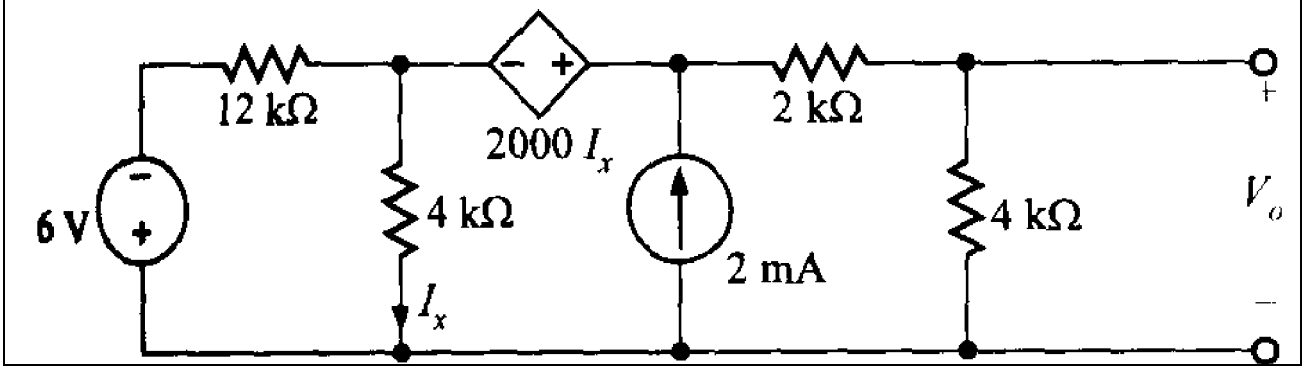
3.6 Find Vo using nodal analysis.



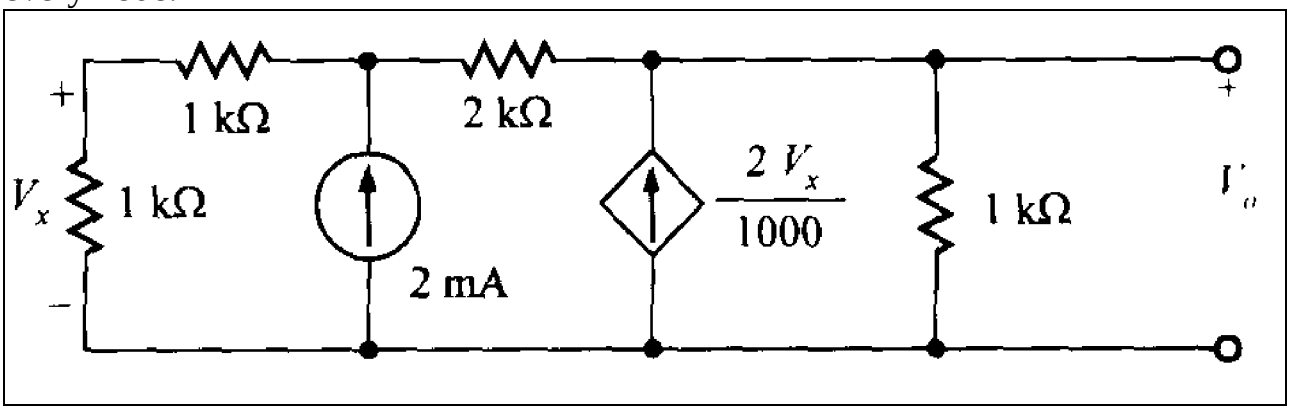
3.14 Find Io using nodal analysis.



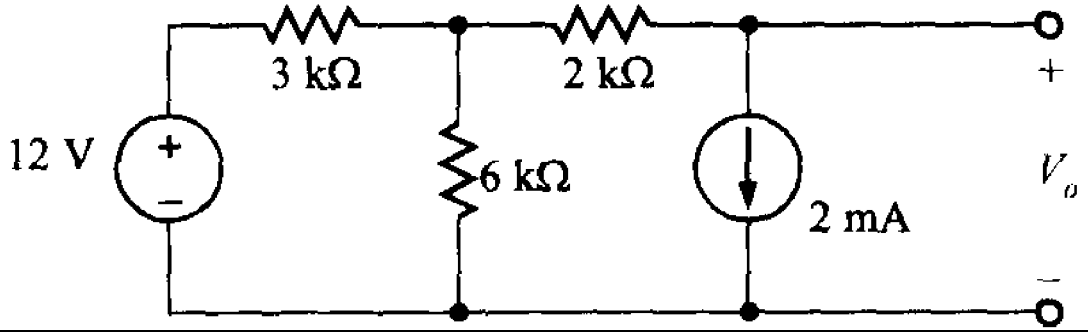
3.24. Find V_o in the circuit in Fig. P3.24. In addition, find all branch currents and check your answers using KCL at every node.



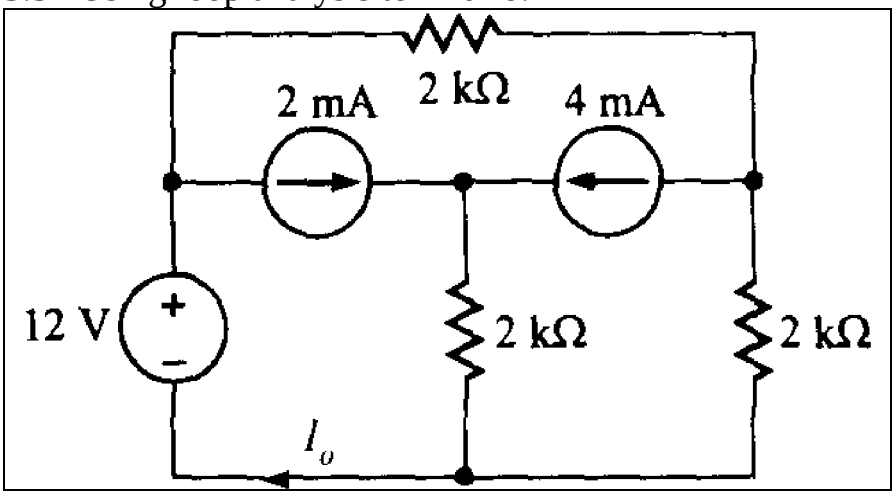
3.26 Find Vo. In addition, find all branch currents and check your answers using KCL at every node.



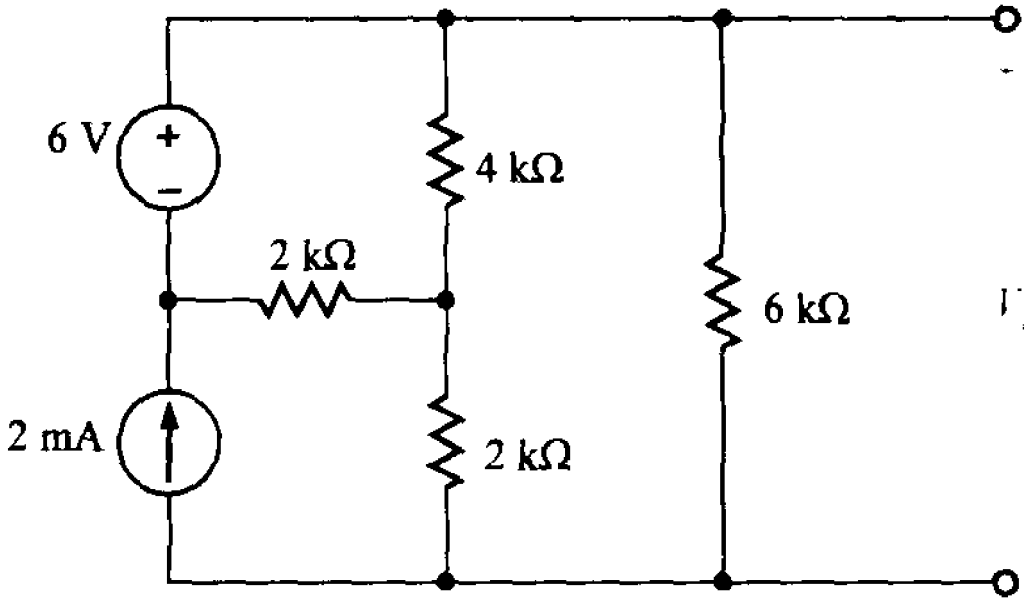
3.31. Use mesh analysis to find V_o in the network in Fig. P3.31.



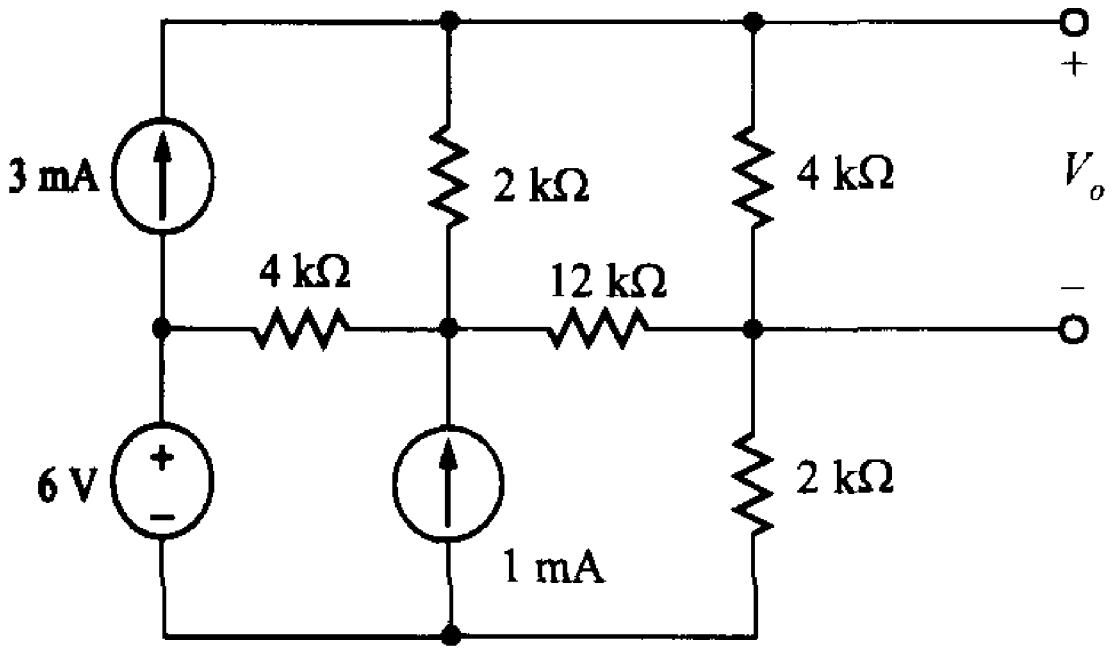
3.34 Using loop analysis to find Io.



3.37. Find V_o in the network in Fig. P3.37.



3.40. Use loop analysis to find V_o in the network in Fig. P3.40.



3.44. Use loop analysis to find V_o in the circuit in Fig. P3.44.