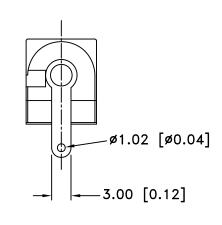


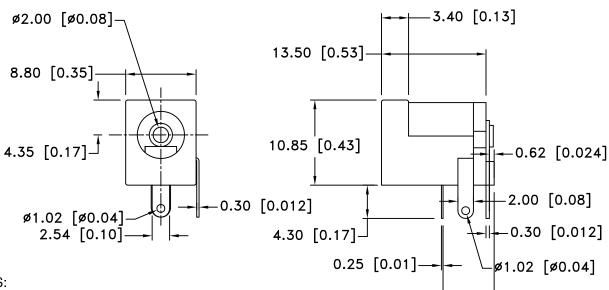
Date	SYM	REVISION RECORD	AUTH	DR.	CK.
01/13/12	D	ADDED PCB LAYOUT	T.B.	B.S.	
05/04/12	Е	UPDATED TITLE	T.B.	B.S.	
12/05/14	F	UPDATED TO ROHS 2 COMPLIANT	T.B.	B.S.	



SCHEMATIC



PCB LAYOUT



NOTES:

MATERIAL:

- 1) PIN TERMINAL: 0.4T BRASS SILVER PLATED
- 2) SLEEVE SPRING: 0.2T PHOSPHOR BRONZE
- 3) BREAK TERMINAL: 0.3T BRASS
- 4) PIN (Ø2.0): COPPER
- 5) HOUSING: PBT
- 6) COVER: PBT

ROHS 2 COMPLIANT

ELECTRICAL SPECS:

- 1. LIFE: 10,000 INSERTION/WITHDRAWAL CYCLE MINIMUM
- 2. CONTACT RESISTANCE: 50mU MAXIMUM
- 3. INSULATION RESISTANCE: 1000mU MINIMUM
- 4. DIELECTRIC WITHSTANDING VOLTAGE: 500AC MAXIMUM
- 5. MAXIMUM RATING: 5A, 12V DC
- 6. TEMPERATURE RATING: -15°C TO 90°C

Tolerance (Except as noted)	MPD Memory Protection Devices, Inc						
(Except as noted)	200 Broad Hollow Road, Farmingdale, New York 11735						
decimal			Scale	Drawn By B.S.			
±.5 (.020)				Approved By T.B.			
Fractional	Title						
± .8 (.030)	DC JACK - PCB MOUNTING 2.1 I.D. x 5.5 O.D.						
Angular	Date	Drawing Nu	mber				
± 3°	12/05/14	EJ508A					

-6.60 [0.26]