|  |
| --- |
| [Type the company name] |
| A Dynamic CAN Mailbox Extension Algorithm for a Time Triggered Hybrid Scheduler |
| [Type the document subtitle] |
|  |
| Chris Barlow |
| [Pick the date] |

|  |
| --- |
| [Type the abstract of the document here. The abstract is typically a short summary of the contents of the document. Type the abstract of the document here. The abstract is typically a short summary of the contents of the document.] |

# Acknowledgements

# Table of Contents

[Acknowledgements 1](#_Toc370237999)

[Table of Contents 2](#_Toc370238000)

[Table of Figures 4](#_Toc370238001)

[1 Introduction 5](#_Toc370238002)

[2 Background 7](#_Toc370238003)

[2.1 Controller Area Network (CAN) 7](#_Toc370238004)

[2.2 Time-Triggered Architecture 10](#_Toc370238005)

[3 Related Work / Literature Review 14](#_Toc370238006)

[4 Review of Existing Software and Technology 15](#_Toc370238007)

[4.1 Electric Commercial Vehicle Telemetry System 15](#_Toc370238008)

[4.2 Message Storage in the Existing Device 16](#_Toc370238009)

[4.3 CAN Acceptance Filtering 16](#_Toc370238010)

[5 Proposal: Dynamic CAN Filtering 18](#_Toc370238011)

[5.1 Overview 18](#_Toc370238012)

[6 Development and Testing 19](#_Toc370238013)

[6.1 Development Process Overview 19](#_Toc370238014)

[6.2 Feasibility Simulation 19](#_Toc370238015)

[6.3 Hardware Implementation incl. Remote Configuration 21](#_Toc370238016)

[7 Discussion 22](#_Toc370238017)

[7.1 Algorithms 22](#_Toc370238018)

[7.2 Simulation and Hardware Implementation Comparison 22](#_Toc370238019)

[7.3 Behaviour 22](#_Toc370238020)

[7.4 Performance Comparison with Existing System 23](#_Toc370238021)

[7.5 Future Development 23](#_Toc370238022)

[8 Conclusions 24](#_Toc370238023)

[9 Works Cited 25](#_Toc370238024)

[Appendix 26](#_Toc370238025)

# Table of Figures

[Figure 4.1: CAN ACCEPTANCE FILTERING ON AN STM STM32F407ZGT6 PROCESSOR [2] 10](#_Toc370237948)

[Figure 4.3: A simple Time-Triggered Co-operative (TTC) scheduler 12](#_Toc370237949)

[Figure 4.4: A SIMPLE TIME-TRIGGERED Hybrid (TTH) SCHEDULER 12](#_Toc370237950)

[Figure 6.1: EXISTING REMOTE DEVICE SOFTWARE OVERVIEW 15](file:///C:\Code\MScRES\MSc_project\MSc_Documents\Documents\Project_Report.docx#_Toc370237951)

[Figure 6.2: SOFTWARE FILTERING IN THE CAN DATA LOGGING PROCESS – EXISTING REMOTE DEVICE 16](file:///C:\Code\MScRES\MSc_project\MSc_Documents\Documents\Project_Report.docx#_Toc370237952)

# Introduction

* Academic bumf

Controller Area Network (CAN) has become a standard method of communication between embedded devices in automotive applications. CAN Messages contain data to be transferred between sub-systems, which are given unique identifiers to provide context about the content of the message. Nodes on the CAN bus use CAN controller hardware to buffer messages that have been transmitted by other nodes. The inherent nature of a bus network is that all nodes can ‘see’ every message that is being transmitted. A node therefore has to interrogate the identifier of a message in order to decide whether it needs to read the content. This interrogation can be carried out in software, by comparing the identifier to a list of ones that should be accepted, or by using ‘acceptance filters’ in hardware to restrict the identifiers allowed into the CAN controller’s buffer.

Both methods have drawbacks. If acceptance filters are used, the number of filters (or ‘mailboxes’) available within the CAN controller limits the number of messages that the node can accept. Therefore, if a node has interest in a large subset of messages on a CAN bus, it the solution has to involve software filtering. Interrogating the identifier in software uses up space in the buffer of the CAN controller regardless of whether the message is of interest to the node. This is particularly troublesome if the node is on a busy network where it is possible to miss messages if the software allows the buffer to become full.

This project focuses on the development of a novel approach to this problem whereby

Identifiers for CAN to be obtained are specified in a ‘logging list’ (a configurable list of CAN IDs that need to be logged) and storing this data in a precise location for retrieval by other software functions. This method will exploit the fact that the order and timing in which individual messages are published over the CAN bus is relatively predictable. The logging list will be collected from the remote server and used along with the known order / timing of CAN bus messages (‘CAN sequence’) to produce a timed ‘logging sequence’.

In a time-triggered environment, we can use this logging sequence to predict the IDs of the next messages on the CAN bus at any given ‘tick’. A periodic task can then modify the bxCAN acceptance filters to only accept the IDs of the expected messages on the bus for any given time.

As the sequence of CAN messages won’t be exactly uniform, we can specify multiple filters in the bxCAN’s identifier list to accept several IDs at the same time. This can then be modified dynamically as messages arrive.

The project draws from the subject of CAN, covered in module B3, Time triggered scheduling, which is a predominant theme in the MSc Reliable embedded systems, in particular the Time-Triggered Hybrid (TTH) scheduler introduced in A2. It also involves the subject of shared resources, which are covered in B2 and B3.

# Background

* Intro will summarise all of this, but go into more detail here

An electric commercial vehicle company uses a telemetry device to log data from a multi-bus CAN network on their vehicles. Data are transmitted over the mobile phone network (GPRS) to an AMQP message queue, where a dedicated server performs the necessary post-processing to store the information in a database.

New hardware and software approaches are now being explored for an updated device, including the ability to remotely modify which CAN messages are logged by the device. Since the data are of high importance to the company, it is imperative that the embedded software operating on the device is reliable and, because of this, a Time Triggered (TT) scheduler has been proposed to replace the predominantly event-triggered architecture currently in use. It is therefore necessary to investigate software logic that complements the inherently predictable nature of the TT scheduler, while still being compatible with the compression and transmission protocols that are currently in use.

Although the use of a TT scheduler should allow performance guarantees to be made to the company, the logging of data events using a TT scheduler is not without its challenges, which will be addressed in this and later chapters.

## Real-time and Time-Triggered Software Architecture

Real-time software is defined as software that must complete tasks to a specified deadline. In embedded systems, software must respond to one or many ‘events’, which include inputs from other systems or devices, interrupts from CPU peripherals, etc.

Time-triggered (TT) architecture is method of guaranteeing when a software operation should run. It is predominantly used for safety-critical embedded systems where it is imperative that operations are performed on time with an accuracy measured in fractions of microseconds **Invalid source specified.**.

The backbone of this architecture is a scheduler driven by a single event; a timer-driven interrupt. This interrupt is used to generate periodic ‘ticks’ that allow the scheduler to keep track of time. Software operations are divided into ‘tasks’. Hard-coded properties control the timing of the tasks using an ‘offset’ (time until first dispatch) and ‘period’ (time between subsequent dispatches). Figure 4.3 shows the behaviour of a ‘Time Triggered Co-operative’ scheduler. Each tick executes the necessary tasks. Task A is given a higher priority than Task B, so Task A is executed first when the two tasks share the same ‘tick’. Figure 4.4 shows a ‘Time Triggered Hybrid’ scheduler. Here, Task B takes longer than one tick to complete, however Task A is configured to execute from the Interrupt Service Routine (ISR). This means that Task A is guaranteed to run on time, and Task B will be suspended until Task A completes.

The one rule that guarantees accurate timing is that only one interrupt is allowed on the CPU, which is the timer interrupt. This ensures that unexpected events will not prevent the CPU from executing a task on time. With knowledge of the CPU instruction timing, it is possible to model and predict software timing very accurately, as well as guaranteeing processor loading [5]. Software driven by more than one interrupt or event is known as ‘Event Trigered’.

A

0

1

2

3

4

5

Tick

B

A

B

B

A

B

B

Task A: Offset = 0 ticks, Period = 2 ticks

Task B: Offset = 1 tick, Period = 1 tick

Figure 4.3: A simple Time-Triggered Co-operative (TTC) scheduler

A

0

1

2

3

4

5

Tick

B

A

Task A: Offset = 0 ticks, Period = 2 ticks (executed in ISR)

Task B: Offset = 1 tick, Period = 2 ticks, WCET = 1.25 ticks

B

A

Figure 4.4: A SIMPLE TIME-TRIGGERED Hybrid (TTH) SCHEDULER

## Controller Area Network (CAN)

CAN is a standard for serial data communications over a 2 wire bus. The CAN specification (2.0) describes several aspects of this communications method.

* CAN overview
  + “Enhanced CAN” Mailboxes
  + Standard CAN
    - FIFO Layers
* current options for collecting data
  + Interrupt
  + Polling
  + Link to MSc

### CAN Hardware

The CAN physical layer comprises of two wires, one held low (CAN L) and the other high (CAN H). The transmission of data is dominant 0, therefore to transmit a 0 bit, CAN L is pulled high, and CAN H is pulled low.

* DIAGRAM HERE
* CAN physical layer
* CAN transceiver
* CAN controller

### Acceptance Filtering

Traditionally, a buffer built into the CAN controller hardware has handled arriving messages. This buffer is usually in First In, First Out (FIFO) arrangement, the depth of which varies between hardware manufacturers **Invalid source specified.**. This means that the client processor must read all messages in the buffer, and interrogate the identifier in order to ascertain the context of the message data field. In order to avoid wasting processing time, such hardware usually has the option to set several ‘acceptance filters’ that ensure that only relevant messages are stored in the buffer. The number of acceptance filters, again, varies between manufacturers.

* + Include table of manufacturers, FIFO depths and acceptance filter sizes.

A typical program flow to retrieve data using an acceptance filter would be as follows:

* A message arrives on the CAN bus.
* The CAN controller interrogates the message identifier.
* The identifier passes an acceptance filter and the CAN controller stores the message in the FIFO.
* The CAN controller will either generate in interrupt, or raise a pollable flag to indicate message arrival to the microcontroller.
* The microcontroller reads the message from the FIFO, and interrogates the identifier in order to determine where to store the data.

In modern hardware, CAN controllers are integrated into the microcontroller silicone. This has the advantage that CAN messages can be stored directly in Direct Memory Access (DMA) registers, allowing for much faster retrieval of data **Invalid source specified.**. This advance has brought with it more sophisticated methods of handling CAN messages.

The STM32F407ZGT6 uses a ‘Filter Match Index’ registry field to store the acceptance filter that each FIFO entry matched. This gives the software visibility of the message context without needing to interrogate the identifier.

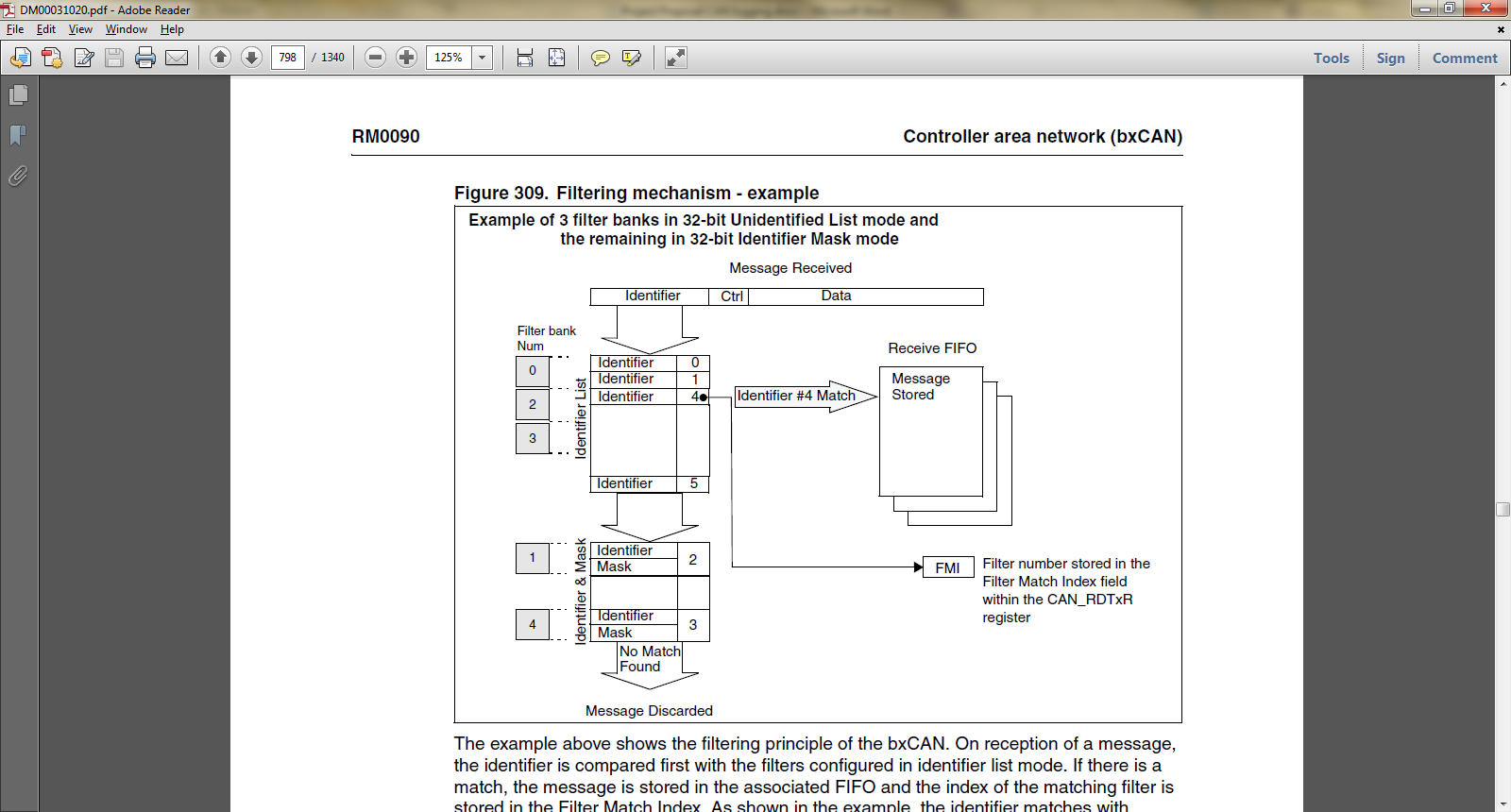


Figure 4.1: CAN ACCEPTANCE FILTERING ON AN STM STM32F407ZGT6 PROCESSOR [2]

Another method is the use of ‘mailboxes’, as used by Texas Instruments [3]. Instead of storing all CAN messages in one FIFO and leaving it up to the microcontroller to determine the context of the message, the CAN controller stores the message in a specific area of memory depending on its identifier. This now means that when the CAN controller indicates a message arrival, either using an interrupt, or a flag, the microcontroller can now read the data directly from the mailbox and knows the context without using any look-up mechanisms.

This method is very useful for devices interested in a relatively small subset of messages, however with the maximum possible number of identifiers of a standard frame being 2048 (000h to 7FFh), a device such as a data logger may need to ‘accept’ a greater number of message identifiers than is allowed by the hardware mailbox mechanism.

## Using CAN with TT Architectures

As mentioned previously, Time-Triggered architecture can only behave predictably if there are no interrupts active other than the timer that drives the scheduler. Depending on the configuration of the buffer, messages arriving when the buffer is full will either be discarded, or replace a message already in the buffer. This means that, in order to handle CAN message arrival events, there is no choice other than to poll the CAN controller periodically to detect CAN messages. In order to see every message on the CAN bus, a buffer-based controller must be polled at least as often as:

Where *fpolling*is the polling frequency, *fmax* is the maximum frequency of incoming messages, and *Dbuffer* is the depth of the CAN controller’s receive buffer.

This could pose problems in situations where there are more unique messages present on the CAN bus than buffer locations, particularly when the data is transmitted in ‘bursts’ by the other node(s) on the network.

Mailbox-based CAN controllers, on the other hand, are much better suited to TT architecture in that they always hold the latest value for the configured CAN message. This means that even if the CAN controller isn’t polled as often as the messages arrive, the hardware will handle the message arrival, and no messages will be lost.

* TT scheduling
  + Can only use polling

# Related Work / Literature Review

* Pont’s work on TT systems (obviously)
* Caching behaviour – link to what we’re trying to achieve but explain the differences.
* Shared resources? We’re treating the mailbox as a resource ‘shared’ by all of the ID’s on the network.

# Review of Existing Software and Technology

* Existing application
  + Some time-released functions
    - Don’t want to call these TT since the timing is not so accurate
  + event-triggered (interrupts, interrupts, interrupts!) functionality going on at the same time – typical of a non-time critical application built by two independent teams
  + No way of guaranteeing hit rate of CAN messages.

## Electric Commercial Vehicle Telemetry System

Vehicle Comms buses

Mass storage (microSD card)

GSM Module

Remote AMQP message queue

Logging

Transmission

Interrupt

Interrupt

Internal hardware timer

Figure 6.1: EXISTING REMOTE DEVICE SOFTWARE OVERVIEW

* **Data logging functions** - performed using timer-driven interrupts.
  + CAN and sensor data are collected from the vehicle and buffered internally.
  + Buffered data is compressed and stored on a micro SD card in data ‘blocks’ of around   
    4 – 10 kB.
* **Data transmission functions** – performed using an interrupt on the GSM module, indicating that the module is connected to the network.
  + A connection is made to a remote AMQP message queue.
  + Data is read from the SD card. The large data blocks are split into several 1kB ‘chunks’ and sent to the message queue.
  + If the device loses network signal, the software sits in one of several ‘while’ loops until a GSM interrupt event occurs. During this time, the data storing functions are still operating on timer interrupts, allowing the device to still collect data during periods of low or no GPRS signal.

## Message Storage in the Existing Device

The compression logic used by the telemetry system requires each CAN message, identified by a unique ID, to be stored in the same location every time so that comparisons can be made between old and new data. For this reason the CAN data is filtered and stored by the software as follows:



Figure 6.2: SOFTWARE FILTERING IN THE CAN DATA LOGGING PROCESS – EXISTING REMOTE DEVICE

This means that whenever a CAN message is seen on the CAN bus, the device has to perform some processing on it, whether the device is interested in the message or not.

## CAN Acceptance Filtering

Plans are to build the new Remote Device around an ARM Cortex M4 processor such as the STM32F407ZGT6. This processor provides 28 filter banks, each capable of holding four 16-bit Identifiers [2]. As a message arrives on the CAN bus, the processor transparently compares the identifier with those in the filter lists, and stores it in a specific memory location. If the message doesn’t match any of the filters, it is discarded.

The use of this acceptance filtering feature at hardware level could potentially free up processing time in the device software as there is no need to perform the comparison to check that the message is needed.

# Proposal: Dynamic CAN Filtering

* Proposed Application

Data logging application running on Time Triggered scheduler occupying busy CAN bus (> 32 Different messages) alongside event-triggered components

* + - Cannot poll fast enough to collect data while performing other processor-intensive tasks
      * Not enough mailboxes in eCAN
      * At worst case, messages arrive in ‘burst’ (immediately after each other with period of cycle time) so FIFO not deep enough.
    - Cannot use interrupts because this breaks the “1 interrupt per CPU” rule.

Solution – Use mailboxes with dynamic assignment

## Overview

* More detail about the proposed system
* Questions
  + Is this feasible given the unpredictable nature of CAN
  + How much do we need to know about a given CAN bus in terms of number of messages, cycle time, time between messages
    - In other words how ‘dynamic’ is dynamic?
  + How does this compare to the existing application?
    - Hit rate
    - Processor utilisation
    - SANITY!

# Development and Testing

## Development Process Overview

Due to the many variable properties for the algorithm, an iterative development process was used to ensure that the resultant algorithm was as refined as possible. The following approach was taken:

### Feasibility simulation

A simulation application was written in ‘C’ that reads through a CAN message log, or ‘trace’, and uses the trace timestamps to determine whether a message would be caught by the filtering algorithm. This meant the algorithm could be adjusted without needing to upload the code to hardware. This allowed for a faster initial development, and allowed for automatic cycling of the simulation with varying control parameters for quicker analysis.

### Embedded software and Configuration and Analysis application

The embedded software was developed using the lessons learned from the feasibility simulation. The algorithm was ported to the target processor using a TTH scheduler.

In order to satisfy the ‘remote configuration’ aspect of the project, an application was written to transmit configuration data over a serial (RS232) connection. The application also displayed feedback on the behaviour of the filter mechanism, and allowed for fine-tuning to be made to the algorithm.

### Existing Device Comparison

Code in the existing telemetry device was ‘instrumented’ to allow further comparison with the existing system.

## Feasibility Simulation

### Overview

In order to determine the feasibility of the proposed algorithm, a simulation was built.

* **Find logging sequence**

Finds all unique ID’s in the CAN trace that are included in the logging list. These ID’s are arranged into a sequence. For now, this is the order that ID’s first appear on the CAN bus.

* **Count sequence**

Counts the number of unique ID’s in the Logging Sequence.

* **Check logability**

This simulates the multi-ID acceptance filter, and performs the main ‘logability’ analysis on the CAN trace. It functions as follows:

* + A time-triggered, periodical logging task is simulated that, in a real embedded system, would read all logged messages from the CAN buffer, and update the acceptance filter. The period of this simulated task is controlled by “LOGGING\_TASK\_PERIOD\_us”.
  + The number of ID’s in the acceptance filter (represented by the array, “acceptanceFilter[]”) is configurable with the argument, “filterSize”.
  + acceptanceFilter[] is loaded from the top of the loggingSequence[] array up to the size, filterSize.
  + A variable, “sequencePointer”, is used to keep track of the location in loggingSequence[].
  + The CAN trace is read, line-by-line, and each CAN ID is extracted.
  + The CAN ID is first checked to see if it falls in the Logging List.
    - If the ID is in the Logging List, acceptanceFilter[] is interrogated to see if the ID is present.
      * If the ID is present in the acceptance filter, the ID has been ‘captured’.
      * A counter relating to the captured ID is incremented, as is a general “IDLogCount”.
      * The ID is marked as ‘logged’ in the acceptance filter.
    - If the ID is not present in the acceptance filter, the ID has been ‘missed’ and IDMissedCount is incremented.
  + The timestamp of each message is interrogated to identify when the simulated logging task should run. When LOGGING\_TASK\_PERIOD\_us has expired, each ID in the acceptance filter that has been marked as ‘logged’ is replaced by the next ID in the logging sequence that isn’t already present in the acceptance filter.

### Questions

### Method

### Results

## Hardware Implementation incl. Remote Configuration

### Metrics

### Method

### Results

# Discussion

## Algorithms

* Two parts
* Multiple mailbox ‘schedules’?
  + Group by similar cycle times – ie 20 ms / 100 ms divide in example trace.

## Simulation and Hardware Implementation Comparison

## Behaviour

With no duplication, sequencePointer skips 6 since the message is processed after the update

|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 0 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 1 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 2 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 3 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 4 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 5 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 7 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 8 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 9 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 0 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 6 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 1 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 2 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 3 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 4 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 5 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 6 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 7 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 8 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 9 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |

With duplication, 6 is allowed to be kept in the filter, despite being answered after the update

|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 0 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 1 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 2 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 3 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 4 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 5 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 7 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 8 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 9 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 0 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 6 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 1 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 2 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 3 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 4 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 5 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 6 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 7 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 8 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| 9 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |

## Performance Comparison with Existing System

## Future Development

* Combined ‘hard’ mailboxes with dynamic mailboxes to guarantee collection of critical messages
* Hardware / VHDL implementation
  + Layer between COTS CAN controller
  + Built into custom CAN controller

# Conclusions

# Works Cited

|  |  |
| --- | --- |
| [2] | STMicroElectronics, “Controller area network (bxCAN),” in *RM0090 Reference Manual: STM32F405xx, STM32F407xx, STM32F415xx and STM32F417xx advanced ARM-based 32-bit MCUs*, 2012, pp. 784 - 825. |
| [3] | Texas Instruments, TMS320F2833x, 2823x Enhanced Controller Area Network (eCAN) Reference Guide, Dallas, Texas, 2009. |
| [5] |  |
| [6] | Microchip Corporation, “PIC32MX5XX/6XX/7XX Family Data Sheet: High-Performance, USB, CAN and Ethernet 32-bit Flash Microcontrollers,” 2011. |
| [7] | Wikipedia, “RS-485,” 25 August 2012. [Online]. Available: http://en.wikipedia.org/wiki/RS-485. [Accessed 11 September 2012]. |

# Appendix