CS 4900
Project: Blacktop
TPS Report
10/27//2019

Team:	Skyler Sheler	skyler.j.sheler@wmich.edu	(616) 438-3527
	Erron Johnson	erron.d.johnson@wmich.edu	(269) 547-8933
	Allin Kahrl	f.allin.kahrl@wmich.edu	(207) 522-4859
	Tyler Henniges	tyler.m.henniges@wmich.edu	(269) 330-4229
Client:	WMU Computer Club	colin.c.maccreery@wmich.edu	(269) 276-3106
Contact:	Colin MacCreery	colin.c.maccreery@wmich.edu	(269) 276-3106
Project Lead	Allin Kahrl	f.allin.kahrl@wmich.edu	(207) 522-4859

Task	Who will complete	Time	Risk 1-10	% complete	Actual time	review
T1	SS AK TH EJ	1 hour or less	1	100%	1 hour	SS AK TH EJ
T2	SS AK TH EJ	1 hour or less	1	100%	1 hour	SS AK TH EJ
T3	SS AK TH EJ	1 hour or less	1	100%	15 min	SS AK TH EJ
T4	SS AK TH EJ	1 hour or less	1	0%	TBD	TBD
T5	AK SS	1-2 hours	3	100%	2 hours	SS AK
T6	SS AK TH EJ	1 week	1	100%	1 week	SS AK TH EJ
T7	SS AK TH EJ	1 hour	1	100%	1 hour	SS AK TH EJ
T8	SS AK TH EJ	1 hour	1	0%	TBD	TBD
T9	SS AK TH EJ	TBD	5	0%	TBD	TBD

T1: Decide on a compiler.

GCC will be used as the compiler for the project. All group members have experience using GCC with the board from time spent in Colin MacCreery's CS2230 course.

T2: Decide on an OS.

Ubuntu 18.04 will be used for the project as it is the operating system currently used in CS 2230. Spikes may also be run on using the board with Mac OSX.

T3: Decide on a version control system.

Github will be used as a version control system since all repositories can be kept private and all members have experience using it from other classes and previous careers.

T4: Install the toolchain for the MSP 430.

The toolchain necessary to use the board will have to be installed before any circuit prototypes can be tested.

T5: Solder the Launchpad Chips to TSSOP-28 Breakout boards.

In order to maximize the number of pins available on the daughter board a larger chip will be used, and must be soldered to a TSSOP-28 breakout board to be used with a breadboard.

T6: Obtain the peripherals used by the class.

The peripherals currently used by the class will be built into our prototype breadboard circuit and potentially changed later for final production of the board assembly.

T7: Decide on a programming standard.

The Barr Standard will be used for the scope of this project.

T8: Decide on a licence for the project.

Licencing has yet to be decided upon. This will have to be discussed with the client at our next meeting.

T9: Begin breadboarding a prototype board.

The components will have to be socketed into a breadboard and tested for full functionality. This is currently the largest portion of the project to overcome and time specifications will have to be further analyzed.