

CS 4910

Project: Blacktop

TPS Report

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Task	Who will complete	Time	Risk 1-10	% complete	Actual time	review
T1	SS	1 hour	1	100%	1 hour	AK TH EJ
T2	SS AK TH EJ	10 hours	3	100%	10 hours	SS AK TH EJ
T3	SS AK TH EJ	10 hours	6	80%	TBD	TBD
T4	AK	5 hours	4	100%	5 hours	SS TH EJ
T5	SS AK	5 hours	1	100%	12 hours	SS AK TH EJ
T6	SS AK	3 hours	5	100%	3 hours	EJ TH
T7	SS	30 min	3	100%	30 min	EJ TH AK

T1: Write the requested deliverables for the week

Write the TPS Report and Stories for the week

T2: Finish breadboarding a prototype board.

The components will have to be socketed into a breadboard and tested for full functionality. This is currently the largest portion of the project to overcome and time specifications will have to be further analyzed.

T3: Develop drivers using SPI to interface with the on-board EEPROM

Drivers must be developed using a serial peripheral interface to transfer data from the main board to the on-board EEPROM

T4: Create cad files and order revision 3 pcb boards after testing the revision 1 and 2 boards.

T5: Refine the preliminary powerpoint presentation to reflect desired improvements listed by the class

T6: Probe the current rev1 and rev2 boards for why the potentiometer does not work.

T7: Solder and assemble a revision 3 board

The revision 3 board must be completed to ensure that the assembly is error free. A test program will be flashed to it to ensure that it functions properly.