CS 4900
Project: Blacktop
TPS Report
11/11//2019

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Task	Who will complete	Time	Risk 1-10	% complete	Actual time	review
T1	SS	1 hour	1	100%	1 hour	AK TH EJ
T2	SS AK TH EJ	1 hour or less	1	75%	TBD	TBD
T3	SS AK TH EJ	1 hour	1	100%	1 hour	AK SS TH EJ
T4	SS AK TH EJ	10 hours	4	30%	TBD	TBD
T5	SS AK TH EJ	10 hours	7	20%	TBD	TBD
T6	SS AK TH EJ	10 hours	8	20%	TBD	TBD

T1: Write the requested deliverables for the week

Write the TPS Report and Stories for the week

T2: Install the toolchain for the MSP 430.

The toolchain necessary to use the board will have to be installed before any circuit prototypes can be tested.

T3: Decide on a licence for the project.

Licencing has yet to be decided upon. This will have to be discussed with the client at our next meeting.

T4: Begin breadboarding a prototype board.

The components will have to be socketed into a breadboard and tested for full functionality. This is currently the largest portion of the project to overcome and time specifications will have to be further analyzed.

T5: Develope drivers using SPI to interface with the on-board EEPROM

Drivers must be developed using a serial peripheral interface to transfer data from the main board to the on-board EEPROM

T6: Develop the CAD files for the production circuit board

The circuit board must be designed via KiCAD before a prototype board can be ordered