CS 4910
Project: Blacktop
TPS Report
2/17/2020

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Task	Who will complete	Time	Risk 1-10	% complete	Actual time	review
T1	SS	1 hour	1	100%	1 hour	AK TH EJ
T2	SS AK TH EJ	5 hours	3	100%	TBD	TBD
T3	SS AK TH EJ	10 hours	3	100%	TBD	TBD
T4	SS AK TH EJ	10 hours	6	75%	TBD	TBD
T5	AK	5 hours	4	25%	TBD	TBD
T6	SS	10 hours	5	50%	TBD	TBD
T7	SS AK	5 hours	1	50%	TBD	TBD
T8	EJ TH	1 hour	1	100%	1 hour	TBD
T9	EJ TH	7 hours	1	100%	7 hours	SS AK

T1: Write the requested deliverables for the week
Write the TPS Report and Stories for the week

T2: Test the board to see if it can handle all of the peripherals being turned on at once
The maximum current load of the board must be determined, and if turning on all peripherals exceeds that load a failsafe must be developed to prevent the board from breaking.

T3: Finish breadboarding a prototype board.

The components will have to be socketed into a breadboard and tested for full functionality. This is currently the largest portion of the project to overcome and time specifications will have to be further analyzed.

T4: Develop drivers using SPI to interface with the on-board EEPROM

Drivers must be developed using a serial peripheral interface to transfer data from the main board to the on-board EEPROM

T5: Create revision 3 boards after testing the revision 1 and 2 boards.

T6: Begin Soldering the revision 1 and 2 boards.

T7: Refine the preliminary powerpoint presentation to reflect desired improvements listed by the class

T8: Fix revision 1 board

A solder bridge and an error in the traces was found in the rev1 board. This will need to be corrected for rev 3 as it is probably present in rev 2. Bodge wire was used to accomplish this

T9 write a test program for the rev1 board