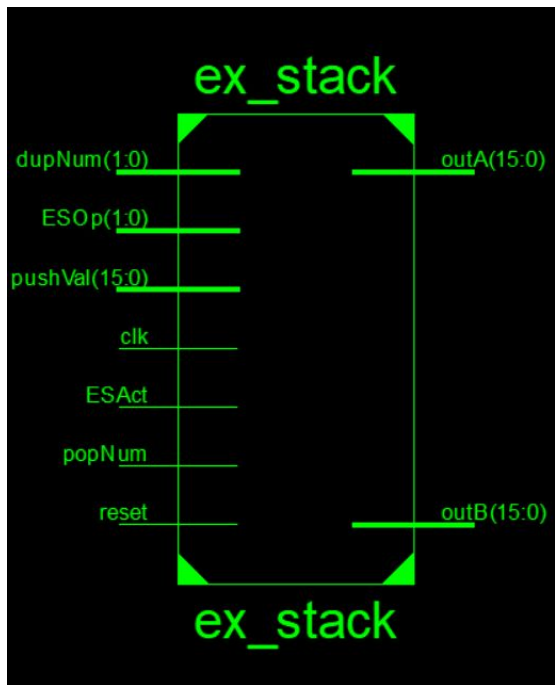


# The Eggo Stack

The Toaster Troop  
William Dalby, Christian Meinzen, Victoria (Tori) Szalay



# The Eggo Stack



Name	Value		999,993 ps	999,994 ps	999,995 ps	999,996 ps	999,997 ps	999,998 ps	999,999 ps
pushVal[15:0]	1111111111111111					1111111111111111			
popNum	0								
dupNum[1:0]	00					00			
ESOp[1:0]	00					00			
ESAct	1								
clk	0								
reset	0								
outA[15:0]	xXXXXXXXXXXXXXXX				XXXXXXXXXXXXXXXXXX				
outB[15:0]	xXXXXXXXXXXXXXXX				XXXXXXXXXXXXXXXXXX				
stack[0:31,15:0]	(XXXXXXXXXXXXXXXXXX)		XXXXXXXXXXXXXXXXXXXXX,XXXXXXXXXXXXXXXXXXXXX,XXXXXXXXXXXXXXXXXXXXX,XXXXXXXXXXXXXXXXXXXXX,XXXXXXXXXXXXXXXXXXXXX,XXXXXXXXXXXXXXXXXXXXX,						
tos[31:0]	0000000000000000				0000000000000000	100000			
impl1[15:0]	xXXXXXXXXXXXXXXX				XXXXXXXXXXXXXXXXXX				
impl2[15:0]	xXXXXXXXXXXXXXXX				XXXXXXXXXXXXXXXXXX				

Address	Disassembly	Comment	Value	Hex	Dec
[26,15:0]	XXXXXXXXXXXXXXXX		XXXXXXXXXXXXXXXX		
[27,15:0]	XXXXXXXXXXXXXXXX		XXXXXXXXXXXXXXXX		
[28,15:0]	XXXXXXXXXXXXXXXX		XXXXXXXXXXXXXXXX		
[29,15:0]	XXXXXXXXXXXXXXXX		XXXXXXXXXXXXXXXX		
[30,15:0]	IIIIIIII10000000		IIIIIIII10000000		
[31,15:0]	IIIIIIIIIIIIIIII		IIIIIIIIIIIIIIII		
tos[31:0]	0000000000000000		00000000000000000000000000000000 100000		



# Instructions



# Instruction Set

List of Commands:

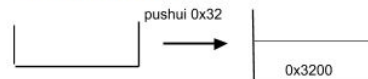
Command	Opcode	Funct	Type
pushM	0x0 / 0b0000	0b00	C
popM	0x1 / 0b0001	0b00	C
pushR	0x0 / 0b0000	0b01	C
popR	0x1 / 0b0001	0b01	C
pushli	0x2 / 0b0010	n/a	C
pushui	0x3 / 0b0011	n/a	C
dup	0x4 / 0b0100	n/a	A
flip	0x5 / 0b0101	n/a	A
or	0x6 / 0b0110	n/a	A
add	0x7 / 0b0111	n/a	A
sub	0x8 / 0b1000	n/a	A
ls	0x9 / 0b1001	n/a	A
as	0xA / 0b1010	n/a	A
<u>slt</u>	0xB / 0b1011	n/a	A
beq	0xC / 0b1100	n/a	B
bne	0xD / 0b1101	n/a	B
j	0xE / 0b1110	n/a	B
js	0xF / 0b1111	n/a	B

6. **pushui** takes the upper 8 bits of a 16 bit immediate, zero extends it, and stores it on the top of the stack.

ISA: C-type

Example: **pushui 0x32**

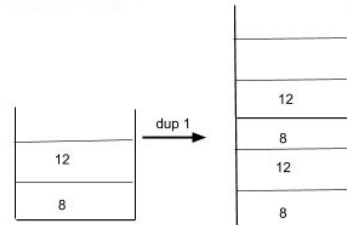
Visualization of the stack



7. **dup** looks at the specified amount of data from the top of the stack, copies the data, and pushes it on to the top of the stack

ISA: A-type

Example: **dup 1**





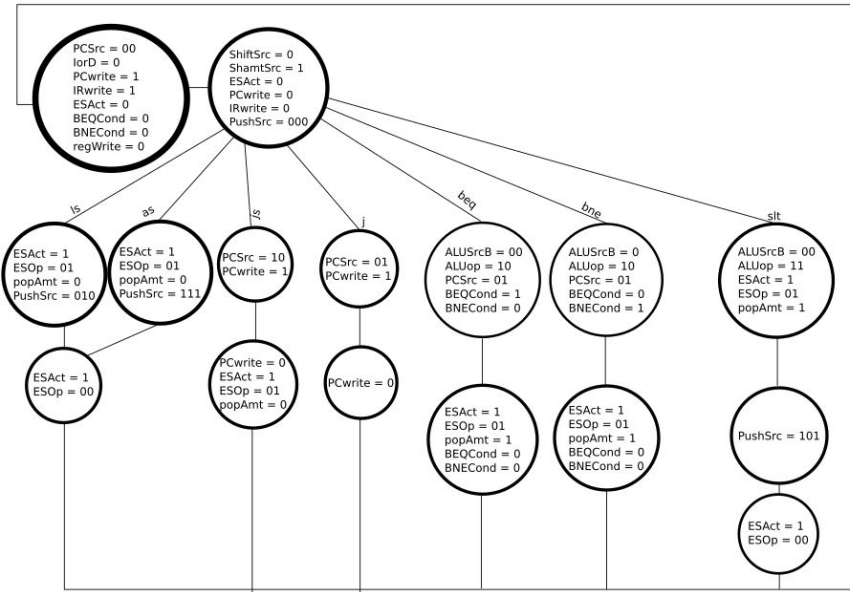
# RTL Diagrams

Step	slt	lsr/lsr	flp	dup	pushU/LI
	newPC = PC + 2 PC = newPC Inst = Mem[PC]				
	A = ES[Top] B = ES[Top + 2]				
				dupAMT = inst[1-0] if dupAMT == 0: ES[Top+2] = ES[Top] Top = Top+2 Done  if dupAMT == 1: ES[Top+2] = ES[Top-2] ES[Top+4] = ES[Top] Top = Top+4  if dupAMT==2: ES[Top+2] = ES[Top-4] ES[Top+4] = ES[Top-2] ES[Top+6] = ES[Top] Top = Top + 6  Else: ES[Top+2] = ES[Top - 6] ES[Top+4] = ES[Top-4] ES[Top+6] = ES[Top-2] ES[Top+8] = ES[Top] Top = Top + 8	UI: ShiftOut = SE(inst[11-4]) << 8  LI: push ZE(inst[11-4])
	ALUOut = A < B ES(pop 1)	ShiftOut = A shifted by Shamt in appropriate direction ES(push ShiftOut)	Push A Push B		
	skip				ES(push ShiftOut)
	ES(push ALUOut)				

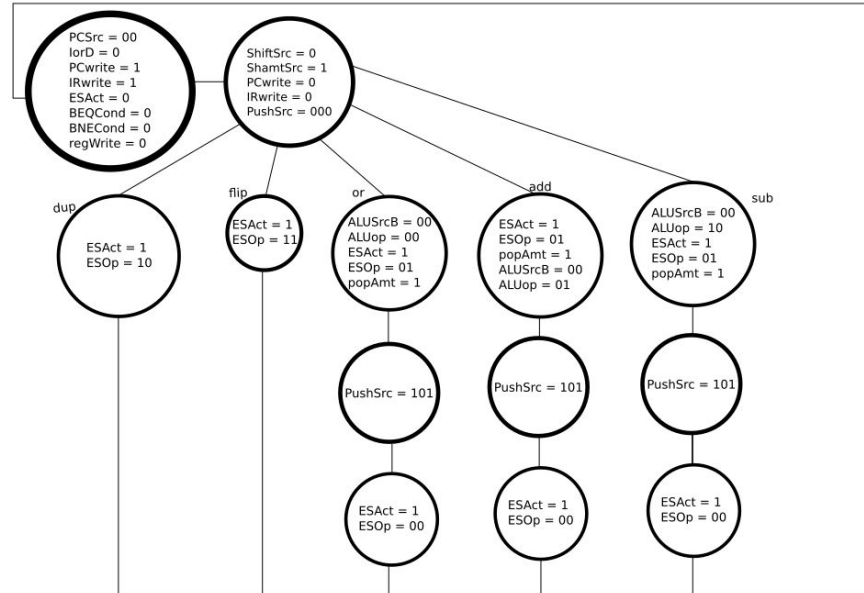
Step	Push/Pop M	Push/Pop R	Arithmetic/ Logic	beq/bne	j	js
	newPC = PC + 2 PC = newPC Inst = Mem[PC]					
	A = ES[Top] B = ES[Top + 2]					
		popR: Reg[inst[1-0]] = A  ES(pop 0)	ALUOut = A op B ES(pop 1)	ALUOut = A == B	PC = PC[15-11]    inst[11-0]	Address = pop PC = address
	popM: Mem[ALUOut] = ES(Top + 2) pushM: ES(pop 0)	pushR: ES(push Reg[inst[1-0]])	Skip	ES(pop 1) if ALUOut == 1 then PC = PC[15-11]    inst[11-0]	skip	ES(pop 0)
	popM: DONE pushM: MemOut = Mem[ALUOut]		ES(push ALUOut)			
	pushM: skip					
	pushM: ES(push MemOut) DONE					

# Multicycle Diagrams

## SLT and Shifting



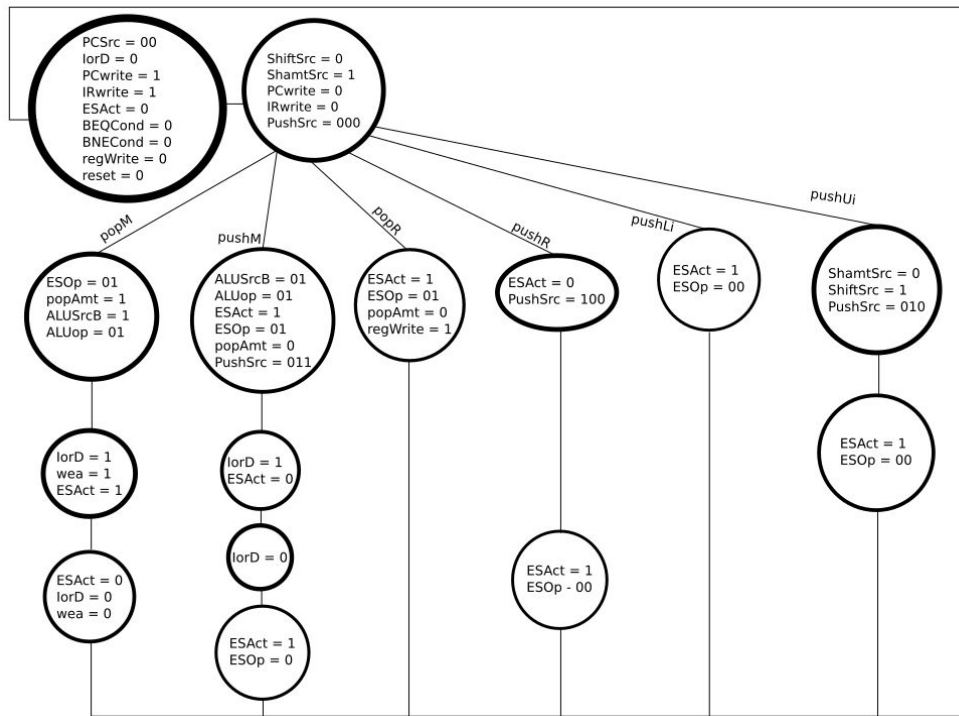
## Arithmetic Instructions





# Multicycle Diagrams

## Push and Pop

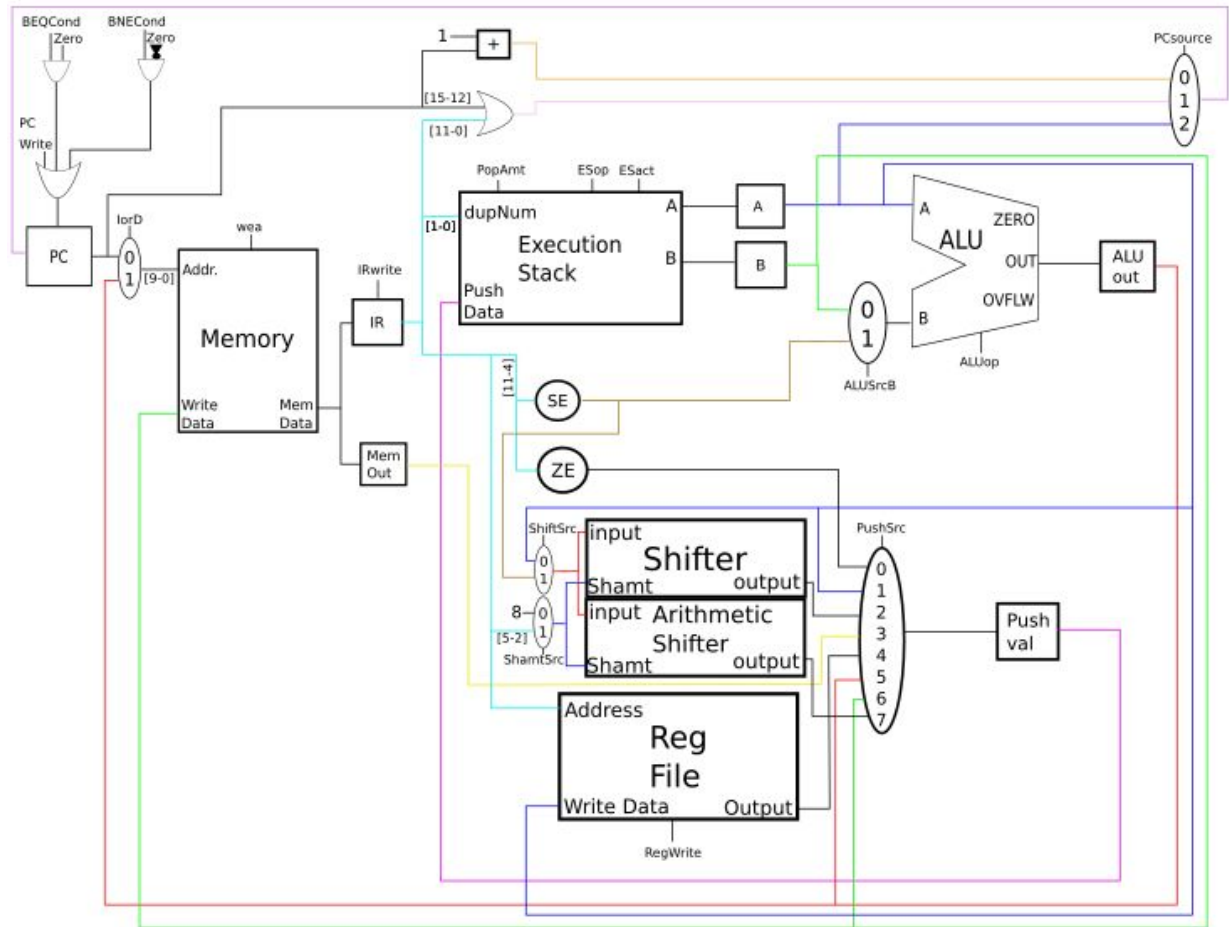






# Datapath

# Datapath

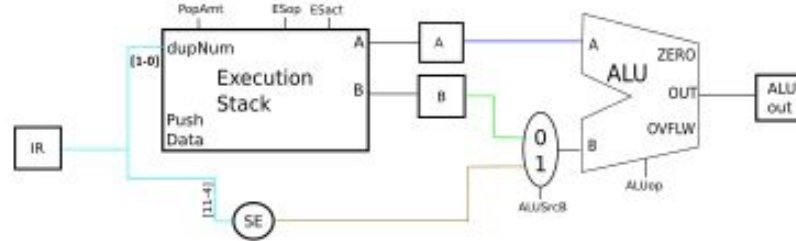
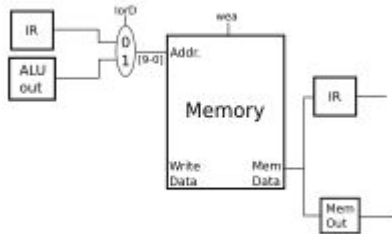
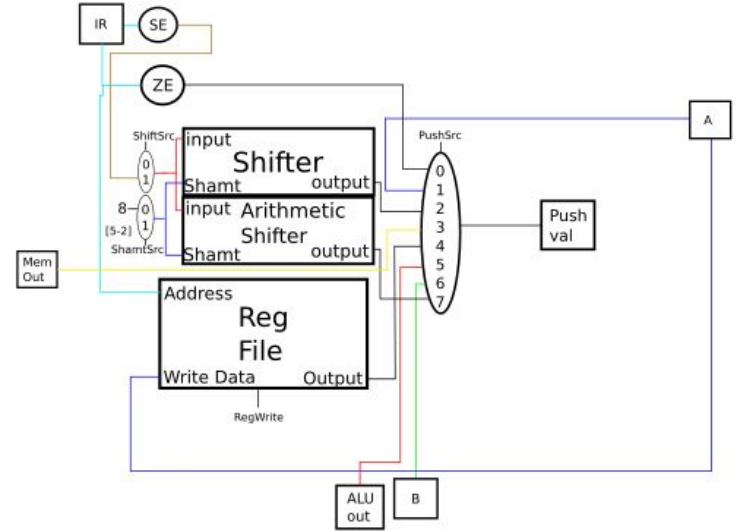
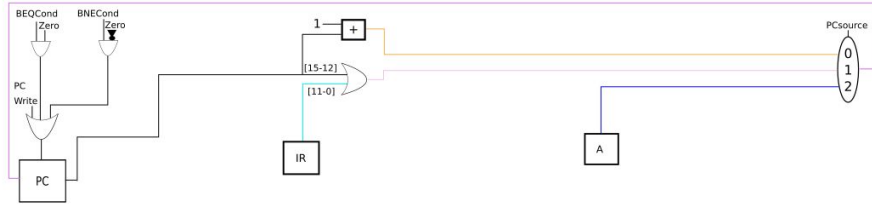




# Testing



# How we tested





# Performance

Instructions: 183774

Cycle frequency: 65.6 MHz

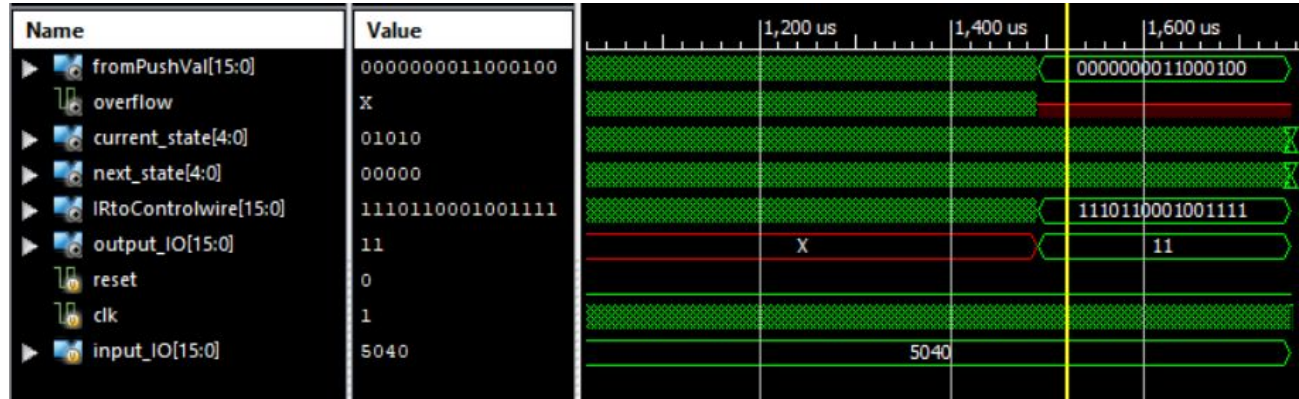
Cycle time: 15.23 ns

Cycles: 745206

CPI: 4.055

Execution Time: 11.35ms

Size of Program: 160 bytes



Device Utilization Summary (estimated values)				<a href="#">[-]</a>
Logic Utilization	Used	Available	Utilization	
Number of Slices	4407	4656	94%	
Number of Slice Flip Flops	774	9312	8%	
Number of 4 input LUTs	8351	9312	89%	
Number of bonded IOBs	77	232	33%	
Number of BRAMs	1	20	5%	
Number of GCLKs	1	24	4%	



# Extras



# Extras

- Arithmetic Shifter
- Memory Mapped I/O

Name	Value
fromPushVal[15:0]	0000000000000000
overflow	0
current_state[4:0]	00001
next_state[4:0]	10110
IRtoControlwire[15:0]	0000000000000000
output_IO[15:0]	XXXXXXXXXXXXXXXXXX
reset	0
clk	0
input_IO[15:0]	0001001110110000



# The Assembler

- 3 pass
- 1st
  - Handles Pseudoinstructions
    - Push
      - Pushes a 16 bit value to the stack
      - pushLi, pushUi, or
    - bge
    - ble
    - blt
    - Bgt
- 2nd
  - Handles addresses and labels
- 3rd
  - Converts to Machine Code





# Conclusion





# Challenges

- How a stack architecture will actually work
- Memory -- testing takes forever
- Input/Output
- Blocking vs. NonBlocking vs. Sequential vs. Combinational
- Timing
- Putting it all together -- the subsystems



# Future Work

- FPGA board implementation
- Advanced Assembler
- Harder, Better, Faster, Stronger!
- Handle Stack Overflow and Interrupts



# Questions