Christof Teuscher

ECE 410/510: Hardware for AI and ML

Course organization and details

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Department of Electrical and Computer Engineering (ECE)

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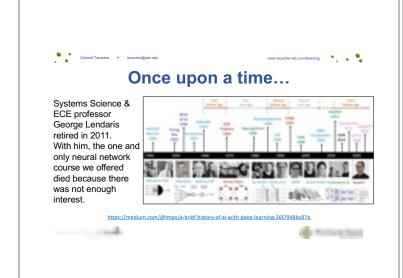




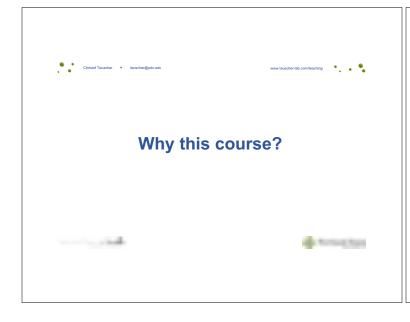
THIS IS AN EXPERIMENT

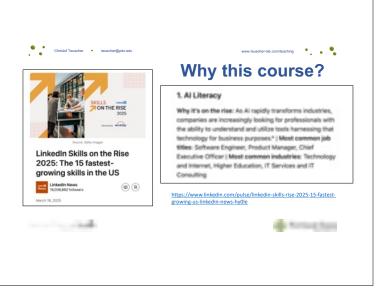
Anything can change anytime.
Failure is an option.
Students will not be carrying the risk.

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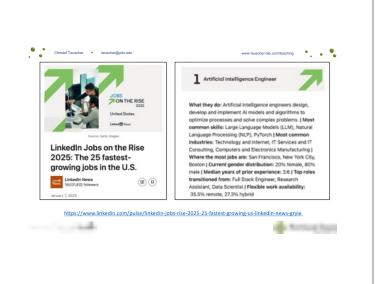














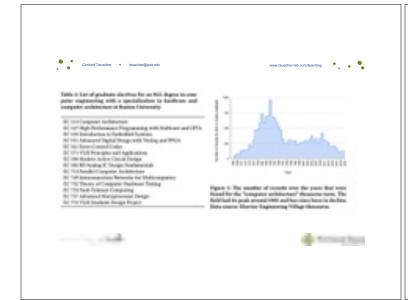




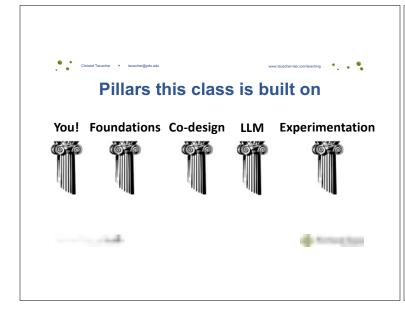




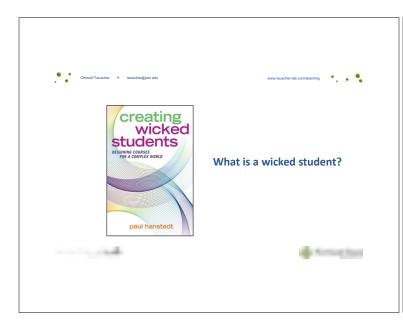


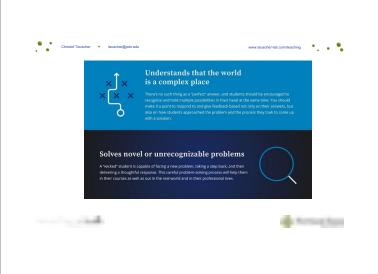




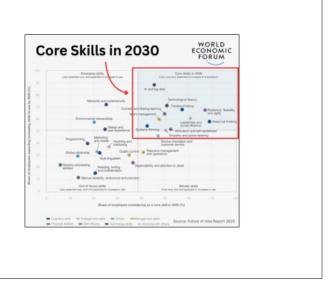


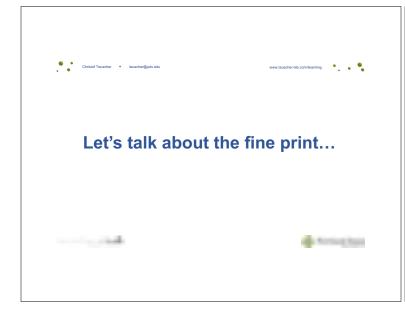


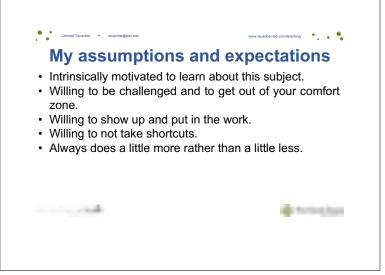














A word about prerequisites

Recommendations:

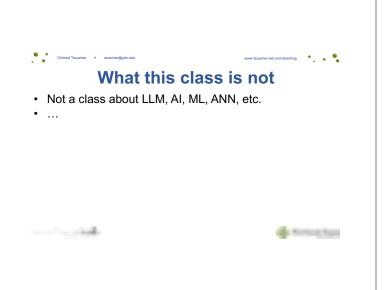
- Undergraduate: ECE 371 and ECE 351
 Graduate: ECE 485

Ideal knowledge:

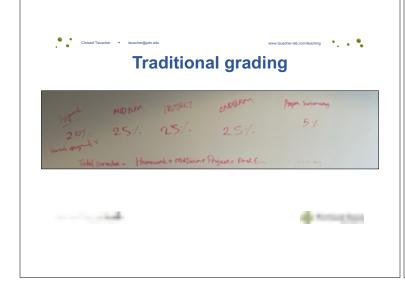
- Python or other language
- Advanced computer architecture, incl. GPUs
- **FPGAs**
- Analog and digital circuits
- Verilog, SystemVerilog
- · Al prompt engineering

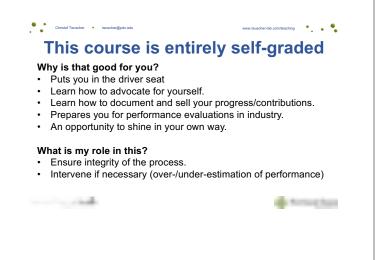


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How will this work?

- Throughout this course, you will document and keep track of all your creations and contributions (e.g., Github, Wiki, Slack,
- At the end the term, you will write a recommendation letter for
- The letter will serve as a self-evaluation.
- You will assign yourself a grade based on your contributions.
- Effort doesn't count. You need to earn your grade with tangle products. Move your own needle!



Additional perks

- I will assess your performance holistically throughout the term using Google's GRAD system:
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 - Transformative Impact (TI)
 Outstanding Impact (OI)

 - Significant Impact (SI)
 - Moderate Impact (MI)
 Not Enough Impact (NE)
- Think of this as your "probation period." You want to get hired, so act accordingly.
- Ol students can ask me for a LinkedIn recommendation.
- TI students can ask me for a recommendation letter.
- · Selected TI students may be offered a summer GRA position.

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Will there be exams and quizzes?

Goal: provide you with opportunities to shine and stand out.

- · Weekly questions and problems.
- Final optional oral examinations?

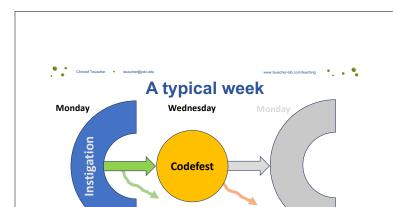




What will we do?

You will each (co-)design, test, evaluate, etc. an AI/ML accelerator for a large class-wide chiplet design.









What will you need?

- Access to your favorite LLM. No pro subscription required, but recommended.
- Ability to bring and work on a laptop.





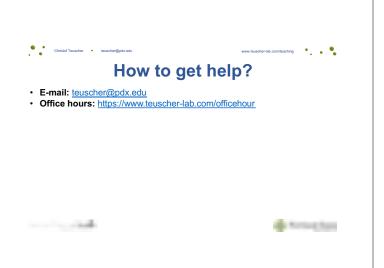


Al tools advice

Before relying heavily on any AI tool, test it on problems/topics/etc. for which you're an expert.

This simple exercise will allow you:

- Calibrate your expectations of the tool's knowledge depth.
- Understand its strengths and limitations.
- Build intuition about its reliability on various topics
- Set a baseline for how much verification you'll need in unfamiliar areas.





- Focus: no side conversations, no laptop and phone addiction, no work for other classes.
- Be present: show up, be on time.
- **Challenge yourself**: do always a little more instead of just the bare minimum. Up your game, set high standards for yourself. Produce work that you can be proud of.
- Help others to be successful. That includes your team.
- Willingness to learn: have a growth mindset, intellectual humility and ambition.
- **Be in charge:** Put yourself in the driver's seat, assume responsibility.



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Participate in an educational study and publication

A family for

- Now: complete an initial skills/knowledge assessment.
- End of term: complete the same survey.
- Agree to have your deliverables/products assessed.
- Participate in a focus group at the end of the term.

Why participate?

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- Help to make this a better class for future students
- Be part of the change
- · Co-author a publication?



Prep for Wednesday

- Bring your laptop
 Accept the Slack invite (to be sent)
 Create a wiki account (to be shared)
 Github (to be shared)

Reading:
 Designing Silicon Brains using LLM: Leveraging ChatGPT for Automated Description of a Spiking Neuron Array, https://arxiv.org/abs/2402.10920

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