CSC 488S/CSC 2107S Lecture Notes

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taking CSC488H1S or CSC2107S in the
Winter 2012/2013 term at the University of Toronto

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are expressly prohibited.

Reading Assignment

Fischer, Cytron and LeBlanc

Sections 10.3, 14.7

Sections 14.2 .. 14.2.3

Sections 14.3 .. 14.3.4

Static Single Assignment

- The Static Single Assignment (SSA) form is an IR used to facilitate certain
  kinds of optimization.

- Key features of SSA

  – Each assignment to a variable is a **definition** of the variable for the particular value
    being assigned.

  – *Rename* all variables in the program using some systematic scheme so that each
    variable is assigned a value exactly *once*.

  – If more than one definition for the same variable is available at some point in the
    program, add an *articulation point* (ϕ function) to identify the conflict.

  – Once the variables have been renamed, each uniique variable has fixed value from
    its point of definition to the end of the program

- The SSA form facilitates optimizations like constant propagation and
  constant folding (Slide 472) and variable folding (Slide 482)

SSA Example (Figure 10.5)

(a)   (b)

Data Flow Analysis

- Data Flow Analysis is a technique for discovering properties of the run-time behavior of programs during compilation.

- These properties are *universal*, i.e. they apply to all possible sequences of control and data flow.

- The information from data flow analysis is used to determine feasibility and safety of various optimizations.

- Control Flow Graphs ($\mathcal{G}_{cf}$) are used to represent the flow of control between statements in a single routine.
  Procedure Call Graphs ($\mathcal{G}_{pc}$) are used to represent the flow of control between functions and procedures.

- Data Flow Analysis and related optimizations are covered in much greater detail in the follow-on course ECE489H/ECE540H .

Analysis and Transformations

Data Flow Analysis	Transformation
Available expressions	Common subexpression elimination
Detection of loop invariants	Invariant code motion
Detection of induction variables	Strength reduction
Copy analysis	Copy propagation
Live variables	Dead code elimination
Reaching definitions	Identifying constants
. . .	. . .

Data Flow Analysis Overview

- Subdivide the program into Basic Blocks or finer grain (e.g. statements).

- Analyze the control structure of the program to determine the interrelationship of the blocks.

- Represent program control flow as a directed graph ($\mathcal{G}_{cf}$) .
  - Each node in $\mathcal{G}_{cf}$ is a basic block or an individual statement.
  - Each edge in $\mathcal{G}_{cf}$ represents a possible node to node transfer of control.

- Analyze each node to determine where some property of interest is defined, used and killed. e.g. where expressions are computed.

- Dataflow analysis can be applied
  Intraprocedurally within a single routine
  Interprocedurally within some set of routines
  Globally to an entire program

- Sets are represented using bit-vectors (one bit/element).
  Iterative solutions to data flow equations are typically $O(B^2 \times V)$ for a problem with B basic blocks and V variables or expressions.

Definitions for Data Flow Analysis

Basic Block A sequence of instructions that is always executed from start to finish. i.e. it contains no branches.

Definition Point Point where some interesting property is established.
(i.e. a variable or an expression is given a value.)

Use Point Point where some interesting property is used.

Killed, Kill Point A point where in interesting property is rendered invalid.
- A variable is assigned a new value.
- *Any* of the variables used to compute the value of an expression is assigned a new value.

Forward Flow What can happen before a given point in a program.

Backward Flow What can happen after a given point in the program.

Any Path (May) analysis What property holds on some path leading to or from a given basic block. Examples: uninitialized variable, live variables.

All Path (Must) analysis What property holds on all paths leading to or from a basic block. Example: availability of an expression.

Predecessors(b) the set of all basic blocks that are *immediate* predecessors of block b in the control flow graph.

There is an edge in the control graph leading *from* each basic block in Predecessors(b) *to b*.

Successors(b) the set of all basic blocks that are *immediate* successors of block b in the control flow graph.

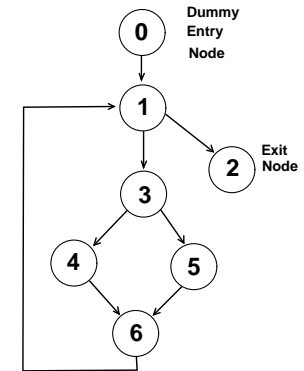There is an edge in the control graph leading *from b to* each basic block in Successors(b).

Basic Block Example

Program and Basic Blocks

```
while ( 1 ) {
    scanf( " %f %f %f", &A, &B, &C )   1
    if( A == 0.0 )
        break ;                        2
    DISC = B * B - 4.0 * A * C ;        3
    if( DISC >= 0.0 ) {
        DROOT = sqrt( DISC ) ;
        R1 = ( - B + DROOT ) / ( 2.0 * A ) ;    4
        R2 = ( -B  - DROOT ) / ( 2.0 * A ) ;
    } else {
        DROOT = sqrt( - DISC ) ;
        R1 = - B / ( 2.0 * A ) ;        5
        R2 = DROOT / ( 2.0 * A ) ;
    } ;
    printf( "%f %f %f ", DISC, R1, R2 ) ;   6
} ;
```

Control Flow Graph

Predecessors

1	{ 0 , 6 }	4	{ 3 }
2	{ 1 }	5	{ 3 }
3	{ 1 }	6	{ 4 , 5 }

Successors

1	{ 2 , 3 }	4	{ 6 }
2	{ }	5	{ 6 }
3	{ 4 , 5 }	6	{ 1 }

Types of Data Flow Problems

Forward Flow problems

- Data flows from the first block to the last block.
- *out* sets are computed from *in* sets within basic blocks.
- *in* sets are computed from *out* sets across basic blocks.

Backward Flow problems

- Data flows from the last block to the first block.
- *in* sets are computed from *out* sets within basic blocks.
- *out* sets are computed from *in* sets across basic blocks.

Any Path problems

Values coming into a block through *any* path are valid.

Use $\bigcup$, start with minimum info[a]

All Path problems

Only values coming into a block through *every* possible path are valid.

Use $\bigcap$, start with maximum info[a]

[a]For first/last block other value may be necessary

Generic Data Flow Analysis

- Data flow analysis can be used to compute a wide variety of properties of basic blocks that are useful for optimization.

- Generic definitions

  $In(b)$ what properties hold on entry to basic block b.

  $Out(b)$ what properties hold on exit from basic block b.

  $Gen(b)$ the properties that are generated in basic block b.

  $Killed(b)$ the properties that are invalidated in basic block b.

- For each problem of interest, the precise rules for determining membership in the sets $In, Out, Gen, Killed$ have to be specified.

- Once the membership rules have been determined, one of the iterations over the control flow graph described on the following slide is used to determine In and Out for all basic blocks in the control flow graph.

Generic Forward Data Flow Equations

Any Path	$Out(b) = Gen(b) \bigcup (In(b) - Killed(b))$ $In(b) = \bigcup\limits_{i \in Predecessors(b)} Out(i)$
All Paths	$Out(b) = Gen(b) \bigcup (In(b) - Killed(b))$ $In(b) = \bigcap\limits_{i \in Predecessors(b)} Out(i)$

Generic Backward Data Flow Equations

Any Path	$In(b) = Gen(b) \bigcup (Out(b) - Killed(b))$ $Out(b) = \bigcup\limits_{i \in Successors(b)} In(i)$
All Paths	$In(b) = Gen(b) \bigcup (Out(b) - Killed(b))$ $Out(b) = \bigcap\limits_{i \in Successors(b)} In(i)$

Forward Data Flow Algorithm (All Paths)

for each block B **do**
    $out[B] := gen[B] \cup (in[B] - kill[B])$
change := true
while *change* **do begin**
    change := false
    for each block B **do begin**
        $in[B] := \bigcap_{P \in Pred(B)} out[P]$
        $oldout := out[B]$
        $out[B] := gen[B] \cup (in[B] - kill[B])$
        if $out[B] \neq oldout$ **then** *change* := true
    end
end

Available Expression Analysis
Forward All-Paths Analysis

- An expression is *available* at a point P in the program graph G if *every* path leading to P contains a definition of the expression which is not subsequently killed.

- Definitions

$In(B)$ the set of expressions available on entry to basic block B .

$Kill(B)$ the set of expressions that are killed in basic block B .

$Gen(B)$ the set of expressions that are defined in basic block B and are not subsequently killed within the block.

$Out(B)$ the set of expressions available at the exit of block B

$In(Init)$ is $\emptyset$

- Use Available Expression Analysis to implement global common sub-expression elimination.

An expression is *available* at the end of a basic block B if

    it is defined in the block and not subsequently killed.

 or it is available on entry to the block B and is not killed within the block.

Therefore

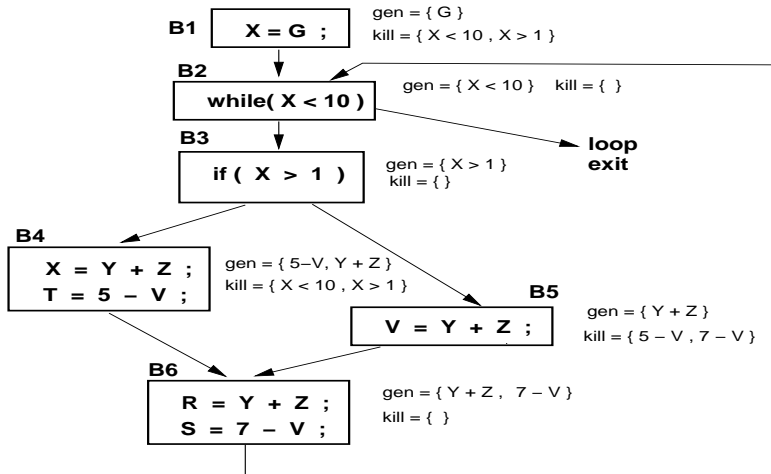$$Out(B) = gen(B) \bigcup (In(B) - Kill(B))$$

An expression is available on entry to a basic block if it is available on *all* paths leading into the block.

For all basic blocks B compute

$$In(B) = \bigcap_{p \; \varepsilon \; Predecessors(B)} Out(\, p \,)$$

Available Expressions Example

B1 `X = G ;`
gen = { G }
kill = { X < 10 , X > 1 }

B2 `while( X < 10 )`
gen = { X < 10 }   kill = { }

B3 `if ( X > 1 )`
gen = { X > 1 }
kill = { }

loop exit

B4 `X = Y + Z ;` `T = 5 − V ;`
gen = { 5−V , Y + Z }
kill = { X < 10 , X > 1 }

B5 `V = Y + Z ;`
gen = { Y + Z }
kill = { 5 − V , 7 − V }

B6 `R = Y + Z ;` `S = 7 − V ;`
gen = { Y + Z , 7 − V }
kill = { }

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Available Expressions Example

in = { }
out = { G }

B1 `X = G ;`
gen = { G }
kill = { X < 10 , X > 1 }

in = { G }
out = { G , X < 10 }

B2 `while( X < 10 )`
gen = { X < 10 }   kill = { }

in = { G , X < 10 }
out = { G , X < 10 , X > 1 }

B3 `if ( X > 1 )`
gen = { X > 1 }
kill = { }

loop exit

in = { G , X < 10 , X > 1 }
out = { G, Y + Z , 5 − V }

B4 `X = Y + Z ;` `T = 5 − V ;`
gen = { 5−V , Y + Z }
kill = { X < 10 , X > 1 }

in = { G , X < 10 , X > 1 }
out = { G , Y + Z , X < 10 , X > 1 }

B5 `V = Y + Z ;`
gen = { Y + Z }
kill = { 5 − V , 7 − V }

in = { G , Y + Z }
out = { G, Y + Z , 7 − V }

B6 `R = Y + Z ;` `S = 7 − V ;`
gen = { Y + Z , 7 − V }
kill = { }
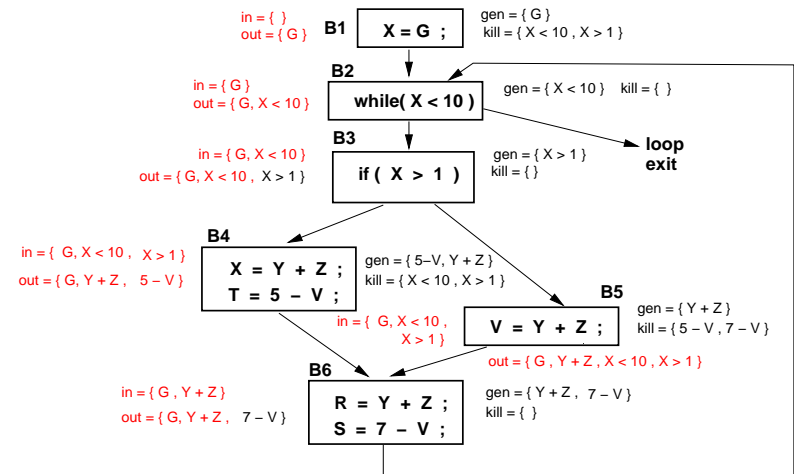
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Common Sub−Expression Elimination Example

in = { }
out = { G }

B1 `X = G ;`
gen = { G }
kill = { X < 10 , X > 1 }

in = { G }
out = { G , X < 10 }

B2 `while( X < 10 )`
gen = { X < 10 }   kill = { }

in = { G , X < 10 }
out = { G , X < 10 , X > 1 }

B3 `if ( X > 1 )`
gen = { X > 1 }
kill = { }

loop exit

common sub expression

in = { G , X < 10 , X > 1 }
out = { G , Y + Z , 5 − V }

B4 `X = `**`Y + Z`** `T = 5 − V ;`
gen = { 5−V , Y + Z }
kill = { X < 10 , X > 1 }

in = { G , X < 10 , X > 1 }
out = { G , Y + Z , X < 10 , X > 1 }

B5 `V = `**`Y + Z`**
gen = { Y + Z }
kill = { 5 − V , 7 − V }

in = { G , Y + Z }
out = { G , Y + Z , 7 − V }

B6 `R = `**`Y + Z`** `S = 7 − V ;`
gen = { Y + Z , 7 − V }
kill = { }
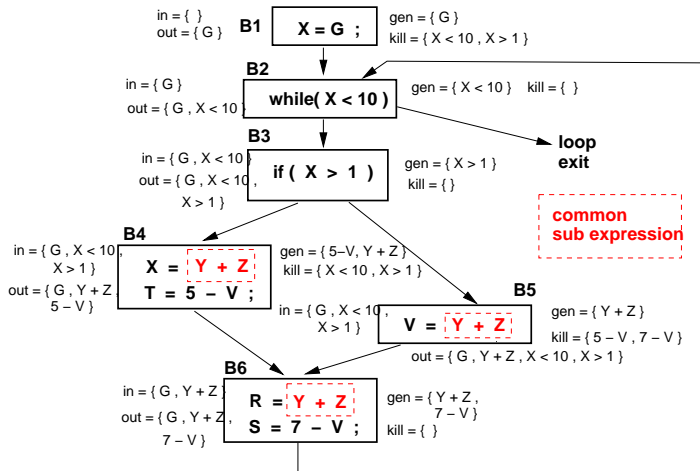
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Forward Data Flow Algorithm (Any Path)

for each block B **do**
    $out[B] := gen[B]$
$change$:= true
while $change$ **do begin**
    $change$:= false
    for each block B **do begin**
        $in[B] := \bigcup_{P \in Pred(B)} out[P]$
        $oldout := out[B]$
        $out[B] := gen[B] \cup (in[B] - kill[B])$
        if $out[B] \neq oldout$ **then** $change$:= true
    end
end

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Reaching Definitions
Forward Any-Path Analysis

- A *definition* of a variable x is a statement that assigns a value to x

- A definition d reaches a point p if there is a path from the point immediately following d to p, such that d is not "killed" along that path

- Definitions

$In(B)$ definitions available upon entry to B

$Gen(B)$ definitions made in B

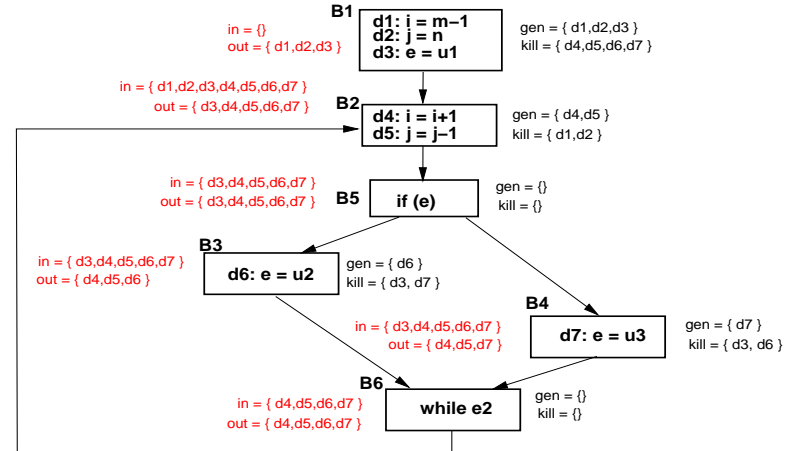$Kill(B)$ definitions invalidated by B

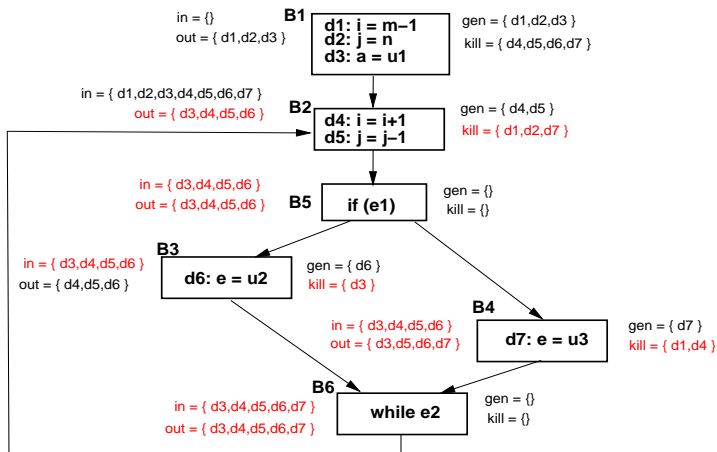$Out(B)$ is $Gen(b) \bigcup (In(B) - Kill(B))$

$In(Init)$ is $\emptyset$

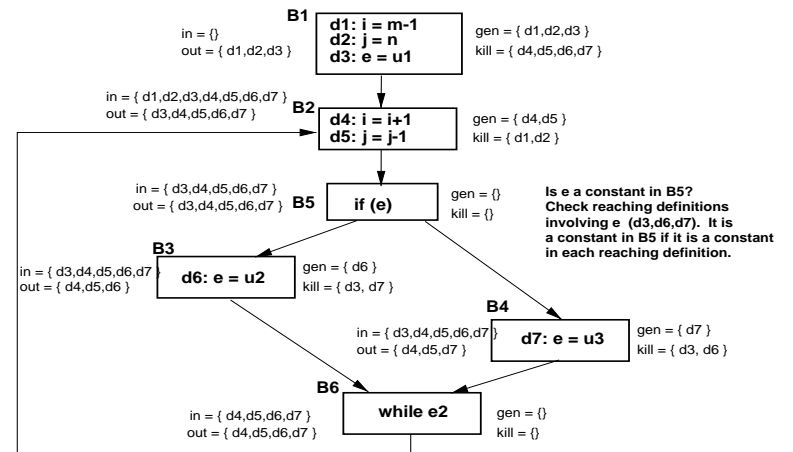
Reaching Definitions Example

B1
d1: i = m−1
d2: j = n
d3: e = u1
in = {}
out = { d1,d2,d3 }
gen = { d1,d2,d3 }
kill = { d4,d5,d6,d7 }

B2
d4: i = i+1
d5: j = j−1
in = { d1,d2,d3,d4,d5,d6,d7 }
out = { d3,d4,d5,d6,d7 }
gen = { d4,d5 }
kill = { d1,d2 }

B5 if (e)
in = { d3,d4,d5,d6,d7 }
out = { d3,d4,d5,d6,d7 }
gen = {}
kill = {}

B3 d6: e = u2
in = { d3,d4,d5,d6,d7 }
out = { d4,d5,d6 }
gen = { d6 }
kill = { d3, d7 }

B4 d7: e = u3
in = { d3,d4,d5,d6,d7 }
out = { d4,d5,d7 }
gen = { d7 }
kill = { d3, d6 }

B6 while e2
in = { d4,d5,d6,d7 }
out = { d4,d5,d6,d7 }
gen = {}
kill = {}

Reaching Definitions Example (2nd Iteration)

B1
d1: i = m−1
d2: j = n
d3: a = u1
in = {}
out = { d1,d2,d3 }
gen = { d1,d2,d3 }
kill = { d4,d5,d6,d7 }

B2
d4: i = i+1
d5: j = j−1
in = { d1,d2,d3,d4,d5,d6,d7 }
out = { d3,d4,d5,d6 }
gen = { d4,d5 }
kill = { d1,d2,d7 }

B5 if (e1)
in = { d3,d4,d5,d6 }
out = { d3,d4,d5,d6 }
gen = {}
kill = {}

B3 d6: e = u2
in = { d3,d4,d5,d6 }
out = { d4,d5,d6 }
gen = { d6 }
kill = { d3 }

B4 d7: e = u3
in = { d3,d4,d5,d6 }
out = { d3,d5,d6,d7 }
gen = { d7 }
kill = { d1,d4 }

B6 while e2
in = { d3,d4,d5,d6,d7 }
out = { d3,d4,d5,d6,d7 }
gen = {}
kill = {}

Identifying Constants

B1
d1: i = m-1
d2: j = n
d3: e = u1
in = {}
out = { d1,d2,d3 }
gen = { d1,d2,d3 }
kill = { d4,d5,d6,d7 }

B2
d4: i = i+1
d5: j = j-1
in = { d1,d2,d3,d4,d5,d6,d7 }
out = { d3,d4,d5,d6,d7 }
gen = { d4,d5 }
kill = { d1,d2 }

B5 if (e)
in = { d3,d4,d5,d6,d7 }
out = { d3,d4,d5,d6,d7 }
gen = {}
kill = {}

Is e a constant in B5? Check reaching definitions involving e (d3,d6,d7). It is a constant in B5 if it is a constant in each reaching definition.

B3 d6: e = u2
in = { d3,d4,d5,d6,d7 }
out = { d4,d5,d6 }
gen = { d6 }
kill = { d3, d7 }

B4 d7: e = u3
in = { d3,d4,d5,d6,d7 }
out = { d4,d5,d7 }
gen = { d7 }
kill = { d3, d6 }

B6 while e2
in = { d4,d5,d6,d7 }
out = { d4,d5,d6,d7 }
gen = {}
kill = {}

Backward Data Flow Algorithm (Any Path)

for each block B **do**

    $in[B] := gen[B]$

change := true

while *change* **do begin**

    change := false

    for each block B **do begin**

        $out[B] := \bigcup_{P \in Succ(B)} in[P]$

        $oldin := in[B]$

        $in[B] := gen[B] \cup (out[B] - kill[B])$

        if $in[B] \neq oldin$ **then** *change* := true

    end

end

Live Variable Analysis
Backward Any Path Data Flow Example

- For each definition/use of a variable V, Live Variable Analysis answers the question:

  Could the value of V computed/used here be used *further on* in the program?

- Used to enable certain kinds of optimizations

  - If V is being stored in a register over some stretch of code it is not necessary to store the register back into V if V is not live.

    Assignments to non-live variables can be deleted.

  - Duration of liveness can be used to pick variables that should be loaded into registers.

  - Example
    ```
    r23 = V         /* V stored in a register */
    r23 = r23 + 1   /* V = V + 1              */
    /* no uses of V below here */
    /* no need to store r23 back into V */
    ```

Live Variable Analysis
Backward Any-Path Analysis

- A path in the graph G is $V - clear$ if it contains no assignments to the variable V.

  A variable V is *live* at a point P in the graph G if there is a $V - clear$ path from P to a use of V, i.e. there is a path from P to somewhere that V is used which does *not* contain a redefinition of V

- Definitions

    $in(B)$ Variables live on entrance to block B

    $out(B)$ Variables live on exit from block B.

    $def(B)$ Variables assigned values (redefined) in block B *before* the variable is used. (same as $Kill(B)$)

    $use(B)$ Variables whose values are used before being assigned to. (same as $Gen(B)$)

$Out(Final)$ is $\emptyset$

A variable V is live at the entrance to a block B if

    it is used in B *before* it is defined in B

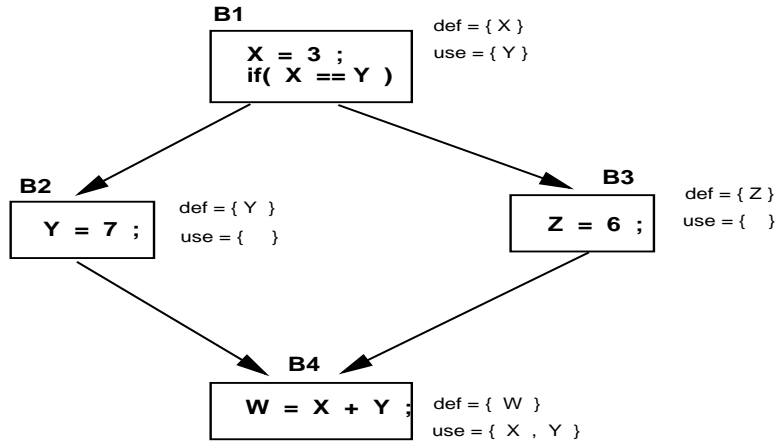 or it is live at the exit to block B and it is not defined within the block

$$in(B) = use(B) \bigcup (out(B) - def(B))$$

A variable V is live coming out of a block B if it is live going into any of B's successors.

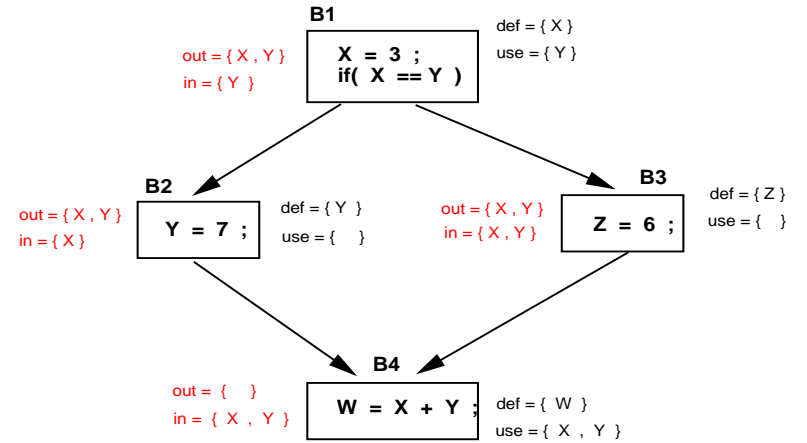$$out(B) = \bigcup_{S \in successors(B)} in(S)$$
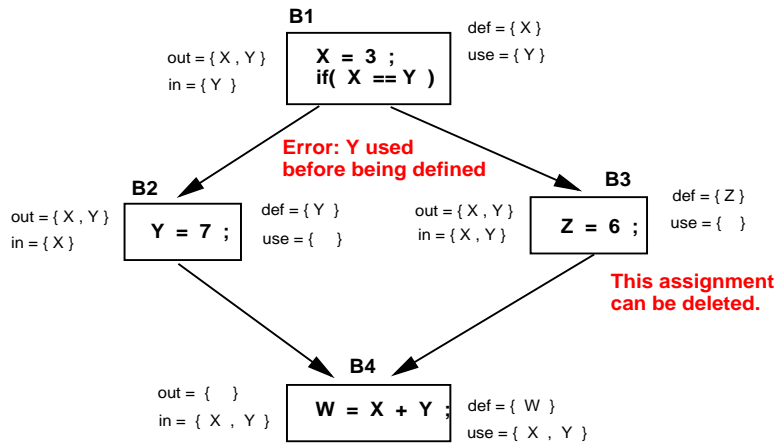
Live Variable Analysis Example

B1

X = 3 ;
if(X == Y)

def = { X }
use = { Y }

B2

Y = 7 ;

def = { Y }
use = { }

B3

Z = 6 ;

def = { Z }
use = { }

B4

W = X + Y ;

def = { W }
use = { X , Y }

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Live Variable Analysis Example

B1

out = { X , Y }
in = { Y }

X = 3 ;
if(X == Y)

def = { X }
use = { Y }

B2

out = { X , Y }
in = { X }

Y = 7 ;

def = { Y }
use = { }

B3

out = { X , Y }
in = { X , Y }

Z = 6 ;

def = { Z }
use = { }

B4

out = { }
in = { X , Y }

W = X + Y ;

def = { W }
use = { X , Y }

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Live Variable Analysis Example

B1

out = { X , Y }
in = { Y }

X = 3 ;
if(X == Y)

def = { X }
use = { Y }

Error: Y used before being defined

B2

out = { X , Y }
in = { X }

Y = 7 ;

def = { Y }
use = { }

B3

out = { X , Y }
in = { X , Y }

Z = 6 ;

def = { Z }
use = { }

This assignment can be deleted.

B4

out = { }
in = { X , Y }

W = X + Y ;

def = { W }
use = { X , Y }

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Backward Data Flow Algorithm (All Path)

for each block B **do**

$\quad in[B] := gen[B] \cup (U - kill[b])$

change := true

while *change* **do begin**

$\quad$ *change* := false

$\quad$ **for each** block B **do begin**

$\quad\quad out[B] := \bigcap_{P \in Succ(B)} in[P]$

$\quad\quad oldin := in[B]$

$\quad\quad in[B] := gen[B] \cup (out[B] - kill[B])$

$\quad\quad$ **if** $in[B] \neq oldin$ **then** *change* := true

$\quad$ **end**

end

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Very Busy Expressions
Backward All-Paths Analysis

- An expression e is *very busy* at point p if not matter what path is taken from p, the expression e will be evaluated before any of its operands are redefined.

- Definitions

$Out(B)$ expressions very busy after B

$Gen(B)$ expressions constructed by B
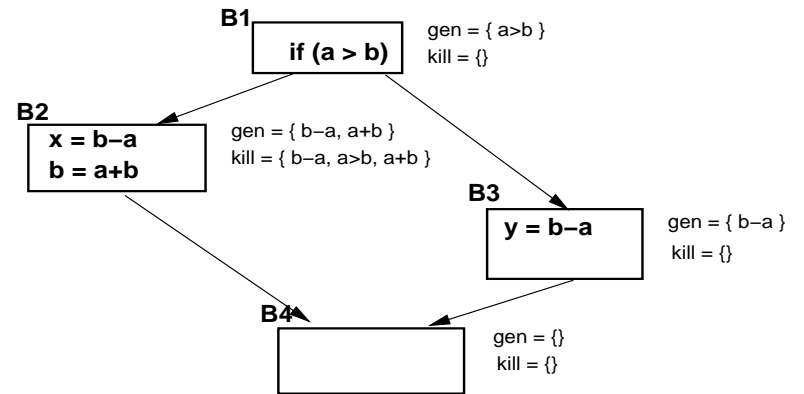
$Kill(B)$ expressions whose operands are redefined in B

$In(B)$ is $Gen(b) \bigcup (Out(B) - Kill(B))$

$Out(Final)$ is $\emptyset$

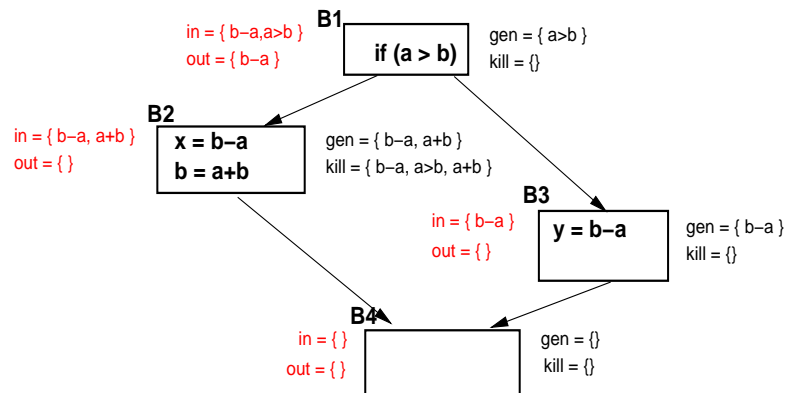- Use Very Busy Expressions analysis to *hoist* expressions
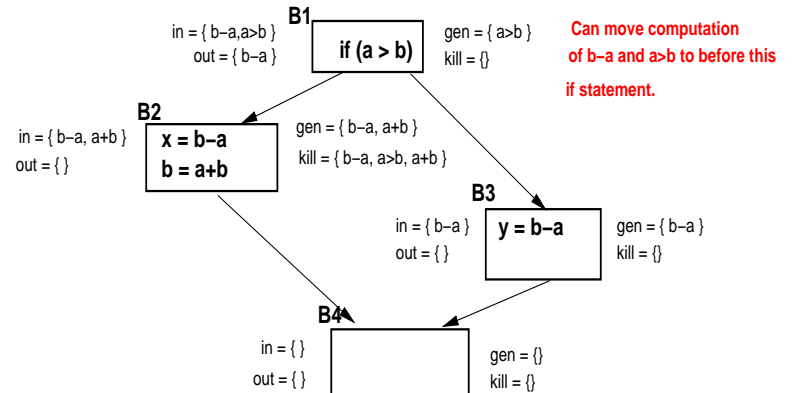

Very Busy Expression Example

B1 if (a > b)   gen = { a>b } kill = {}

B2 x = b−a b = a+b   gen = { b−a, a+b } kill = { b−a, a>b, a+b }

B3 y = b−a   gen = { b−a } kill = {}

B4   gen = {} kill = {}

Very Busy Expression Example

B1 in = { b−a,a>b } out = { b−a } if (a > b)   gen = { a>b } kill = {}

B2 in = { b−a, a+b } out = { } x = b−a b = a+b   gen = { b−a, a+b } kill = { b−a, a>b, a+b }

B3 in = { b−a } out = { } y = b−a   gen = { b−a } kill = {}

B4 in = { } out = { }   gen = {} kill = {}

Code Hoisting

B1 in = { b−a,a>b } out = { b−a } if (a > b)   gen = { a>b } kill = {}   **Can move computation of b−a and a>b to before this if statement.**

B2 in = { b−a, a+b } out = { } x = b−a b = a+b   gen = { b−a, a+b } kill = { b−a, a>b, a+b }

B3 in = { b−a } out = { } y = b−a   gen = { b−a } kill = {}

B4 in = { } out = { }   gen = {} kill = {}