ECE 178 - Embedded Systems Spring 2015 - Assignment 6

Self Design Hardware/Software Co-design Interface

In this self-design assignment it is expected that you apply your learning experience to hardware/software co-design using NIOS II processor for an external interface with the DE 2 Expansion Header shown. The design interface should be integrated as another peripheral into you existing embedded system to experiment a different parallel communications such as keypad or Motor Controls.

