

Breadboard Computer Schematic
Computer Science 4

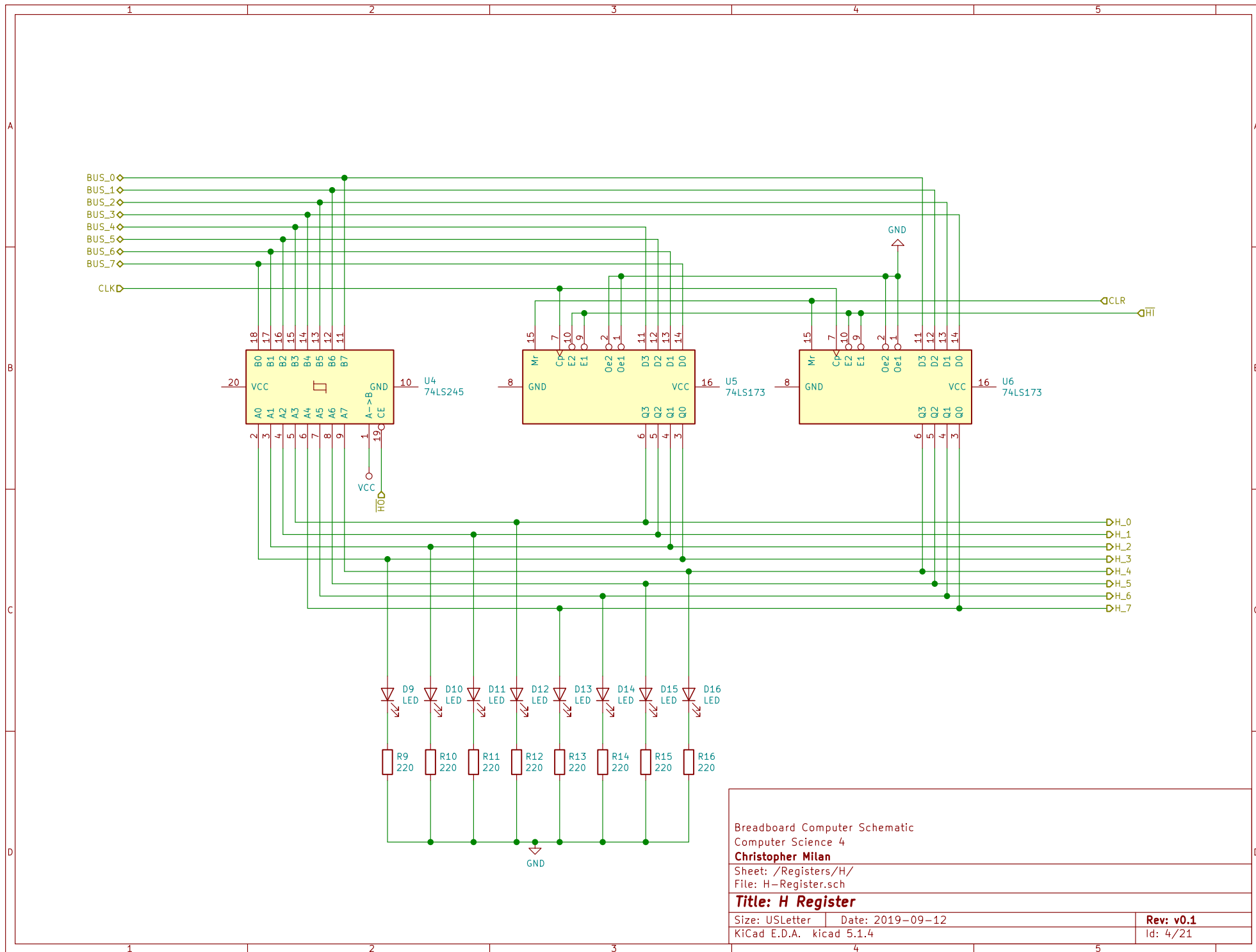
Christopher Milan

Sheet: /Registers/
File: Register.sch

Title: Register Block

Size: USLetter | Date: 2019-09-12
KiCad E.D.A. | kicad 5.1.4

Rev: v0.1
Id: 2/21

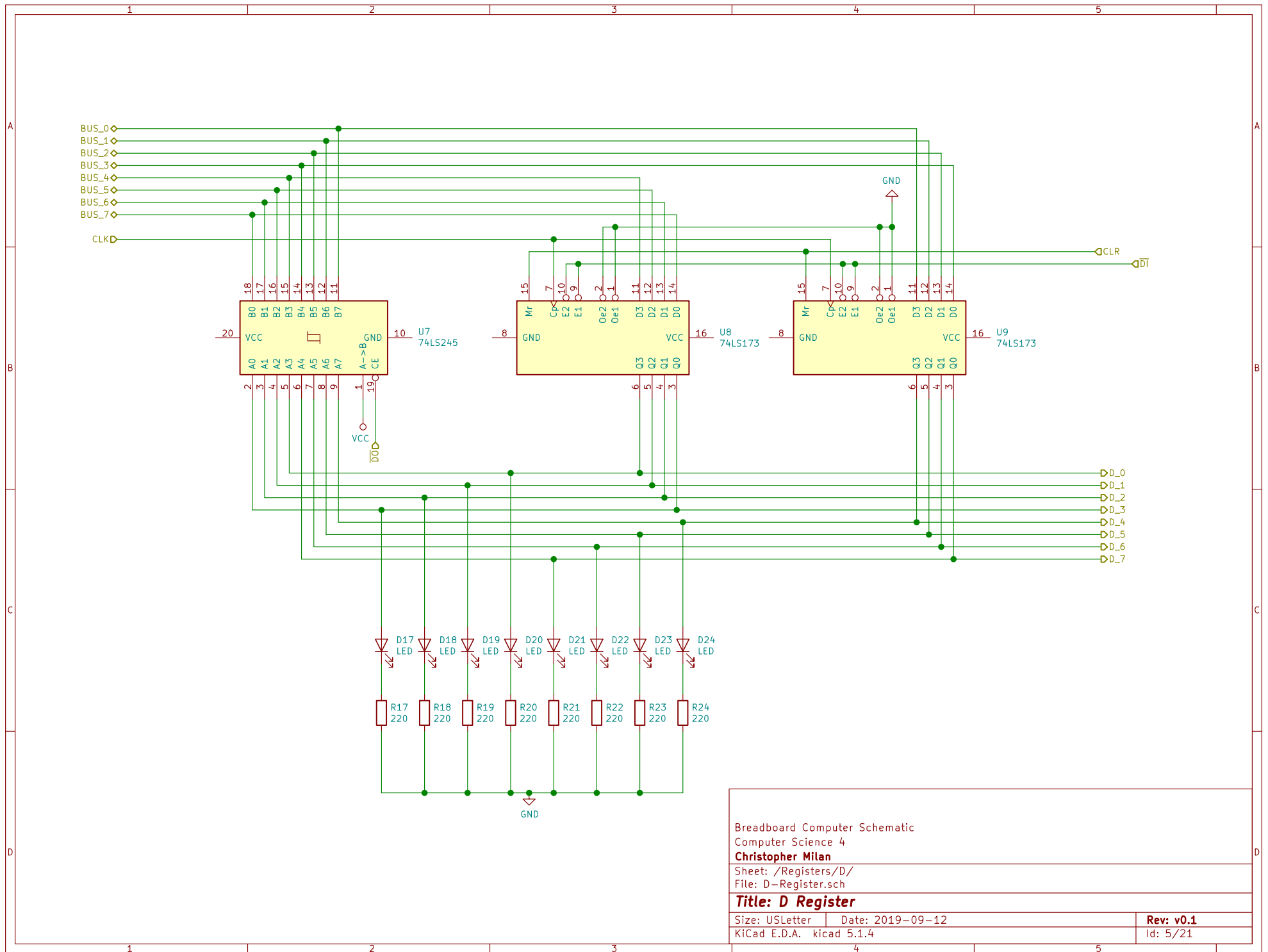


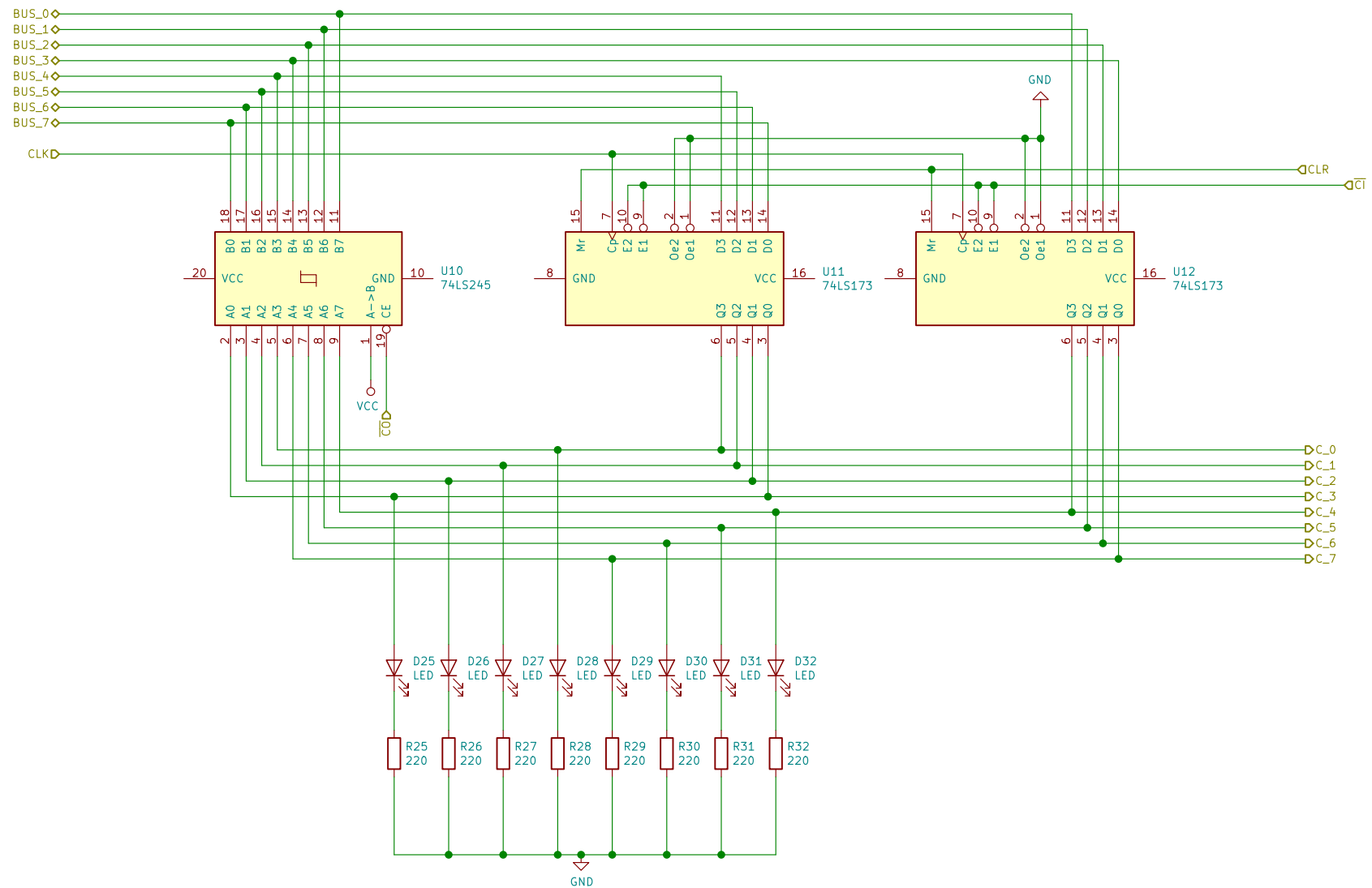
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Christopher Milan
Sheet: /Registers/H/
File: H-Register.sch

Title: H Register

Size: USLetter | Date: 2019-09-12
KiCad E.D.A. kicad 5.1.4

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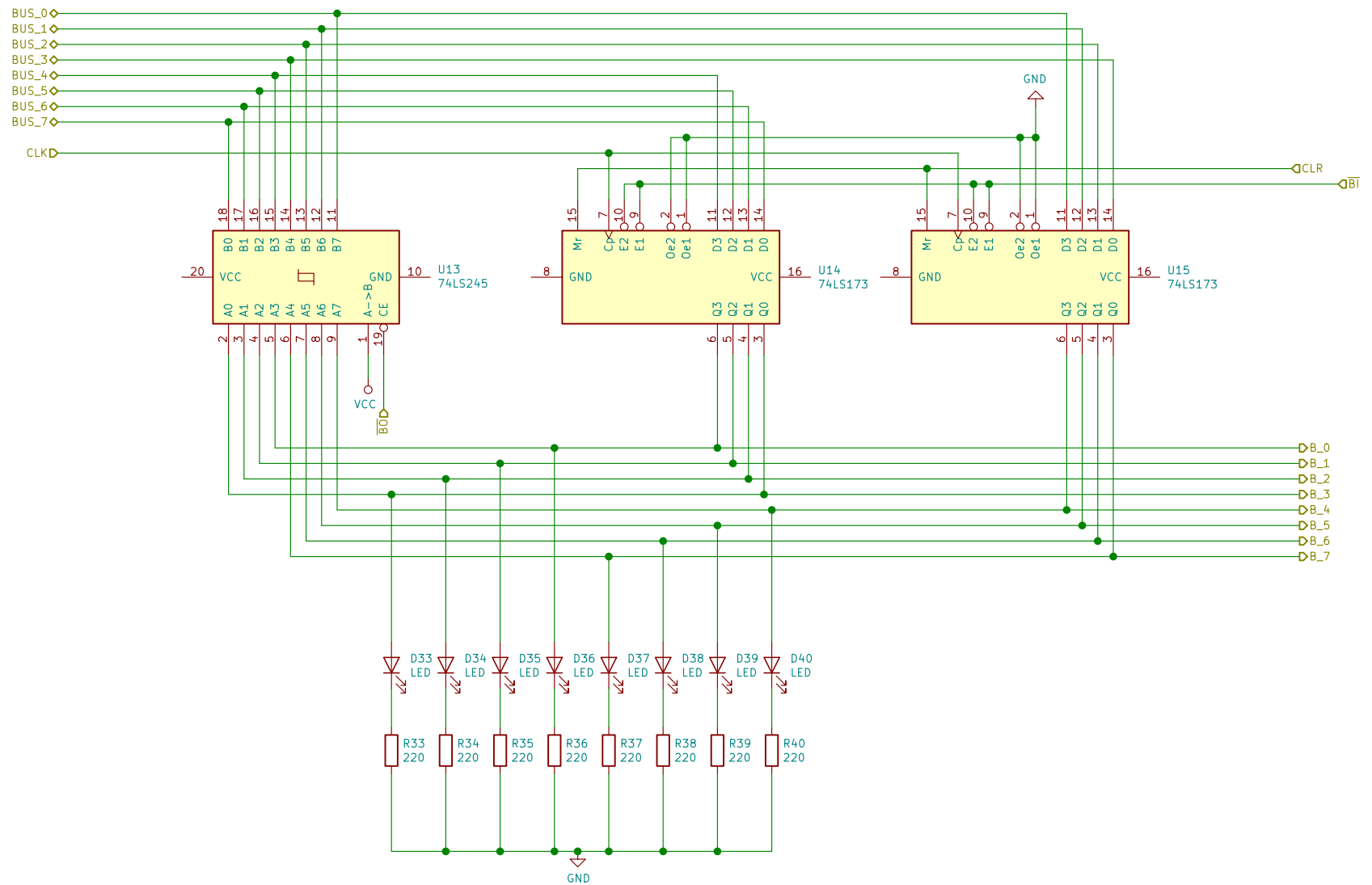
Christopher Milan

Sheet: /Registers/C/
File: C-Register.sch

Title: C Register

Size: USLetter | Date: 2019-09-12
KiCad E.D.A. kicad 5.1.4

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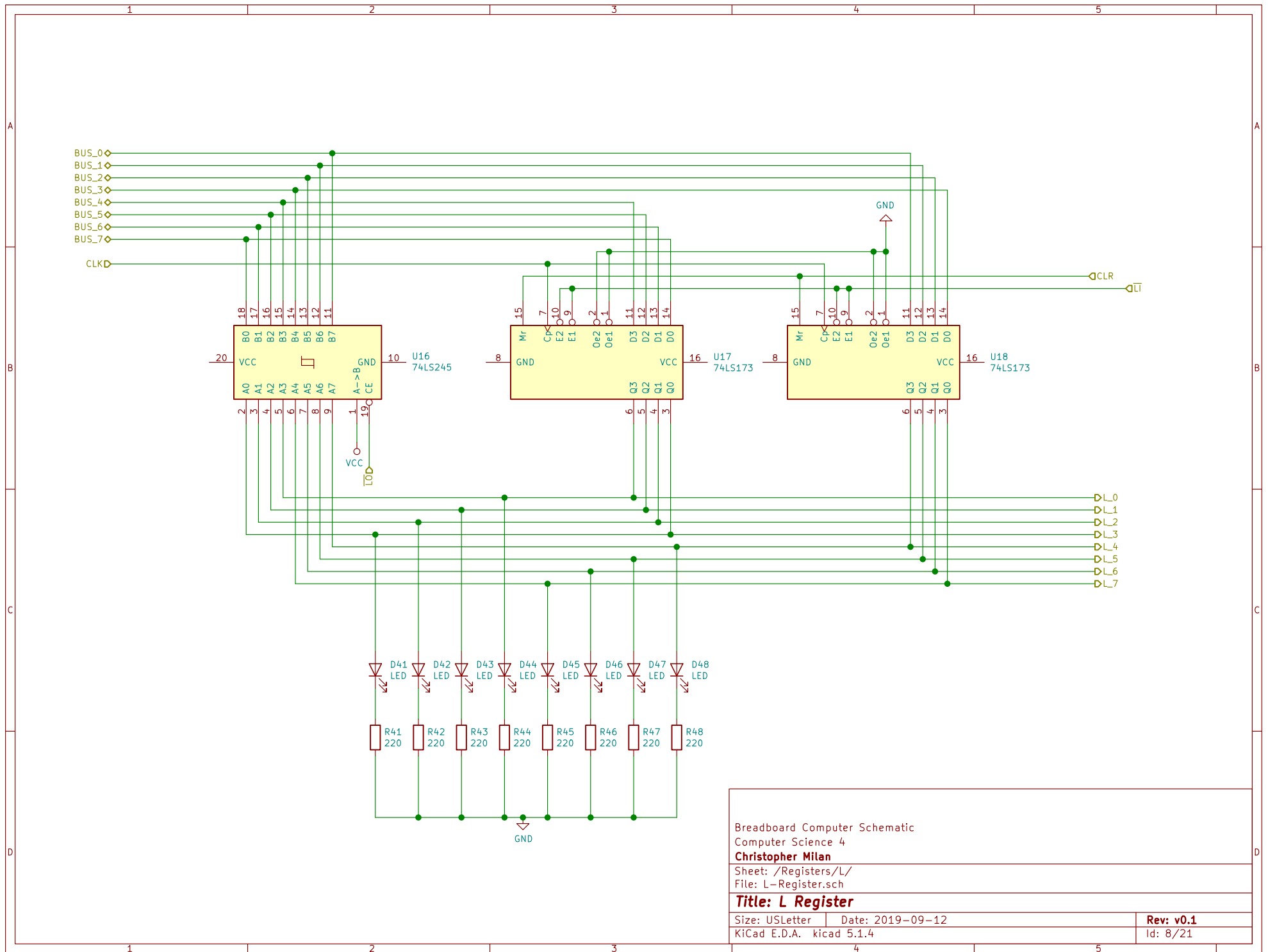
Christopher Milan

Sheet: /Registers/B/
File: B-Register.sch

Title: B Register

Size: USLetter | Date: 2019-09-12
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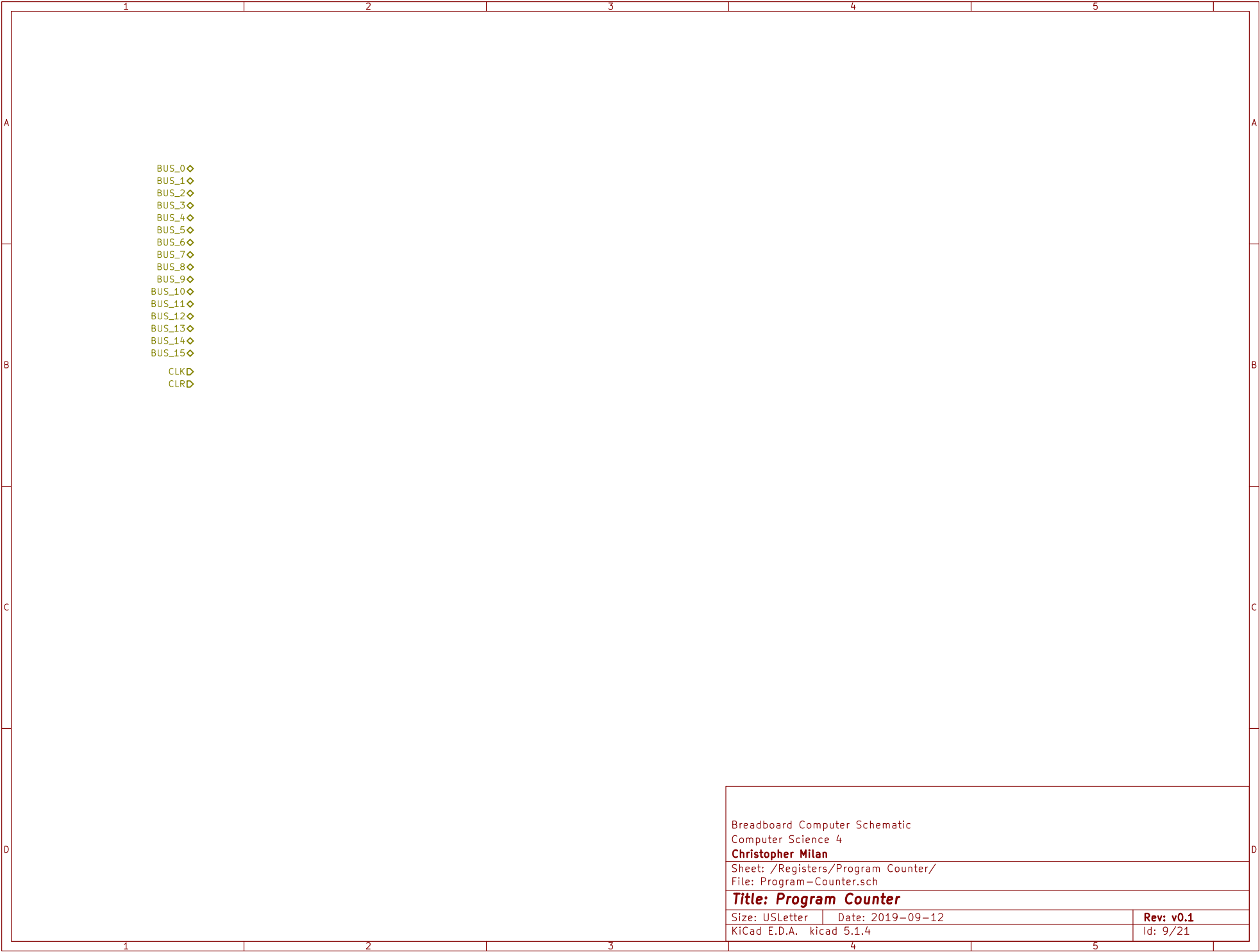
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Sheet: /Registers/L/
File: L-Register.sch

Title: L Register

Size: USLetter Date: 2019-09-12
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BUS_0◇
BUS_1◇
BUS_2◇
BUS_3◇
BUS_4◇
BUS_5◇
BUS_6◇
BUS_7◇
BUS_8◇
BUS_9◇
BUS_10◇
BUS_11◇
BUS_12◇
BUS_13◇
BUS_14◇
BUS_15◇
CLK◇
CLR◇

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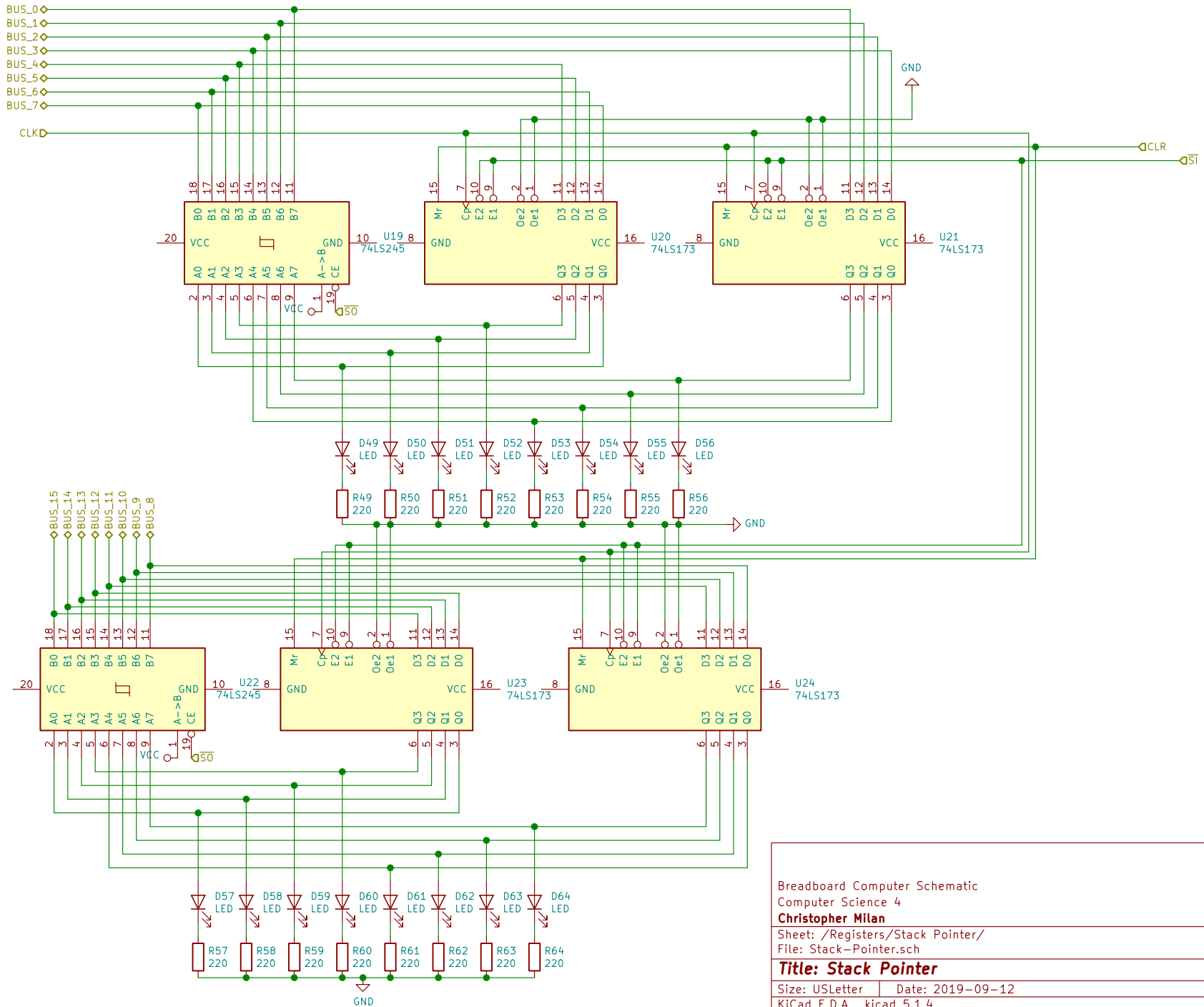
Sheet: /Registers/Program Counter/
File: Program-Counter.sch

Title: Program Counter

Size: USLetter Date: 2019-09-12
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Note: Stack pointer should not reset to 00000 and INC and DEC logic should be added for PUSH and POP instructions.



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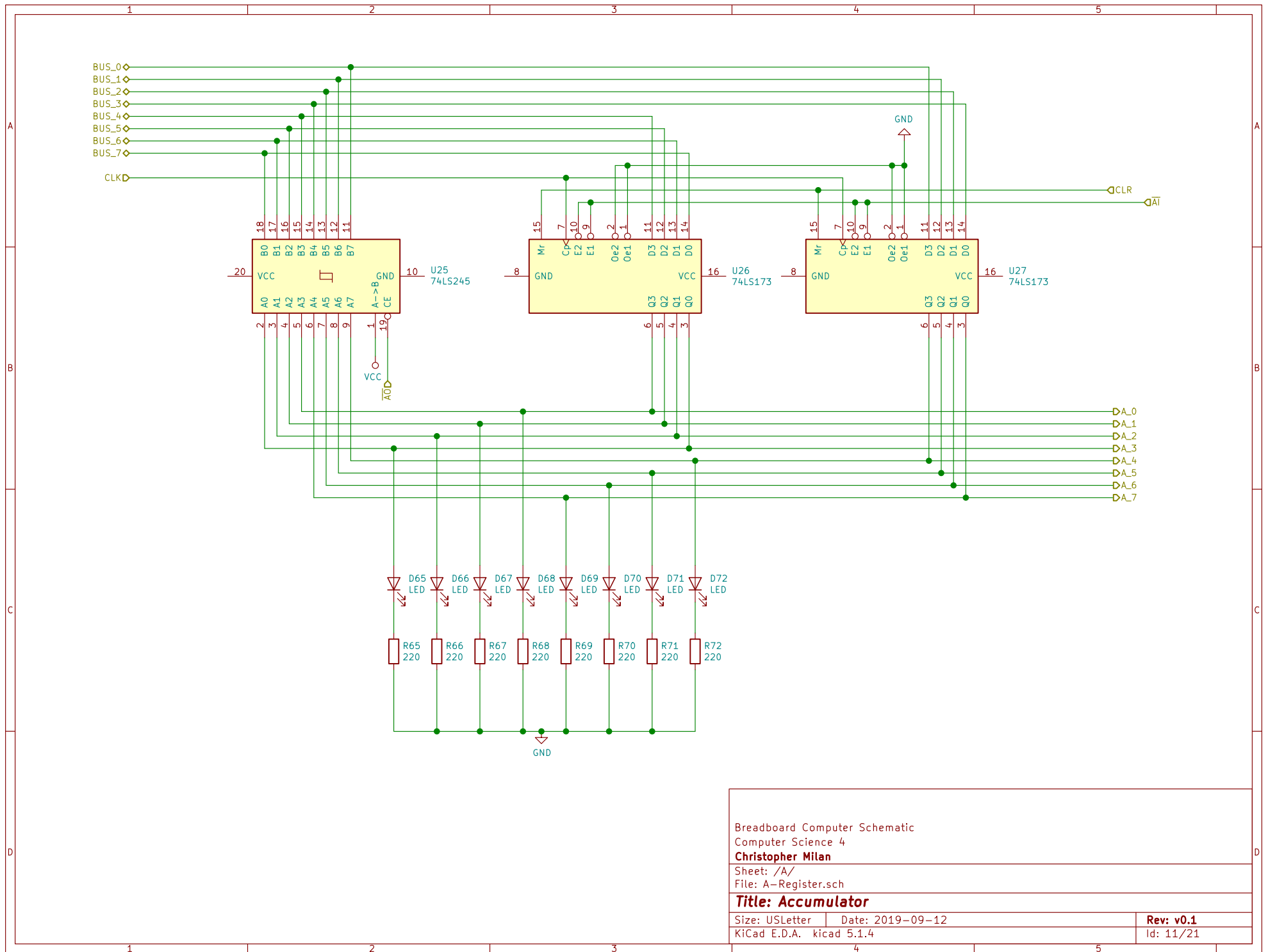
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Sheet: /Registers/Stack Pointer/
File: Stack-Pointer.sch

Title: Stack Pointer

Size: USLetter Date: 2019-09-12
KiCad E.D.A. kicad 5.1.4

Rev: v0.1
Id: 10/21

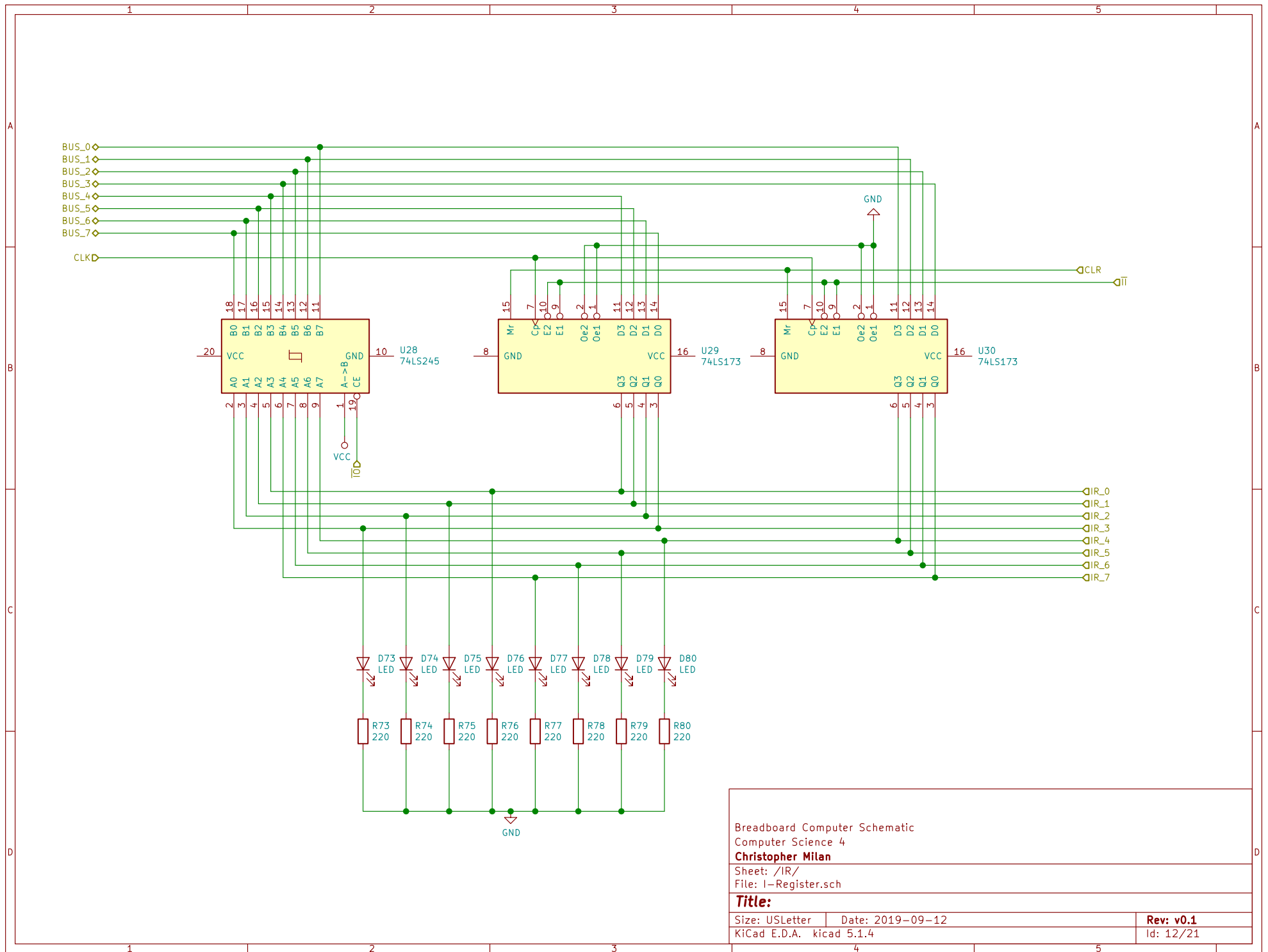


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Sheet: /A/
File: A-Register.sch

Title: Accumulator

Size: USLetter | Date: 2019-09-12
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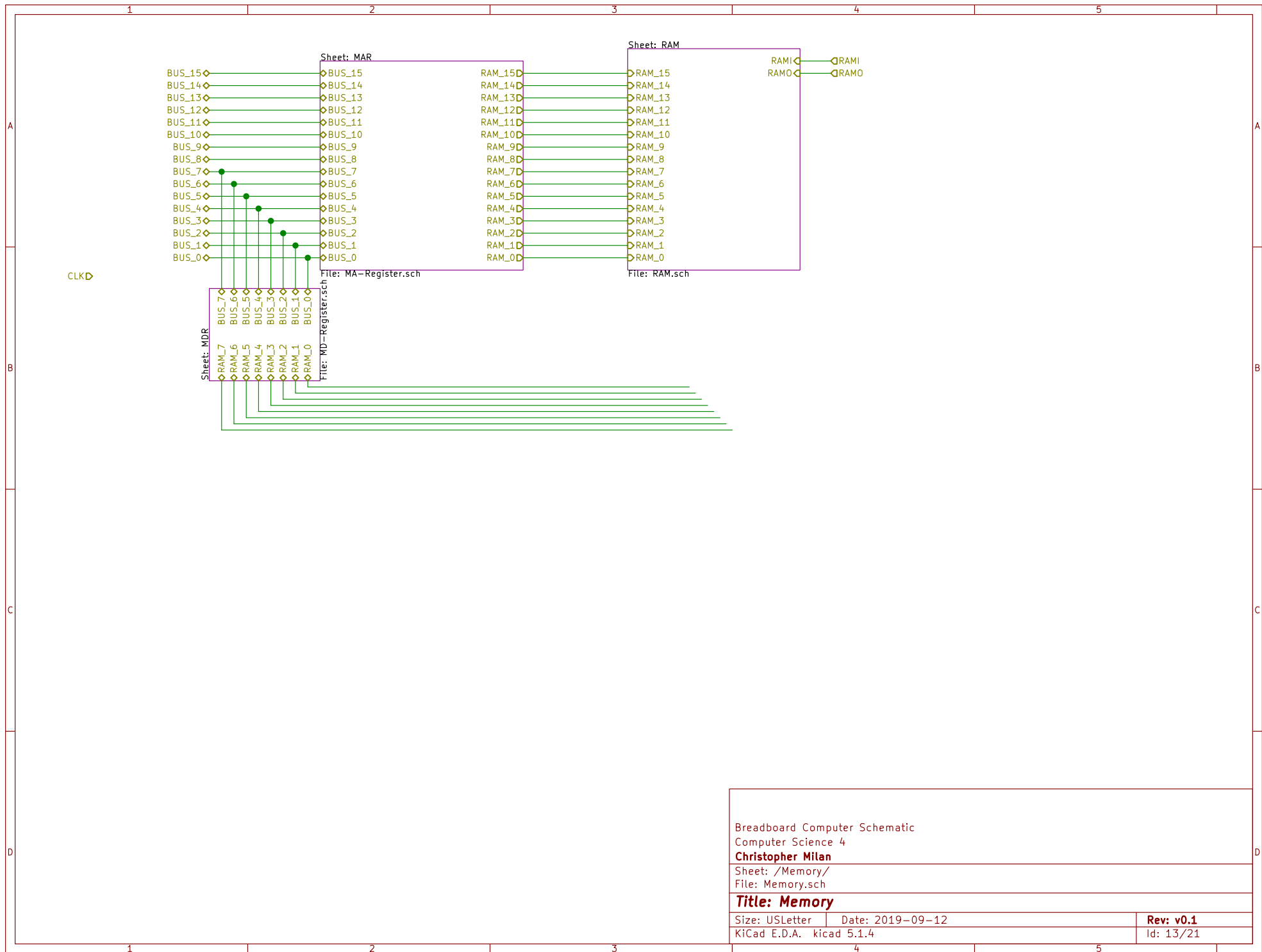
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Sheet: //IR//
File: I-Register.sch

Title:

Size: USLetter | Date: 2019-09-12
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BUS_0D
BUS_1D
BUS_2D
BUS_3D
BUS_4D
BUS_5D
BUS_6D
BUS_7D
BUS_8D
BUS_9D
BUS_10D
BUS_11D
BUS_12D
BUS_13D
BUS_14D
BUS_15D

RAM_0D
RAM_1D
RAM_2D
RAM_3D
RAM_4D
RAM_5D
RAM_6D
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RAM_10D
RAM_11D
RAM_12D
RAM_13D
RAM_14D
RAM_15D

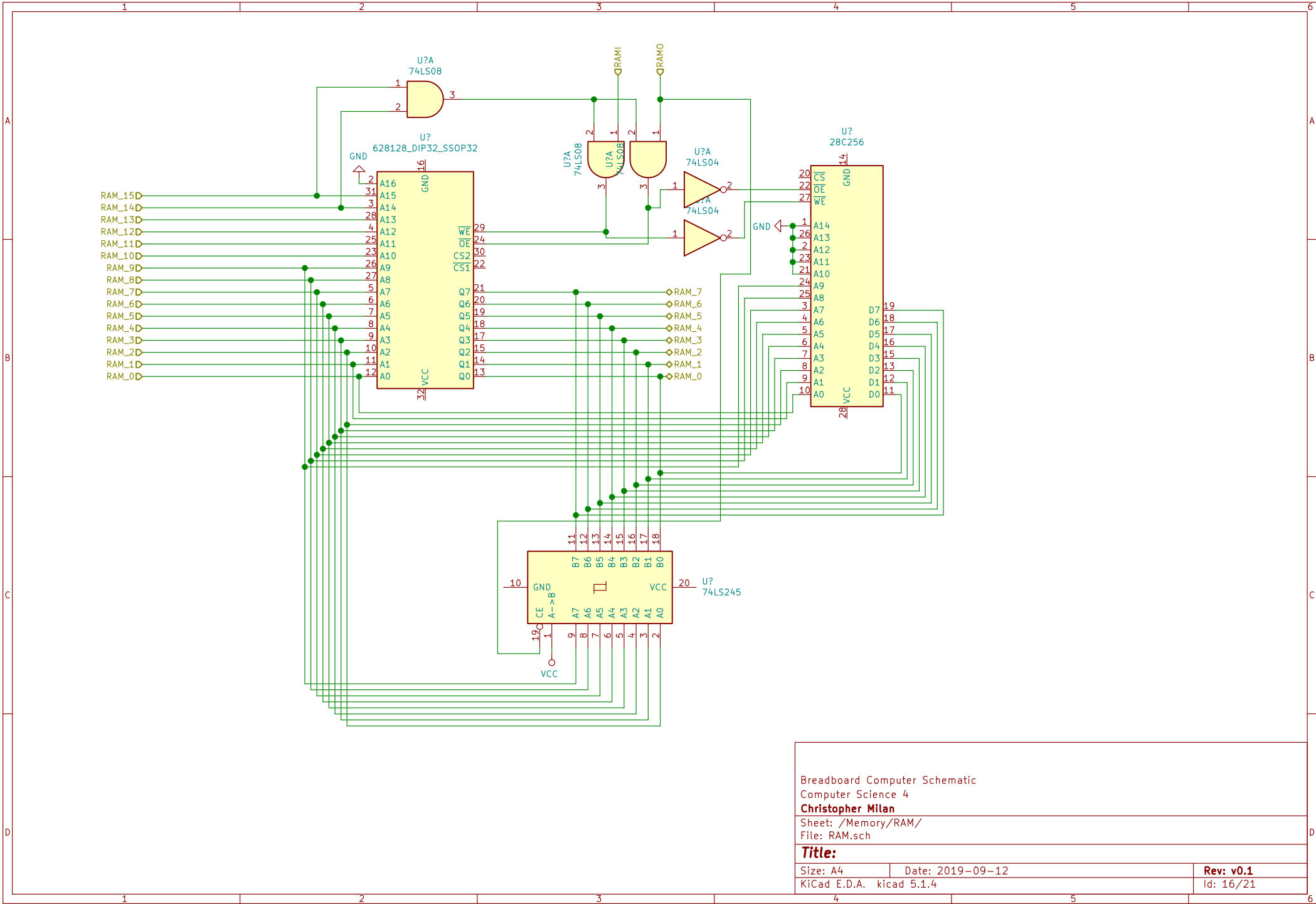
Breadboard Computer Schematic Computer Science 4 Christopher Milan		
Sheet: /Memory/MAR/ File: MA-Register.sch		
Title:		
Size: A4	Date: 2019-09-12	Rev: v0.1
KiCad E.D.A. kicad 5.1.4		Id: 14/21



BUS_0◀
BUS_1◀
BUS_2◀
BUS_3◀
BUS_4◀
BUS_5◀
BUS_6◀
BUS_7◀

RAM_0◊
RAM_1◊
RAM_2◊
RAM_3◊
RAM_4◊
RAM_5◊
RAM_6◊
RAM_7◊

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Sheet: /Memory/MDR/		
File: MD-Register.sch		
Title:		
Size: A4	Date: 2019-09-12	Rev: v0.1
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Sheet: /Memory/RAM/

File: RAM.sch

Title:

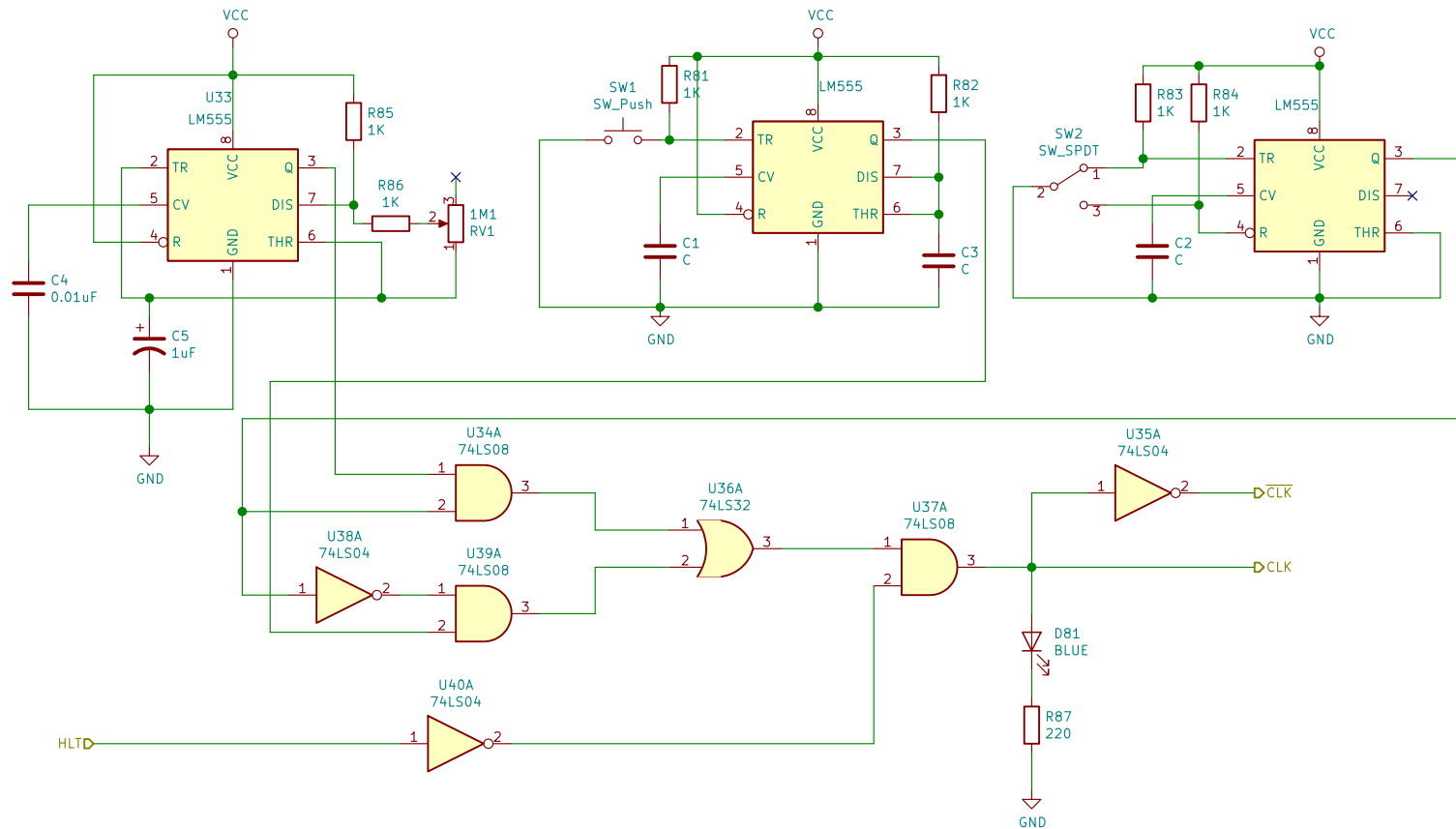
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Date: 2019-09-12

Rev: v0.1

KiCad E.D.A. kicad 5.1.4

Id: 16/21



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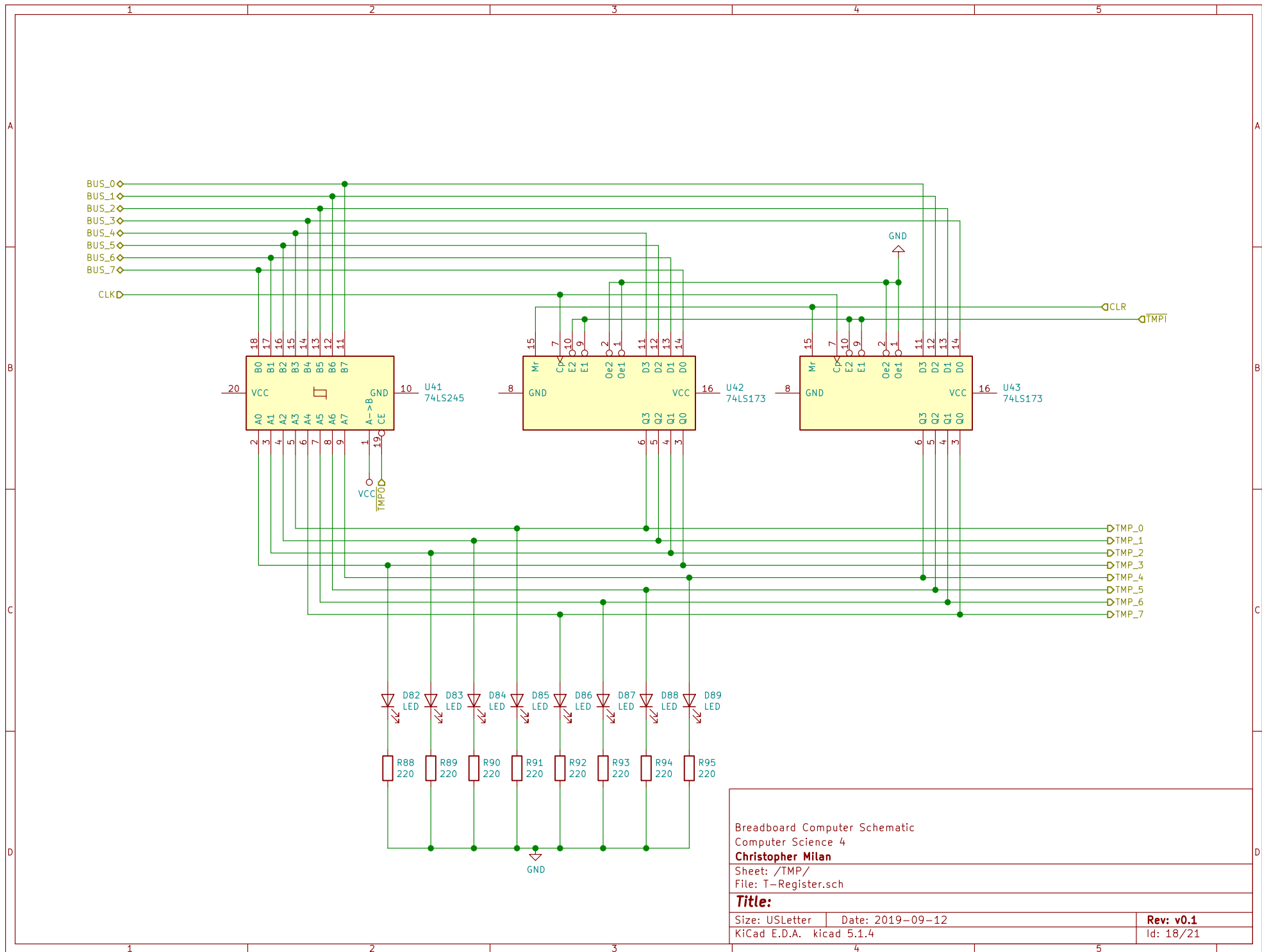
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Sheet: /Clock/
File: clock.sch

Title: Clock

Size: USLetter Date: 2019-09-12
KiCad E.D.A. kicad 5.1.4

Rev: v0.1
Id: 17/21

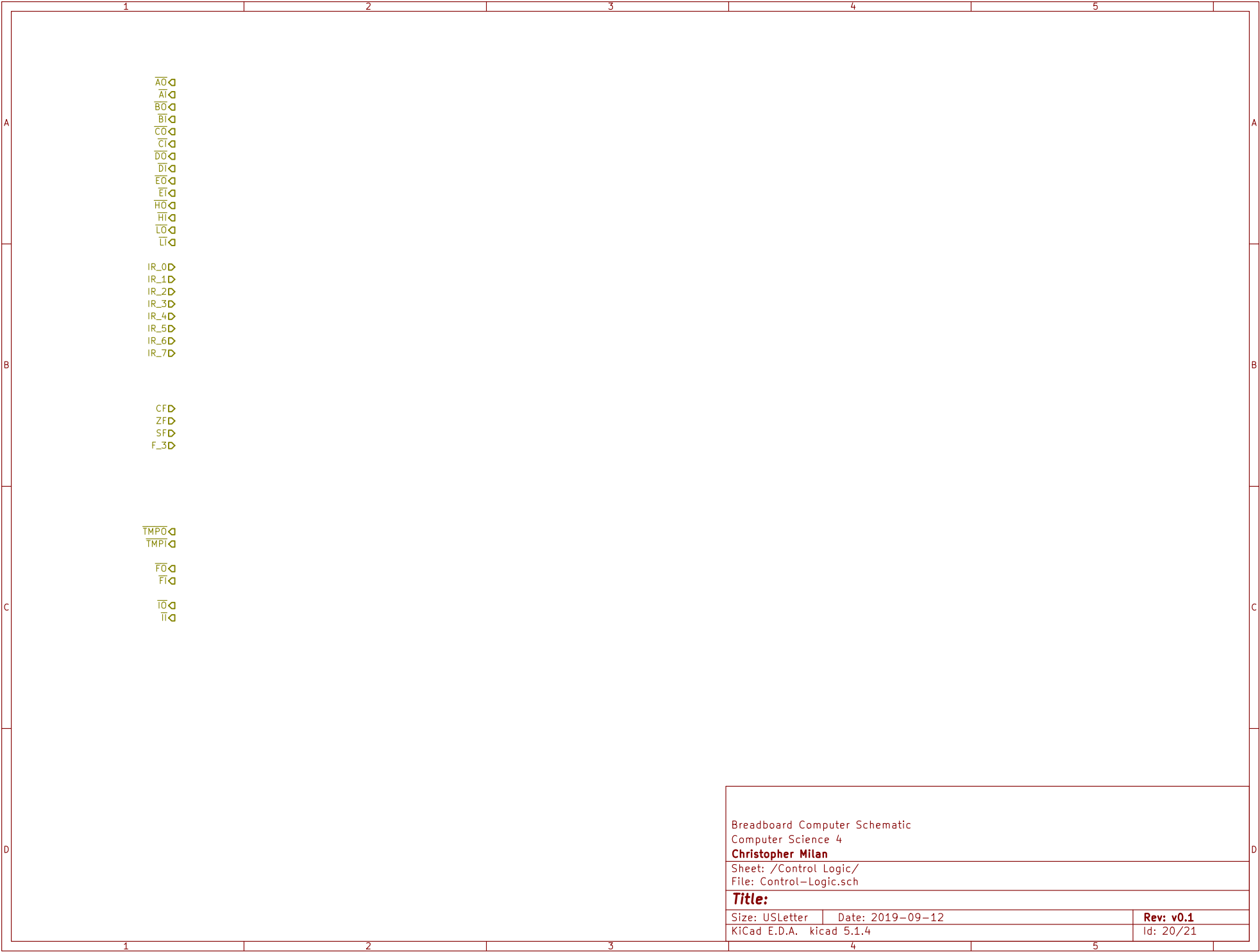


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 Sheet: /TMP/
 File: T-Register.sch

Title:

Size: USLetter Date: 2019-09-12
 KiCad E.D.A. kicad 5.1.4

Rev: v0.1
 Id: 18/21



Rev: v0.1
Id: 21/21