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1  /*****
2  Project : buz_examp l e . c
3  Version : v1.1 for AVR-GCC
4  Date    : 10/30/2003
5  Author  : Chris Troutner
6  Company : MyRobot
7  Chip    : ATMEGA128
8  Platform: Model -T Prototype
9  Comments: This program was the first successful test program for controlling the onboard ADC. After
10             initializing the LCD, it constantly reads ADC3 (PORTF.3) which is the Battery voltage
11             reference and displays the battery voltage on the LCD. This function uses the ADC
12             interrupt. It also uses AVCC (PIN 64) as the voltage reference.
13
14
15  Clock frequency      : 16.0000 MHz
16  Memory model        : Small
17  Internal SRAM size   : 4096
18  External SRAM size   : 0
19  Data Stack size      : 1024
20  *****/
21
22  #include <model t. h>
23
24  #define DT 2000
25
26  INTERRUPT(SIG_OUTPUT_COMPARE0) {
27  //local variables
28
29      PORTE ^= 0x08;
30
31  }
32
33  //Main
34  int main(void)
35  {
36
37      //Local Variables
38      char lcdstr[16]; //Variable to store a string we want to display on the LCD.
39                      //Makeing this a local.
40                      //variable is more efficient than a global variable.
41
42      reset(); //Initialize the microcontroller.
43
44      lcd_init(); //Initialize the LCD
45
46      ms_spi n(100); //Wait for 100 mS to allow power to stablize.
47
48      //PERIPHERAL AND INTERRUPT INITIALIZATION//
49      //!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!
50      //*****
51      // Timer0/Counter0 Initialization
52      //*****
53      ASSR=0x00; // Clock source: System Clock.
54      TCCR0=0x0D; // Clock value: 16Mhz / 64 = 250Khz (4 uS period) - And set Bit 4 to clear

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55                                     //TCNT0 when it equals OCR0.
56 TCNT0=0x00;                        // Make sure counter buffer is clear.
57 OCR0=0xFA;                          // Set Compare interrupt when OCR0 = TCNT0 = 250 (0xFA in hex).
58                                     //(Interrupt occurs every 1 mS)
59 // OCR0=0x7D;                      // Set Compare interrupt when OCR0 = TCNT0 = 125 (0x7D in hex).
60                                     //(Interrupt occurs every 500 nS)
61
62 // Timer(s)/Counter(s) Interrupt(s) initialization
63 TIMSK=0x02;                        //Counter0 Compare Match Interrupt Enabled.
64 ETIMSK=0x00;                      //No other Timer/Counters are used.
65 //////////////////////////////////////
66
67 DDRE = 0x08;                      //Turn on PE3 for buzzer driving.
68
69 // Global enable interrupts
70 asm volatile ("sei");
71
72 while (1)                          //Loop Forever
73 {
74     ms_spi n(DT);
75     OCR0=250;
76
77     ms_spi n(DT);
78     OCR0=125;
79
80     ms_spi n(DT);
81     OCR0=75;
82
83     ms_spi n(DT);
84     OCR0=35;
85
86     ms_spi n(DT);
87     OCR0=17;
88
89 };
90 }
91
92
93

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