```
2
     Project: mtrsp.c
 3
     Version: v1.0 for AVR-GCC
 4
          : 8/4/2003
     Date
 5
    Author : Chris Troutner
 6
     Company: MyRobot
          : ATMEGA128
 7
     Chip
     Platform: Model -T Prototype
8
9
     Comments: This program sends a 9-bit resolution PWM signal at 32Khz to PB4 and PB6 which are the
10
               signal lines to the motor driver. The duty cycle of the signal can be set using the 4
               buttons on the board.
11
12
13
               This program uses an button menu system to let the user select the duty cycle of the PWM signal
               that gets sent to each track. The following table shows what each button does:
14
15
                            ______
                                 SW1 | SW2 | SW3 | SW4 |
16
17
                            | Left Track | Left Track|Right Track|Right Track|
18
                            | + Speed | - Speed | - Speed
19
20
21
22
     Clock frequency : 16.0000 MHz
23
     Memory model : Small
     Internal SRAM size : 4096
24
25
     External SRAM size : 0
26
     Data Stack size : 1024
                              27
28
29
     //Compiler Derectives
30
31
        //Includes
32
        #i ncl ude <model t. h>
33
34
        //Defines
35
36
37
     //Global Variables
38
        char t[16];
39
        ui nt8_t sw=0;
40
41
     //Sub-Function Prototypes
42
43
     //Interrupt Handlers
44
45
        INTERRUPT(SIG_OUTPUT_COMPARE1A) {
46
47
            PORTB &= OxEF:
                           //Clear PB4
48
49
        }
50
51
        INTERRUPT(SIG OUTPUT COMPARE1B) {
52
53
            PORTB &= OxBF;
                             //Clear PB6
54
```

```
}
 55
 56
 57
           INTERRUPT(SIG OUTPUT COMPARE1C) {
 58
                                   //Set PB4 and PB6
               PORTB | = 0x50;
 59
               TCNT1 = 0;
 60
           }
 61
 62
           //Switch 3
 63
           INTERRUPT (SIG_INTERRUPTO)
 64
 65
               //REMEMBER: SET UP FOR Falling EDGE
               asm volatile ("cli"); //Disable global interrupts while servicing this interrupt.
 66
 67
               ms_spi n(2);
                                   //Wait 20ms to let mechanical oscilations to die down.
 68
 69
               if(0CR1B > 0)
 70
                   OCR1B--;
                                            //Increment duty cycle for Right Track (Track 2).
 71
 72
               SW = 3:
 73
 74
               EIFR = 0x0F;
                                        //Clear all the interrupt flags. This instruction makes it so that you
 75
                                        //can't trigger an interrupt from another button until the first button's
 76
                                        //interrupt routine has been serviced.
 77
 78
               asm volatile ("sei");
                                       //Re-enable global interrupts
 79
           }
 80
 81
           //Switch 4
 82
           INTERRUPT (SIG_INTERRUPT1)
 83
 84
               //REMEMBER: SET UP FOR Falling EDGE
 85
               asm volatile ("cli"); //Disable global interrupts while servicing this interrupt.
 86
               ms_spi n(2);
                                        //Wait 20ms to let mechanical oscilations to die down.
 87
 88
               if(0CR1B < 511)
 89
                   OCR1B++:
                                        //Increment duty cycle for Right Track (Track 2).
 90
 91
                                        //Display new duty cycle.
 92
               SW = 4;
 93
 94
               EIFR = 0x0F;
                                        //Clear all the interrupt flags. This instruction makes it so that you
 95
                                        //can't trigger an interrupt from another button until the first button's
 96
                                        //interrupt routine has been serviced.
 97
 98
               asm volatile ("sei");
                                       //Re-enable global interrupts
 99
           }
100
           //Switch 2
101
102
           INTERRUPT (SIG_INTERRUPT2)
103
104
               //REMEMBER: SET UP FOR Falling EDGE
105
               asm volatile ("cli"); //Disable global interrupts while servicing this interrupt.
106
               ms_spi n(2);
                                       //Wait 20ms to let mechanical oscilations to die down.
107
108
               if(OCR1A < 511)
```

```
109
                  OCR1A++;
                                      //Increment duty cycle for Right Track (Track 2).
110
111
              SW = 2;
112
113
114
              EIFR = 0x0F;
                                      //Clear all the interrupt flags. This instruction makes it so that you
115
                                      //can't trigger an interrupt from another button until the first button's
116
                                      //interrupt routine has been serviced.
117
118
              asm volatile ("sei");
                                      //Re-enable global interrupts
119
          }
120
121
          //Switch 1
122
          I NTERRUPT (SI G_I NTERRUPT3)
123
124
              //REMEMBER: SET UP FOR Falling EDGE
125
              asm volatile ("cli"); //Disable global interrupts while servicing this interrupt.
126
              ms_spi n(2);
                                      //Wait 20ms to let mechanical oscilations to die down.
127
128
              if(OCR1A > 0)
129
                  OCR1A--:
                                      //Increment duty cycle for Right Track (Track 2).
130
131
                                      //Display new duty cycle.
132
              SW = 1;
133
134
              EIFR = 0x0F;
                                      //Clear all the interrupt flags. This instruction makes it so that you
135
                                      //can't trigger an interrupt from another button until the first button's
                                      //interrupt routine has been serviced.
136
137
138
              asm volatile ("sei");
                                      //Re-enable global interrupts
139
          }
140
141
      //Mai n
142
      int main(void)
143
          //Local Variables
144
145
          //Initialization
146
147
              reset();
148
149
              l cd_i ni t();
150
151
              //
                              PERIPHERIAL AND INTERRUPT INITIALIZATION
152
153
              154
155
                               TimerO/CounterO Initialization
156
157
                              // Nothing going on in this register.
              TCCR1A=0x00;
158
              TCCR1B=0x01;
                              // set clock to 16Mhz
159
              TCCR1C=0x00;
                              // Nothing going on in this register.
160
                              // This counter buffer is actually composed of 2 8-bit registers TCNT1H & TCNT1L
              TCNT1=0;
161
162
              // These registers should have a value between 0 and 511. (1 - OCR1n/511) = Duty Cycle %.
```

```
163
             OCR1A=384;
                           // PWM Duty Cycle for Track1
164
             OCR1B=384;
                           // PWM Duty Cycle for Track2
                           // Top of Cycle Window
165
             OCR1C=511;
166
167
             // Timer(s)/Counter(s) Interrupt(s) initialization
             TIMSK=0x18:
                           //Compare A & B interrupts enabled. (pg. 137 of M128 datasheet)
168
169
             ETI MSK=0x01:
                           //Compare C interrupt enabled. (pg. 137 of M128 datasheet)
170
             171
172
             173
174
                            External Interrupt 0-3 Initialization
175
             EI CRA=0xAA;
176
                           //Set INTO-3 to trigger by a falling edge. (pg. 86 of M128 datasheet)
177
             EI CRB=0x00;
                           //This controlls INT4-7. (pg. 87 of M128 datasheet)
                           //Enable INTO-3 interrupts (Turn them on). (pg. 88 of M128 datasheet)
178
             EIMSK=0x0F;
179
             180
181
             //Send initial Duty Cycle Setting
182
             strcpy(t, "Change w/ switch");
183
             line1(t):
             sprintf(t, "L: %3.1f R: %3.1f", 100.0 * ((float) 0CR1A/512.0), 100.0 * ((float) 0CR1B/512.0));
184
185
             line2(t);
186
187
         //Enable global interrupts (Keep this instruction at the end of initialization).
188
             asm volatile ("sei");
189
190
             DDRB | = 0 \times F0;
                                          //Enable Motor Driver Port
191
             //PORTB = (0x50|(PORTB&0x0F)); //Initialize Motor Driver
192
             PORTB | = 0xF0;
193
194
         //Main Execution Code
195
             while (1)
196
197
                 if(SW != 0) {
198
                    //Display new duty cycle.
                                              ",sw);
199
                    sprintf(t, "Switch %d
200
                    line1(t);
201
                                            ");
202
                    strcpy(t, "
                    line2(t);
203
204
                    sprintf(t, "L: %4.2f R: %4.2f", 100.0 * ((float) 0CR1A/512.0), 100.0 * ((float) 0CR1B/512.0));
205
                    line2(t):
206
207
                    SW = 0:
208
                 }
209
210
             };
         }
211
212
```

213214