
Cycle Count : 1
PC : 0x3002
IR : 0x0000

STATE_NUMBER: 0x0021 (33) mem_cycle:1

BUS : 0x3000 MDR : 0x0000 MAR : 0x3000 CCs: N = 0 Z = 1 P = 0

Registers: 0: 0x0000 1: 0x0000 2: 0x0000 3: 0x0000 4: 0x0000 5: 0x0000

6: 0x0000 7: 0x0000

MAR <- PC PC <- PC + 2 MDR <- M R R R 35 IR <- MDR BEN<-IR[11] & N + IR[10] & Z + IR[9] & P [IR[15:12]]

Current register/bus values :

Cycle Count : 2
PC : 0x3002
IR : 0x0000

STATE_NUMBER: 0x0021 (33) mem_cycle:2

BUS : 0x0000 MDR : 0x0000 MAR : 0x3000 CCs: N = 0 Z = 1 P = 0 Registers:

Registers:
0: 0x0000
1: 0x0000
2: 0x0000
3: 0x0000
4: 0x0000
5: 0x0000
6: 0x0000
7: 0x0000

Cycle Count : 3
PC : 0x3002
IR : 0x0000

STATE_NUMBER: 0x0021 (33) mem_cycle:3

BUS : 0x0000 MDR : 0x0000 MAR : 0x3000 CCs: N = 0 Z = 1 P = 0

Registers:
0: 0x0000
1: 0x0000
2: 0x0000
3: 0x0000
4: 0x0000
5: 0x0000
6: 0x0000
7: 0x0000

Current register/bus values :

Cycle Count : 4
PC : 0x3002
IR : 0x0000

STATE_NUMBER: 0x0021 (33) mem_cycle:4

BUS : 0x0000 MDR : 0x0000 MAR : 0x3000 CCs: N = 0 Z = 1 P = 0

Registers: 0: 0x0000 1: 0x0000 2: 0x0000 3: 0x0000 4: 0x0000 5: 0x0000 6: 0x0000

Cycle Count : 5
PC : 0x3002
IR : 0x0000

STATE_NUMBER: 0x0021 (33) mem_cycle:5

BUS : 0x0000 MDR : 0x0000 MAR : 0x3000 CCs: N = 0 Z = 1 P = 0

Registers:
0: 0x0000
1: 0x0000
2: 0x0000
3: 0x0000
4: 0x0000
5: 0x0000
6: 0x0000
7: 0x0000

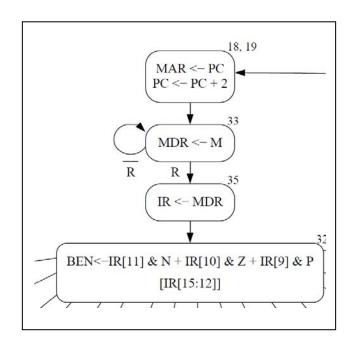
Current register/bus values :

Cycle Count : 6
PC : 0x3002
IR : 0x0000

STATE_NUMBER: 0x0023 (35)

BUS : 0x0000 MDR : 0x5020 MAR : 0x3000 CCs: N = 0 Z = 1 P = 0

Registers:
0: 0x0000
1: 0x0000
2: 0x0000
3: 0x0000
4: 0x0000
5: 0x0000
6: 0x0000
7: 0x0000



Cycle Count : 7
PC : 0x3002
IR : 0x5020

STATE_NUMBER: 0x0020 (32)

BUS : 0x5020 MDR : 0x5020 MAR : 0x3000 CCs: N = 0 Z = 1 P = 0

Registers: 0: 0x0000 1: 0x0000 2: 0x0000 3: 0x0000 4: 0x0000 5: 0x0000 6: 0x0000 7: 0x0000

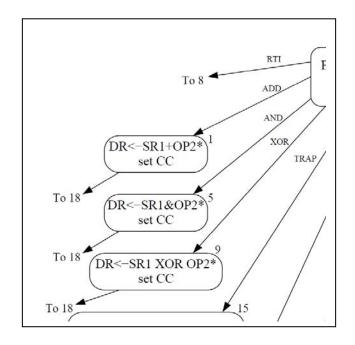
Current register/bus values :

Cycle Count : 8
PC : 0x3002
IR : 0x5020

STATE_NUMBER: 0x0005

BUS : 0x0000 MDR : 0x5020 MAR : 0x3000 CCs: N = 0 Z = 1 P = 0

Registers:
0: 0x0000
1: 0x0000
2: 0x0000
3: 0x0000
4: 0x0000
5: 0x0000
6: 0x0000
7: 0x0000



Cycle Count : 9
PC : 0x3002
IR : 0x5020

STATE_NUMBER : 0x0012 (18)

BUS : 0x0000 MDR : 0x5020 MAR : 0x3000 CCs: N = 0 Z = 1 P = 0

Registers:

0: 0x0000

1: 0x0000

2: 0x0000

3: 0x0000

4: 0x0000

5: 0x0000

6: 0x0000

7: 0x0000

Current register/bus values :

Cycle Count : 10
PC : 0x3004
IR : 0x5020

STATE_NUMBER: 0x0021

BUS : 0x3002 MDR : 0x5020 MAR : 0x3002 CCs: N = 0 Z = 1 P = 0

Registers:

0: 0x0000

1: 0x0000

2: 0x0000

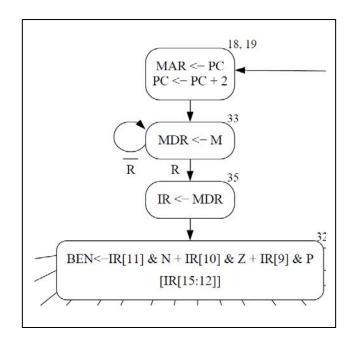
3: 0x0000

4: 0x0000

5: 0x0000

J. 0A0000

6: 0x0000



Cycle Count : 11
PC : 0x3004
IR : 0x5020

STATE_NUMBER: 0x0021

BUS : 0x0000 MDR : 0x5020 MAR : 0x3002 CCs: N = 0 Z = 1 P = 0

Registers: 0: 0x0000 1: 0x0000 2: 0x0000 3: 0x0000

4: 0x0000 5: 0x0000

6: 0x0000

7: 0x0000

Current register/bus values :

Cycle Count : 12
PC : 0x3004
IR : 0x5020

STATE_NUMBER: 0x0021

BUS : 0x0000 MDR : 0x5020 MAR : 0x3002 CCs: N = 0 Z = 1 P = 0

Registers: 0: 0x0000 1: 0x0000

1: 0x0000 2: 0x0000

3: 0x0000

4: 0x0000

5: 0x0000

6: 0x0000

Cycle Count : 13
PC : 0x3004
IR : 0x5020

STATE_NUMBER: 0x0021

BUS : 0x0000 MDR : 0x5020 MAR : 0x3002 CCs: N = 0 Z = 1 P = 0

Registers: 0: 0x0000

4 0 0000

1: 0x0000

2: 0x0000

3: 0x0000

4: 0x0000

5: 0x0000

6: 0x0000

7: 0x0000

Current register/bus values :

Cycle Count : 14
PC : 0x3004
IR : 0x5020

STATE_NUMBER: 0x0021

BUS : 0x0000 MDR : 0x5020 MAR : 0x3002 CCs: N = 0 Z = 1 P = 0

Registers:

0: 0x0000

1: 0x0000

2: 0x0000

3: 0x0000

4: 0x0000

5: 0x0000

6: 0x0000

Cycle Count : 15
PC : 0x3004
IR : 0x5020

STATE_NUMBER: 0x0023

BUS : 0x0000 MDR : 0x1e25 MAR : 0x3002 CCs: N = 0 Z = 1 P = 0

Registers: 0: 0x0000 1: 0x0000 2: 0x0000 3: 0x0000

4: 0x0000 5: 0x0000

6: 0x0000

7: 0x0000

Current register/bus values :

Cycle Count : 16
PC : 0x3004
IR : 0x1e25

STATE_NUMBER: 0x0020

BUS : 0x1e25 MDR : 0x1e25 MAR : 0x3002

CCs: N = 0 Z = 1 P = 0 Registers: 0: 0x0000 1: 0x0000 2: 0x0000 3: 0x0000 4: 0x0000 5: 0x0000

6: 0x0000

Cycle Count : 17
PC : 0x3004
IR : 0x1e25

STATE_NUMBER: 0x0001

BUS : 0x0000 MDR : 0x1e25 MAR : 0x3002 CCs: N = 0 Z = 1 P = 0

Registers:
0: 0x0000
1: 0x0000
2: 0x0000
3: 0x0000
4: 0x0000
5: 0x0000
6: 0x0000
7: 0x0000

Current register/bus values :

Cycle Count : 18
PC : 0x3004
IR : 0x1e25

STATE_NUMBER: 0x0012

BUS : 0x0005 MDR : 0x1e25 MAR : 0x3002 CCs: N = 0 Z = 0 P = 1

Registers: 0: 0x0000 1: 0x0000 2: 0x0000 3: 0x0000 4: 0x0000 5: 0x0000

6: 0x0000 7: 0x0005

Cycle Count : 19
PC : 0x3006
IR : 0x1e25

STATE_NUMBER: 0x0021

BUS : 0x3004 MDR : 0x1e25 MAR : 0x3004 CCs: N = 0 Z = 0 P = 1

Registers: 0: 0x0000 1: 0x0000 2: 0x0000

3: 0x0000 4: 0x0000

5: 0x0000

6: 0x0000

7: 0x0005

Current register/bus values :

Cycle Count : 20 PC : 0x3006 IR : 0x1e25

STATE_NUMBER: 0x0021

BUS : 0x0000 MDR : 0x1e25 MAR : 0x3004 CCs: N = 0 Z = 0 P = 1

Registers: 0: 0x0000 1: 0x0000

2: 0x0000

3: 0x0000

4: 0x0000

5: 0x0000

6: 0x0000

Cycle Count : 21
PC : 0x3006
IR : 0x1e25

STATE_NUMBER: 0x0021

BUS : 0x0000 MDR : 0x1e25 MAR : 0x3004 CCs: N = 0 Z = 0 P = 1

Registers: 0: 0x0000 1: 0x0000

2: 0x0000

3: 0x0000

4: 0x0000

5: 0x0000

6: 0x0000

7: 0x0005

Current register/bus values :

Cycle Count : 22
PC : 0x3006
IR : 0x1e25

STATE_NUMBER: 0x0021

BUS : 0x0000 MDR : 0x1e25 MAR : 0x3004 CCs: N = 0 Z = 0 P = 1

Registers:

0: 0x0000

1: 0x0000

2: 0x0000

3: 0x0000

4: 0x0000

5: 0x0000

6: 0x0000

Cycle Count : 23
PC : 0x3006
IR : 0x1e25

STATE_NUMBER: 0x0021

BUS : 0x0000 MDR : 0x1e25 MAR : 0x3004 CCs: N = 0 Z = 0 P = 1

Registers: 0: 0x0000 1: 0x0000 2: 0x0000 3: 0x0000

4: 0x0000 5: 0x0000

6: 0x0000

7: 0x0005

Current register/bus values :

Cycle Count : 24
PC : 0x3006
IR : 0x1e25

STATE_NUMBER: 0x0023

BUS : 0x0000 MDR : 0x19c7 MAR : 0x3004 CCs: N = 0 Z = 0 P = 1

Registers: 0: 0x0000 1: 0x0000

2: 0x0000

3: 0x0000

4: 0x0000

5: 0x0000

6: 0x0000

Cycle Count : 25
PC : 0x3006
IR : 0x19c7

STATE_NUMBER: 0x0020

BUS : 0x19c7 MDR : 0x19c7 MAR : 0x3004 CCs: N = 0 Z = 0 P = 1

Registers:
0: 0x0000
1: 0x0000
2: 0x0000
3: 0x0000
4: 0x0000
5: 0x0000
6: 0x0000
7: 0x0005

Current register/bus values :

Cycle Count : 26
PC : 0x3006
IR : 0x19c7

STATE_NUMBER: 0x0001

BUS : 0x0000 MDR : 0x19c7 MAR : 0x3004 CCs: N = 0 Z = 0 P = 1

Registers:
0: 0x0000
1: 0x0000
2: 0x0000
3: 0x0000
4: 0x0000
5: 0x0000
6: 0x0000
7: 0x0005

Cycle Count : 27
PC : 0x3006
IR : 0x19c7

STATE_NUMBER: 0x0012

BUS : 0x000a MDR : 0x19c7 MAR : 0x3004 CCs: N = 0 Z = 0 P = 1

Registers: 0: 0x0000 1: 0x0000 2: 0x0000 3: 0x0000

4: 0x000a 5: 0x0000 6: 0x0000

7: 0x0005

Current register/bus values :

Cycle Count : 28
PC : 0x3008
IR : 0x19c7

STATE_NUMBER: 0x0021

BUS : 0x3006 MDR : 0x19c7 MAR : 0x3006 CCs: N = 0 Z = 0 P = 1

Registers: 0: 0x0000 1: 0x0000 2: 0x0000 3: 0x0000 4: 0x000a 5: 0x0000 6: 0x0000

Cycle Count : 29
PC : 0x3008
IR : 0x19c7

STATE_NUMBER: 0x0021

BUS : 0x0000 MDR : 0x19c7 MAR : 0x3006 CCs: N = 0 Z = 0 P = 1

Registers: 0: 0x0000 1: 0x0000 2: 0x0000

3: 0x0000 4: 0x000a

5: 0x0000

6: 0x0000

7: 0x0005

Current register/bus values :

Cycle Count : 30 PC : 0x3008 IR : 0x19c7

STATE_NUMBER: 0x0021

BUS : 0x0000 MDR : 0x19c7 MAR : 0x3006 CCs: N = 0 Z = 0 P = 1

Registers: 0: 0x0000

1: 0x0000

2: 0x0000

3: 0x0000

4: 0x000a

5: 0x0000

6: 0x0000

Cycle Count : 31
PC : 0x3008
IR : 0x19c7

STATE_NUMBER: 0x0021

BUS : 0x0000 MDR : 0x19c7 MAR : 0x3006 CCs: N = 0 Z = 0 P = 1

Registers: 0: 0x0000 1: 0x0000 2: 0x0000 3: 0x0000

4: 0x000a 5: 0x0000

6: 0x0000

7: 0x0005

Current register/bus values :

Cycle Count : 32
PC : 0x3008
IR : 0x19c7

STATE_NUMBER: 0x0021

BUS : 0x0000 MDR : 0x19c7 MAR : 0x3006 CCs: N = 0 Z = 0 P = 1

Registers: 0: 0x0000 1: 0x0000

2: 0x0000

3: 0x0000

4: 0x000a

5: 0x0000

6: 0x0000

Cycle Count : 33
PC : 0x3008
IR : 0x19c7

STATE_NUMBER: 0x0023

BUS : 0x0000 MDR : 0xf025 MAR : 0x3006 CCs: N = 0 Z = 0 P = 1

Registers: 0: 0x0000 1: 0x0000 2: 0x0000 3: 0x0000

4: 0x000a 5: 0x0000

6: 0x0000

7: 0x0005

Current register/bus values :

Cycle Count: 34
PC: 0x3008
IR: 0xf025

STATE_NUMBER: 0x0020

BUS : 0xf025 MDR : 0xf025 MAR : 0x3006 CCs: N = 0 Z = 0 P = 1

Registers:
0: 0x0000
1: 0x0000
2: 0x0000
3: 0x0000
4: 0x0000
5: 0x0000

6: 0x0000

Cycle Count : 35
PC : 0x3008
IR : 0xf025

STATE_NUMBER: 0x000f

BUS : 0x0000 MDR : 0xf025 MAR : 0x3006 CCs: N = 0 Z = 0 P = 1

Registers:
0: 0x0000
1: 0x0000
2: 0x0000
3: 0x0000
4: 0x000a
5: 0x0000
6: 0x0000

7: 0x0005

Current register/bus values :

Cycle Count: 36
PC: 0x3008
IR: 0xf025

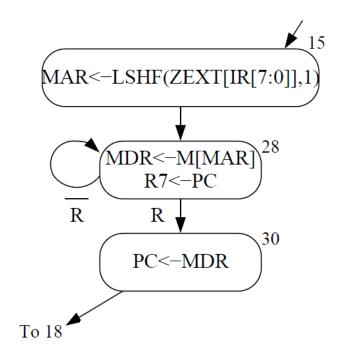
STATE_NUMBER: 0x001c (28)

BUS : 0x004a (0x0025 << 1)

MDR : 0xf025MAR : 0x004aCCs: N = 0 Z = 0 P = 1

0: 0x0000 1: 0x0000 2: 0x0000 3: 0x0000 4: 0x000a 5: 0x0000 6: 0x0000 7: 0x0005

Registers:



Cycle Count : 37
PC : 0x3008
IR : 0xf025

STATE_NUMBER: 0x001c

BUS : 0x3008 MDR : 0xf025 MAR : 0x004a CCs: N = 0 Z = 0 P = 1

Registers: 0: 0x0000 1: 0x0000

2: 0x0000

3: 0x0000

4: 0x000a

5: 0x0000 6: 0x0000

7: 0x3008

Current register/bus values :

Cycle Count: 38
PC: 0x3008
IR: 0xf025

STATE_NUMBER: 0x001c

BUS : 0x3008 MDR : 0xf025 MAR : 0x004a CCs: N = 0 Z = 0 P = 1

Registers: 0: 0x0000

1. 0..0000

1: 0x0000

2: 0x0000

3: 0x0000

4: 0x000a

5: 0x0000

6: 0x0000

Cycle Count : 39
PC : 0x3008
IR : 0xf025

STATE_NUMBER: 0x001c

BUS : 0x3008 MDR : 0xf025 MAR : 0x004a CCs: N = 0 Z = 0 P = 1

Registers: 0: 0x0000 1: 0x0000 2: 0x0000

3: 0x0000 4: 0x000a

5: 0x0000

6: 0x0000

7: 0x3008

Current register/bus values :

Cycle Count: 40
PC: 0x3008
IR: 0xf025

STATE_NUMBER: 0x001c

BUS : 0x3008 MDR : 0xf025 MAR : 0x004a CCs: N = 0 Z = 0 P = 1

Registers: 0: 0x0000 1: 0x0000

2: 0x0000

3: 0x0000

4: 0x000a

5: 0x0000

6: 0x0000

Cycle Count: 41
PC: 0x3008
IR: 0xf025

STATE_NUMBER: 0x001e (30)

BUS : 0x3008 MDR : 0x0000 MAR : 0x004a CCs: N = 0 Z = 0 P = 1

Registers: 0: 0x0000 1: 0x0000 2: 0x0000 3: 0x0000

4: 0x000a 5: 0x0000

6: 0x0000

7: 0x3008

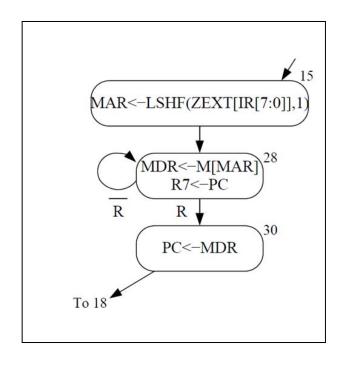
Current register/bus values :

Cycle Count : 42
PC : 0x0000
IR : 0xf025

STATE_NUMBER : 0x0012 (18)

BUS : 0x0000 MDR : 0x0000 MAR : 0x004a CCs: N = 0 Z = 0 P = 1

Registers:
0: 0x0000
1: 0x0000
2: 0x0000
3: 0x0000
4: 0x000a
5: 0x0000
6: 0x0000
7: 0x3008



(Done)