Sasank Chilamkurthy

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Education

2011–15 Bachelor of Technology, Indian Instition of Technology, Bombay, Mumbai.

Major: Electrical Engineering

Minor : Mathematics CGPA : 9.54/10

Interests: Signal Processing, Information Theory

2009–11 Board of Intermediate Education, Sri Chaitanya Junior College, Vijayawada.

Branch: Mathematics, Physics, Chemistry (MPC)

Marks: 955/1000

Research

Autumn Scheduling for Energy Harvesting Wireless Networks,

2014 Guide: Prof. Abhay Karandikar, IIT Bombay.

Consider a point to point link which is being charged by a renewable energy source where the amount of recharging energy entering the system varies inversely with the amount of energy available. How do we allocate power to such a link so as to maximize throughput? To reduce the search space, we restrict ourselves to waterfilling allocation schemes.

- Simulated the link and showed that average expected throughput over slots admit a quasi concave relation with waterlevel parameter of waterfilling policy.
- Proposed a Stochastic Approximation algorithm based on infinitesimal perturbation analysis to maximize throughput.

Summer Coded Modulation for Coherent Optical Communication Systems,

2014 Guide: Prof. LA Rusch, Centre d'optique, photonique et laser (COPL), Quebec.

To combat high phase noise from lasers in coherent optical communication systems, we need to use specialized signal constellations and coding schemes. How do various such phase noise optimized constellations stack up when used along with proposed Multi-Level Coded Modulation (MLCM) heuristics?

- Simulated 16 QAM coherent modulation for optical communication system and obtained BER vs OSNR curves for various lasers and signal constellations
- Collected raw data from back-to-back experiments and extracted phase data from raw data with offline carrier Phase Recovery DSP algorithms
- Analysed this data to evaluate the coding gain of MLCM for various constellations

Summer Fractional Fourier Transform and Chirp Parameter Estimation,

2013 Guide: Prof. V.M.Gadre, IIT Bombay.

Given samples of a noisy chirp signal, can we use a novel signal processing technique called Fractional Fourier Transform to determine the parameters of underlying chirp?

- Surveyed literature on Fractional Fourier Transform and on various ways of discretizing it
- Formulated and proved correctness of a DSP algorithm to estimate chirp parameters from noisy samples. Evaluated accuracy of the algorithm in presence of the noise by simulation
- Proved Uncertainity Principle for a new generalised transform extending fractional Fourier transform

Awards and Achievements

- \circ Undergraduate Research Award for work on Fractional Fourier Transform
- Institute Academic Prize for academic performance in the year 2013-14
- 28th rank in India in IIT JEE 2011 exam taken by more than 500,000 students
- 3rd rank in EAMCET 2011 exam written by 300,000 students
- o Gold medal in Indian National Chemistry Olympiad

• Top 300 in the country to be selected for Indian national Physics olympiad and Indian national Astronomy olympiad

Projects

Autumn Traffic Sign Recognition,

2014 Guide: Prof. Ajit Rajwade, IIT Bombay.

- o Trained classifier using challenging German Traffic Sign Recognition Database
- \circ Implemented Random Forests, Linear Discriminant Analysis and Fisher's Linear Discriminant on Histogram of Oriented Gradients (HOG) featureset to achieve test set classification accuracy of about 97%

Spring LZW compression algorithm and decoding LDPC codes,

2014 Guide: Prof. Ganesh Ramakrishnan, IIT Bombay.

- Programmed Lempel-Zev-Welch compression and decompression algorithm
- Achieved about 50 % compression ratio in the compressing large text files
- Implemented decoding of LDPC codes using sum-product algorithm in Java using specially designed data structure: Factor Graph

Spring Pipelined ARM Processor,

2014 Guide: Prof. Virendra Singh, IIT Bombay.

- $\circ\,$ Architected a 6-stage pipelined processor based on the ARM7TDMI Instruction Set
- Simulated the execution of instructions after designing the processor using Verilog HDL

Autumn Wireless Communication using Amplitude Shift Keying (ASK),

2013 Guide: Prof. J.Mukherjee, IIT Bombay.

- o Designed Analog circuits for ASK modulation and demodulation for medium wave band
- Transmitted and received the modulated waveforms through monopole antennae
- Used a microcontroller to send bit data and another to receive the data using UART protocol over this channel, thereby transmitting a text message wirelessly

Seminars and Workshops attended

December Indo-European Winter Academy, IIT Guwahati : One of 5 selected to represent

2013 IIT Bombay. Presented a 1-hour seminar on Optoelectronic Emitters covering physical principles and devices

January ITCSC-INC Winter School, CUHK, Hong Kong: One of 14 invited from India.

2014 Topics covered include Information Theory and Fourier transform for binary functions

Teaching Experience

Tutored 40 strength class once a week helping them solve tutorial sheets and clarifying doubts among other duties like scrutinizing homeworks and answer sheets.

MA 105 Calculus Autumn 2012,13,14

MA 106 Linear Algebra

Spring 2014

MA 108 Differential equations

Spring 2014

Key Courses

Electical Information Theory, Error Correcting Codes, Stochastic Optimisation, Digital Signal Pro-Engineering cessing, Digital Communications, Probability and Random Processes

Mathematics Image Processing, Data structures & Algorithms, Graph Theory, Quantum Information and & CS Computing, Real Analysis, Complex Analysis, Fourier Analysis, Abstract Algebra

Extra-Curricular Activities

- o Joint Secretary for Electrical Engineers Student Association (EESA), IIT Bombay. Planned and successfully executed two outings for 300 students
- \circ Publicity Manager for Aagomani 2013, Annual festival of EE Department. Increased outreach of events leading to increase in footfall by 200%
- Awarded bien grade in Basic French course attested by Alliance Francaise de Bombay