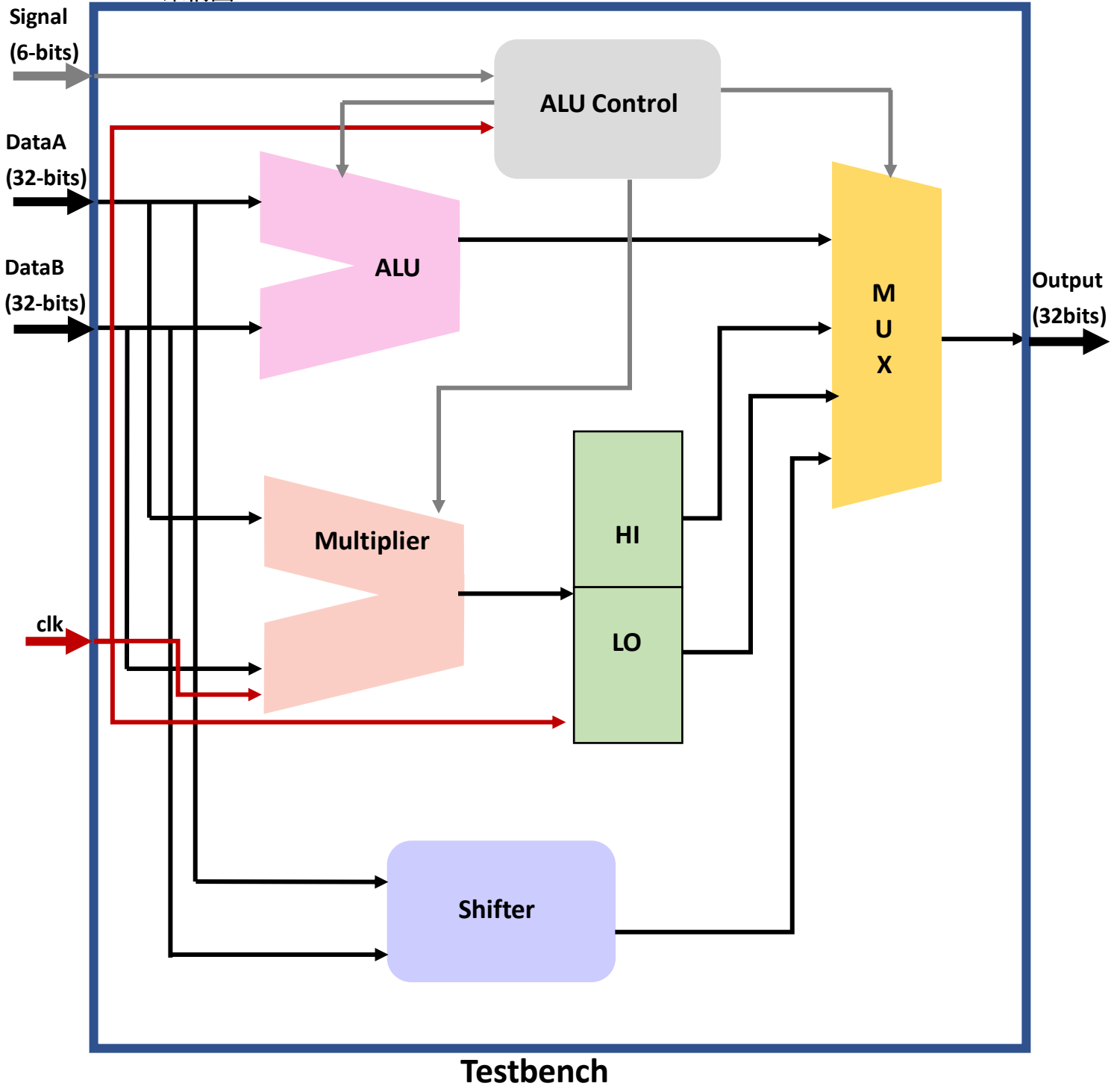
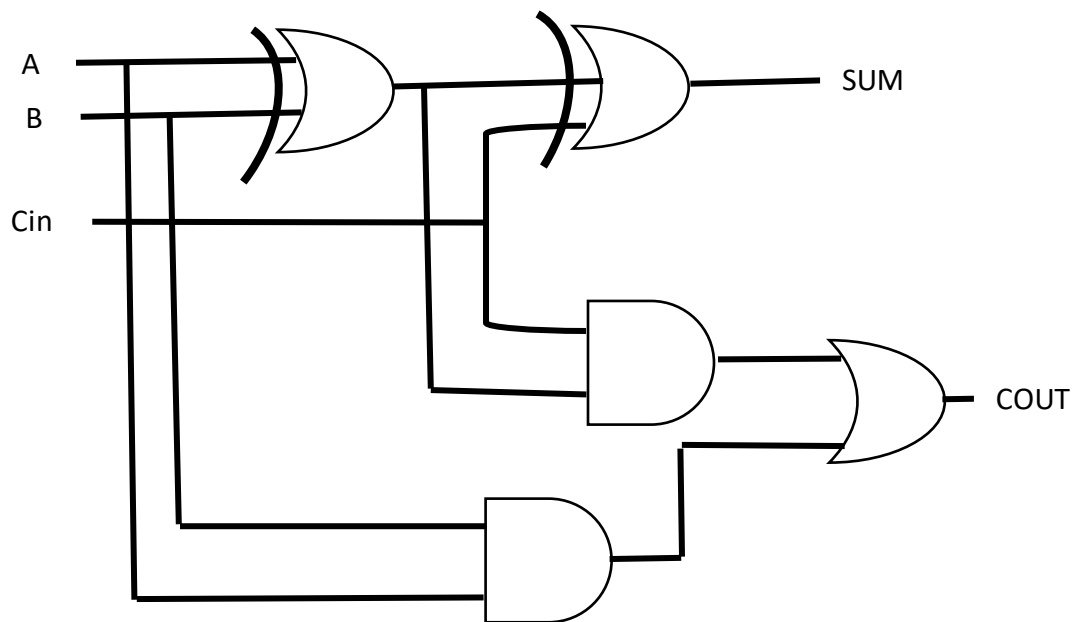


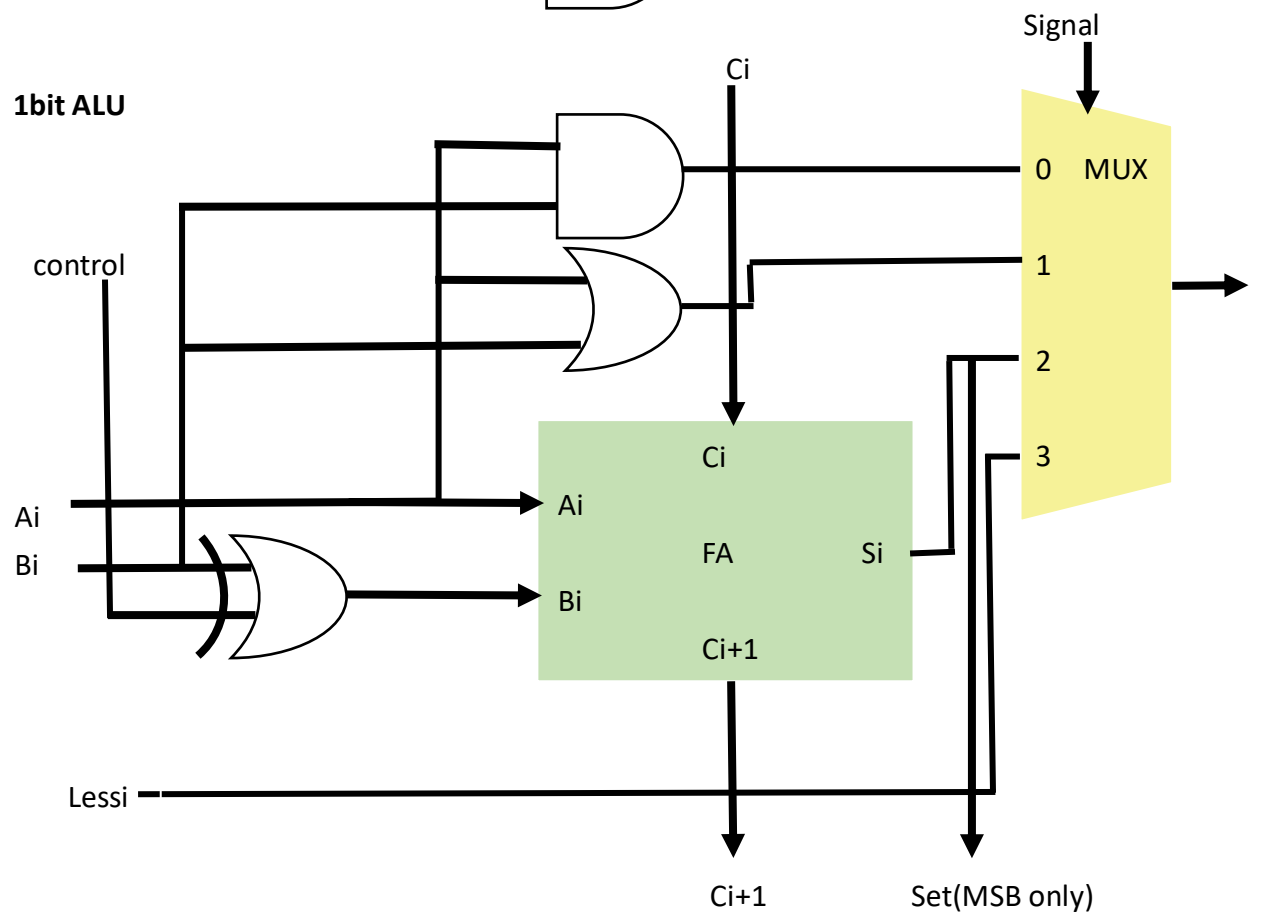
架構圖 test bench



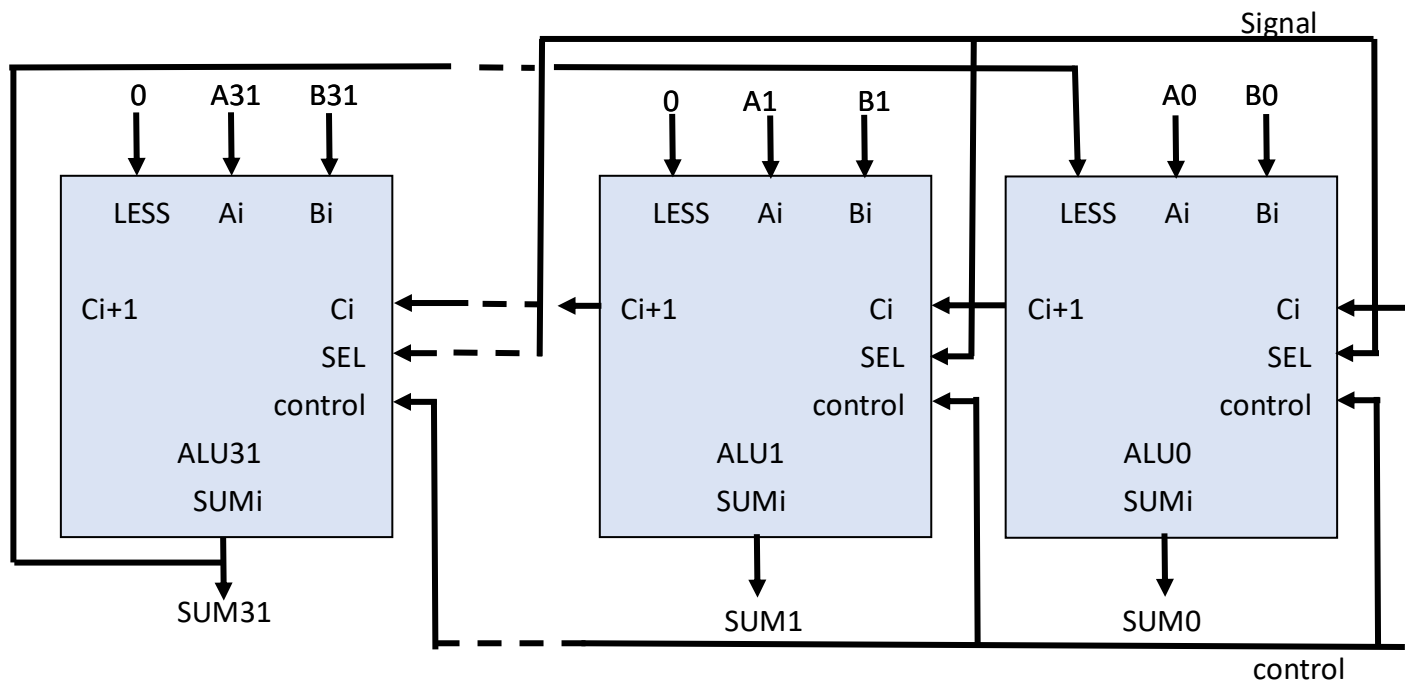
FULL ADDER(FA)



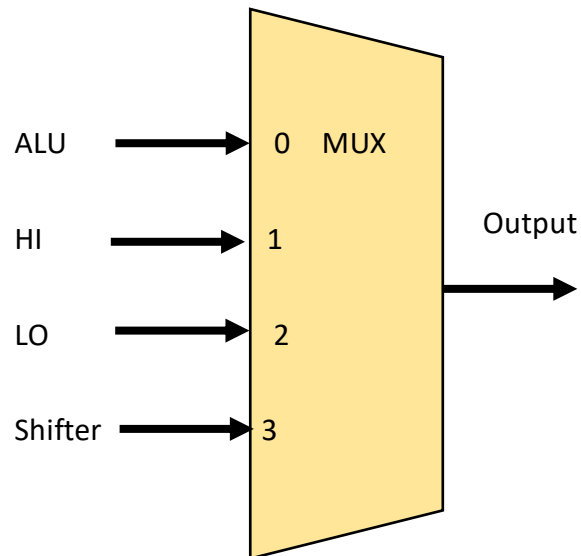
1bit ALU



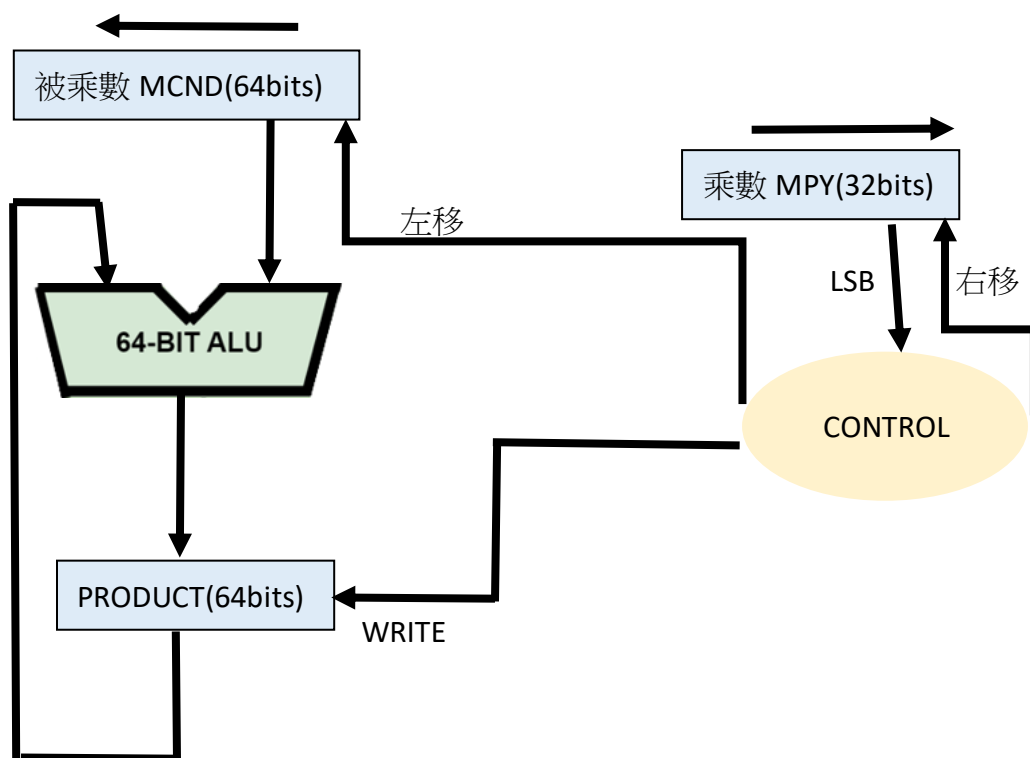
32bit ALU



MUX



乘法器



移位器

