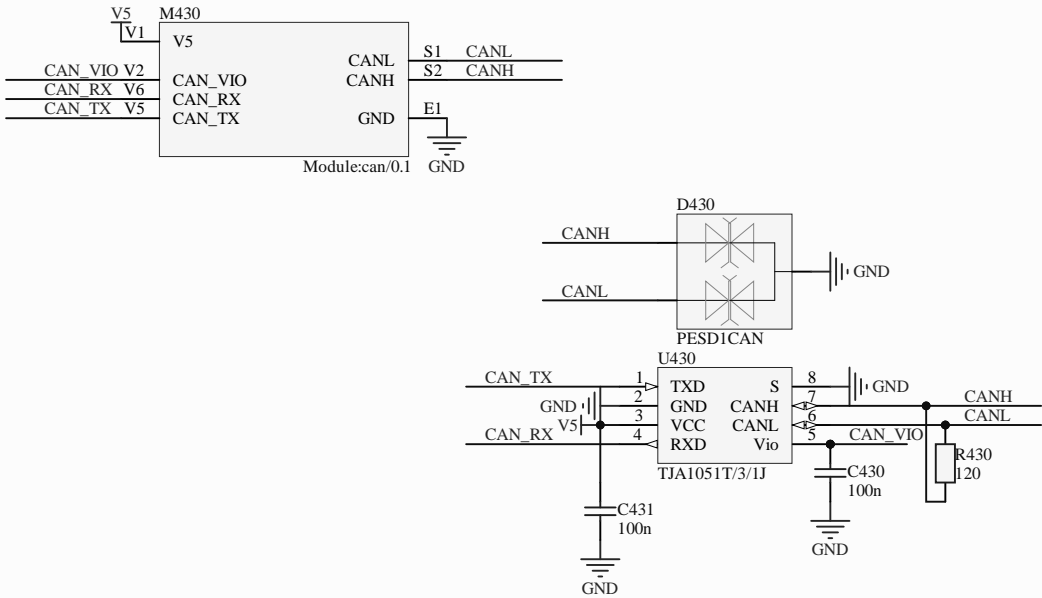
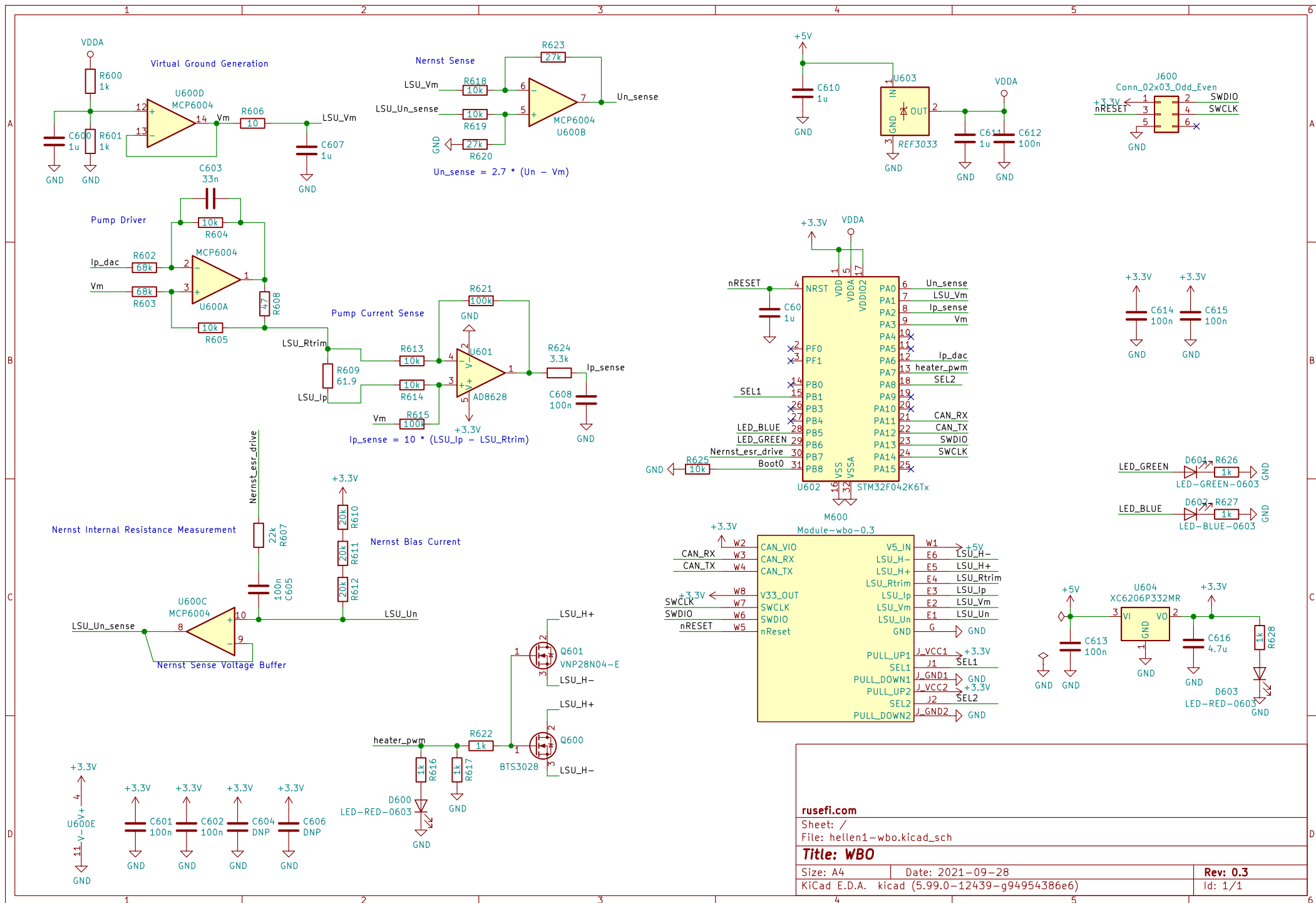


Helien-One-PCB-Logo







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Sheet: /

File: hellen1-wbo.kicad\_sch

**Title: WBO**

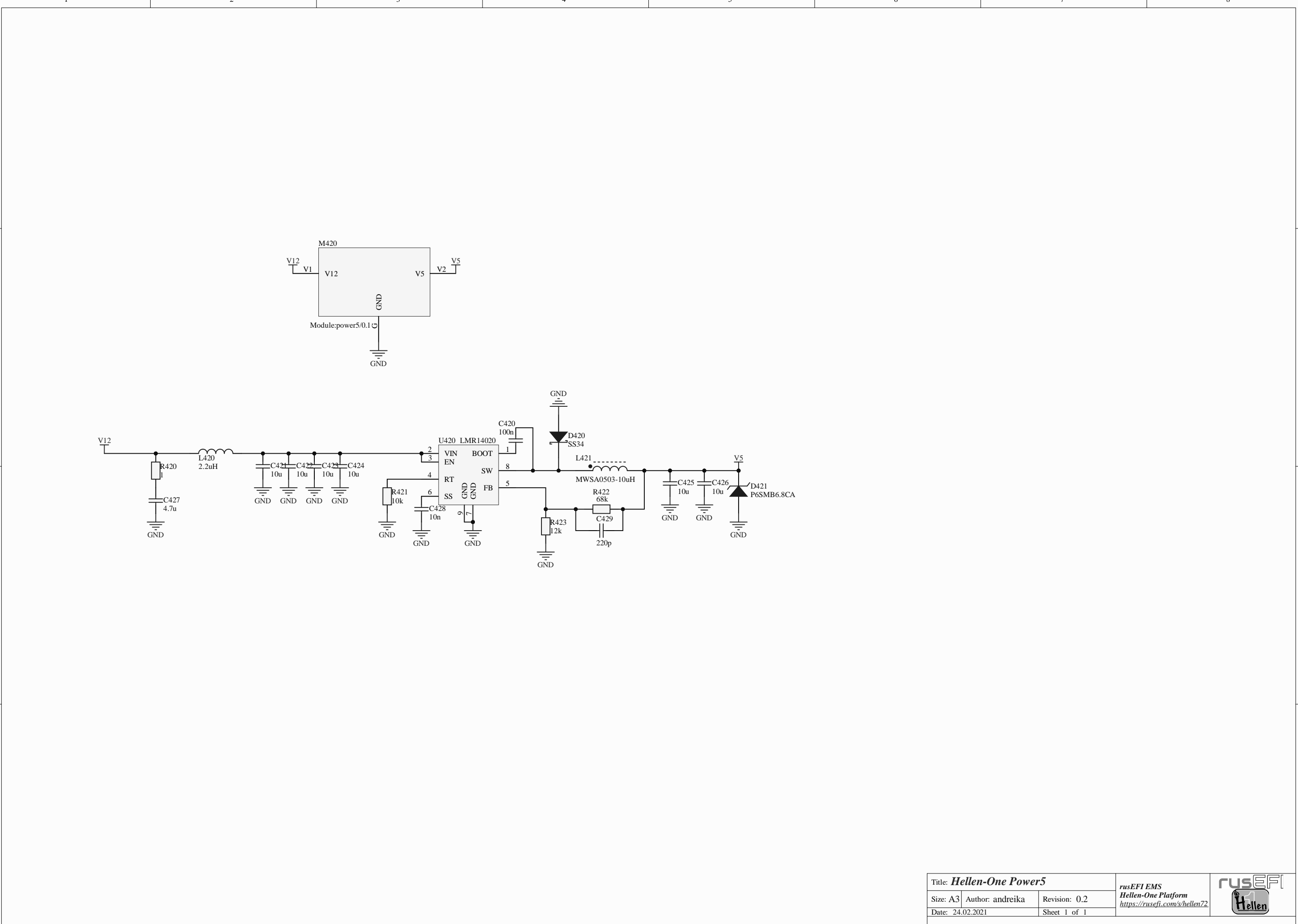
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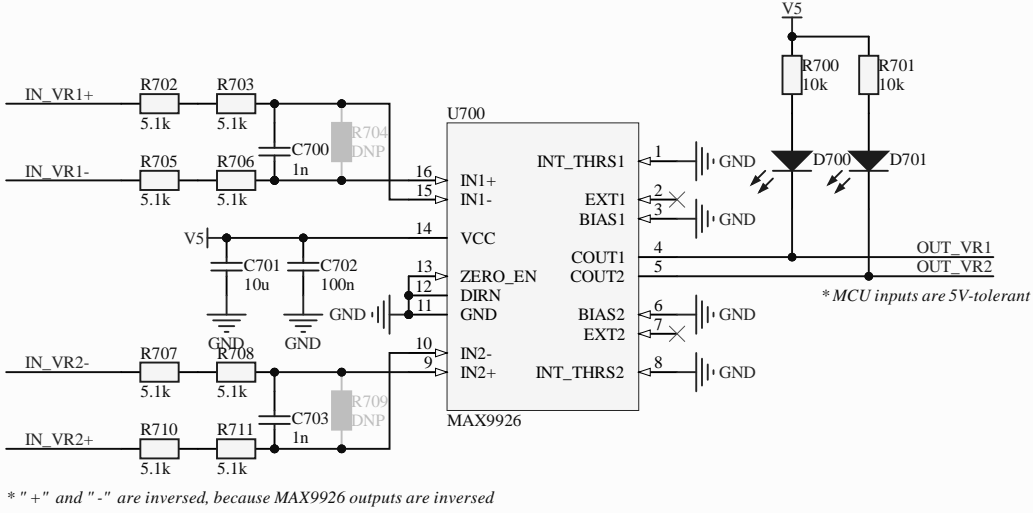
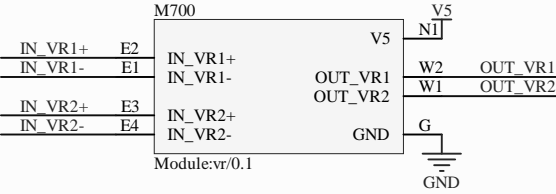
Date: 2021-09-28

KiCad E.D.A. kicad (5.99.0-12439-g94954386e6)

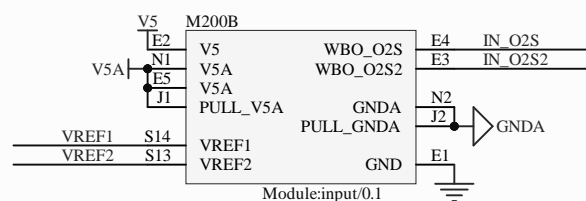
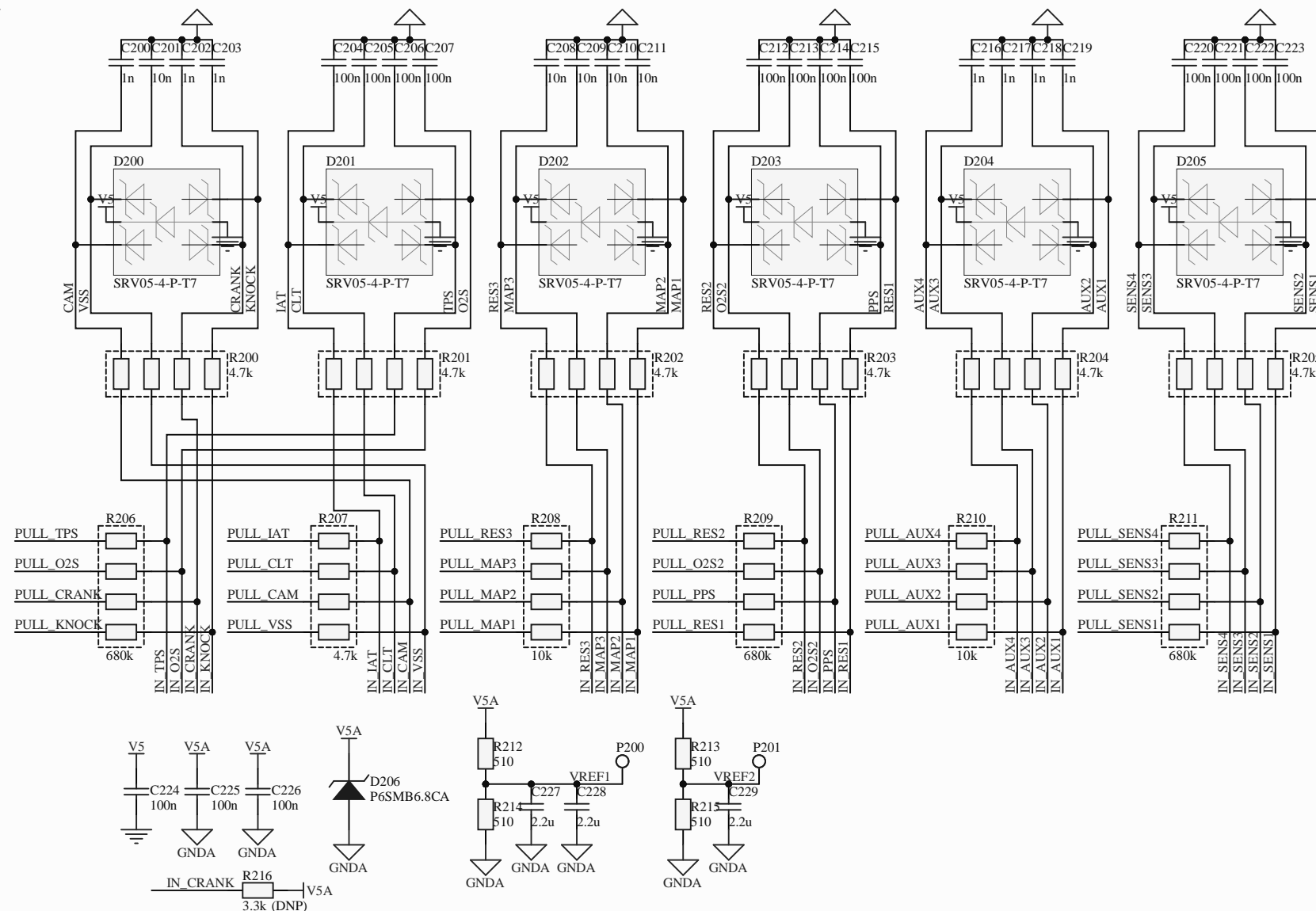
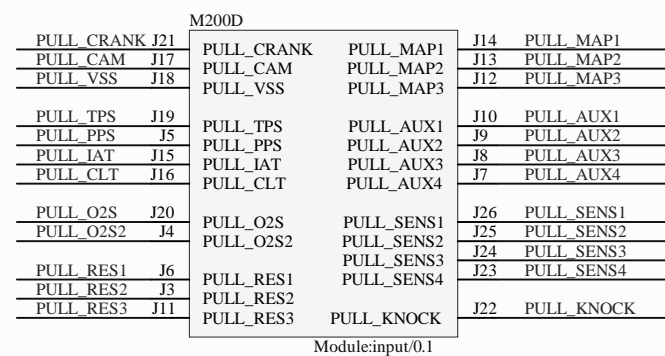
**Rev: 0.3**

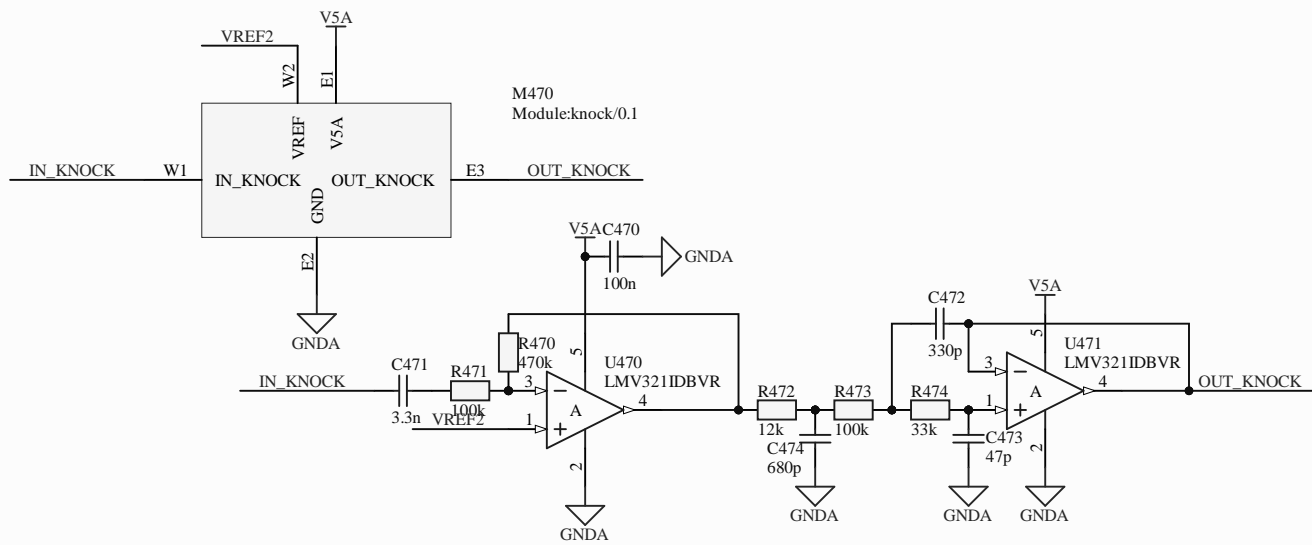
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\* "+" and "-" are inversed, because MAX9926 outputs are inversed







A

B

C

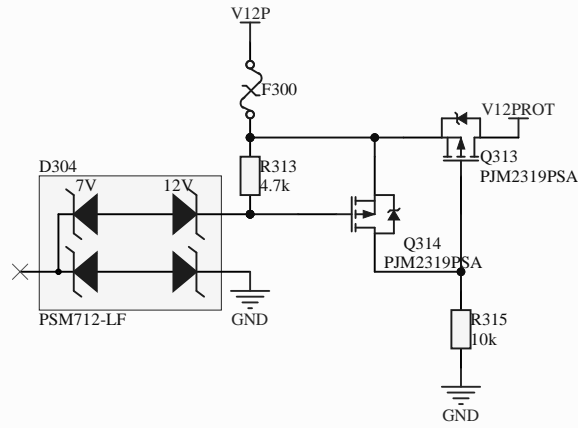
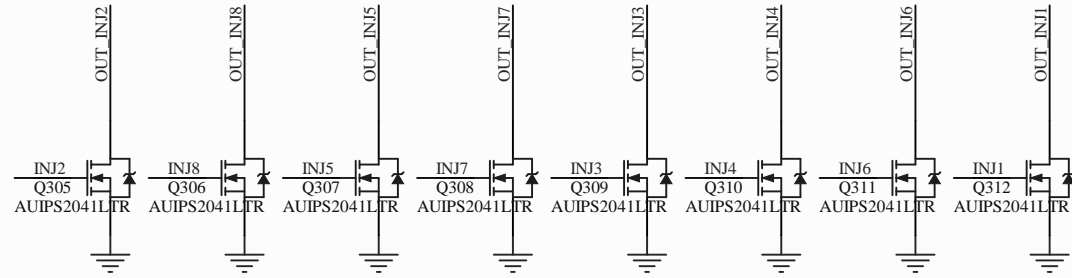
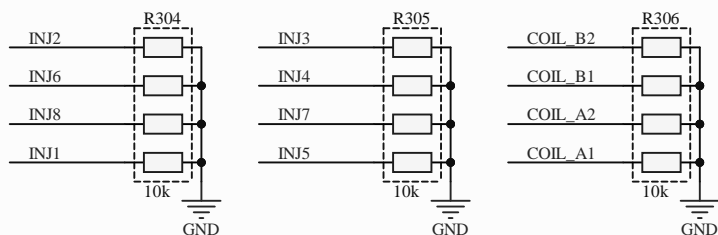
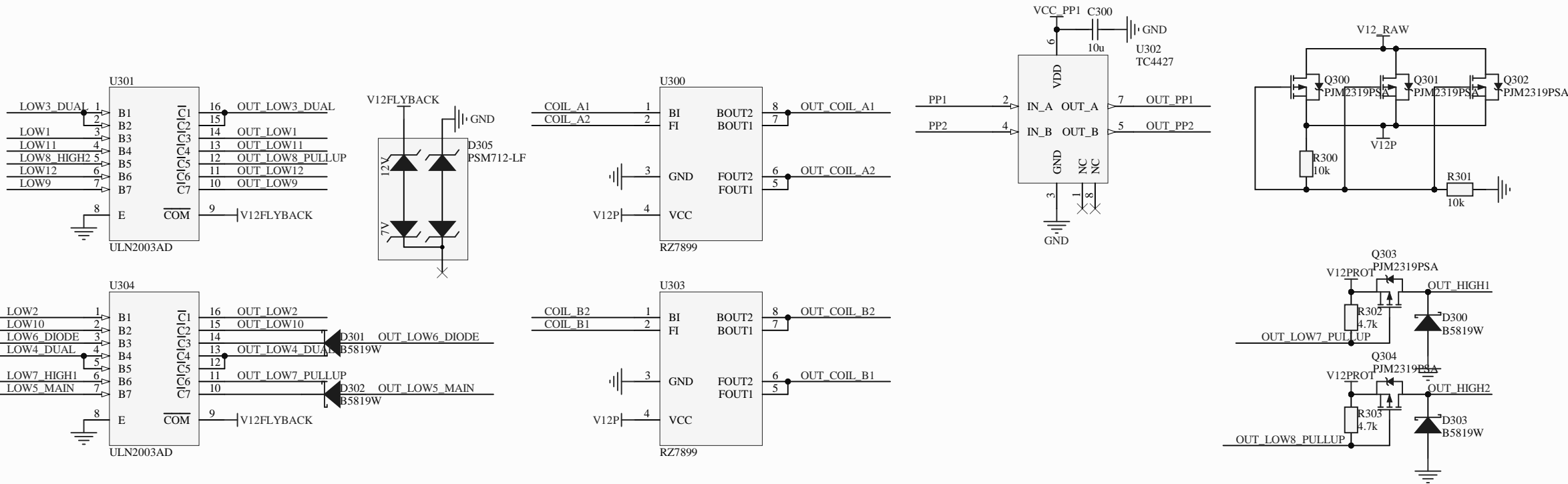
D

A

B

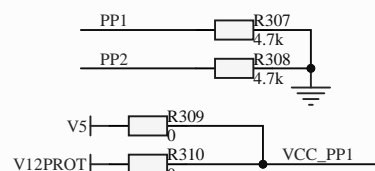
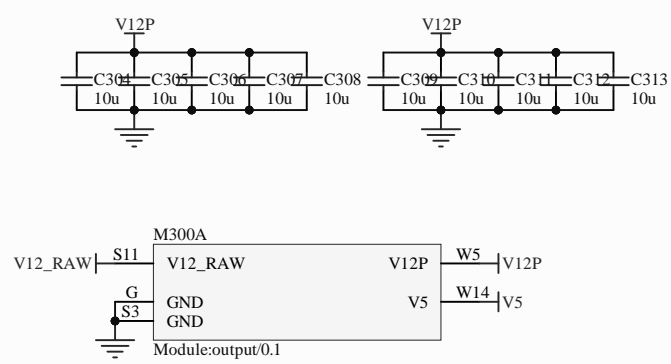
C

D



INJ1	W24	INJ1
INJ2	W23	INJ2
INJ3	W20	INJ3
INJ4	W19	INJ4
INJ5	W18	INJ5
INJ6	W17	INJ6
INJ7	W16	INJ7
INJ8	W15	INJ8
PP1	W28	PP1
PP2	W22	PP2
COIL_A1	W33	SOLENOID_A1
COIL_A2	W32	SOLENOID_A2
COIL_B1	W31	SOLENOID_B1
COIL_B2	W30	SOLENOID_B2
LOW1	W21	LOW1
LOW2	W29	LOW2
LOW3_DUAL	W38	LOW3_DUAL
LOW4_DUAL	W34	LOW4_DUAL
LOW5_MAIN	W40	LOW5_MAIN
LOW6_DIODE	W25	LOW6_DIODE
LOW7_HIGH1	W39	LOW7_HIGH1
LOW8_HIGH2	W37	LOW8_HIGH2
LOW9	W27	LOW9
LOW10	W26	LOW10
LOW11	W35	LOW11
LOW12	W36	LOW12

OUT_INJ1	S18	OUT_INJ1
OUT_INJ2	S16	OUT_INJ2
OUT_INJ3	S17	OUT_INJ3
OUT_INJ4	W7	OUT_INJ4
OUT_INJ5	W10	OUT_INJ5
OUT_INJ6	S15	OUT_INJ6
OUT_INJ7	W12	OUT_INJ7
OUT_INJ8	W11	OUT_INJ8
OUT_PP1	W6	OUT_PP1
OUT_PP2	S6	OUT_PP2
OUT_SOLENOID_A1	S1	OUT_COIL_A1
OUT_SOLENOID_A2	S2	OUT_COIL_A2
OUT_SOLENOID_B1	S4	OUT_COIL_B1
OUT_SOLENOID_B2	S5	OUT_COIL_B2
OUT_LOW1	S8	OUT_LOW1
OUT_LOW2	W1	OUT_LOW2
OUT_LOW3_DUAL	S7	OUT_LOW3_DUAL
OUT_LOW4_DUAL	W4	OUT_LOW4_DUAL
OUT_LOW5_MAIN	W13	OUT_LOW5_MAIN
OUT_LOW6_DIODE	W3	OUT_LOW6_DIODE
OUT_LOW7_PULLUP	W9	OUT_LOW7_PULLUP
OUT_LOW8_PULLUP	S12	OUT_LOW8_PULLUP
OUT_LOW9	S14	OUT_LOW9
OUT_LOW10	W2	OUT_LOW10
OUT_LOW11	S10	OUT_LOW11
OUT_LOW12	S13	OUT_LOW12
OUT_HIGH1	W8	OUT_HIGH1
OUT_HIGH2	S9	OUT_HIGH2



\* These can be removed in the board compilation file

