

U3E  
LIN1 LINBUS

U3A  
molex\_48pin\_MRE

U3D  
5V J801 J802 12V\_MREL  
PB8 J803 J804 VDD  
PC11 J805 J806 PB9  
PA15 J807 J808 PC10  
GND J809 J810 PC12

Communication Header  
J4

Conn\_02x05\_Counter\_Clockwise  
5V 1 2 12V\_MREL  
PB8 3 4 VDD  
PC11 5 6 PB9  
PA15 7 8 PC10  
GND 9 10 PC12

5V TP5  
GND TP0  
CAM\_MCU TP2  
CRANK TP4  
12V\_MREL TP12  
VDD TP3

D44 1N4148WS Voltage selector 5V 3V3  
VDD 1 3 5V  
GND 4 20 U3Q

ANALOG INPUTS.  
ADC 1-4 HAVE  
BIAS RESISTORS  
FOR TEMP SENSORS

CAN ALSO BE USED  
AS DIGITAL INPUTS

AT1 AT2 AT3 AT4  
AV1 AV2 AV3 AV4 AV5 AV6 AV7 AV8 AV9 AV10

CAM\_SENSOR  
12V\_MREL  
5V

Sheet: ADC

AT1\_MCU AT2\_MCU AT3\_MCU AT4\_MCU  
AV1\_MCU AV2\_MCU AV3\_MCU AV4\_MCU AV5\_MCU AV6\_MCU AV7\_MCU AV8\_MCU AV9\_MCU AV10\_MCU  
CAM\_MCU Vbat\_MCU  
AV4 USB\_D- USB\_D+  
AGND

File: adc.sch

6A H-BRIDGE

E-THRATTLE+ E-THRATTLE-  
12V\_MREL  
VDD  
GND

File: TLE9201SG.sch

Sheet: FlashMemory

VDD  
PC13  
HOLD  
WP  
PE0

File: FlashMemory.sch

Sheet: SDCard

PB9  
PC10  
PC11  
PC12

File: SDCard.sch

GP OUT 5 AND 6 ARE HIGH SIDE DRIVEN

VDD  
GND  
PD12 PD13 PD14 PD15  
PE14 PE13 PE12 PE11  
PE10 PE9 PE8 PE7  
PB6 PB12 PB4  
PD5 PD8 PD10 PD11  
NRST  
PE1  
VBAT  
5V

File: stm32.sch

Sheet: H-BRIDGE  
OUT1 OUT2  
12V\_MREL  
VDD  
GND

Sheet: FlashMemory  
VDD  
PC13  
HOLD  
WP  
PE0

Sheet: SDCard  
PB9  
PC10  
PC11  
PC12

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PB9  
PC10  
PC11  
PC12

File: stm32.sch

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Sheet: TLE8888-1QK

PD12 PD13 PD14 PD15  
PE14 PE13 PE12 PE11  
PE10 PE9 PE8 PE7  
PB6 PB12 PB4  
PD5 PD8 PD10 PD11  
NRST  
PE1  
VBAT  
5V

File: TLE8888-1QK.sch

Sheet: hi-lo  
IGN1\_MCU IGN2\_MCU IGN3\_MCU IGN4\_MCU  
GP5\_MCU GP6\_MCU  
IGN1 IGN2 IGN3 IGN4  
GP5 GP6

File: hi-lo.sch

IGN1\_MCU IGN2\_MCU IGN3\_MCU IGN4\_MCU  
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GP5 GP6

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by JRD McLAREN  
Donald Becker

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Sheet: /

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Title: microRusEFI-2L

Size: B

Date: 2022-03-16

Rev: R0.5.5rc2

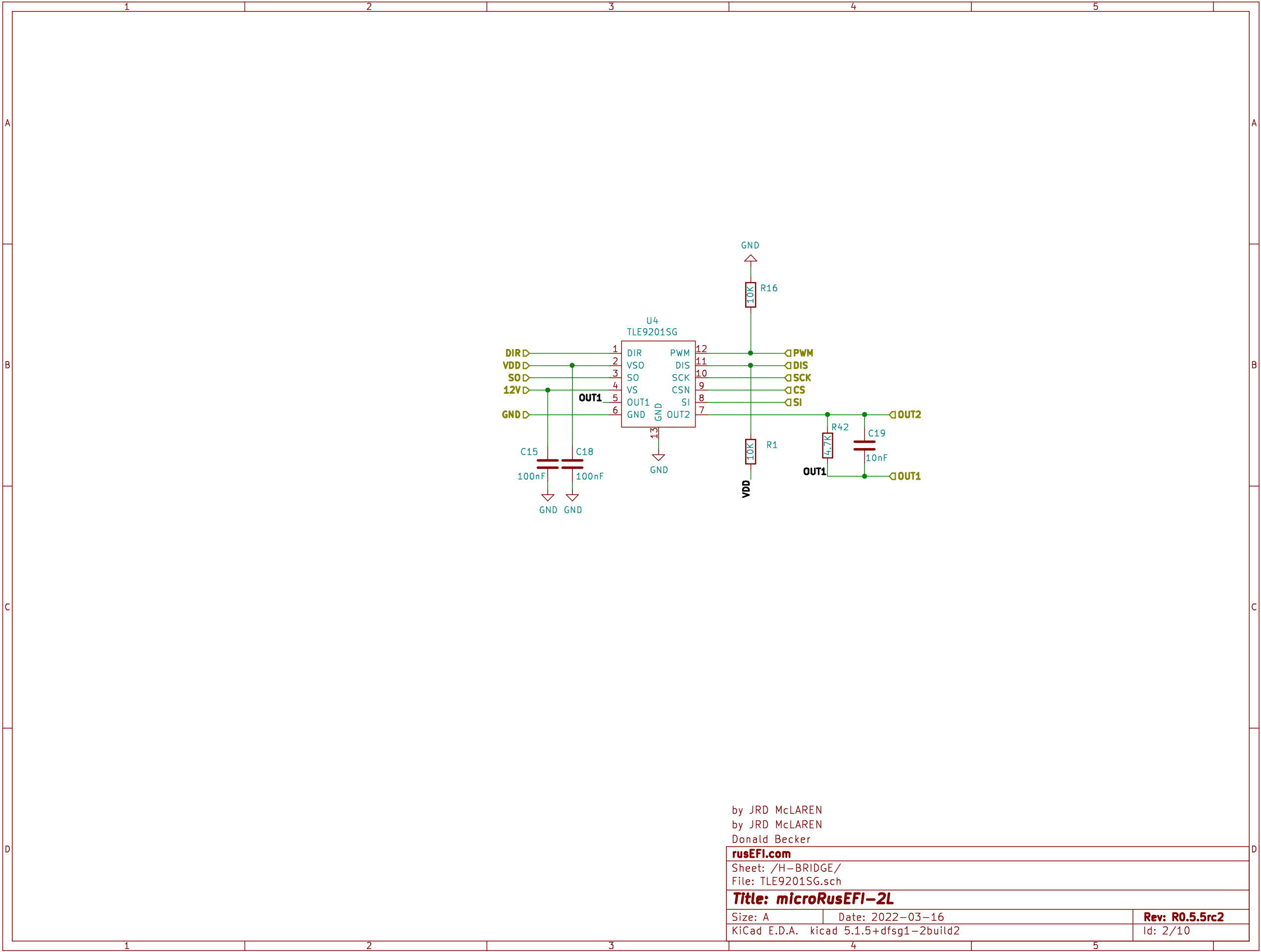
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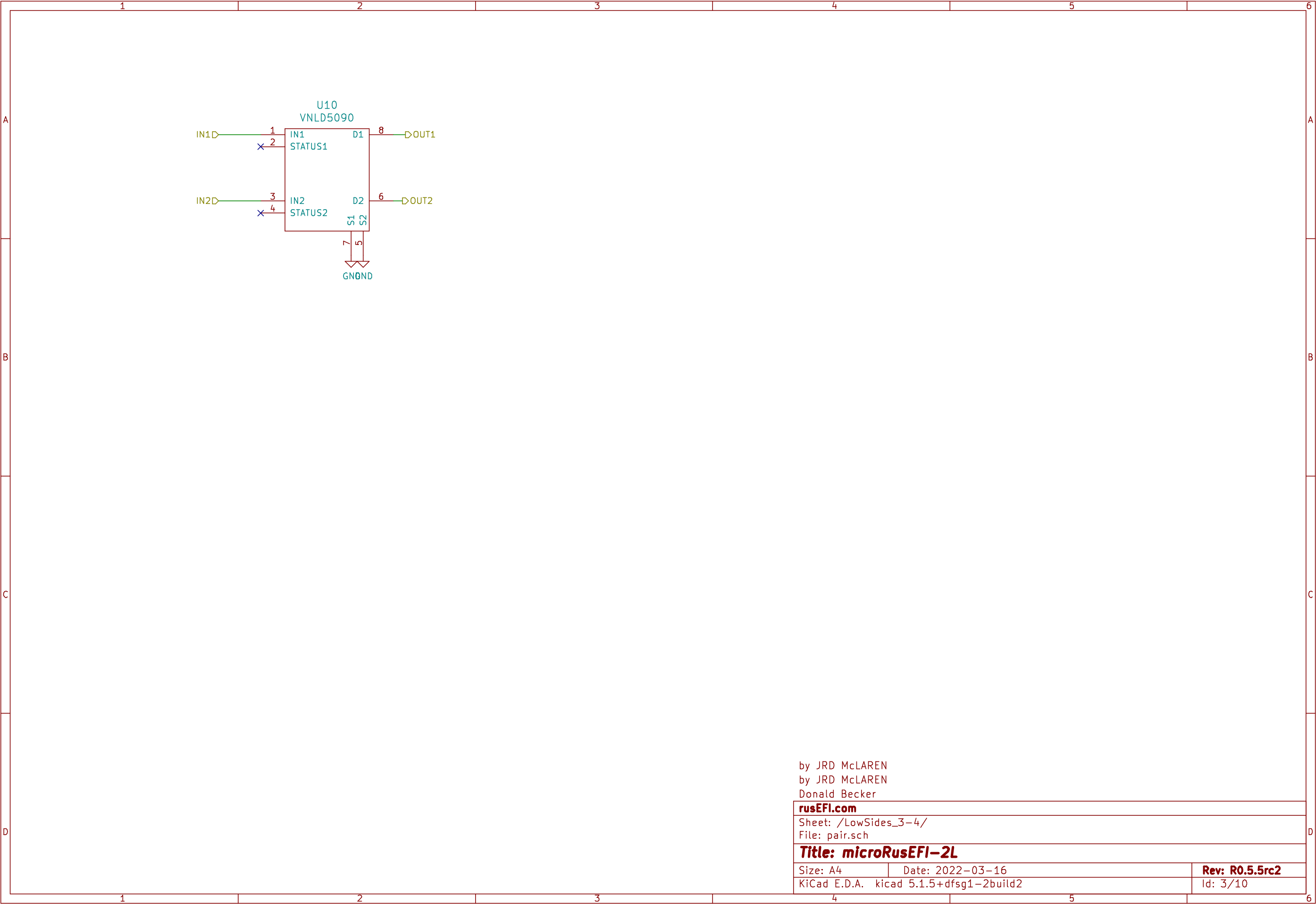
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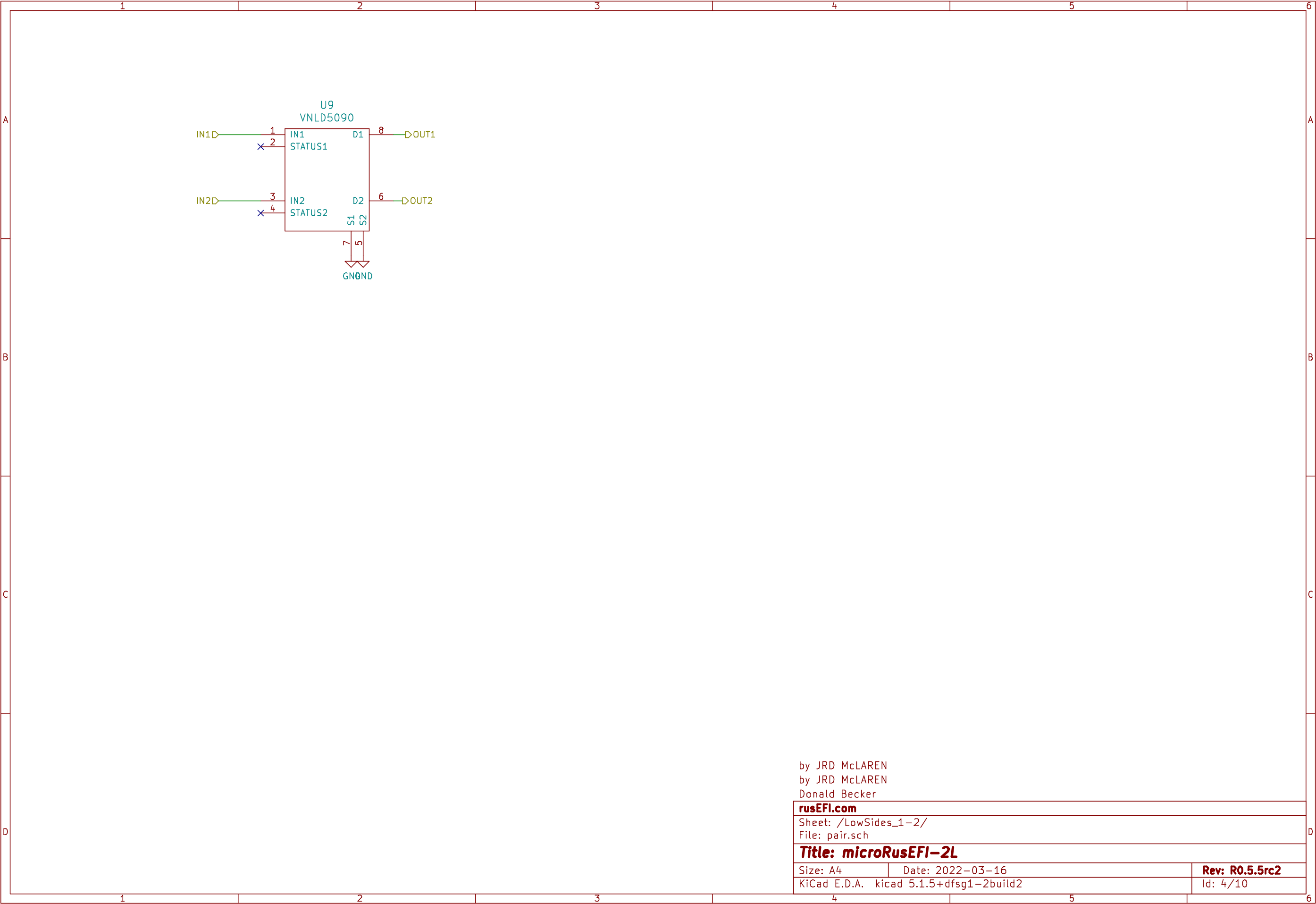
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FOR OFF ROAD PURPOSES ONLY  
This is not for applications with  
emissions or safety regulations  
(AKA not for street use). This is  
for closed stages, track  
and equipment.



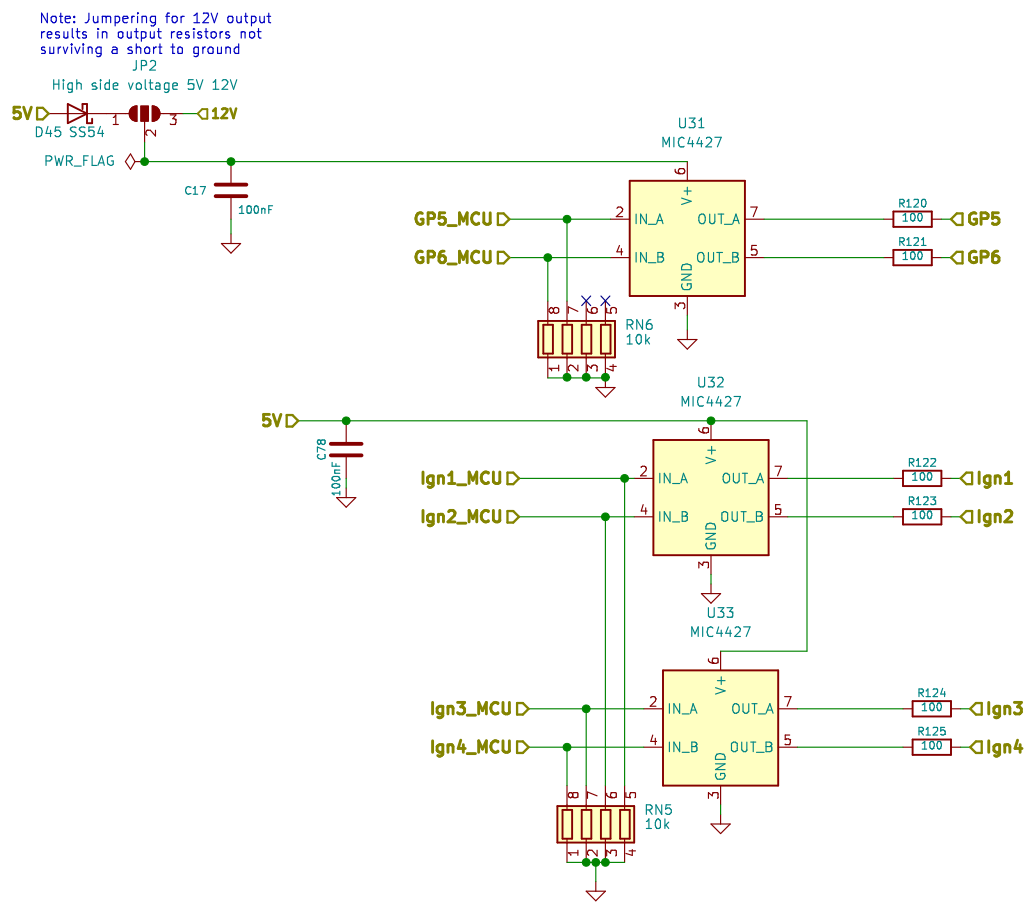


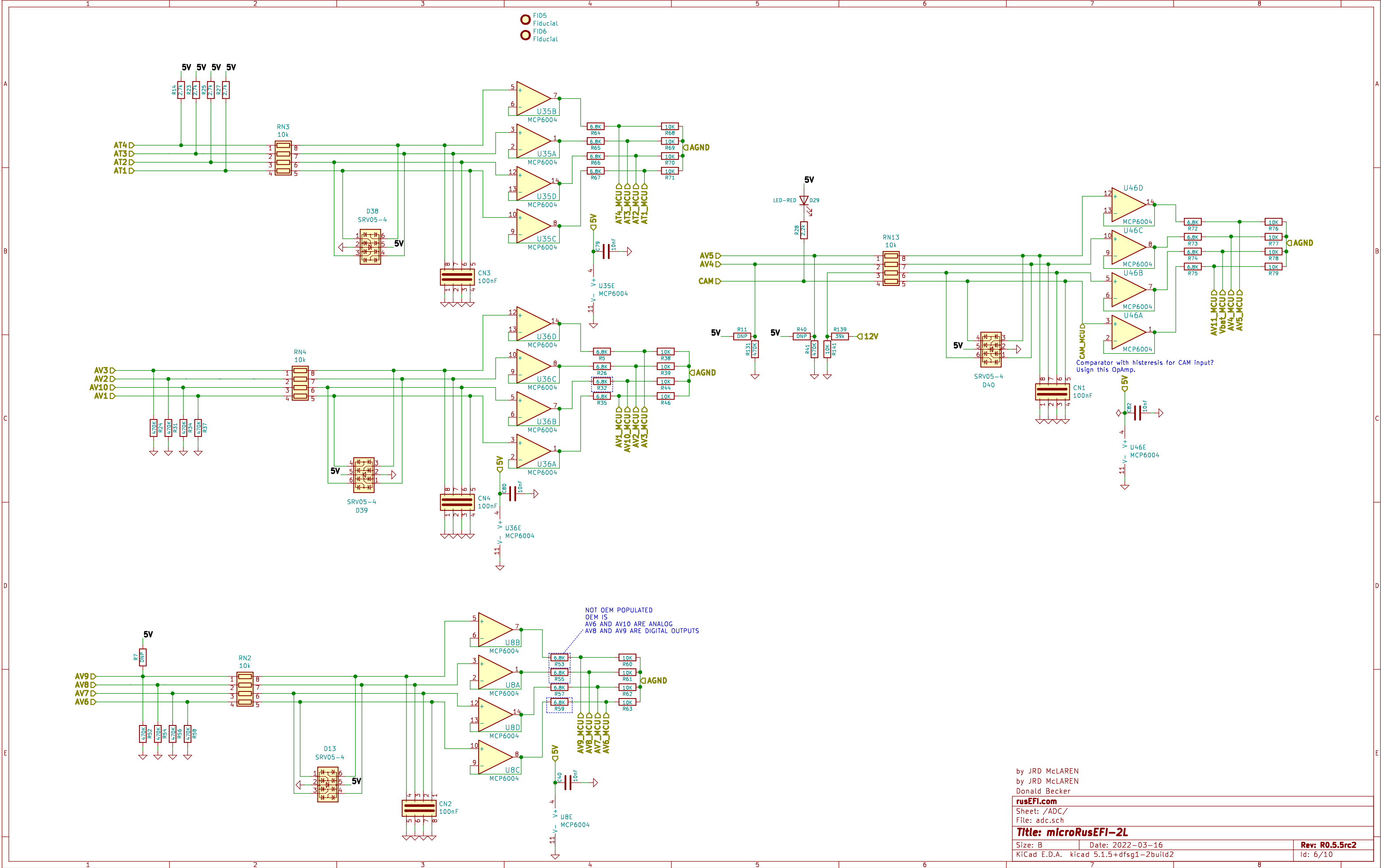


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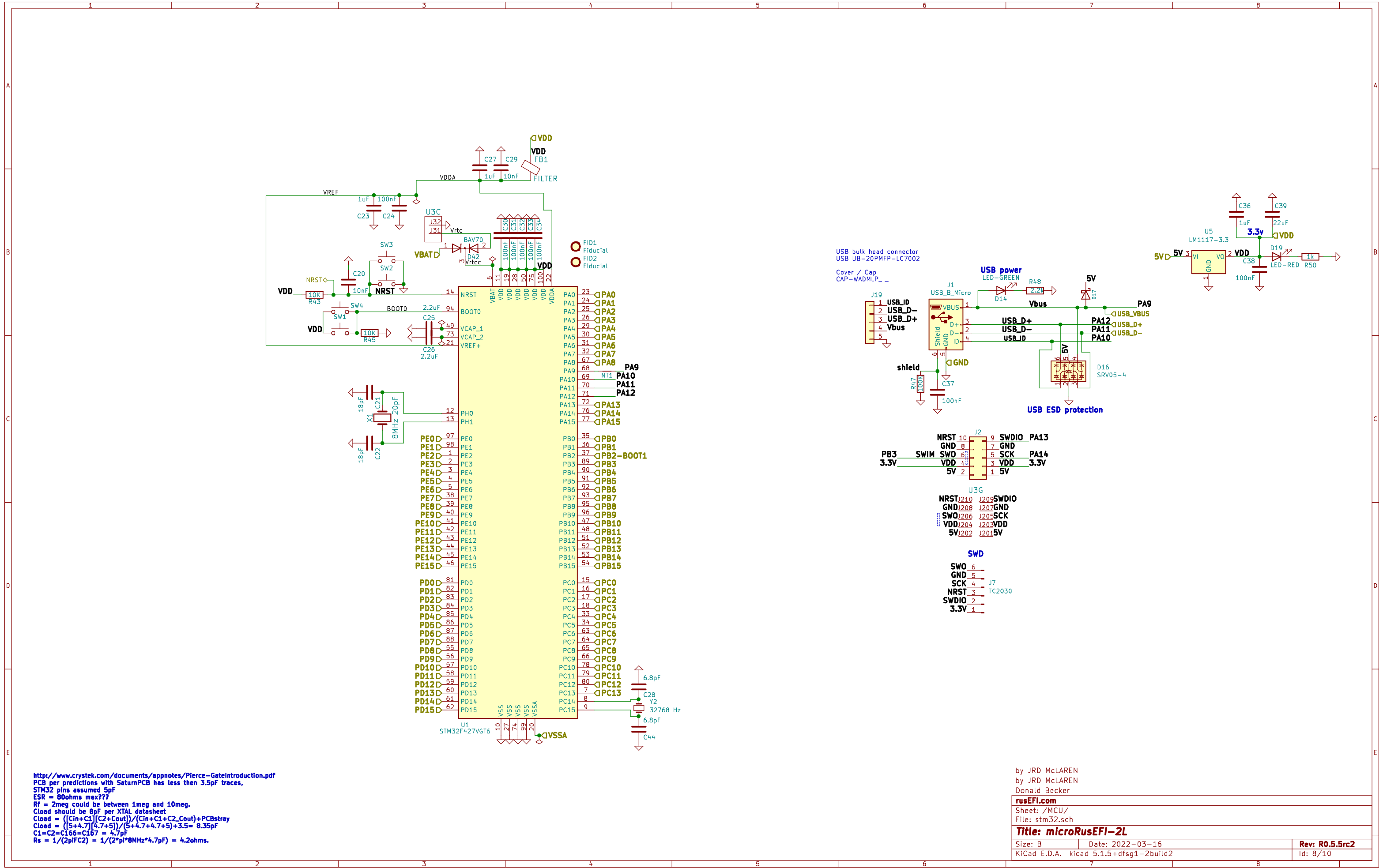
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Size: A4	Date: 2022-03-16	Rev: R0.5.5rc2
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6 channel high / low side driver









<http://www.crystek.com/documents/appnotes/Pierce-GateIntroduction.pdf>  
PCB per predictions with SaturnPCB has less then 3.5pF traces,  
STM32 pins assumed 5pF  
ESR = 80ohms max???  
Rf = 2meg could be between 1meg and 10meg.  
Cload should be 8pF per XTAL datasheet  
Cload = ((Cin+C1)[C2+Cout])/((Cin+C1+C2.Cout))+PCBstray  
Cload = (([5+4.7][4.7+5])/((5+4.7+4.7+5))+3.5= 8.35pF  
C1=C2=C166=C167 = 4.7pF  
Rs = 1/(2pIFC2) = 1/(2\*pi\*8MHz\*4.7pF) = 4.2ohms.

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Size: B Date: 2022-03-16

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