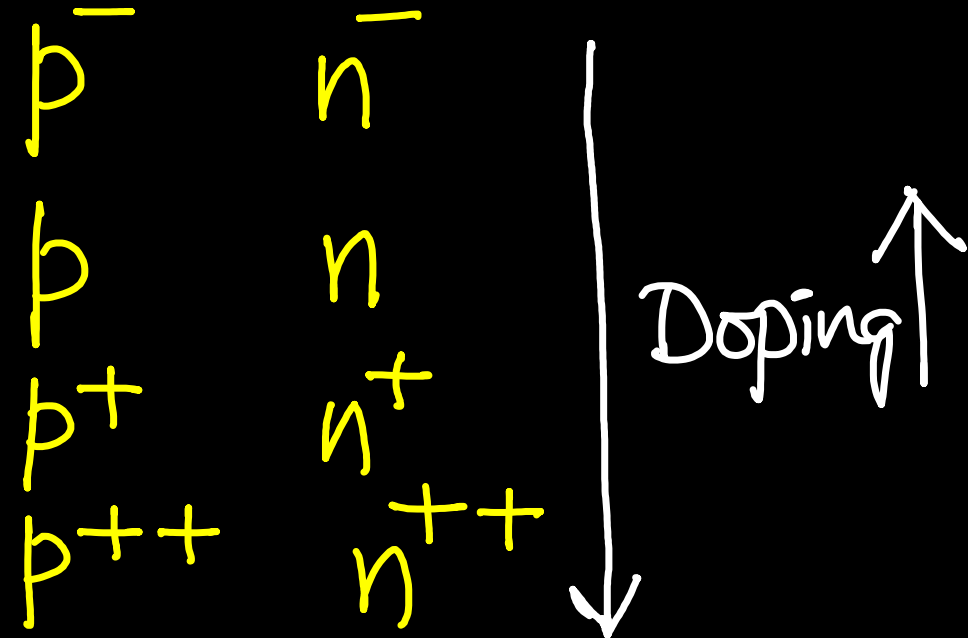


SPECIAL PURPOSE DIODES

- Varactor Diode
- Zener Diode
- Tunnel Diode

Avalanche diode
pn Junction
Zener diode
Tunnel diode



VARACTOR DIODE

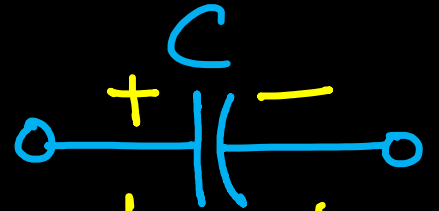
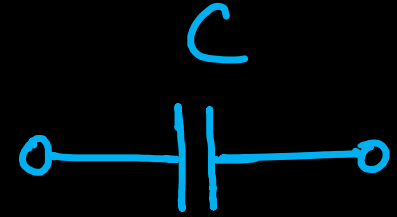
- Variable reactor \Rightarrow Varactor
- Varicap
- Reverse bias

Depletion capacitance
(C_M)

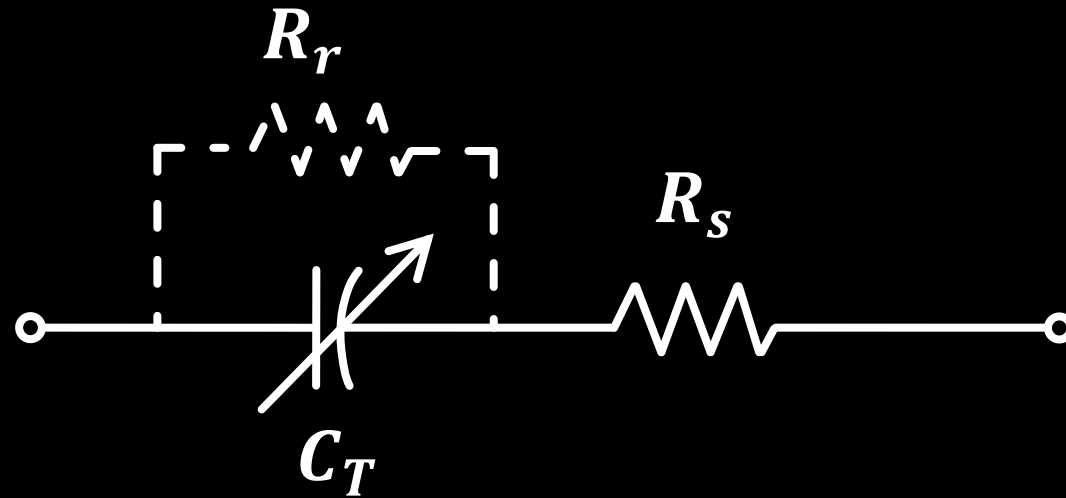
Transition capacitance
(C_T)



Circuit Symbol



Electrolytic
Capacitor

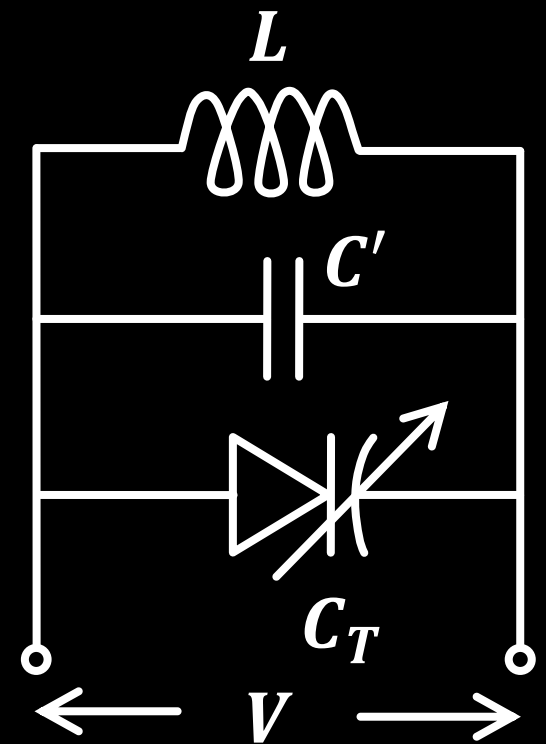
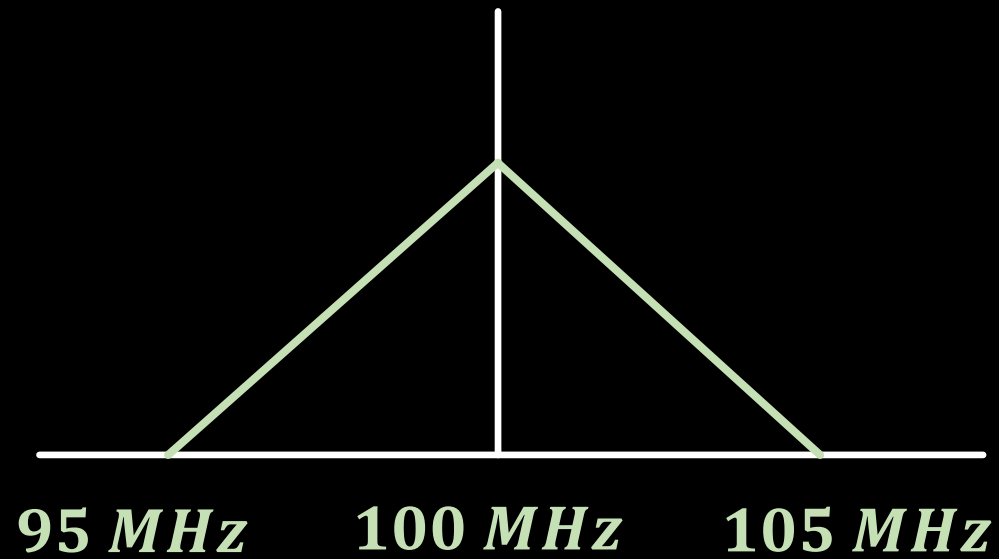


Equivalent Circuit

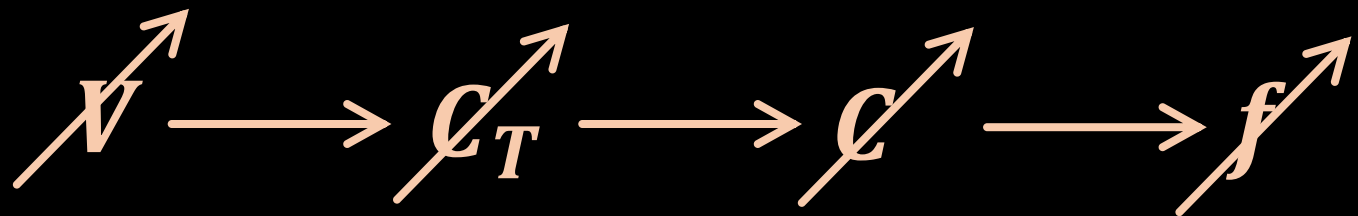
$$C_T = \frac{C_{T0}}{\sqrt{\left(1 + \frac{V_R}{V_0}\right)}}$$

Applications

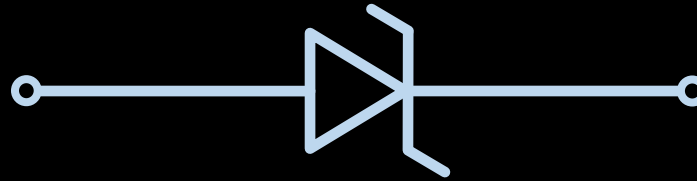
- Tuning Circuits
- Harmonic Generation
- Microwave Frequency Multiplication
- Active Filters
- Self Balancing Bridges
- Parametric Amplifiers



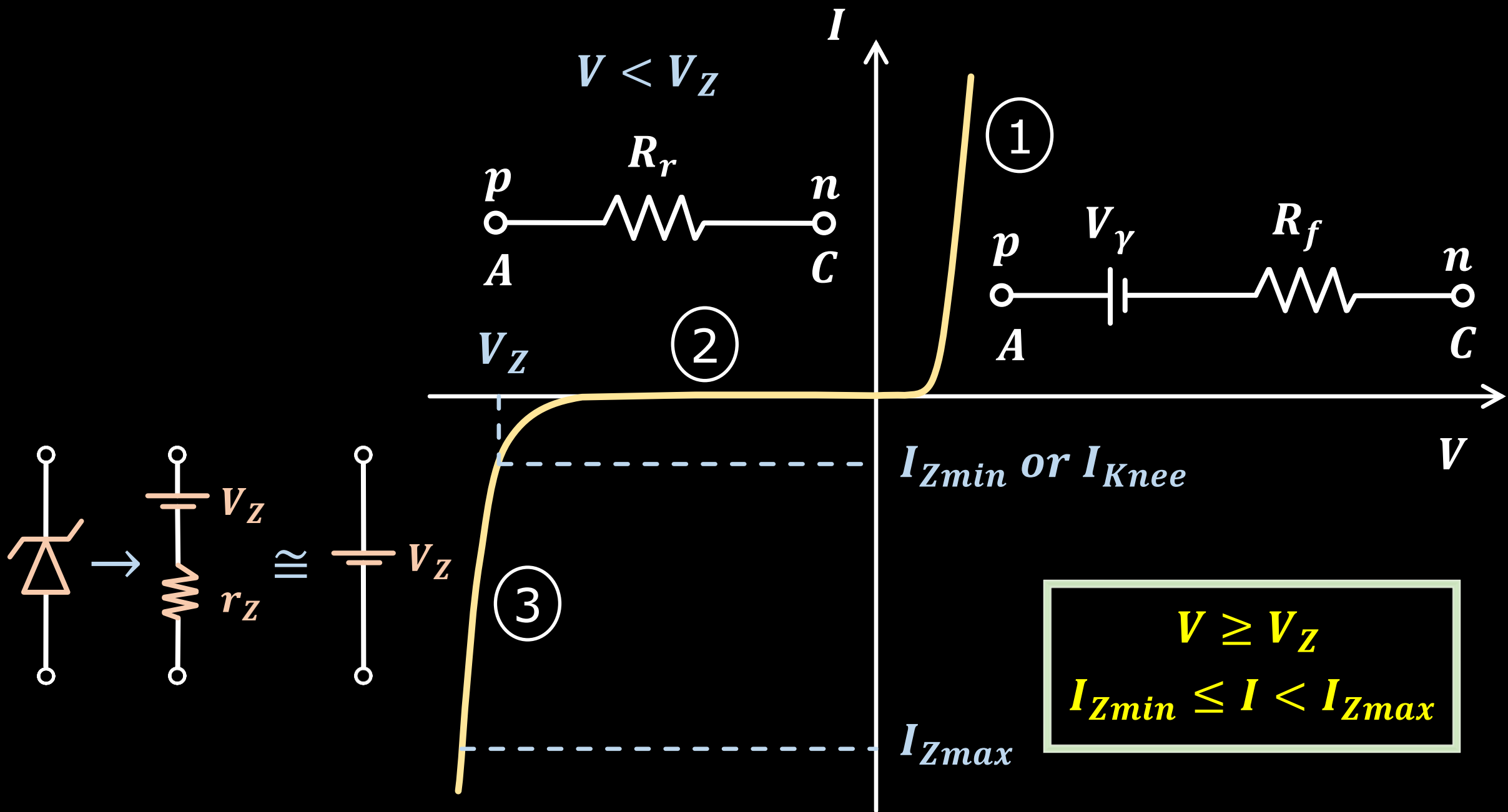
$$f = \frac{1}{2\pi\sqrt{LC}} \quad C = C' + C_T$$



ZENER DIODE

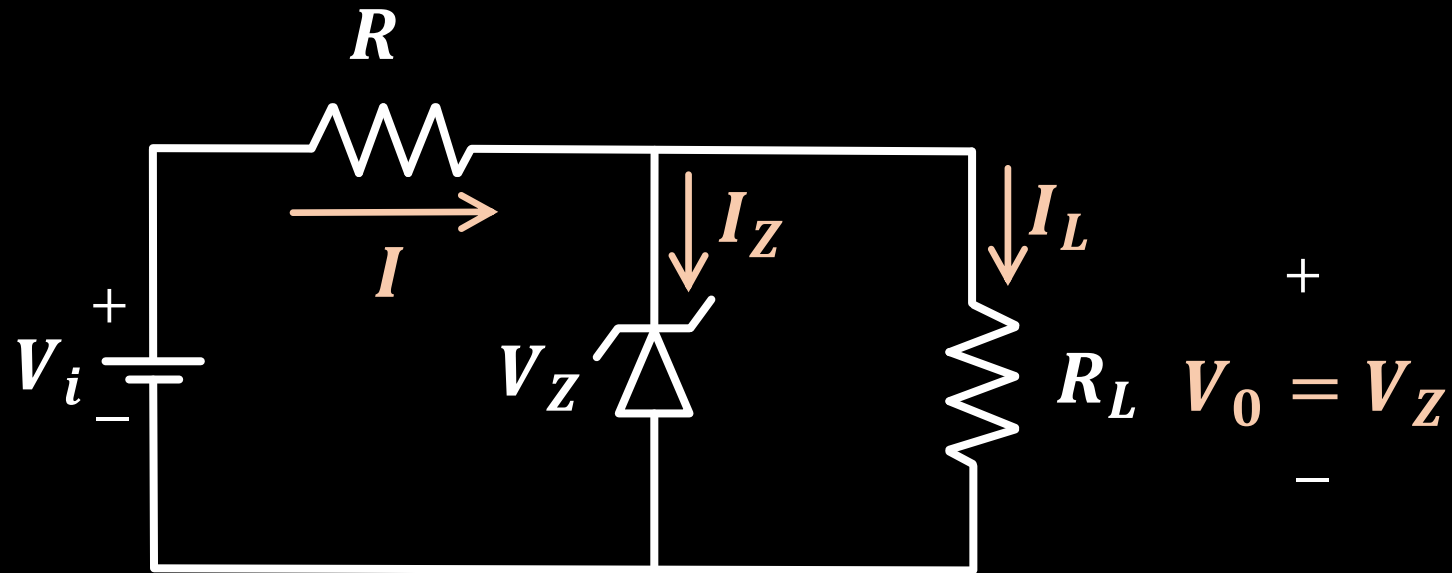


Circuit Symbol



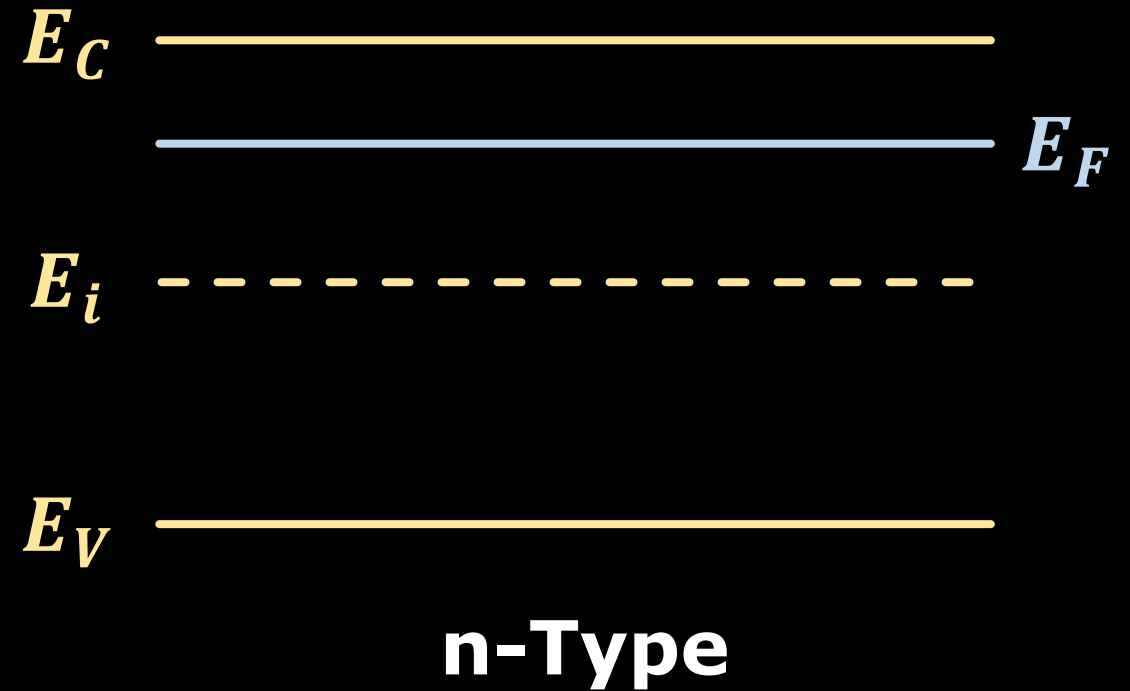
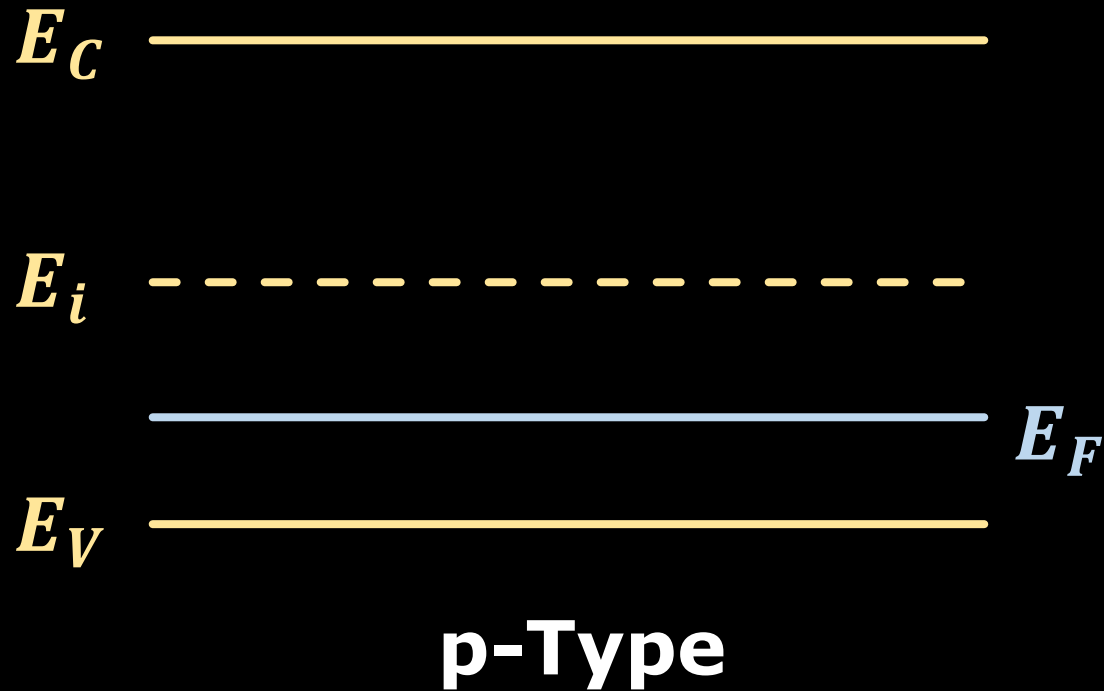
Zener diode as voltage regulator

Voltage regulation is the process of generating constant output voltage w.r.t variations present at the input (line) and the load.



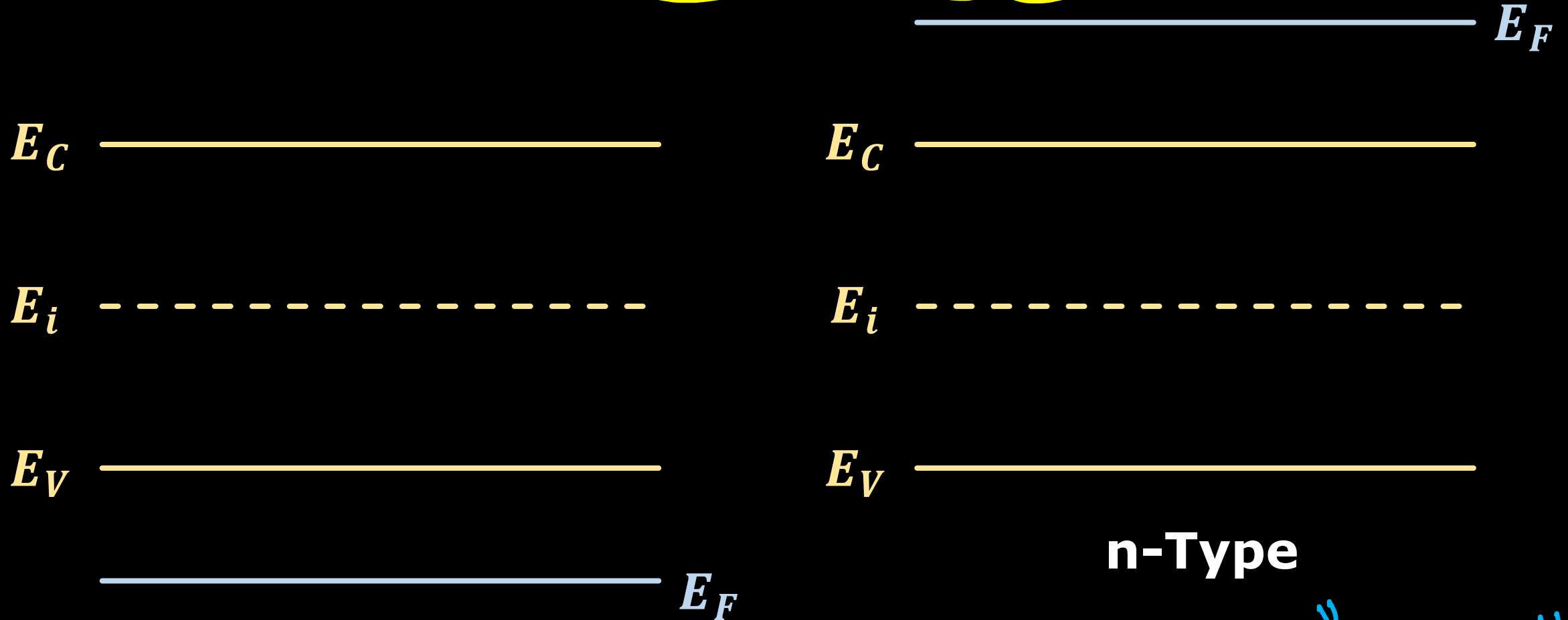
$$I = I_Z + I_L \quad I_{Zmin} \leq I_Z < I_{Zmax} \quad V_i = IR + V_Z$$

TUNNEL DIODE (ESAKI DIODE)



In pn junction the doping ratio is "1 in 10^8 ".
This results in width of the depletion region
in the order of "micrometres (microns)".

p-Type "Degenerate Semiconductors"



In Tunnel diode the doping ratio is 1 in 10^3 .
This results in width of the depletion region
in the order of Å.

p-type

$$E_F = E_V + kT \ln\left(\frac{N_V}{N_A}\right)$$

n-type

$$E_F = E_C - kT \ln\left(\frac{N_C}{N_D}\right)$$

pn junction $\rightarrow N_A < N_V$ and $N_D < N_C$

If doping concentration is increased

$$N_A > N_V$$

$$N_D > N_C$$

Tunnel diode is junction between degenerate p-type and degenerate n-type semiconductor.



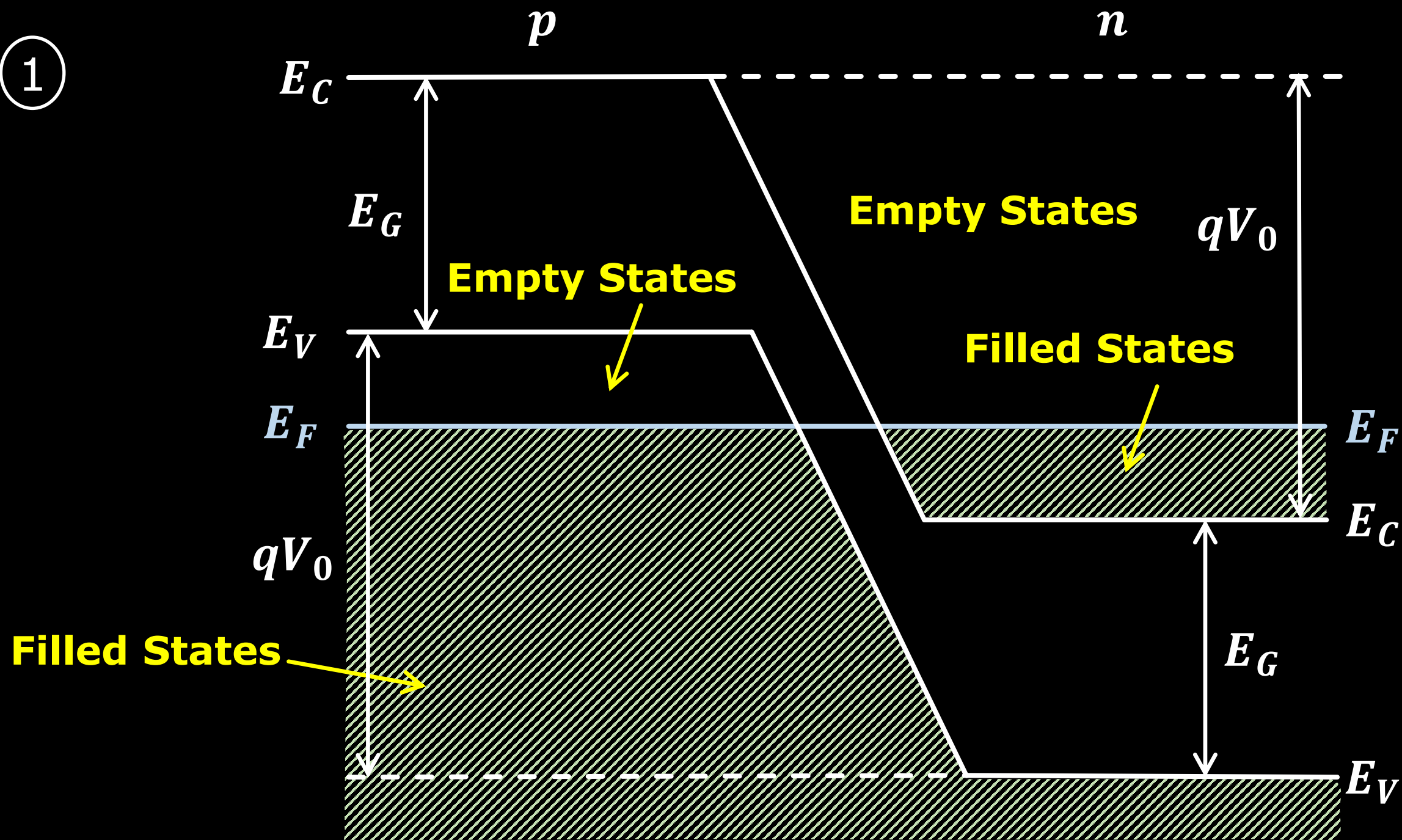
Tunnelling can happen only if there are corresponding filled and empty states on either side (p and n) of the junction.

Open Circuit (Equilibrium)

Under equilibrium

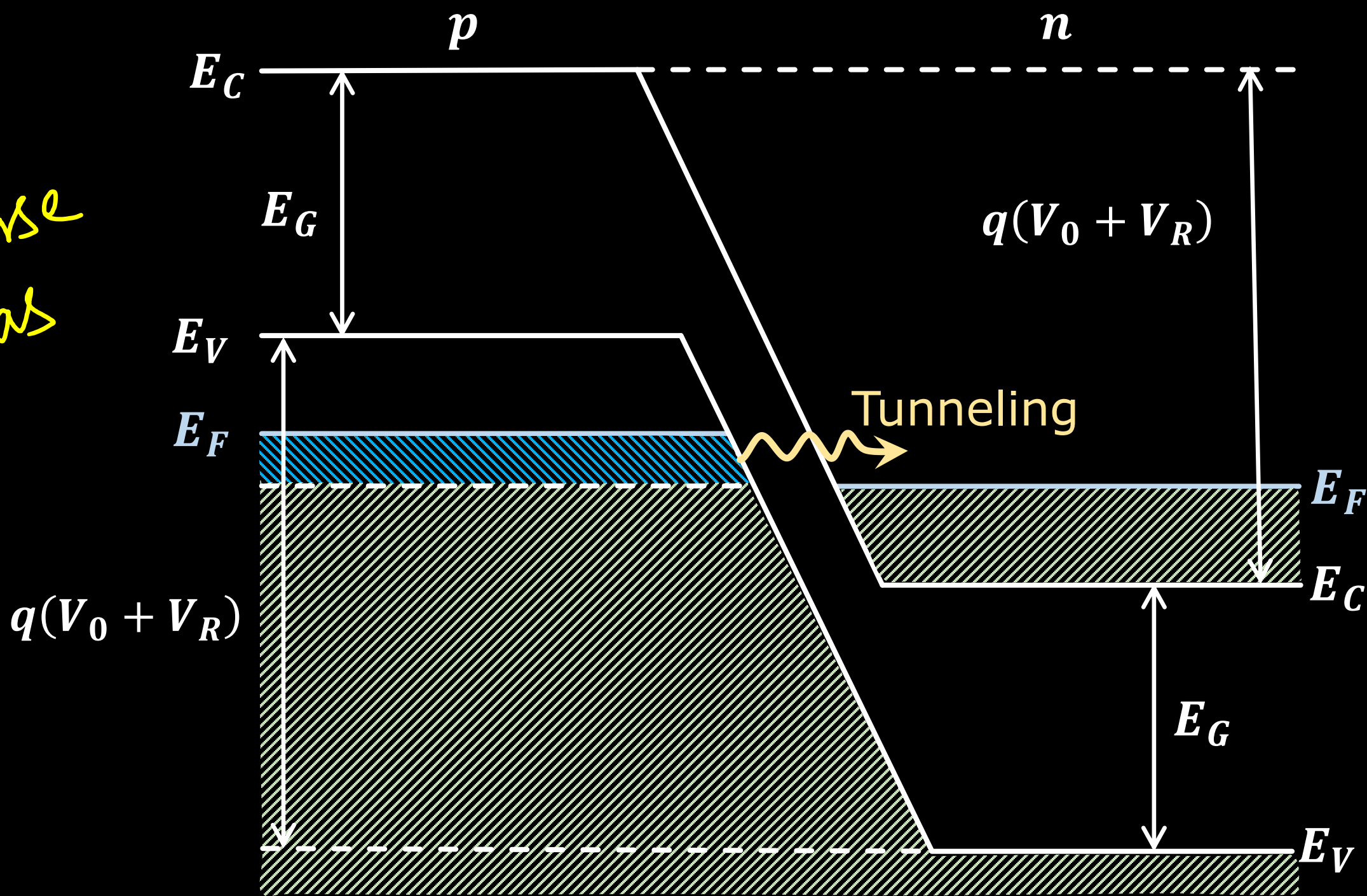
$$\frac{\partial E_F}{\partial x} = 0$$

①

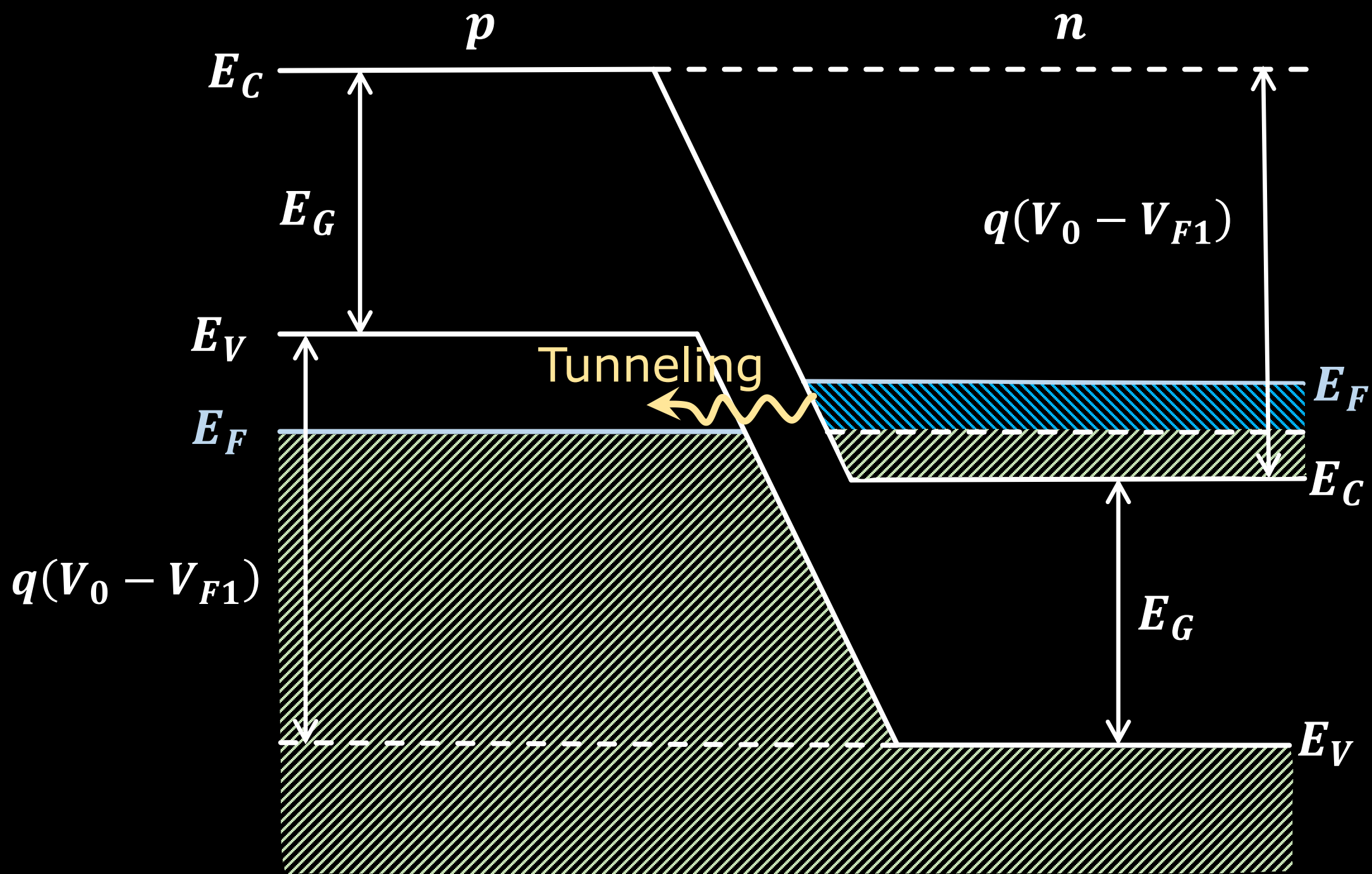


②

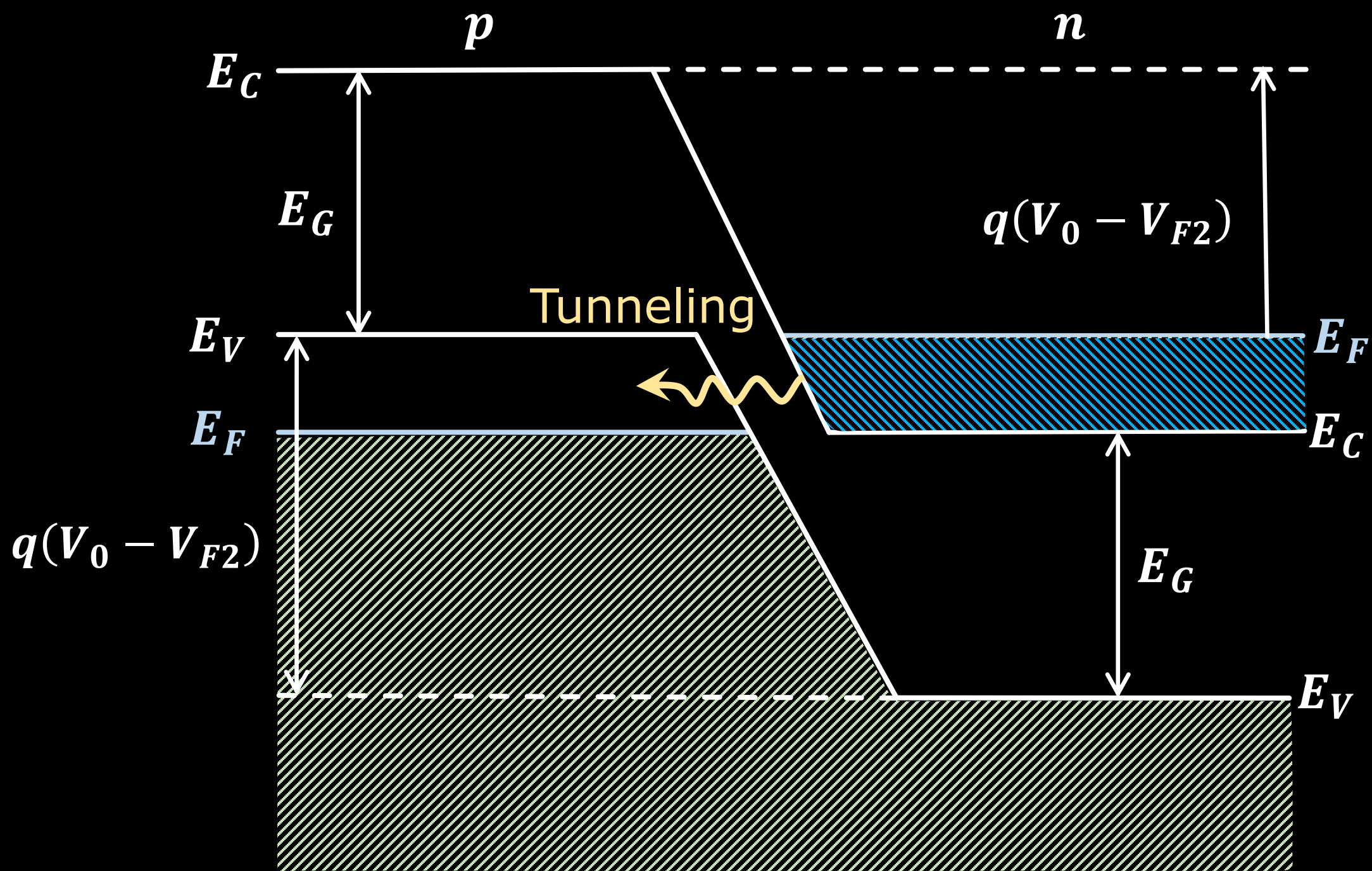
Reverse
bias



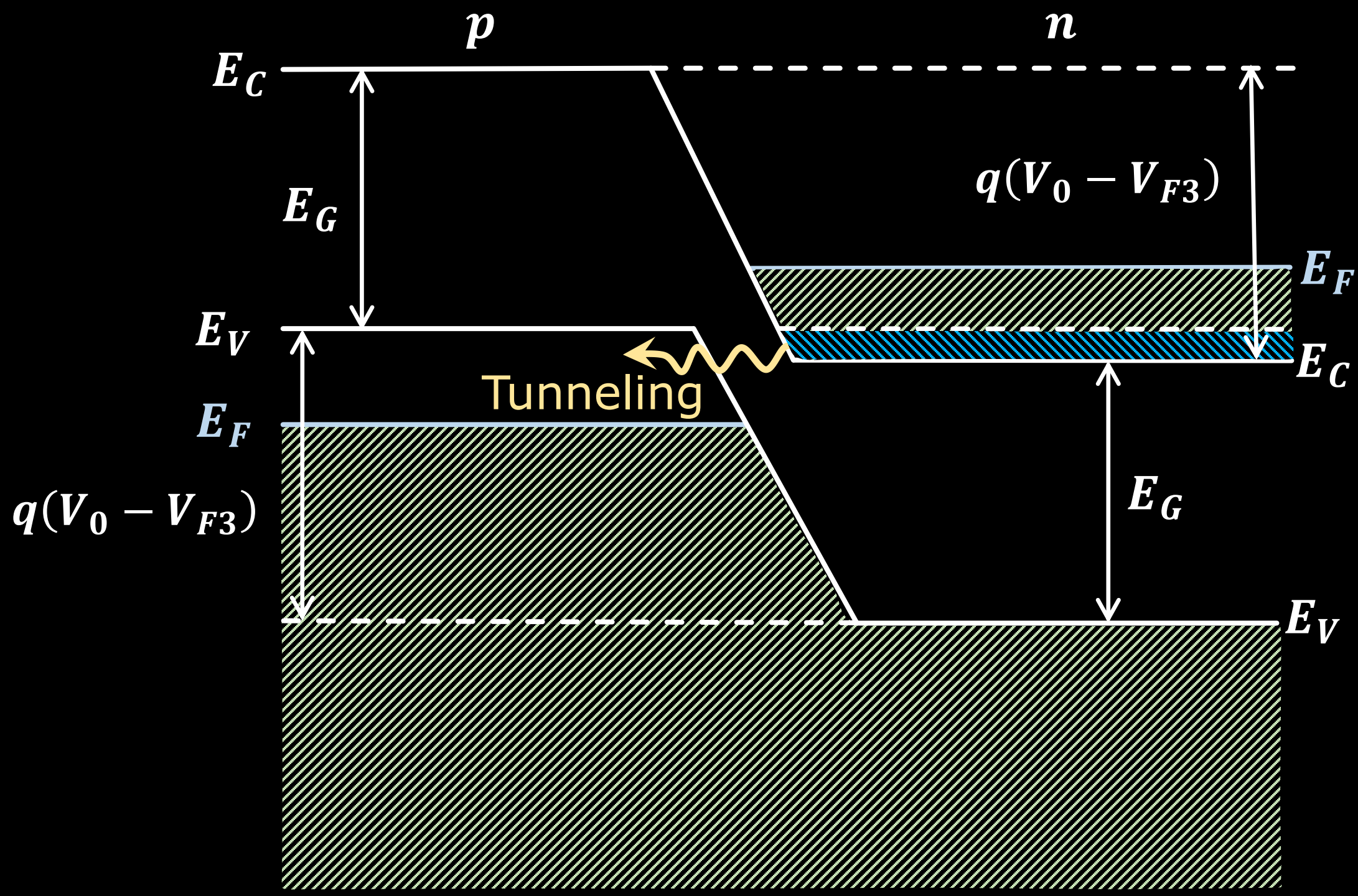
③



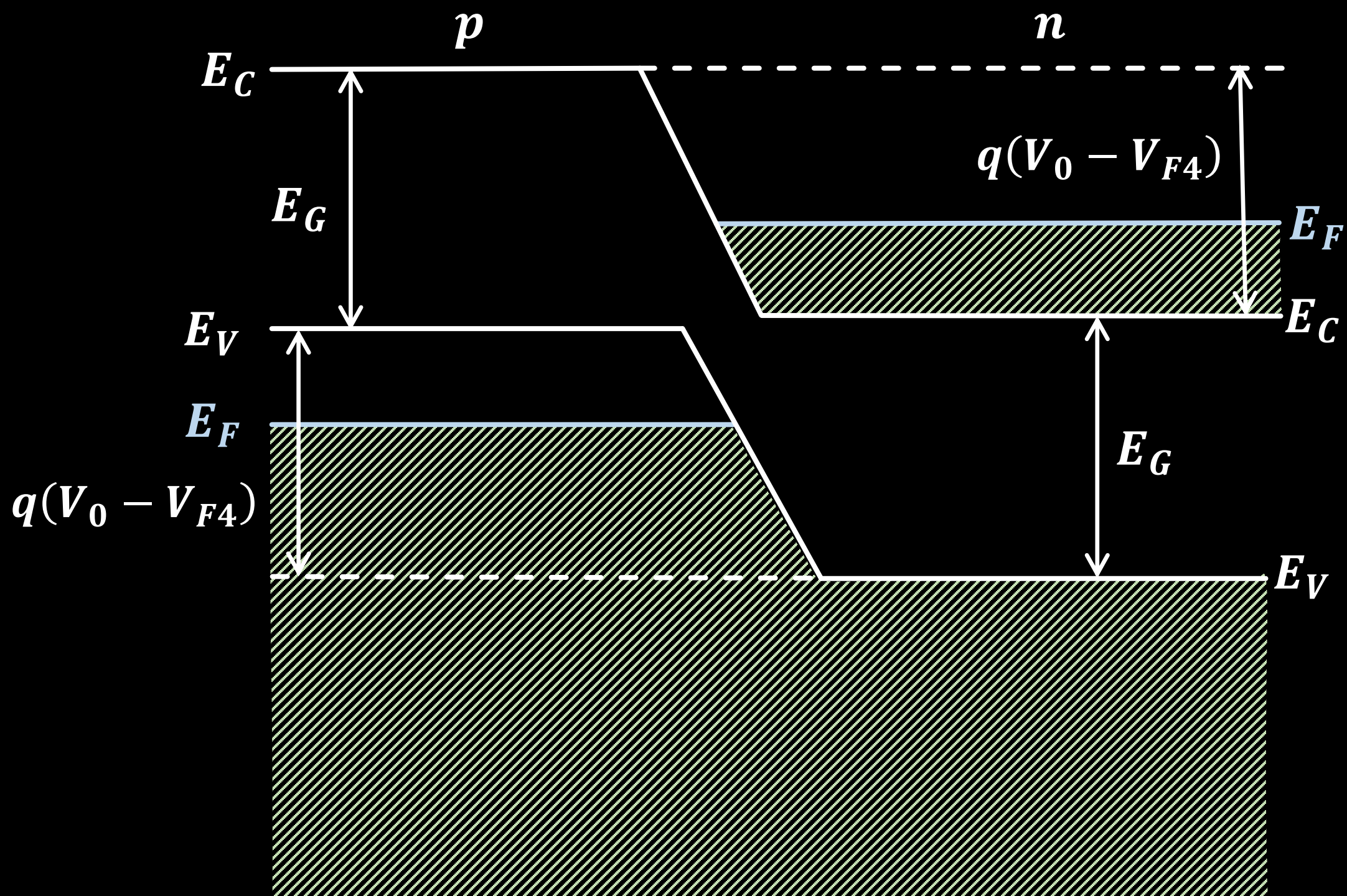
④

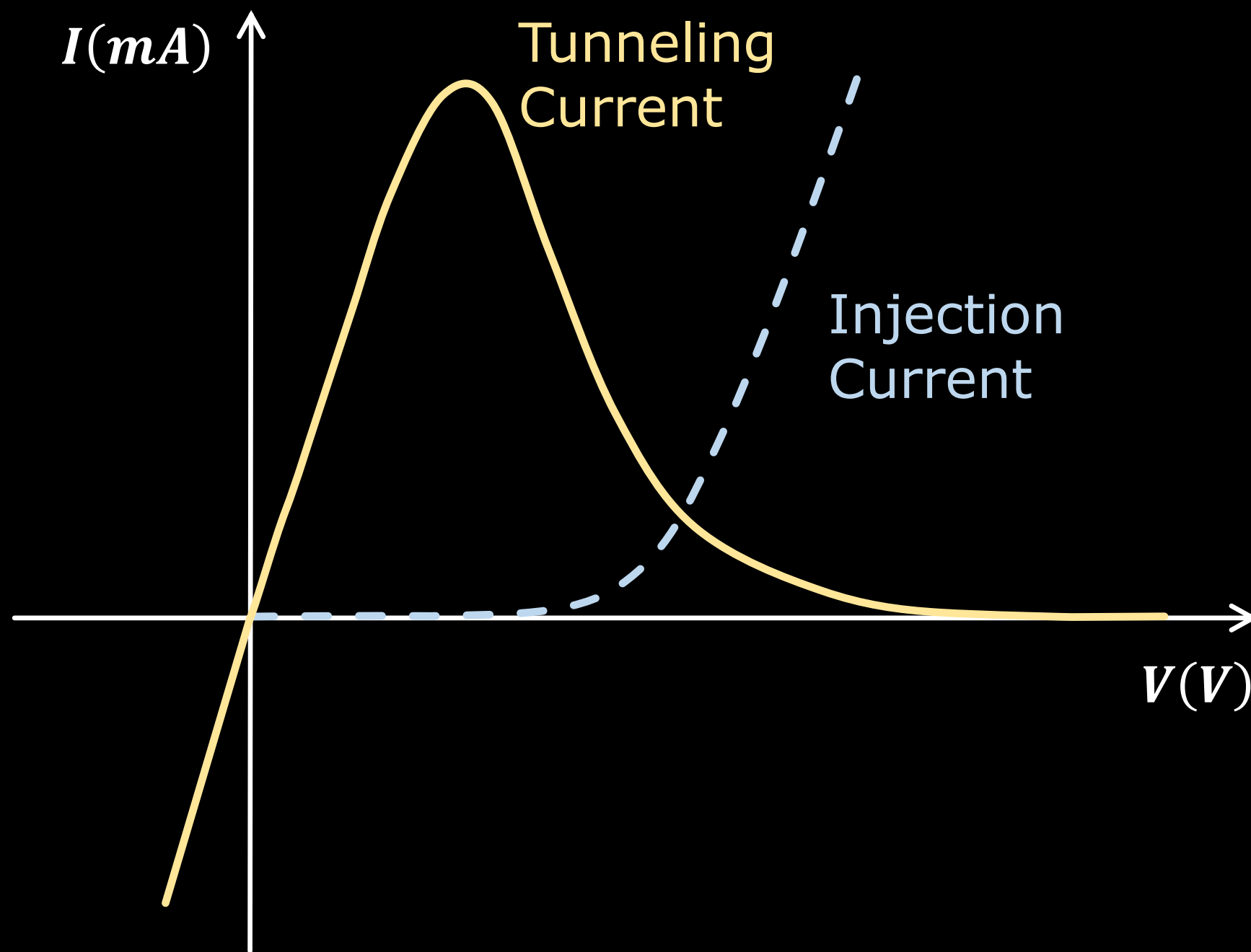


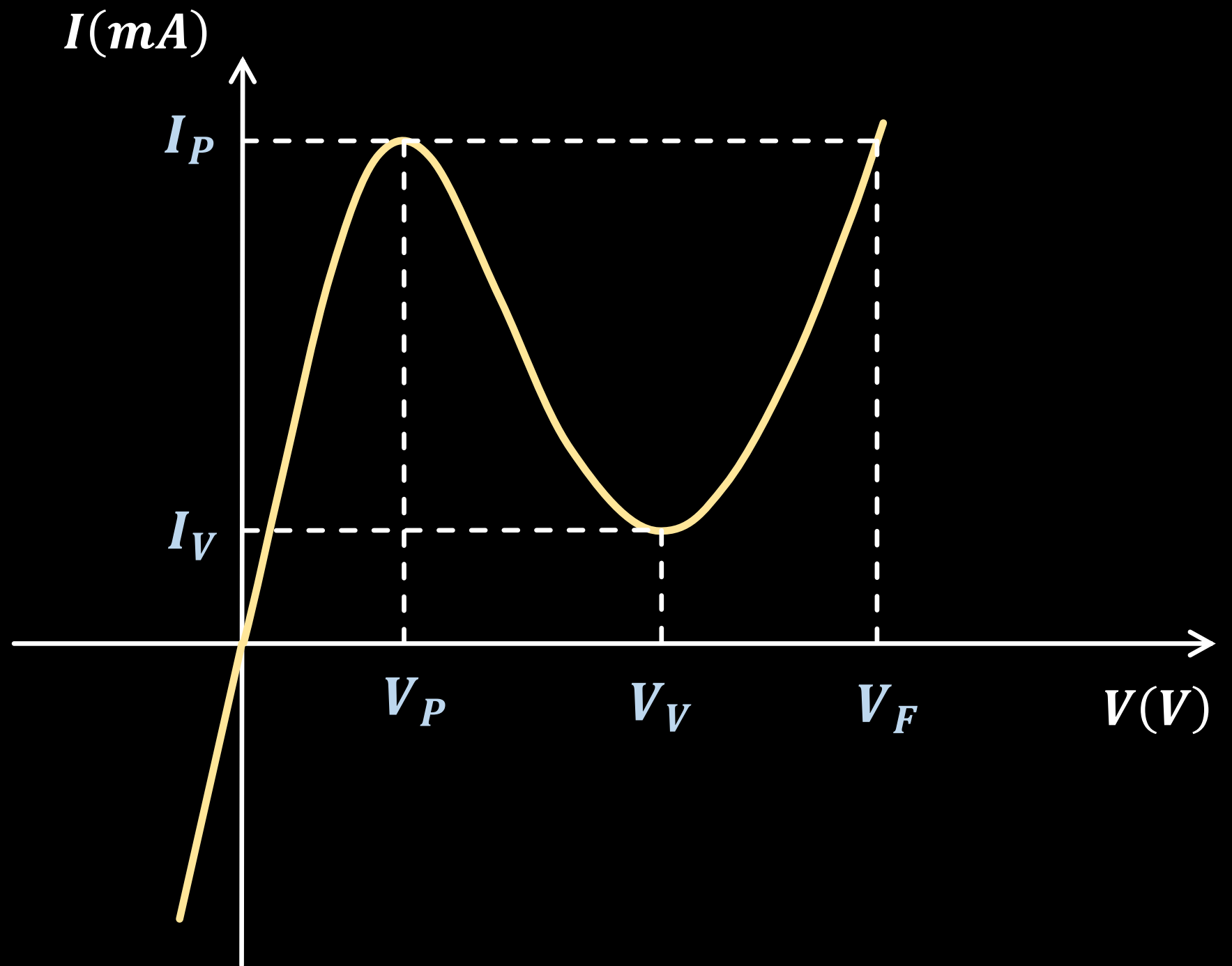
⑤

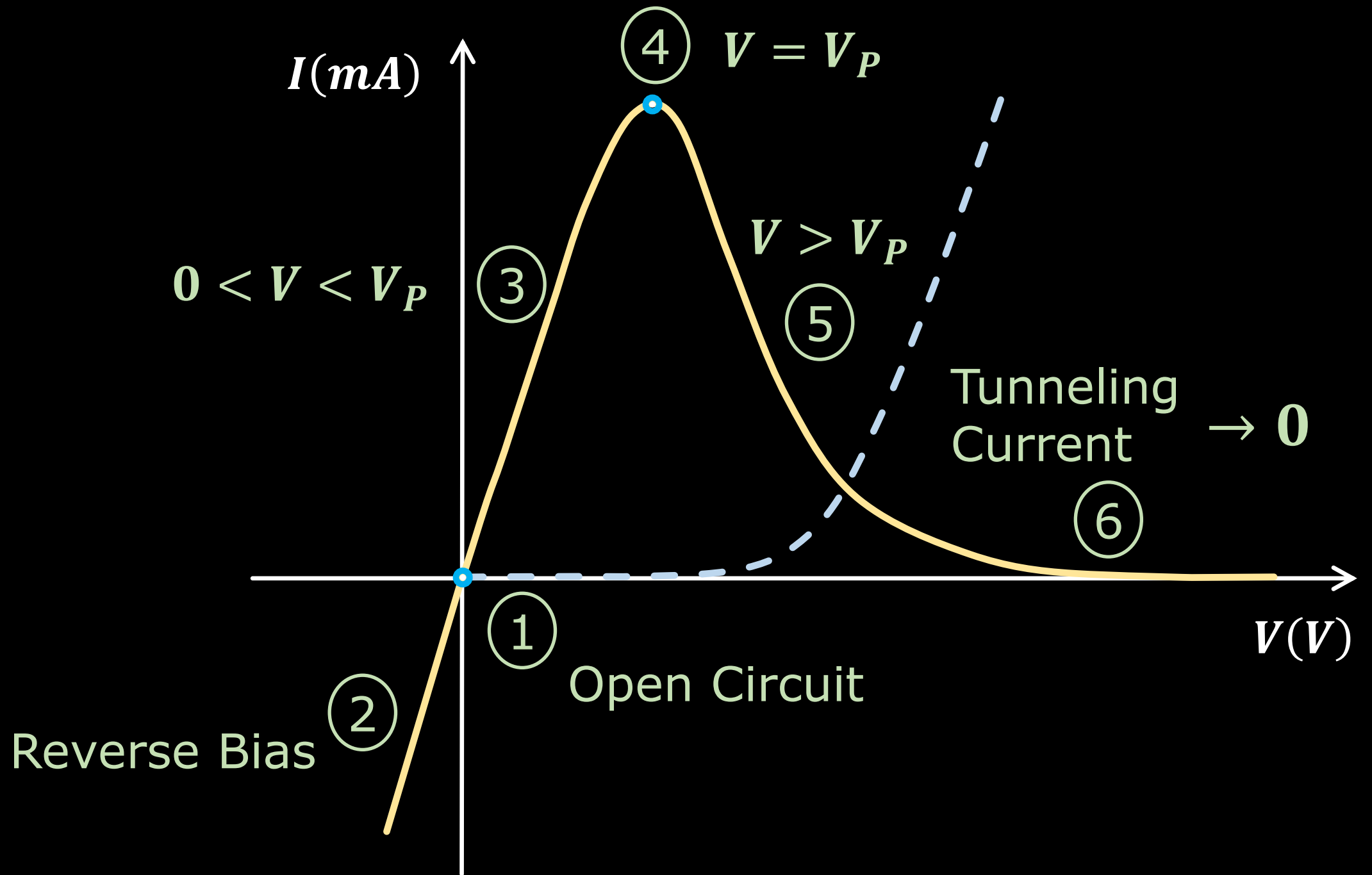


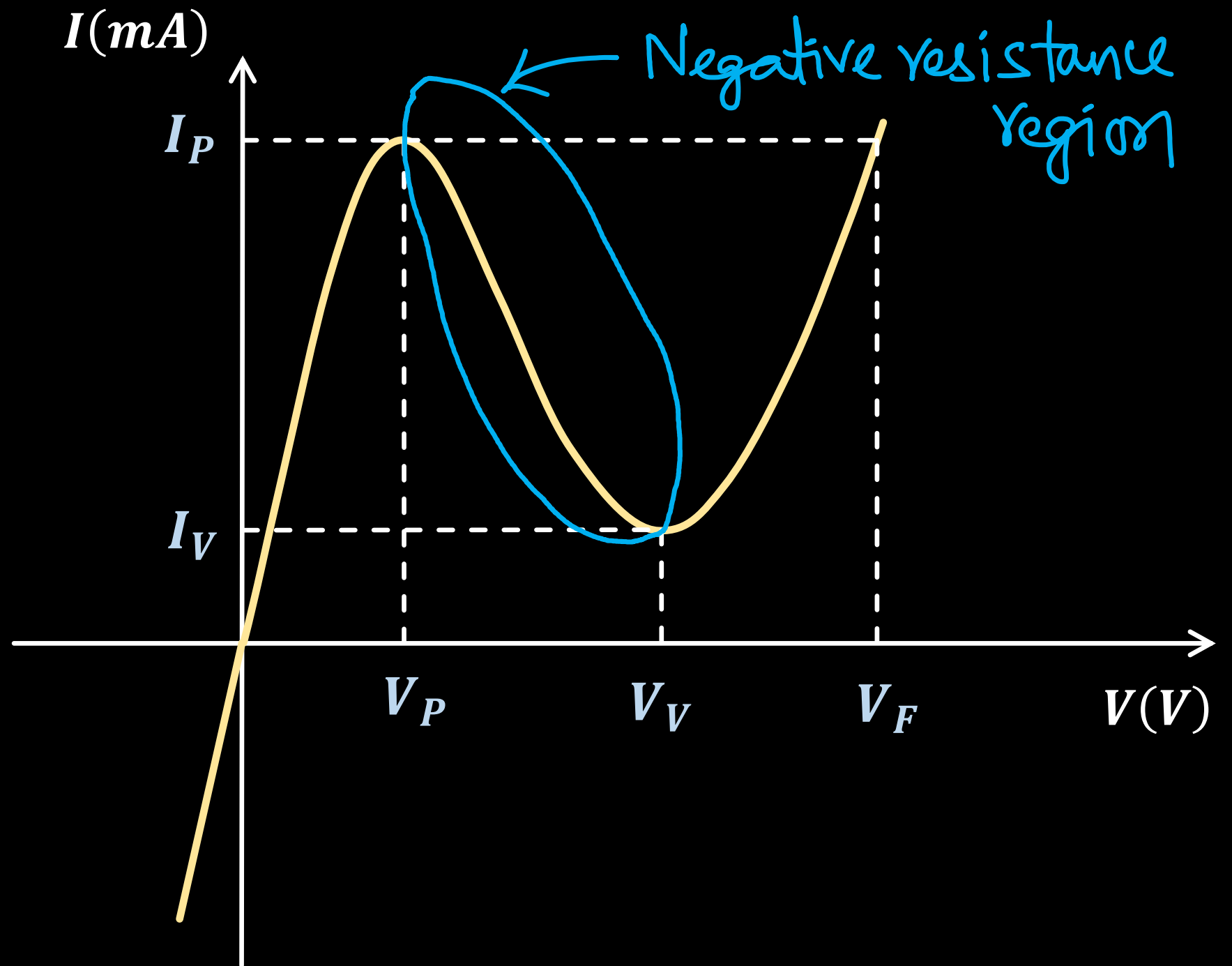
⑥





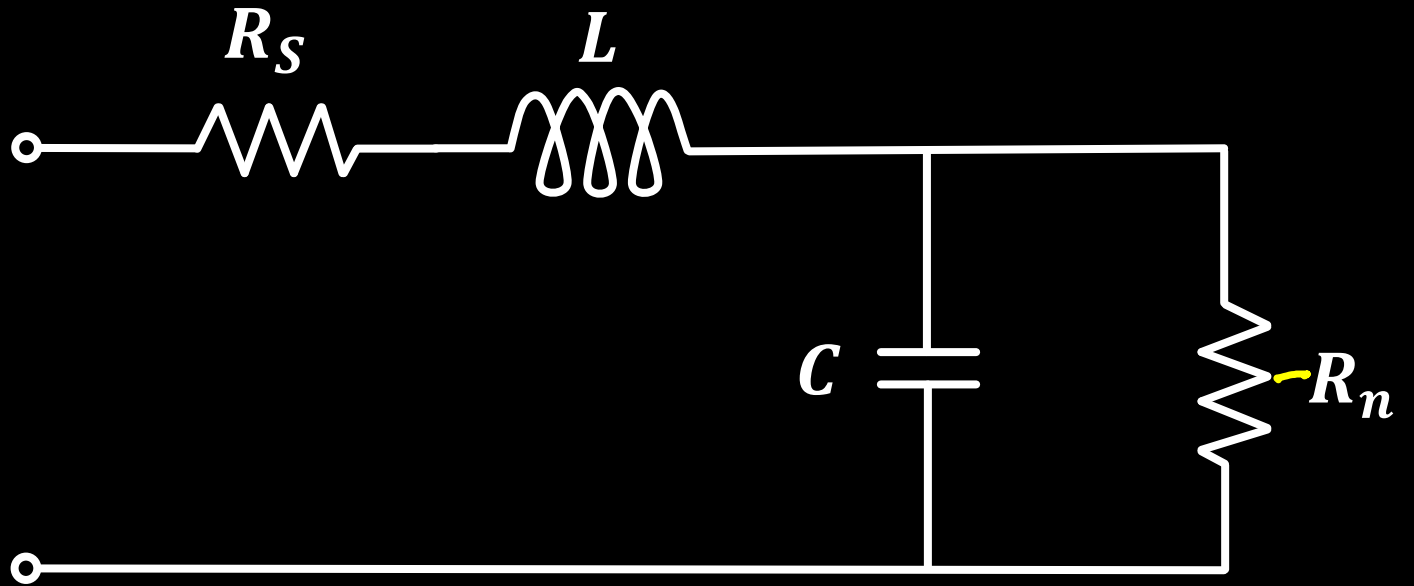
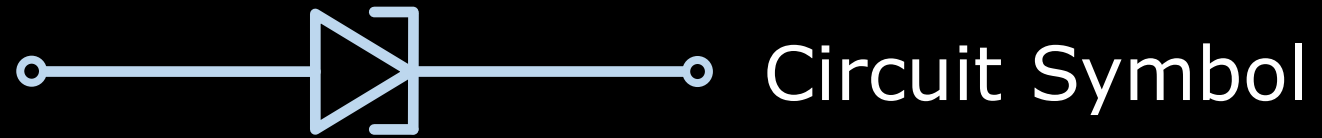






	GaAs	Ge	Si
$\frac{I_P}{I_V}$	15	8	3.5
V_P	0.15 V	0.055 V	0.065 V
V_V	0.5 V	0.35 V	0.42 V
V_F	1.1 V	0.5 V	0.7 V

$$\left(\frac{I_P}{I_V}\right)_{GaAs} > \left(\frac{I_P}{I_V}\right)_{Ge} > \left(\frac{I_P}{I_V}\right)_{Si}$$



Equivalent Circuit

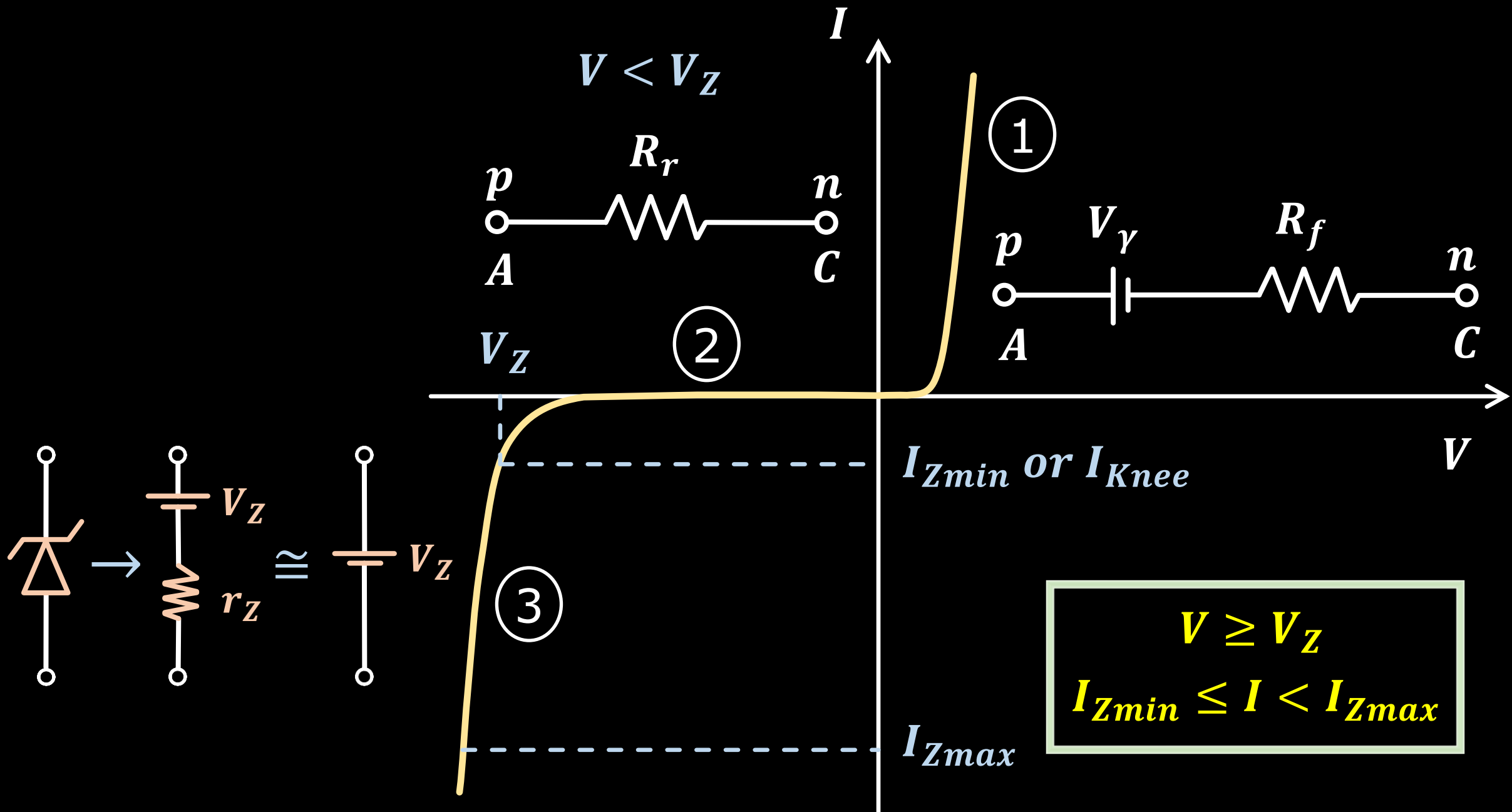
Applications

- High frequency oscillator
- Very high speed switch

A Zener diode when used in voltage stabilization circuits, is biased in

$$V < V_Z \text{ (Reverse bias)}$$

- (a) reverse bias region below the breakdown voltage ~~X~~
- (b) reverse breakdown region ✓ $V \geq V_Z$
- (c) forward bias region
- (d) forward bias constant current mode



Consider the following statements pertaining to tunnel diodes

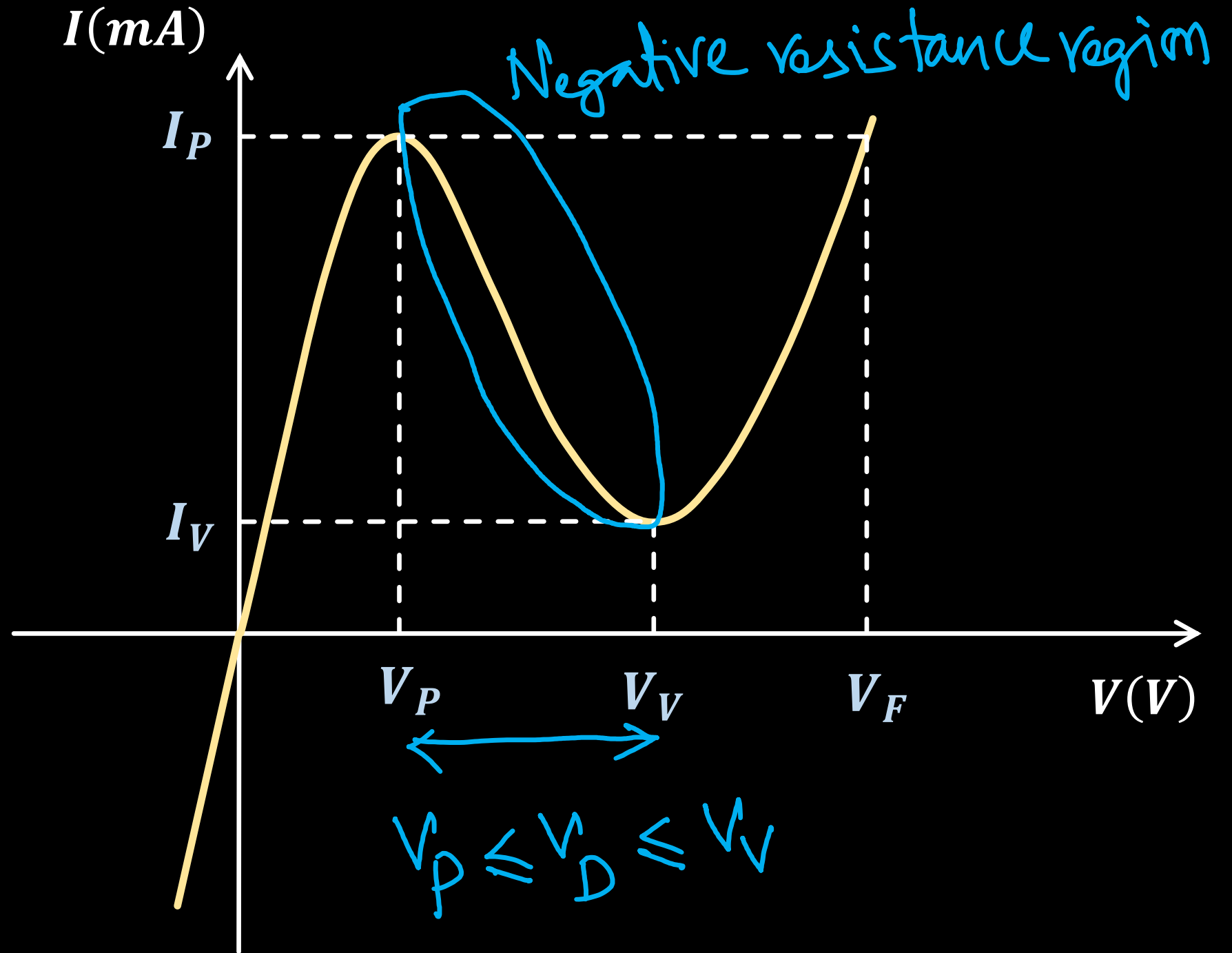
- (1) Impurity concentration is high ✓
- X (2) Carrier velocities are low high
- X (3) They have current-controlled V-I characteristics
voltage

Which of the statements given above are correct ?

- (a) 1 only
- (b) 2 and 3 only
- (c) 1 and 3 only
- (d) 1 and 2 only

The values of voltage (V_D) across a tunnel diode corresponding to peak and valley currents are V_P and V_V respectively. The range of tunnel-diode voltage V_D for which the slope of $I - V_D$ characteristics is negative would be

- (a) $V_D < 0$
- (b) $0 \leq V_D \leq V_P$
- (c) $V_P \leq V_D \leq V_V$
- (d) $V_D \geq V_V$



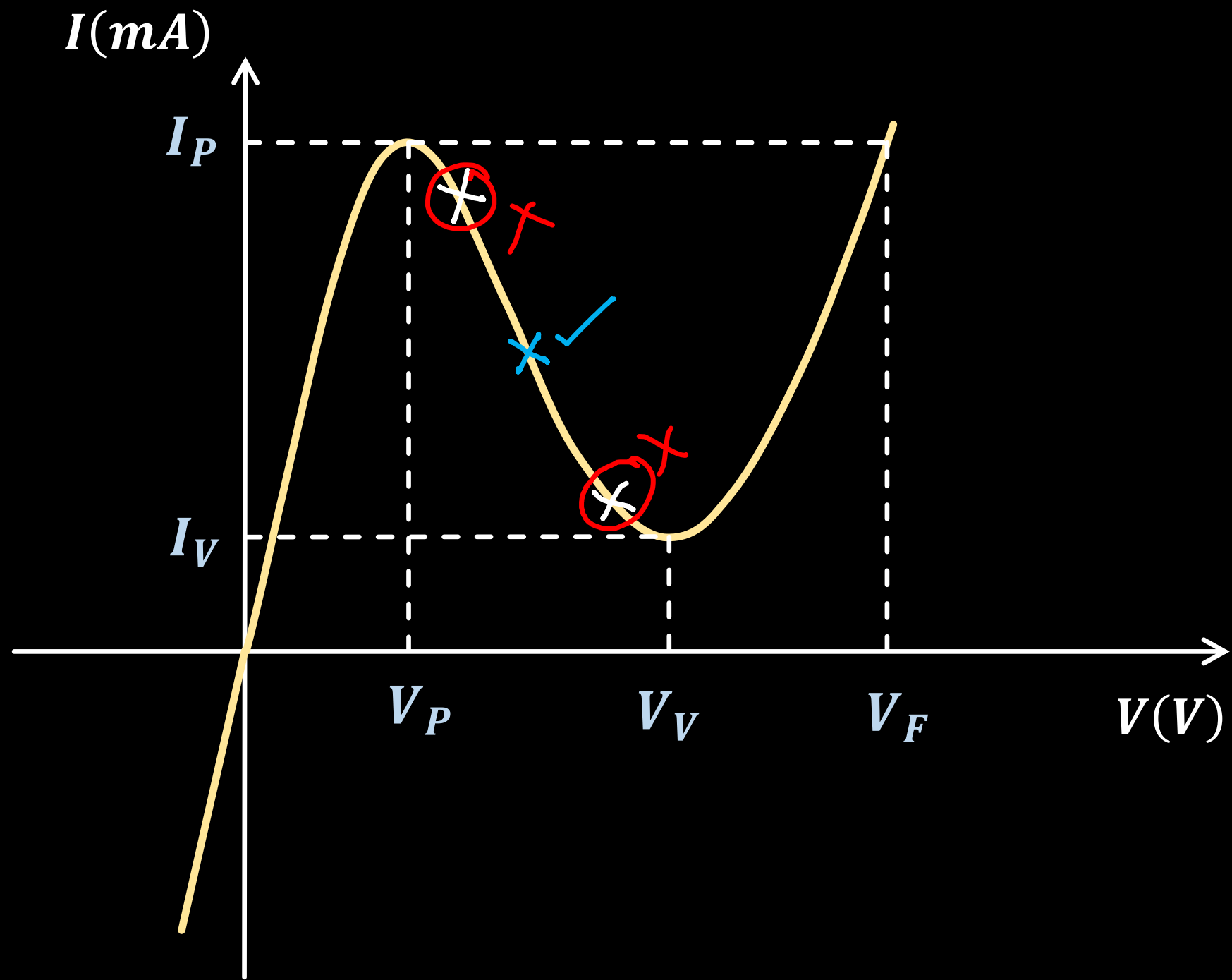
Which of the following statement is correct? A tunnel diode is "always" biased

(a) by a dc source

(b) in the middle of its negative resistance region ✓

(c) in the positive resistance region nearest to zero ✗

(d) in the reverse direction ✗



A tunnel diode is p^{++} and n^{++}

- (a) High resistivity p-n junction diode ~~X~~
- ~~X~~ (b) A slow switching device *Fast*
- (c) An amplifying device ~~X~~ *an oscillator*
- (d) A very heavily doped p-n junction diode ✓