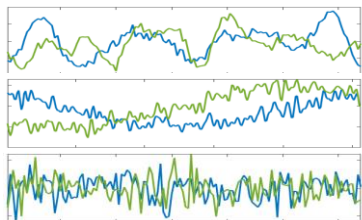
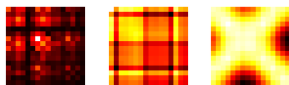


**Input I/Q**

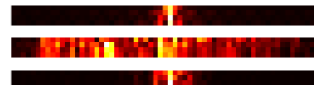


## Feature Extraction

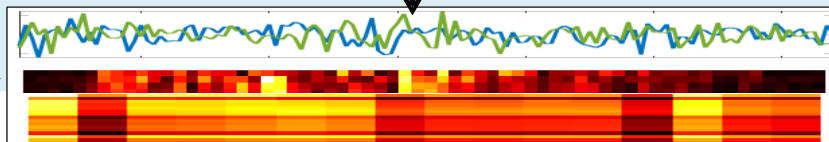
**Time-Domain**



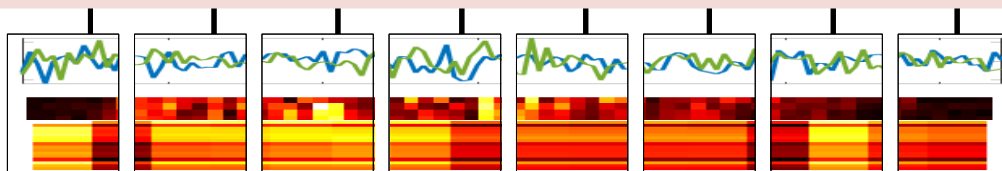
**Frequency-Domain**



**Flatten**

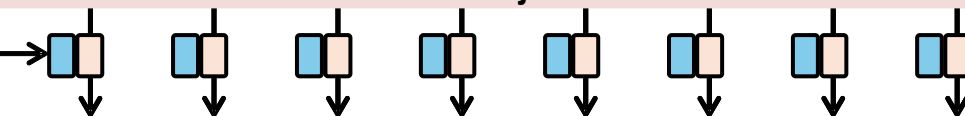


**Patchify**



**Linear Projection**

**Patch/Positional  
Embedding**



## Transformer Encoder

**MLP Head**

**Modulation Class**