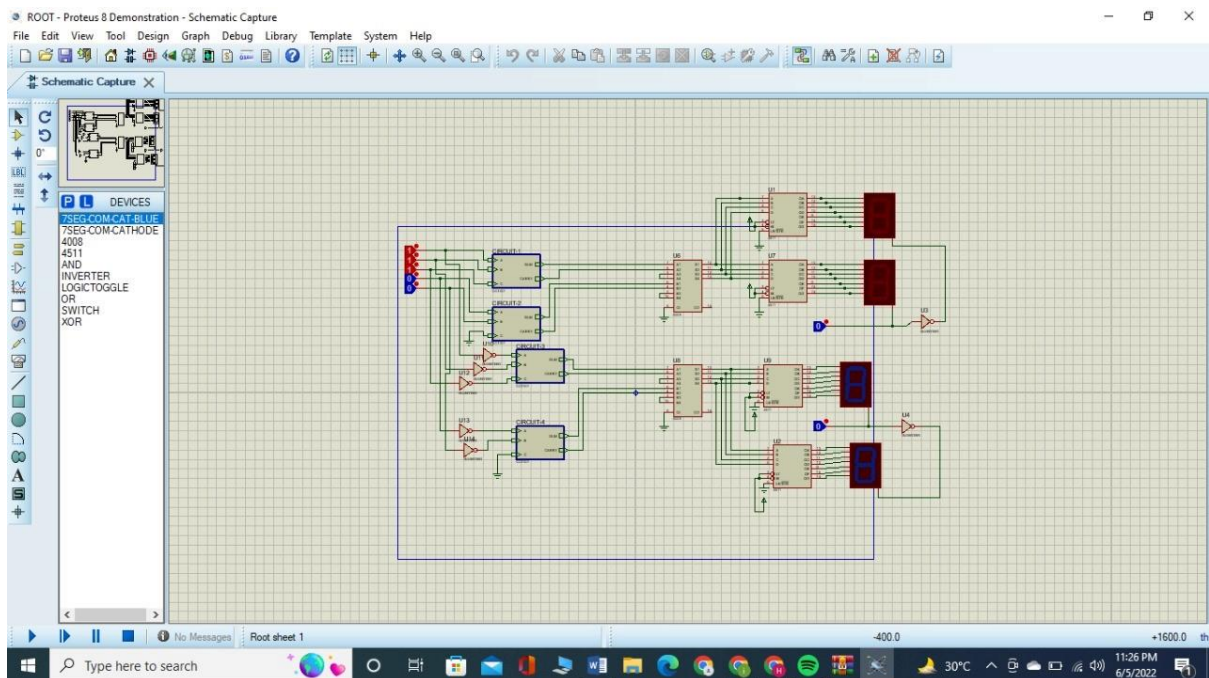# DLD Project Report

I21-1661 – Hammad Javaid – DS-M

There were five inputs to this voting machine.

I used four full adders and connected two NOT gates. There is one sum output AND carry out of each of the 4 full adders.

Then I used 2 MUX (4008) which is consequently connected to the decoder (4511). The function of the BCD to 7 segment decoder is to convert logic states at outputs of BCD. It is connected to 7 segment display to show the results.

Attached below is the proteus screenshot:

Here is the picture of my hardware: