

Lab 2: HW Syn Lab

1) common anode because it's active low.

2) 0/low

3) Assuming the desired refresh rate is 20 fps and there are 4-digits. The clock must be $1/(20 \times 4)$ seconds. You'll need to divide the clock by $\log_2((1/80) \text{ seconds} / 10\text{ns})$ base 2 which is about 20.