

① 将 DM 数组拷贝到 CUDA device 上。

```

184 // Allocate DMs and copy to device
185 dm=(float *) malloc(sizeof(float)*ndm);
186 for (idm=0;idm<ndm;idm++)
187     dm[idm]=dm_start+(float) idm*dm_step;
188 checkCudaErrors(cudaMalloc((void **) &ddm,sizeof(float)*ndm));
189 checkCudaErrors(cudaMemcpy(ddm,dm,sizeof(float)*ndm,cudaMemcpyHostToDevice));
190

```

② CUFFT 库设置一个 complex-to-complex 一维傅里叶变换计划

```

191 // Generate FFT plan (batch in-place forward FFT) nbin
192 idist=nbins; odist=nbins; iembed=nbins; oembed=nbins; istride=1; ostride=1;
193 checkCudaErrors(cufftPlanMany(&ftc2cf,1,&nbins,&iembed,istride,idist,&oembed,ostride,odist,CUFFT_C2C,nfft*nsub));
194
195 // Generate FFT plan (batch in-place backward FFT) 计划把外接内存
196 idist=mbins; odist=mbins; iembed=mbins; oembed=mbins; istride=1; ostride=1;
197 checkCudaErrors(cufftPlanMany(&ftc2cb,1,&mbins,&iembed,istride,idist,&oembed,ostride,odist,CUFFT_C2C,nchan*nfft*nsub));
198

```

↓ ↓ ↘ ↘ ↘ ↘
输入 FFT 大小 线性数据块尺寸和布局 输出

③ 计算 chirp 相位的数值。

```

199 // Compute chirp
200 blocksize.x=32; blocksize.y=32; blocksize.z=1;
201 gridsize.x=nsub/blocksize.x+1; gridsize.y=nchan/blocksize.y+1; gridsize.z=ndm/blocksize.z+1;
202 compute_chirp<<<gridsize,blocksize>>>(h5.fcen,nsub*h5.bwchan,ddm,nchan,nbin,nsub,ndm,dc);

```

↓ ↓ ↓
整块带宽 nbins × nsub × ndm

④ 读取文件命名及读取

h5buf[i] → 分配 sizeof(char)*nsamp*nsub

dh5buf[i] → 分配 GPU 上内存 ↑

↓ for iblock=0 → nblock

```

231 // Loop over input file contents
232 for (iblock=0;iblock++ {
233     // Read block
234     startclock=clock();
235     for (i=0;i<4;i++)
236         nread=fread(h5buf[i],sizeof(char),nsamp*nsub,rawfile[i]); // nsub: 文件大小
237     if (nread==0) break;
238     printf("Block: %d: Read %d MB in %.2f s\n",iblock,sizeof(char)*nread*nsub*4/(1<<20),(float) (clock()-startclock)/CLOCKS_PER_SEC);
239
240     // Copy buffers to device
241     startclock=clock();
242     for (i=0;i<4;i++)
243         checkCudaErrors(cudaMemcpy(dh5buf[i],h5buf[i],sizeof(char)*nread*nsub,cudaMemcpyHostToDevice));

```

↓ 循环读取输入文件的内容，输出有关读取数据的信息。

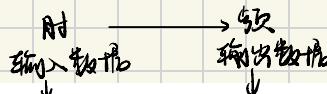
→ 将主机上的缓冲数据复制到 CUDA。

⑤ unpack data and padd data 数据解压+填充.

```
246 // Unpack data and padd data
247 blocksize.x=32; blocksize.y=32; blocksize.z=1;
248 gridSize.x=nbin/blocksize.x+1; gridSize.y=nfft/blocksize.y+1; gridSize.z=nsub/blocksize.z+1;
249 unpack_and_padd<<<gridsize,blocksize>>>(dh5buf[0],dh5buf[1],dh5buf[2],dh5buf[3],nread,nbin,nfft,nsub,nooverlap,cp1p,cp2p);
```

↓
32x32

⑥ 逆向前向傅里叶(时域→频域)



```
251 // Perform FFTs
252 checkCudaErrors(cufftExecC2C(ftc2cf,(cufftComplex *) cp1p,(cufftComplex *) cp1p,CUFFT_FORWARD));
253 checkCudaErrors(cufftExecC2C(ftc2cf,(cufftComplex *) cp2p,(cufftComplex *) cp2p,CUFFT_FORWARD));
```

⑦ swap_spectrum_halves 交换频谱中的两半

```
255 dim3 blocksize n halves for large FFTs
256 blocksize.x=32; blocksize.y=32; blocksize.z=1;
257 gridSize.x=nbin/blocksize.x+1; gridSize.y=nfft*nsub/blocksize.y+1; gridSize.z=1;
258 swap_spectrum_halves<<<gridsize,blocksize>>>(cp1p,cp2p,nbin,nfft*nsub);
259
```

↑
32x32

↓ for idm → ndm.

⑧ 对数据上采样cp1p和cp2p分别与chirp函数相乘

```
263 // Perform complex multiplication of FFT'ed data with chirp
264 blocksize.x=32; blocksize.y=32; blocksize.z=1;
265 gridSize.x=nbin*nsub/blocksize.x+1; gridSize.y=nfft/blocksize.y+1; gridSize.z=1;
266 PointwiseComplexMultiply<<<gridsize,blocksize>>>(cp1p,dc,cp1,nbin*nsub,nfft,idm,1.0/(float) nbin);
267 PointwiseComplexMultiply<<<gridsize,blocksize>>>(cp2p,dc,cp2,nbin*nsub,nfft,idm,1.0/(float) nbin);
268
```

↓
nbin*nsub] [ndm]

```
533 // Pointwise complex multiplication (and scaling)
534 static __global__ void PointwiseComplexMultiply(cufftComplex *a,cufftComplex *b,cufftComplex *c,int nx,int ny int float scale)
535 {
536     int i,j,k;
537     i=blockIdx.x*blockDim.x+threadIdx.x; → nbin*nsub
538     j=blockIdx.y*blockDim.y+threadIdx.y; → nfft
539
540     if (i<nx && j<ny) {
541         k=i+nx*j;
542         c[k]=ComplexScale(ComplexMul(a[k],b[i+nx*j]),scale);
543     }
544 }
```

idm.

$$\begin{cases} z_1 = a + bi \\ z_2 = b(i + nx \cdot l) \end{cases} \quad \begin{cases} x = z_1 \cdot x \times z_2 \cdot x - z_1 \cdot y \times z_2 \cdot y \\ y = z_1 \cdot x \times z_2 \cdot y - z_1 \cdot y \times z_2 \cdot x \end{cases}$$

$$\begin{cases} z_1 = a(lk) \\ z_2 = b(l + nx \cdot l) \end{cases} \quad \begin{cases} x = z_1 \cdot x \times z_2 \cdot x - z_1 \cdot y \times z_2 \cdot y \\ y = z_1 \cdot x \times z_2 \cdot y - z_1 \cdot y \times z_2 \cdot x \end{cases}$$

⑨ 两次 swap_spectrum_halves 交换数据

```

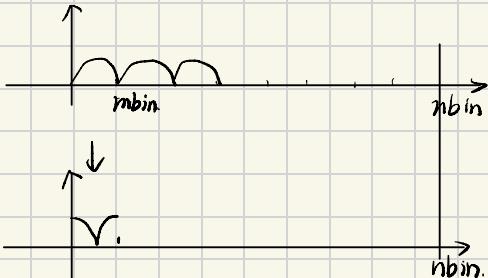
269 // Swap spectrum halves for small FFTs
270 blocksize.x=32; blocksize.y=32; blocksize.z=1;
271 gridsize.x=mbin/blocksize.x+1; gridsize.y=nchan*nfft*nsub/blocksize.y+1; gridsize.z=1;
272 swap_spectrum_halves<<<gridsize,blocksize>>>(cp1,cp2,mbin,nchan*nfft*nsub);
273

```

\downarrow
 $[mbin][nchan * nfft * nsub]$

$cp1 = [nbins][nfft][nsub]$

\downarrow
 $[mbin][nchan]$



⑩ 进行两次FFT，将 cp1, cp2 → 附加信息

```

274 // Perform FFTs
275 checkCudaErrors(cufftExecC2C(ftc2cb,(cufftComplex *) cp1,(cufftComplex *) cp1,CUFFT_INVERSE));
276 checkCudaErrors(cufftExecC2C(ftc2cb,(cufftComplex *) cp2,(cufftComplex *) cp2,CUFFT_INVERSE));

```

⑪ 使用 transpose_unpadd_and_detect 对数据进行转置和去填充。

```

78 // Detect data
79 blocksize.x=32; blocksize.y=32; blocksize.z=1;
80 gridsize.x=mbin/blocksize.x+1; gridsize.y=nchan/blocksize.y+1; gridsize.z=nfft/blocksize.z+1;
81 transpose_unpadd_and_detect<<<gridsize,blocksize>>>(cp1,cp2,mbin,nchan,nfft,nsub,nooverlap/nchan,nread/nchan,dfbuf);

```

\uparrow
 $[mbin][nchan][nfft]$
 $nbin$

并在 dfbuf 中存储 cp1 和 cp2 模拟平方和 Stoke!

⑫ 使用 compute_block_sums。在 nchan 下方 msum=1024 为 mblock。

```

283 // Compute block sums for redigitization
284 blocksize.x=32; blocksize.y=32; blocksize.z=1;
285 gridsize.x=mchan/blocksize.x+1; gridsize.y=mblock/blocksize.y+1; gridsize.z=1;
286 compute_block_sums<<<gridsize,blocksize>>>(dfbuf,mchan,mblock,msum,bs1,bs2);

```

\uparrow
 $[nchan][mblock]$

\uparrow
 $msamp/msum \rightarrow nsamp/nchan / 1024$

每个 block 和和平方和

⑬ 使用函数 compute_channel_statistics 来计算每个 channel 的 short 值

```
288     // Compute channel stats  
289     blocksize.x=32; blocksize.y=1; blocksize.z=1;  
290     gridsize.x=mchan/blocksize.x+1; gridsize.y=1; gridsize.z=1;  
291     compute_channel_statistics<<<gridsize,blocksize>>>(mchan,mblock,msum,bs1,bs2,zavg,zstd);  
~~~  
          ↑ mchan          ↓ nsub * nchan          ↓ nsamp / msum          ↓ (mchan)
```

⑭ 对流波数组进行归一化，将短整型化为 8 位。

```
293     // Redigitize data to 8bits  
294     blocksize.x=32; blocksize.y=32; blocksize.z=1;  
295     gridsize.x=mchan/blocksize.x+1; gridsize.y=mblock/blocksize.y+1; gridsize.z=1;  
296     if (ndec==1)  
297         redigitize<<<gridsize,blocksize>>>(dbuf,mchan,mblock,msum,zavg,zstd,3.0,5.0,dcbuf);  
298     else  
          ↑ (mchan) ↑ (mblock)  
299     decimate_and_redigitize<<<gridsize,blocksize>>>(dbuf,ndec,mchan,mblock,msum,zavg,zstd,3.0,5.0,dcbuf);
```

⑮ 将 GPU 上的 dcbuf 读取复制到 CPU 上的 cbuf (短整型)

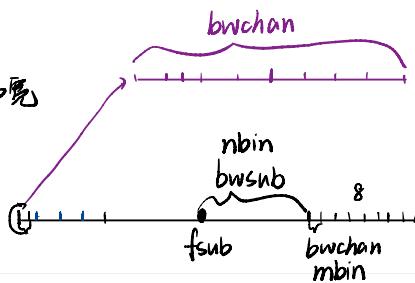
$$\text{字节数} = [\text{msamp}] [\text{mchan}] [\text{ndec}]$$

```
299     decimate_and_redigitize<<<gridsize,blocksize>>>(dbuf,ndec,mchan,mblock,msum,zavg,zstd,3.0,5.0,dcbuf);  
300  
301     // Copy buffer to host  
302     checkCudaErrors(cudaMemcpy(cbuf,dcbuf,sizeof(unsigned char)*msamp*mchan/ndec,cudaMemcpyDeviceToHost));  
303  
304     // Write buffer  
305     fwrite(cbuf,sizeof(char),nread*nsub/ndec,outfile[idm]);  
306 }  
307 printf("Processed %d DMs in %.2f s\n",ndm,(float)(clock()-startclock)/CLOCKS_PER_SEC);  
308 }  
309 }
```

compute_chirp 源碼分析

```

547 __global__ void compute_chirp(double fcen,double bw,float *dm,int nchan,int nbins,int nsub,int ndm,cufftComplex *c)
548 {
549     int ibin,ichan,isub,idm,mbin,idx;
550     double s,rt,t,f,fsub,fchan,bwchan,bwsb;
551
552     // Number of channels per subband
553     mbin=nbins/nchan;
554
555     // Subband bandwidth
556     bwsb=bw/nsub; → 子帶的帶寬
557
558     // Channel bandwidth
559     bwchan=bw/(nchan*nsub); → 一個子帶下一個通道的帶寬
560
561     // Indices of input data
562     isub=blockIdx.x*blockDim.x+threadIdx.x;
563     ichan=blockIdx.y*blockDim.y+threadIdx.y;
564     idm=blockIdx.z*blockDim.z+threadIdx.z;
565
566     // Keep in range
567     if (isub<nsub && ichan<nchan && idm<ndm) {
568         // Main constant
569         s=2.0*M_PI*dm[idm]/DMCONSTANT;
570
571         // Frequencies
572         fsub=fcen-0.5*bw+bw*(float) isub/(float) nsub+0.5*bw/(float) nsub;
573         fchan=fsub-0.5*bwsb+bwchan*(float) ichan/(float) nchan+0.5*bwsb/(float) nchan; → nchan 與子帶中頻
574
575         // Loop over bins in channel
576         for (ibin=0;ibin<mbin;ibin++) {
577             // Bin frequency
578             f=-0.5*bwchan+bwchan*(float) ibin/(float) mbin+0.5*bwchan/(float) mbin;
579
580             // Phase delay
581             rt=-f*f*s/((fchan+f)*fchan*fchan); ??
582
583             // Taper
584             t=1.0/sqrt(1.0+pow((f/(0.47*bwchan)),80));
585
586             // Index
587             idx=ibin+ichan*mbin+isub*mbin*nchan+idm*nsub*mbin*nchan;
588
589             // Chirp
590             c[idx].x=cos(rt)*t;
591             c[idx].y=sin(rt)*t;
592         }
593     }
594 }
```



$$t = \frac{1}{\sqrt{1 + [f/(0.47 \cdot bwchan)]^8}}$$

$$T_{idm} = T_{idm} T_{ichan} T_{imbin} T_{isub}$$

nbin

unpack and padd

```

598 // Unpack the input buffer and generate complex timeseries. The output
599 // timeseries are padded with nooverlap samples on either side for the
600 // convolution.
601 global_ void unpack_and_padd(char *dbuf0,char *dbuf1,char *dbuf2,char *dbuf3,int nsamp,int nbin,int nfft,int nsub,int noverlap,cufftComplex *cp1,cufftComplex *cp2)
602 {
603     int64_t ibin,ifft,isamp,isub,indx1,indx2;
604
605     // Indices of input data
606     ibin=blockIdx.x*blockDim.x+threadIdx.x;
607     ifft=blockIdx.y*blockDim.y+threadIdx.y;
608     isub=blockIdx.z*blockDim.z+threadIdx.z;
609
610     // Only compute valid threads
611     if (ibin<nbin && ifft<nfft && isub<nsub) {
612         idx1=ibin+nbin*isub+nsub*nbin*ifft;
613         isamp=ibin+(nbin-2*noverlap)*ifft-noverlap;
614         idx2=isub+nsub*isamp;
615         if ((idx1<0 || isamp>nsamp) {
616             cp1[idx1].x=0.0;
617             cp1[idx1].y=0.0;
618             cp2[idx1].x=0.0;
619             cp2[idx1].y=0.0;
620         } else {
621             cp1[idx1].x=(float)dbuf0[idx2];
622             cp1[idx1].y=(float)dbuf1[idx2];
623             cp2[idx1].x=(float)dbuf2[idx2];
624             cp2[idx1].y=(float)dbuf3[idx2];
625         }
626     }
627
628     return;
629 }

```

7048 cp1p cp2p

dbuf nsampxnsub cp1p, cp2p = nbin * nfft * nsub
 ||
nfft * nvalid

indx2 = isub + nsub(ibin + (nbin - 2 * nooverlap) * ifft - overlap)

if ifft = 1 isamp = ibin + nvalid - overlap

transpose-unpadd-and-detect f_θ f_ϕ

[nsamp] [nsub]

```

665 // After the segmented FFT the data is in a cube of nbins by nchan by
666 // nnfft, where nbins and nnfft are the time indices. Here we rearrange
667 // the 3D data cube into a 2D array of frequency and time, while also
668 // removing the overlap regions and detecting (generating Stokes I).
669 __global__ void transpose_unpadd_and_detect(cufftComplex *cp1,cufftComplex *cp2,int nbins,int nchan,int nnfft,int nsub,int sub)
670 {
671     int64_t ibin,ichan,ifft,isub,isamp, idx1, idx2;
672
673     ibin=blockIdx.x*blockDim.x+threadIdx.x;
674     ichan=blockIdx.y*blockDim.y+threadIdx.y;
675     ifft=blockIdx.z*blockDim.z+threadIdx.z;
676     if (ibin<nbins && ichan<nchan && ifft<nnfft) {
677         // Loop over subbands
678         for (isub=0;isub<nsub;isub++) {
679             // Padded array index
680             // idx1=ibin+nbins*isub+nsub*nbin*(ichan+nchan*ifft);
681             idx1=ibin+ichan*nbin+(nsub-isub-1)*nbins*nchan*ifft+nchan*nsub; [nnfft]nbins[nchan]nsub]
682
683             // Time index
684             isamp=ibin+(nbins-2)*nooverlap*ifft-nooverlap;
685
686             // Output array index
687             idx2=(nchan-ichan-1)+isub*nchan+nsub*nchan*isamp;
688
689             // Select data points from valid region
690             if (ibin>=nooverlap && ibin<=nbins-nooverlap && isamp>=0 && isamp<nsamp)
691             fbuf[idx2]=cp1[idx1].x*cp1[idx1].x+cp1[idx1].y*cp1[idx1].y+cp2[idx1].x*cp2[idx1].x+cp2[idx1].y*cp2[idx1].y; [校对]
692         }
693     }
694
695     return;
696 }

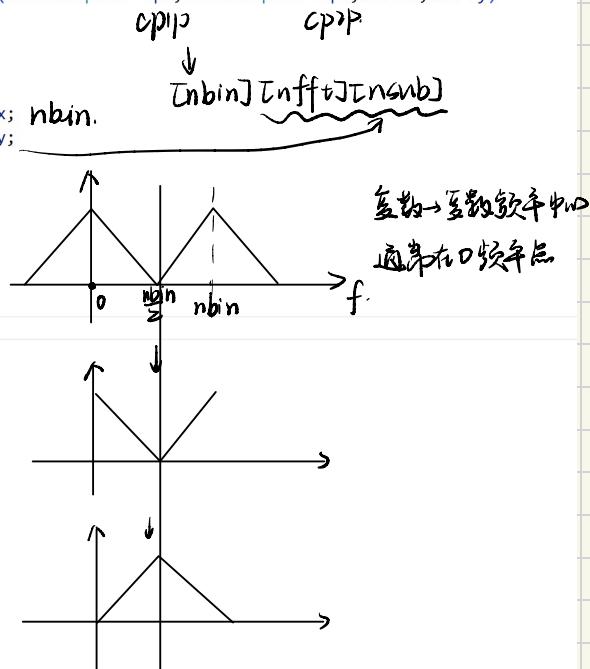
```

swap-spectrum-halves

```

631 // Since complex-to-complex FFTs put the center frequency at bin zero
632 // in the frequency domain, the two halves of the spectrum need to be
633 // swapped.
634 __global__ void swap_spectrum_halves(cufftComplex *cp1,cufftComplex *cp2,int nx,int ny)
635 {
636     int64_t i,j,k,l,m;
637     cufftComplex tp1,tp2;
638
639     i=blockIdx.x*blockDim.x+threadIdx.x; nbin.
640     j=blockIdx.y*blockDim.y+threadIdx.y;
641     if (i<nx/2 && j<ny) {
642         if (i<nx/2) →
643             k=i+nx/2;
644         else
645             k=i-nx/2;
646         l=i+nx*j; → 未置換
647         m=k+nx*j; → 已置換
648         tp1.x=cp1[1].x;
649         tp1.y=cp1[1].y;
650         tp2.x=cp2[1].x;
651         tp2.y=cp2[1].y;
652         cp1[1].x=cp1[m].x;
653         cp1[1].y=cp1[m].y;
654         cp2[1].x=cp2[m].x;
655         cp2[1].y=cp2[m].y;
656         cp1[m].x=tp1.x;
657         cp1[m].y=tp1.y;
658         cp2[m].x=tp2.x;
659         cp2[m].y=tp2.y;
660     }
661
662     return;
663 }

```



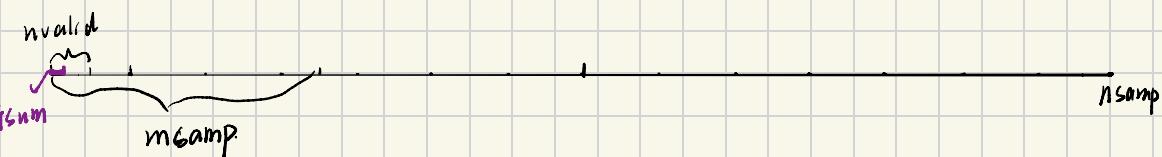
compute_block_sums

[nfft] [mblock] [mchan] [nsub]

```

786 // Compute segmented sums for later computation of offset and scale
787 __global__ void compute_block_sums(float *z,int nchan,int nblock,int nsum,float *bs1,float *bs2)
788 {
789     int64_t ichan,iblock,isum,idx1,idx2;
790
791     ichan=blockIdx.x*blockDim.x+threadIdx.x;
792     iblock=blockIdx.y*blockDim.y+threadIdx.y;
793     if (ichan<nchan && iblock<nblock) { mblock [mchan]
794         idx1=ichan+nchan*iblock;
795         bs1[idx1]=0.0;
796         bs2[idx1]=0.0;
797         for (isum=0;isum<nsum;isum++) { mblock [mchan] [nsamp]
798             idx2=ichan+nchan*(isum+iblock*nsum);
799             bs1[idx1]+=z[idx2];
800             bs2[idx1]+=z[idx2]*z[idx2];
801         }
802     }
803
804     return;
805 }

```



compute_channel_statistics

```

807 // Compute segmented sums for later computation of offset and scale
808 __global__ void compute_channel_statistics(int nchan,int nblock,int nsum,float *bs1,float *bs2,float *zavg,float *zstd)
809 {
810     int64_t ichan,iblock,idx1; mchan mblock [mblock] [mchan] [nsamp] → [mchan]
811     double s1,s2;
812
813     ichan=blockIdx.x*blockDim.x+threadIdx.x;
814     if (ichan<nchan) { ✓ ✓
815         s1=0.0;
816         s2=0.0;
817         for (iblock=0;iblock<nblock;iblock++) { mchan [mblock] [mchan] → [mchan]
818             idx1=ichan+nchan*iblock;
819             s1+=bs1[idx1];
820             s2+=bs2[idx1];
821         }
822         zavg[ichan]=s1/(float)(nblock*nsum); → 算平均
823         zstd[ichan]=s2/(float)(nblock*nsum)-zavg[ichan]*zavg[ichan]; → 方差
824         zstd[ichan]=sqrt(zstd[ichan]);
825     }
826
827     return;
828 }

```

Diagram illustrating the computation of channel statistics. A horizontal axis represents "nsamp" with points labeled "nchan" and "nsun". Brackets indicate the range of "ichan" for each "iblock" iteration, spanning from "0" to "nchan". Handwritten annotations include "mchan", "mblock", and "nsamp" near the start of the axis, and "zavg" and "zstd" near the end of the loop. A red circle highlights line 823.

Redigitize #3, #4.

```

830 // Redigitize the filterbank to 8 bits in segments
831 global_ void redigitize(float *z,int nchan,int nblock,int nsum,float *zavg,float *zstd,float zmin,float zmax,unsigned char *cz)
832 {
833     int64_t ichan,iblock,isum,idx1;
834     float zoffset,zscale;
835
836     ichan=blockIdx.x*blockDim.x+threadIdx.x;
837     iblock=blockIdx.y*blockDim.y+threadIdx.y;
838     if (ichan<nchan && iblock<nblock) {
839         zoffset=zavg[ichan]-zmin*zstd[ichan];
840         zscale=(zmin+zmax)*zstd[ichan];
841
842         for (isum=0;isum<nsum;isum++) {
843             idx1=ichan+nchan*(isum+iblock*nsum);
844             z[idx1]=zoffset;
845             z[idx1]*=256.0/zscale;
846             cz[idx1]=(unsigned char) z[idx1];
847             if (z[idx1]<0.0) cz[idx1]=0;
848             if (z[idx1]>255.0) cz[idx1]=255;
849         }
850     }
851     return;
852 }
853

```

$mchan \quad mblock \quad msum \quad mchan \quad z \quad b \quad d \quad buf$

$[nsamp] [nchan]$
 $[index]$

$\xrightarrow{X-3}$

$msamp = nsamp / nchan$
 $mblock = msamp / msum$
 $mchan = nsub * nchan$

\downarrow

$8 \times 7 \times 16 \times 16$

Decimate-and-redigitize #3, #4.

```

855 // Decimate and Redigitize the filterbank to 8 bits in segments
856 global_ void decimate_and_redigitize(float *z,int ndec,int nchan,int nblock,int nsum,float *zavg,float *zstd,float zmin,float zmax,unsigned char *cz)
857 {
858     int64_t ichan,iblock,isum, idx1, idx2, idec;
859     float zoffset,zscale,ztmp;
860     mchan.
861     ichan=blockIdx.x*blockDim.x+threadIdx.x;
862     iblock=blockIdx.y*blockDim.y+threadIdx.y;
863     if (ichan<nchan && iblock<nblock) {
864         zoffset=zavg[ichan]-zmin*zstd[ichan];
865         zscale=(zmin+zmax)*zstd[ichan];
866
867         for (isum=0;isum<nsum;isum+=ndec) {
868             idx2=ichan+nchan*(isum/ndec+iblock*nsum/ndec);
869             for (idec=0,ztmp=0.0;idec<ndec;idec++) {
870                 idx1=ichan+nchan*(isum+ndec+iblock*nsum);
871                 ztmp+=z[idx1];
872             }
873             ztmp/=float(ndec);
874             ztmp-=zoffset;
875             ztmp*=zscale;
876             cz[idx2]=(unsigned char) ztmp;
877             if (ztmp<0.0) cz[idx2]=0;
878             if (ztmp>255.0) cz[idx2]=255;
879         }
880     }
881     return;
882 }
883

```

$mblock \quad mchan \quad nchan \quad ndec \neq 1$