RESUME

Name	Xin Cheng	**Degree**	Master
College	Huazhong University of Science and Technology	**Major**	AI and Automation
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Education

2014.09--2018.06	HUST (B.S)	School of Automation
2018.09--now	HUST (M.S)	School of AI and Automation

Research：

FPGA network optimization　FPGA high-performance network　Intelligent hardware and sensor

Projects

Courses Design	C language、Embedded System、FPGA、Labview
Internship	Schneider Electric intern、MSRA FPGA intern
Exchanges	Hong Kong University of Science and Technology, National University of Singapore
Competition	Siemens Cup process control development
Research	A paper in Sensors and Actuators: A physical、A paper in IEEE sensors letters

Experience

2017.7-2017.8　Challenge of Siemens Cup China Process Control Development

- Continuous process control, using PLC to implement continuous and stable operation of the chemical production process
- 1st prize in China central region

2018.3-2019.6　Oblique fiber fuel level sensor

- Optical fiber sensors designed for fuel level measurement in aircraft tanks to achieve high-precision measurements under conditions of strong mechanical electromagnetic interference
- New fiber optic sensor structure, measurement system design, hardware circuit design, embedded development

2019.3-2019.8　Internship in MSRA

- Participate in the design and implementation of Project Terminus, a new FPGA parallel network of the Network Research Group
- Optimize the performance of FPGA network data transmission, data streams pipeline, improve the maximum frequency
- Use FPGA to decode and scale JPEG images

2018.12-2019.10　Experiments and Published papers

- Publish paper “Oblique end face coupling optical fiber sensor for point fuel level measurement”

in Sensors and Actuators: A Physical

- Publish paper “ Reflected light intensity-modulated continuous liquid level sensor based on oblique end face coupling optical fibers” in IEEE Sensors Journal

Rewards

- NCRE computer network technology
- 2019 National Scholarship (¥20000)

NCRE network engineer

2018、2019 National Encouragement scholarship

Introduce

- Positive and optimistic, strong cooperation and communication skills, experienced internships, high project participation
- familiar with parallel computing, FPGA parallel network transmission, FPGA heterogeneous computing
- Solid hardware basics with strong capabilities of circuit design and development