Chirag Kantharia

Summary

Hands-on software architect/tech lead with over 15 years experience in systems programming on Linux and FreeBSD. Expertise programming with C, C++, Shell, Perl and Python in UNIX environment. In my spare time, I like to read and write suckless software.

Professional Experience Juniper Networks, Bangalore, India

STAFF ENGINEER

Nov 2004 - present

Currently designing a **cross platform SDK** for data center customers to enable high IQ applications on Juniper switches. Designed and developed the **control plane software**, comprising of chassis management, line-card discovery, power management and software upgrade process, for several Juniper platforms including **M-series**, **MX-series**, **T-series** routers, and **CSE2000**. Delivered infrastructure for **In-Service Software Upgrade** with minimal traffic disruption for single-chassis and multi-chassis Juniper routers. Wrote diagnostics software for **Intel 1GbE NIC**. Wrote scripts to monitor the sizes of prime data structures across releases in JUNOS to help monitor the scaling limits. Board bringup of JUNOS on **M120**, **MX960** routers and **T4000** line card. Presented tech talks to developer community, reviewed several thousand lines of code.

Hewlett-Packard, Bangalore, India

SENIOR SOFTWARE ENGINEER

Jun 2003 - Oct 2004

Release manager for Linux device drivers for Compaq CISS and Compaq CPQARRAY hardware RAID cards for multiple Linux distributions including RedHat. SUSE, and Debian, across multiple architectures. Responsible for parallel cluster boot and process monitoring on OpenSSI. Assisted beta customers to help scale vendor applications on OpenSSI. Trained and mentored developers.

Timesys Inc, Bangalore, India

LINUX KERNEL ENGINEER

Mar 2002 - Apr 2003

Responsible for Timesys BSP on Embedded Planet SBC, IBM Walnut PowerPC SBC and on Applied Data Systems Graphics Client Plus SBC. Helped develop build infrastructure for Timesys Linux distribution bundled with Eclipse based IDE.

Epigon Audiocare Pvt Ltd, Bangalore, India

SOFTWARE ENGINEER

Dec 2000 - Feb 2002

Responsible for Linux 2.4 & Linux 2.2 device driver for C-sound PCI music synthesis card. Developed clean-room implementation of Java Virtual Machine conforming to subset of J2ME specification.

Programming

C, Python, Perl, BASH, CVS, SVN, GIT, Apache HTTPD, Gnats, RedMine, async/event-driven programming, distributed/grid computing, large scale software design, test-driven development.

EDUCATION

Indian Institute of Technology, Mumbai, India. B. Tech Computer Science and Engineering

1995-2000

CONTACT Information 201, Rahat Residency, Nagavarapalya-Varthur Road, C.V.Ramanagar,

Bangalore, India

mobile: +91 98455 68691

chyrag@yahoo.com kantharia.in/chirag linkedin.com/pub/chirag-kantharia/1/160/371 github.com/chyrag