

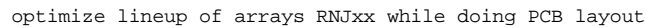
D

C

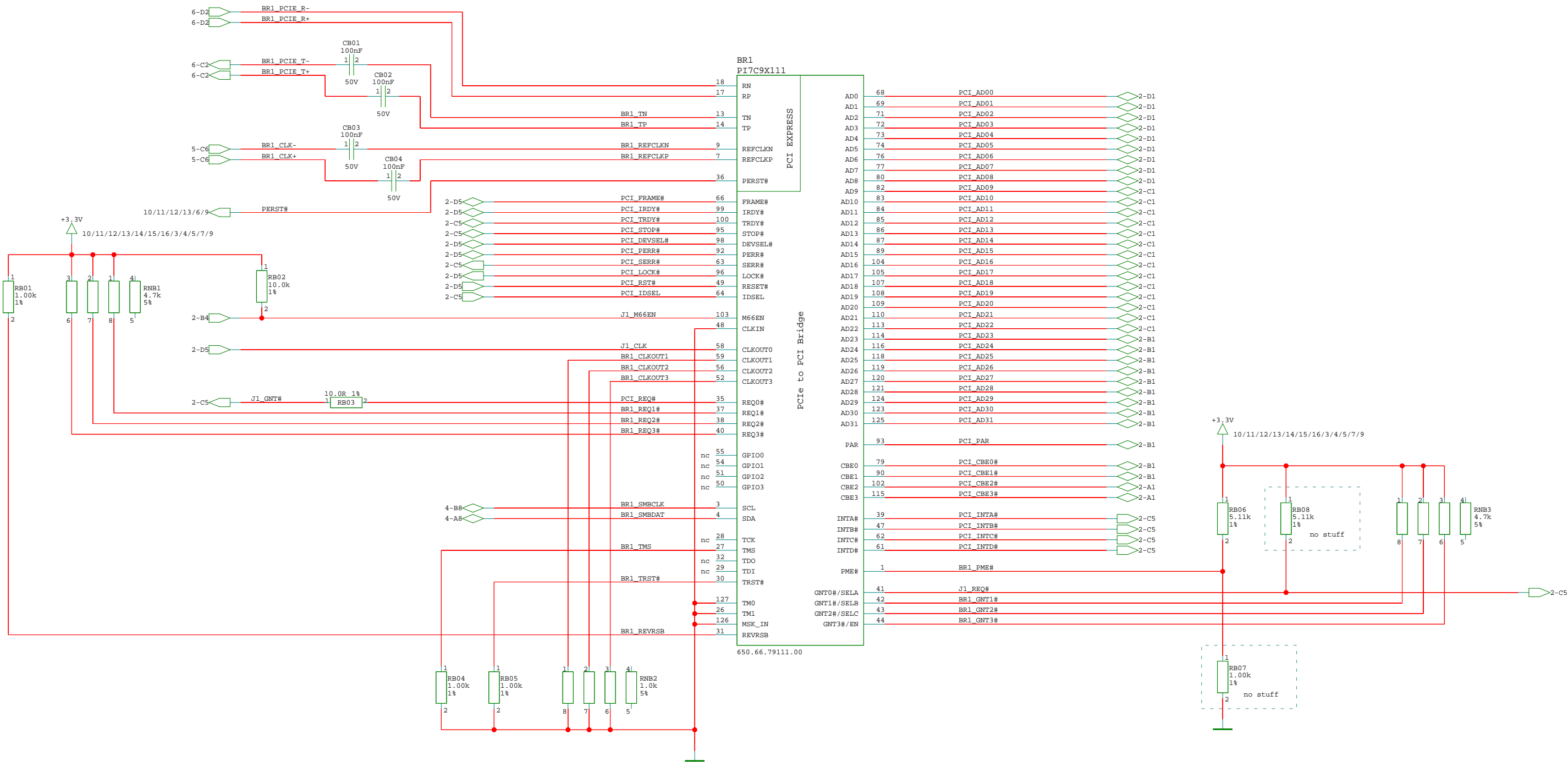
B

A

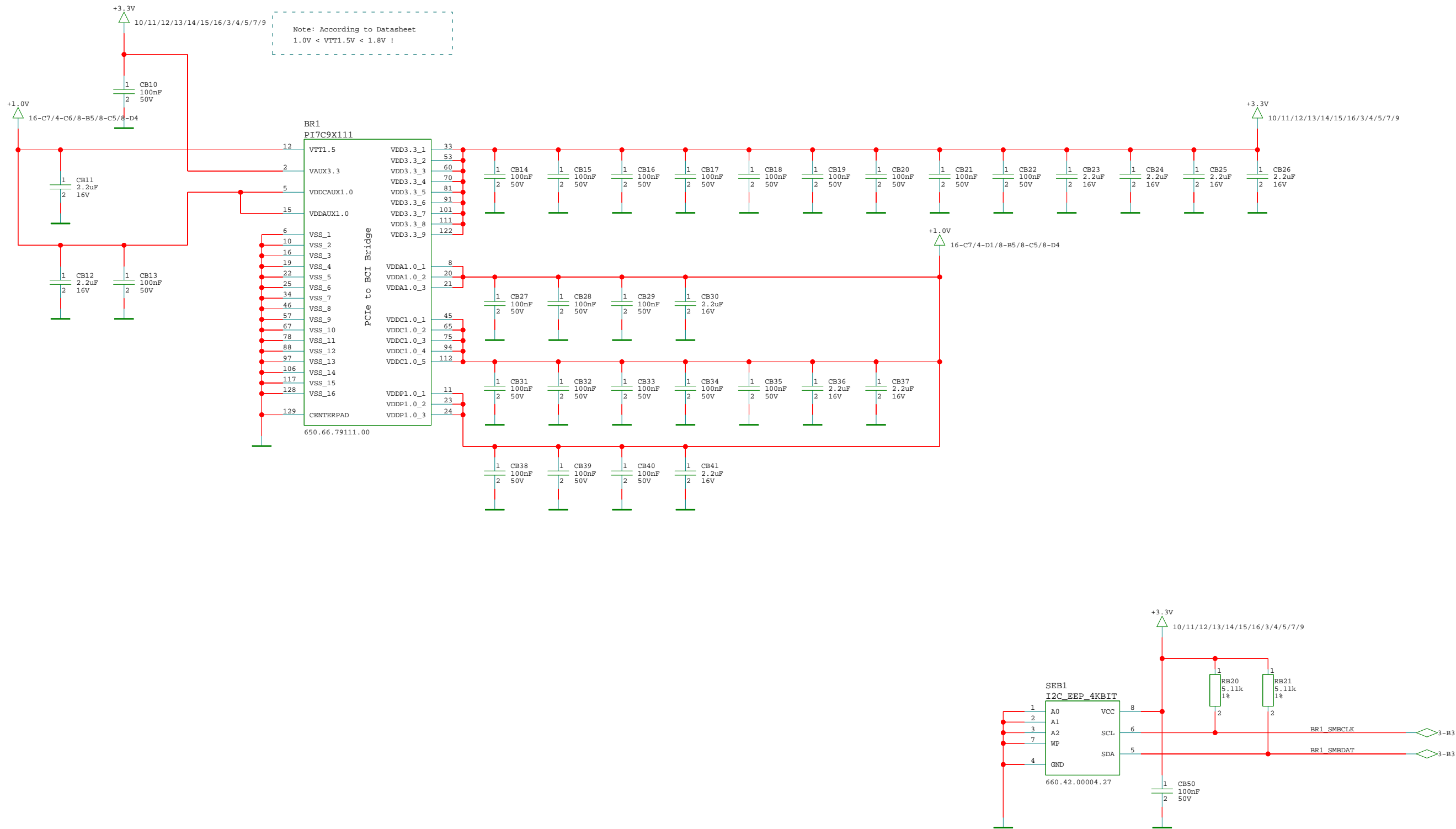
Board	CN8-REVERB	Rev. 1.0
Sheet Name	01: Overview	
Date, Design	2022-04-05 wro/am	Sheet 1 of 16



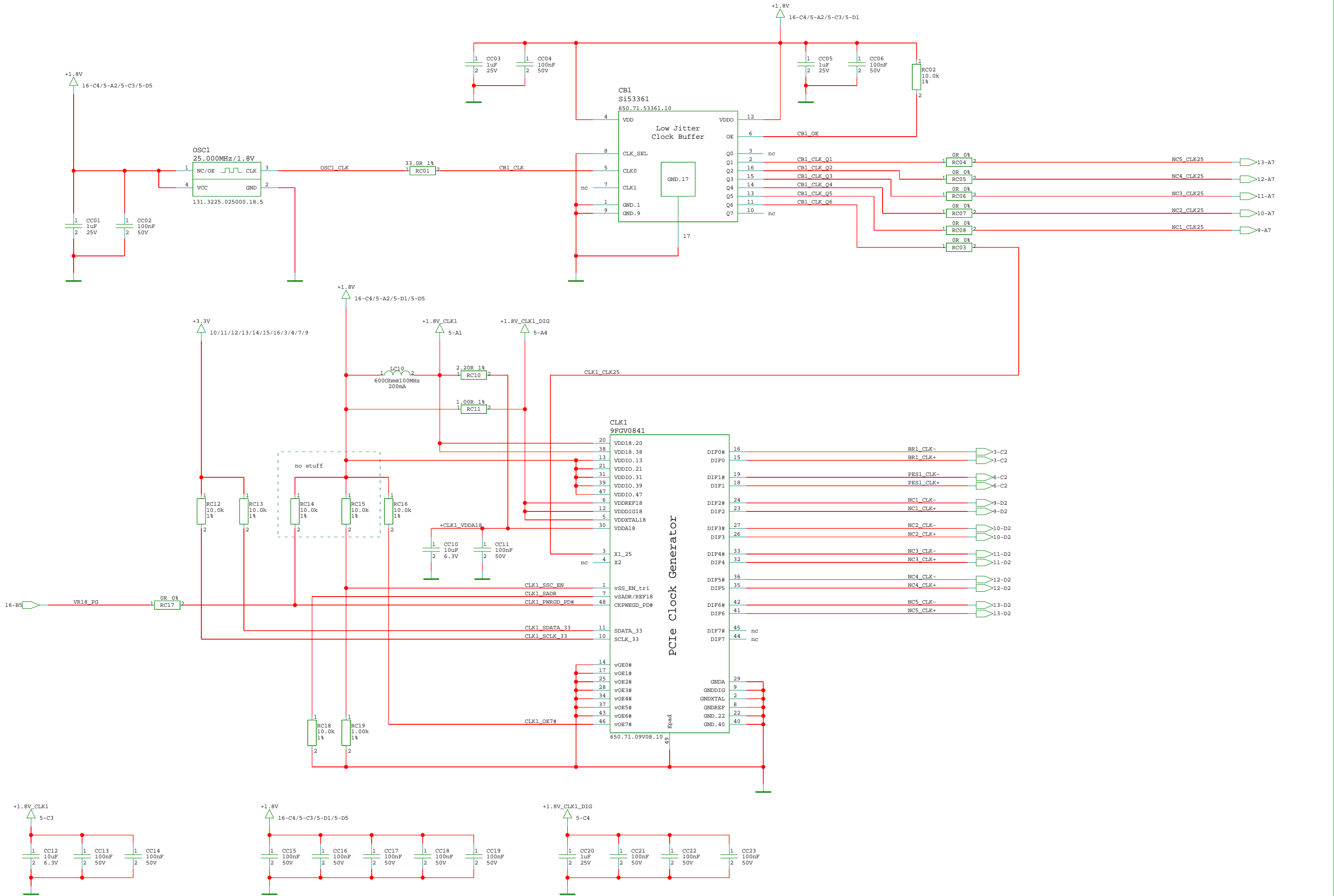
Board	CN8-REVERB	Rev. 0.0
Sheet Name	02: J1 Connector J	
Date, Design	2021-04-11 wro	Sheet 2 of 16



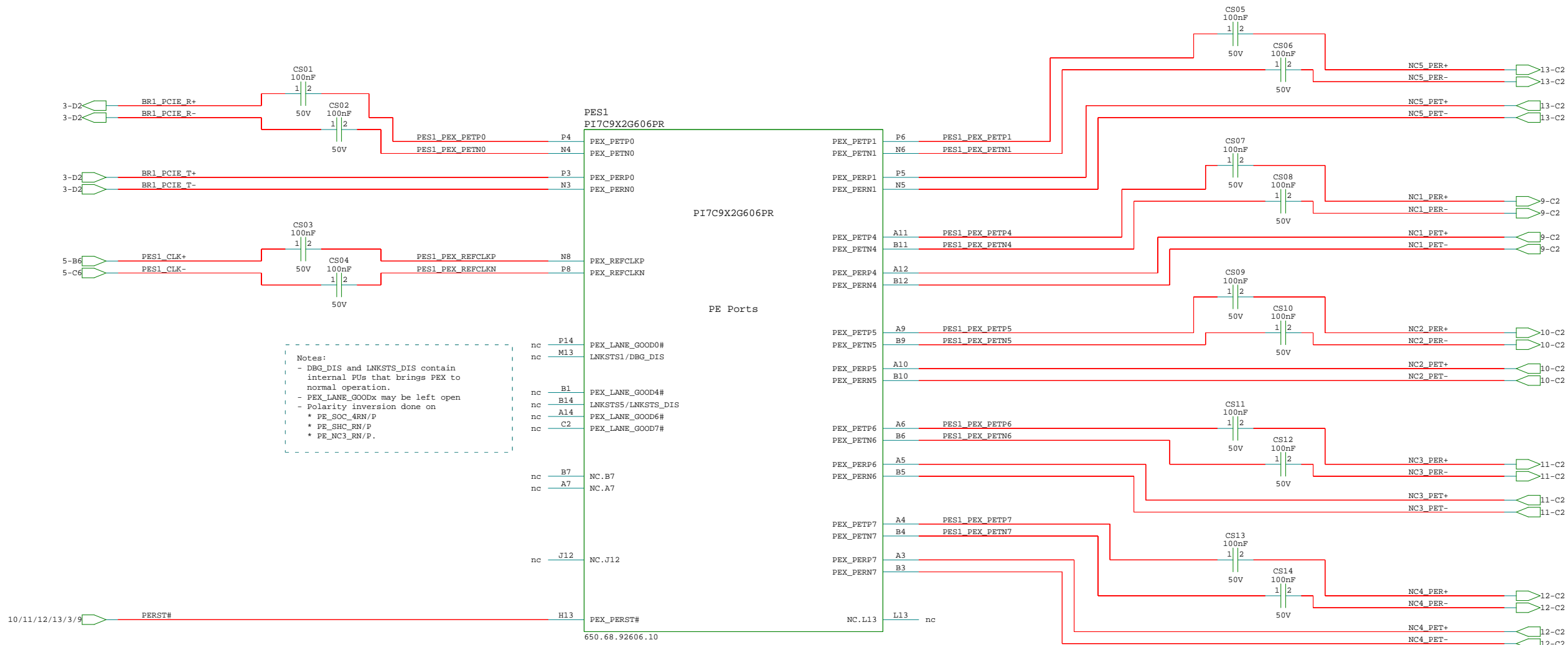
Board	CN8-REVERB	Rev.	1.0
Sheet Name	03: PCI to PCIe Bridge (1 of 2)		B
Date, Design	2022-04-06	wro/am	Sheet 3 of 16

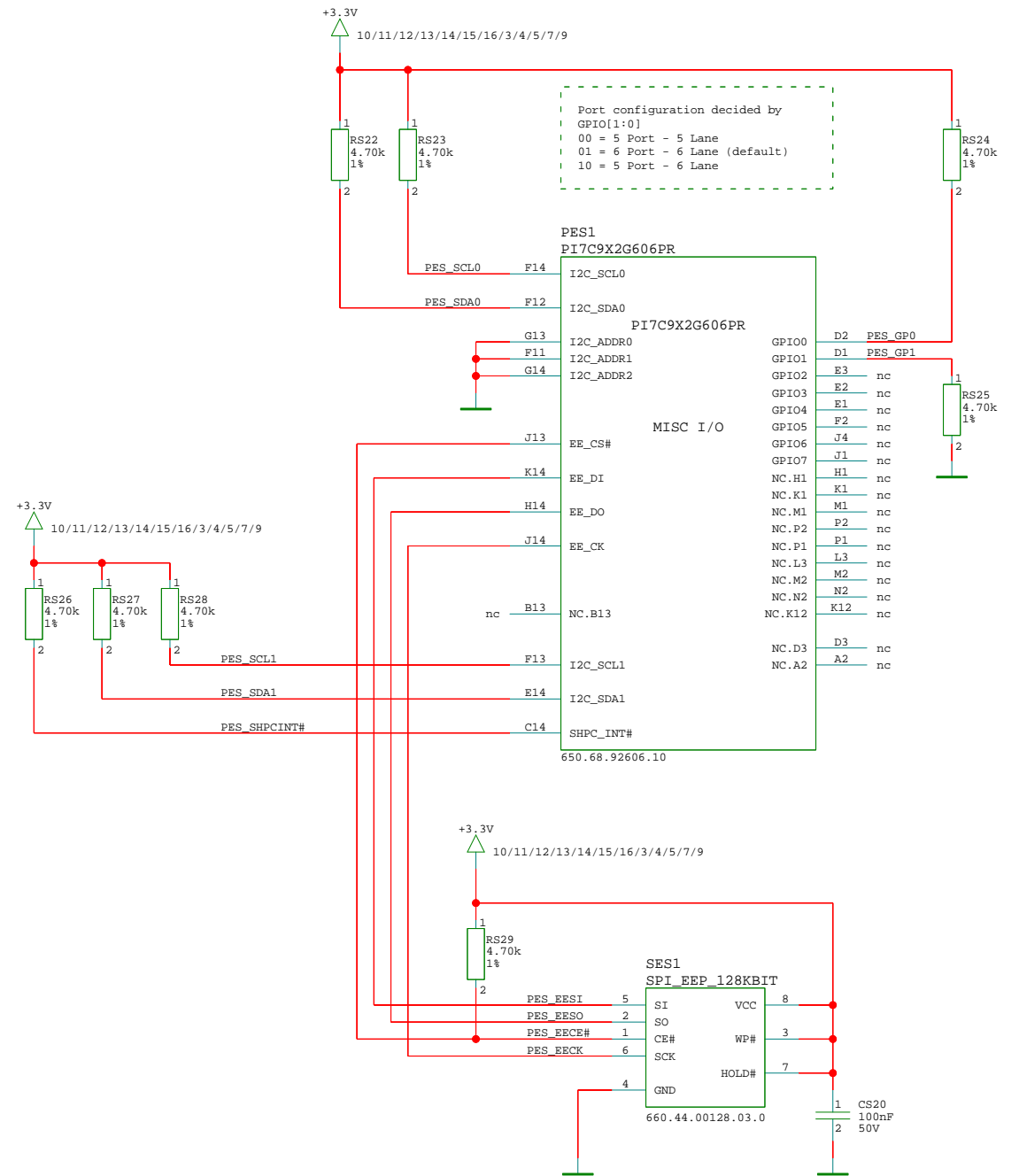
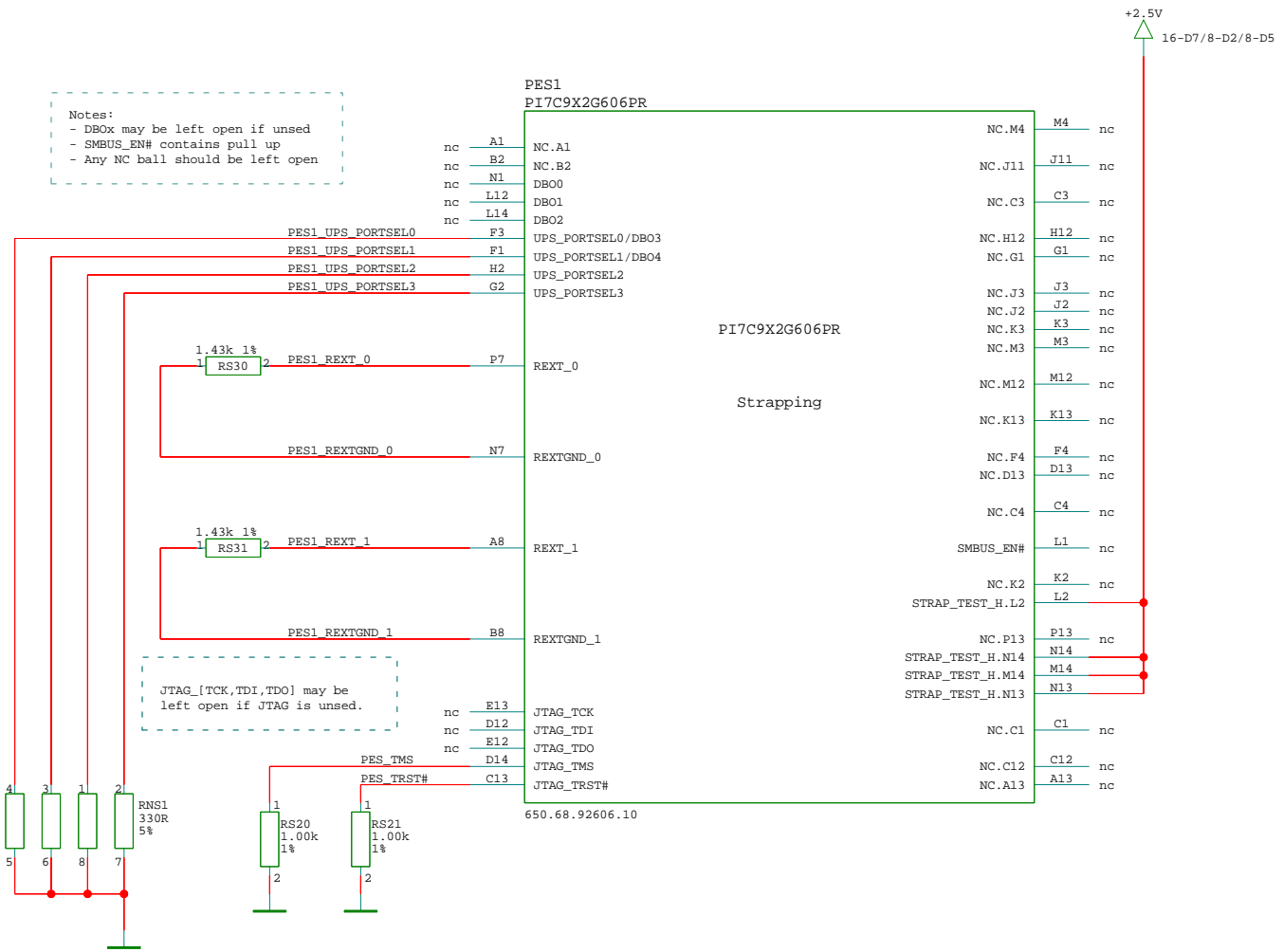


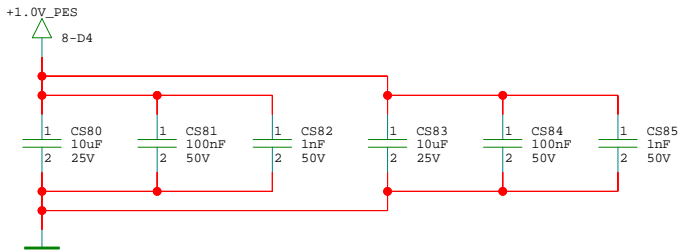
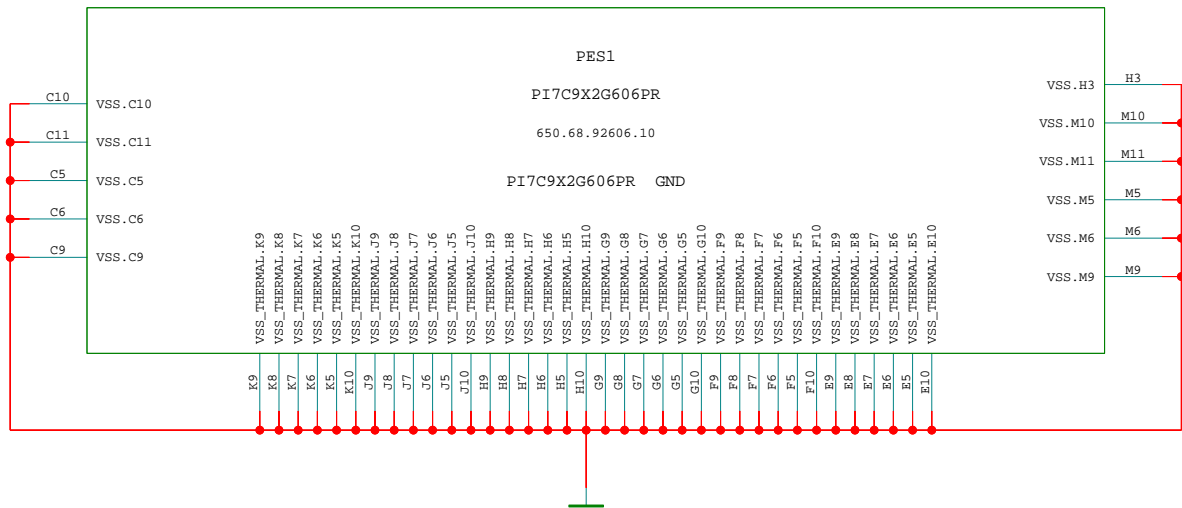
Board	CN8-REVERB	Rev.	0.0
Sheet Name	04: PCI to PCIe Bridge (2 of 2)		B
Date, Design	2021-01-18	wro	Sheet 4 of 16



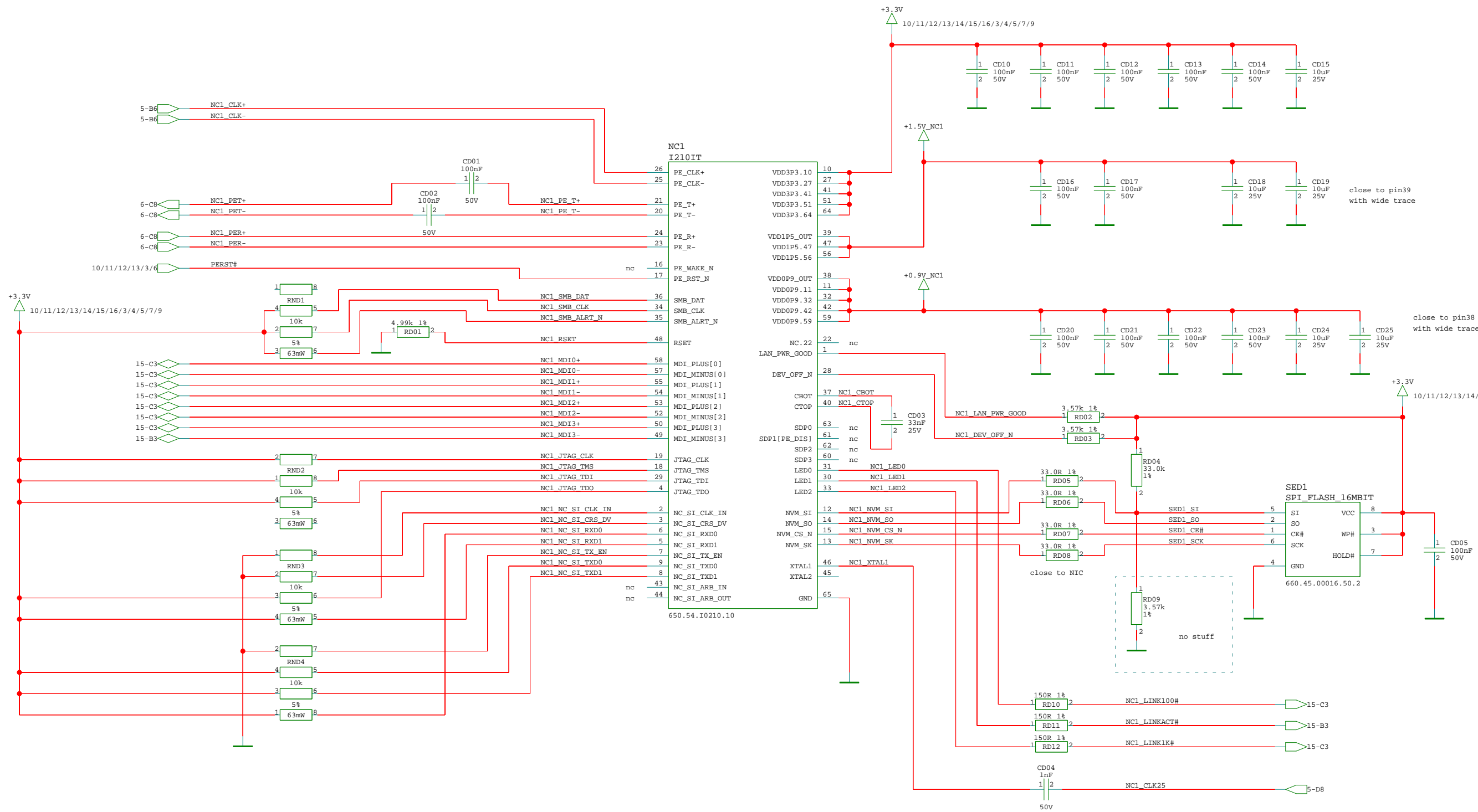
Board	CN8-REVERB	Rev.	0.0
Sheet Name	05: Clock Generator	C	
Date, Design	2021-01-18	wro	Sheet 5 of 16







Board	CN8-REVERB	Rev.	0.0
Sheet Name	08: PCIE SWITCH (3 of 3) S		
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Sheet Name	09: Ethernet Controller NIC1		D
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Board	CN8-REVERB		Rev. 0.0
Sheet Name	10: Ethernet Controller NIC2		E
Date, Design	2021-01-18	wro	Sheet 10 of 16



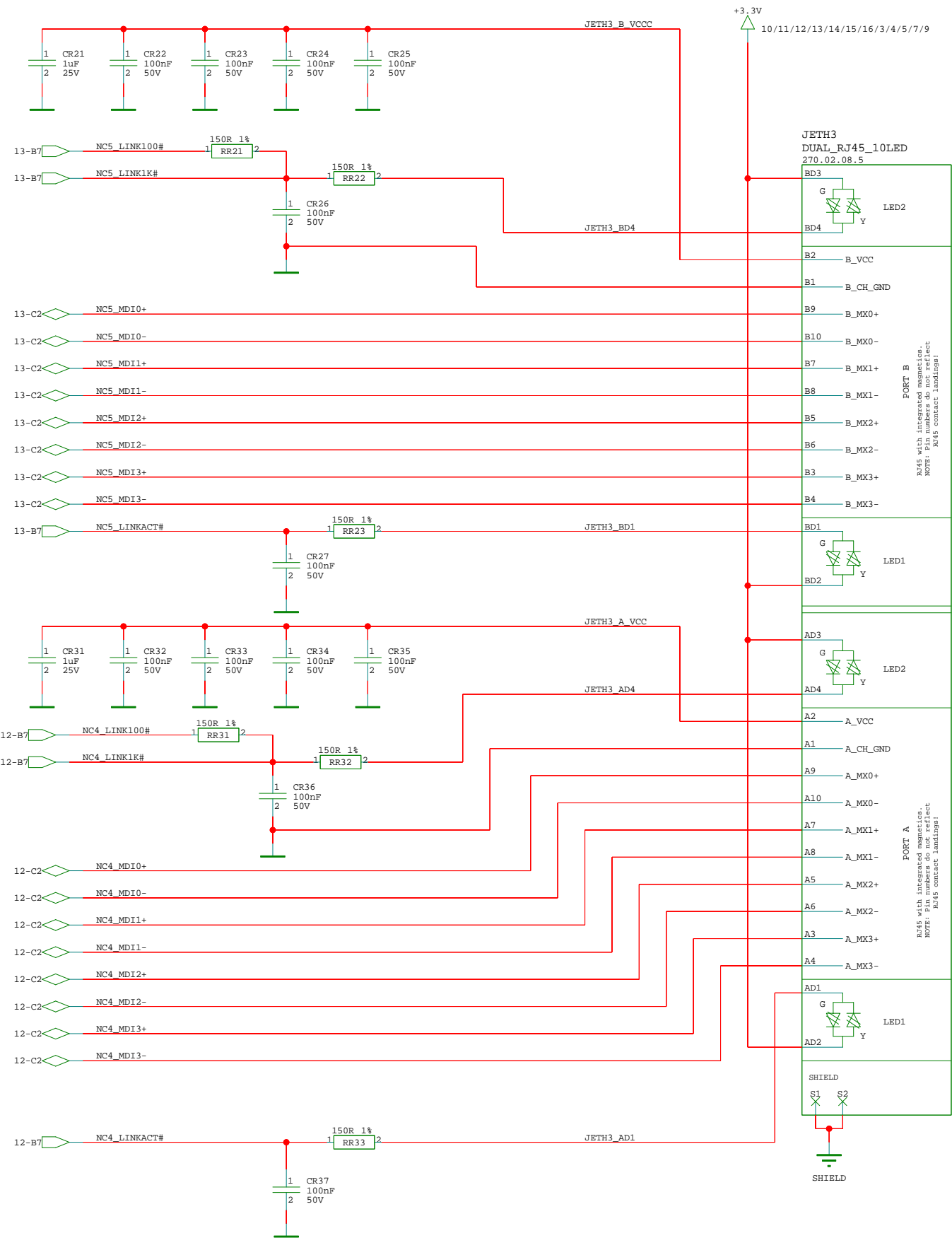
Board	CN8-REVERB		Rev. 0.0
Sheet Name	11: Ethernet Controller NIC3		F
Date, Design	2021-01-18	WRO	Sheet 11 of 16



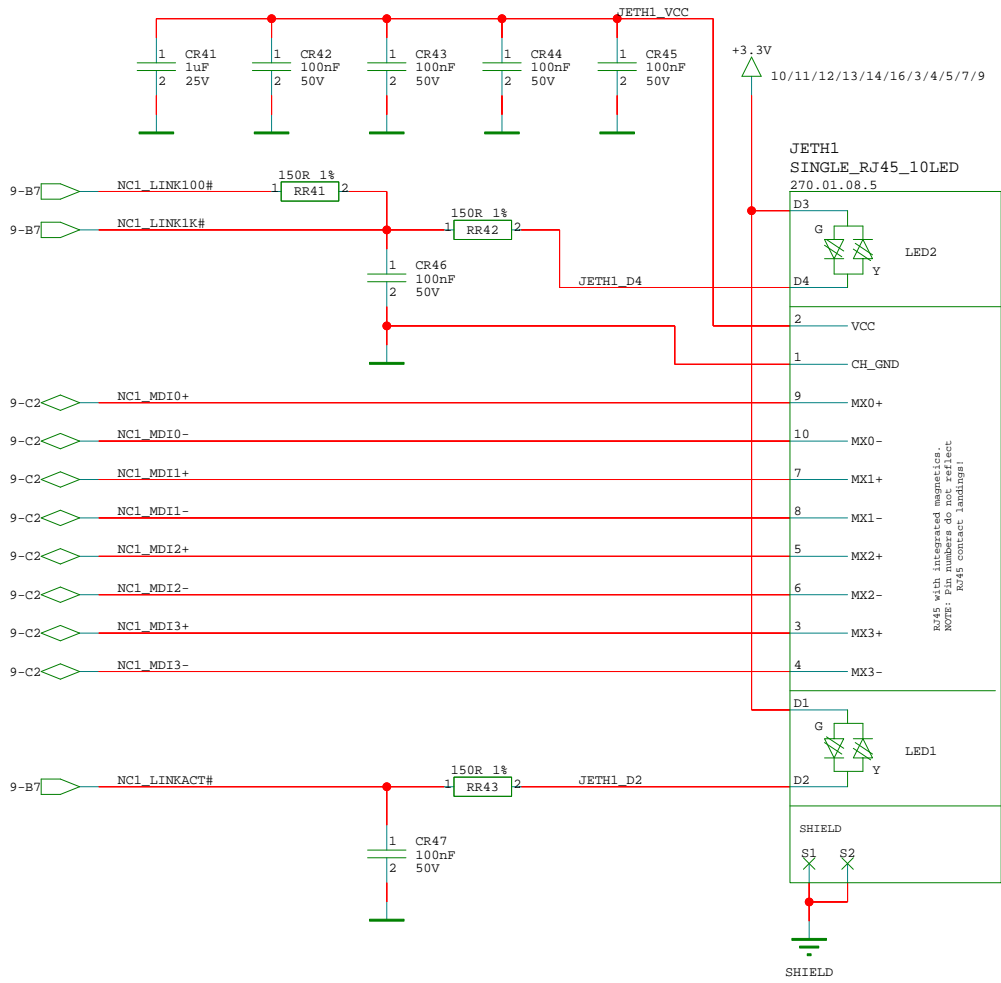
Board	CN8-REVERB	Rev. 0.0
Sheet Name	12: Ethernet Controller NIC4 G	
Date, Design	2021-01-18 wro	Sheet 12 of 16



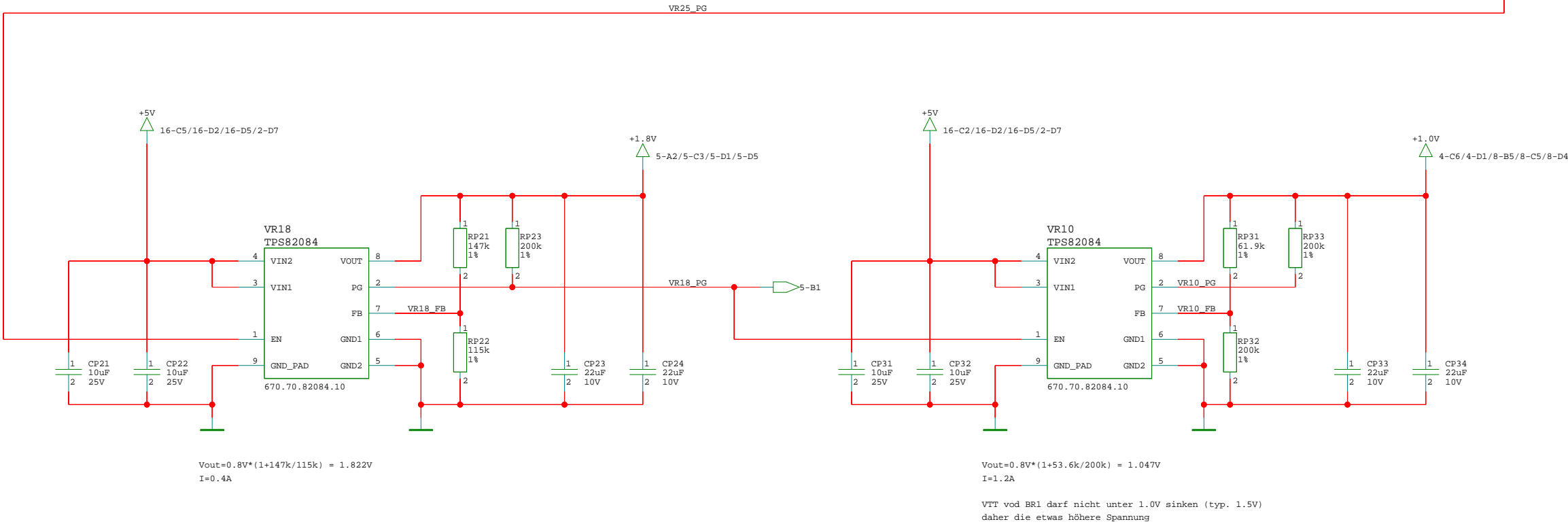
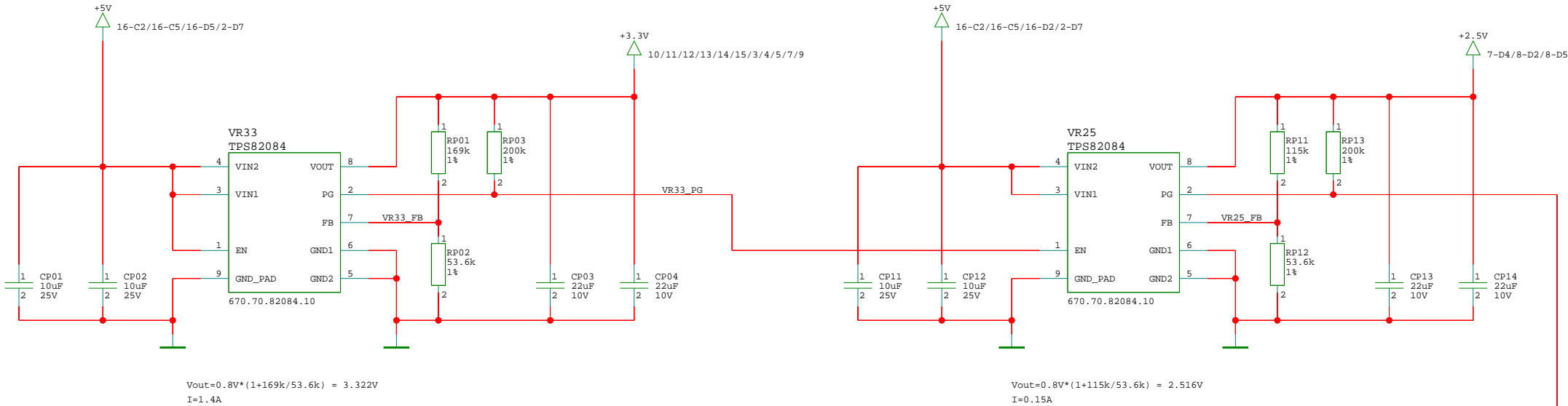
Board	CN8-REVERB			Rev. 0.0
Sheet Name	13: Ethernet Controller NIC5			H
Date, Design	2021-01-18	WRO	Sheet 13 of 16	



Board	CN8-REVERB	Rev. 1.0
Sheet Name	14: Ethernet Connectors R	
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Sheet Name	15: Ethernet Connectors		R
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Board	CN8-REVERB	Rev.	0.0
Sheet Name	16: Voltage Regulators	P	
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