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Rev.0.0	2016-08-04/CF

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Sheet Name	01: Contents	
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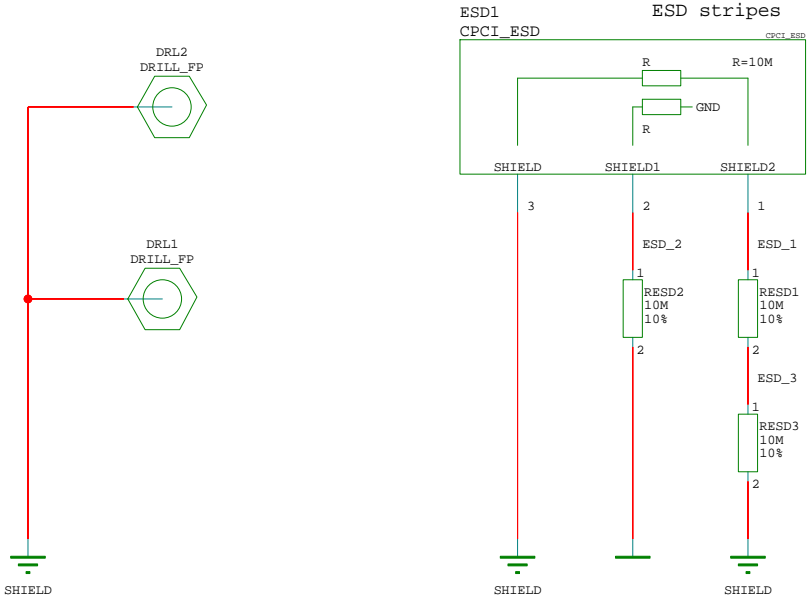
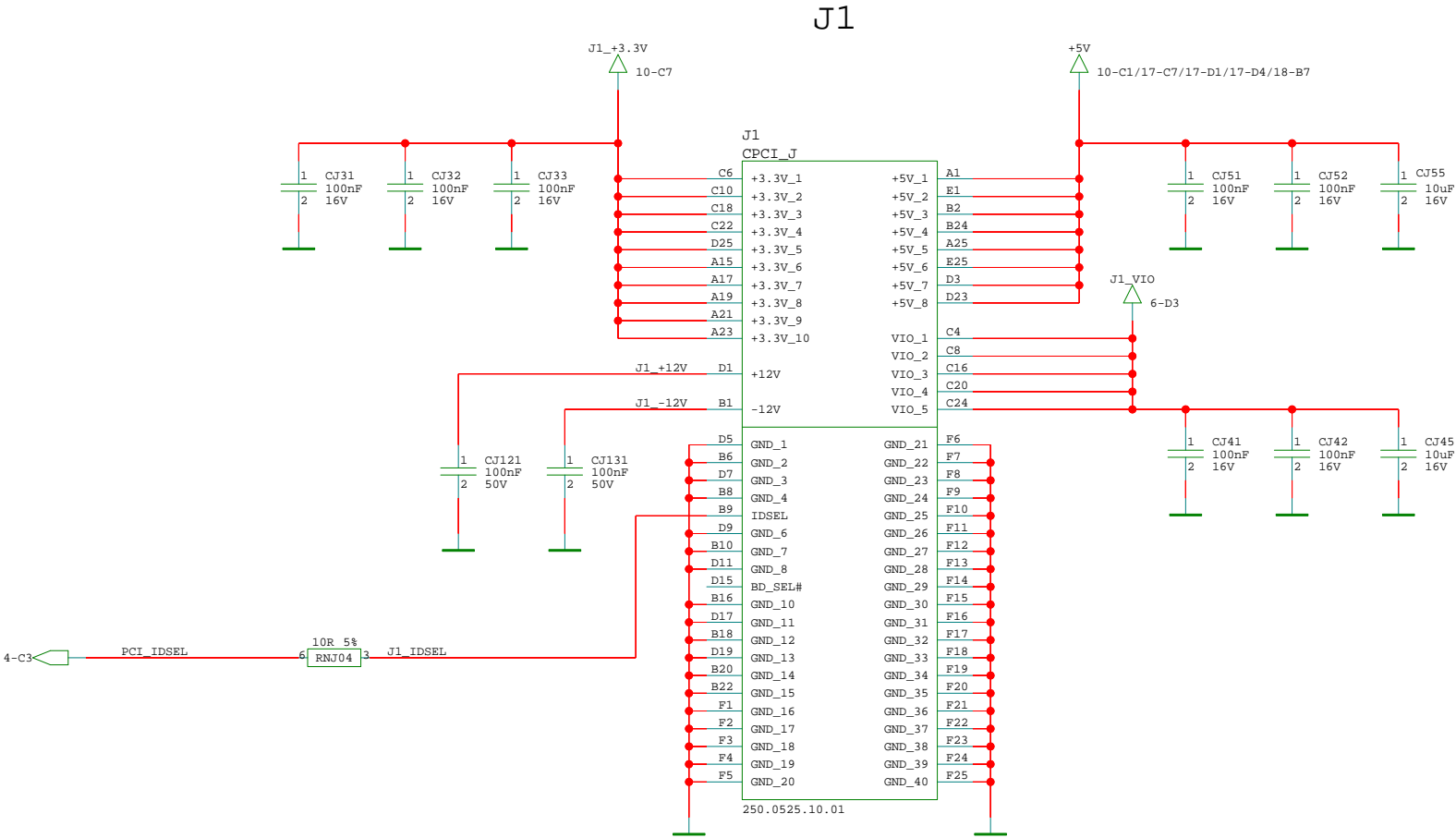
## J1



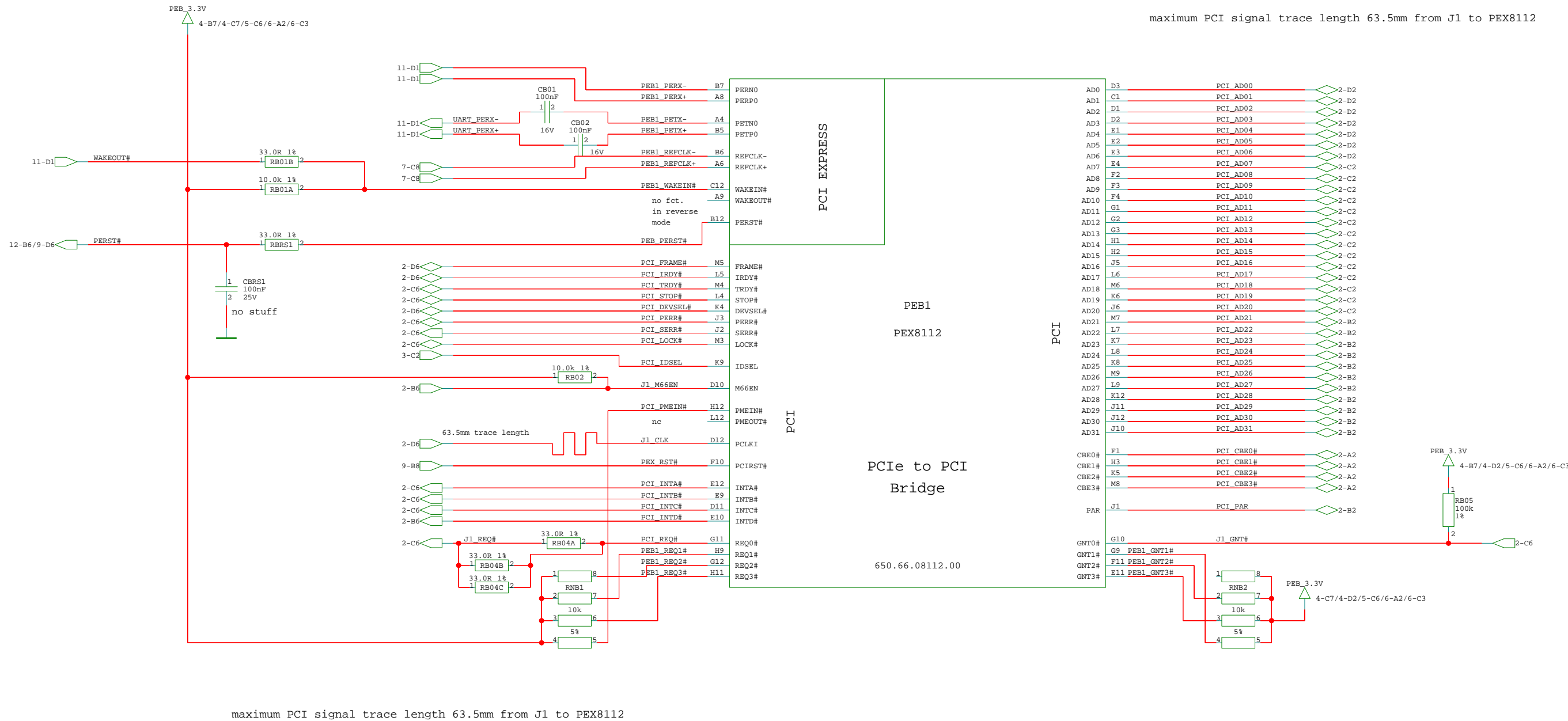
force 33MH  
if stuffed

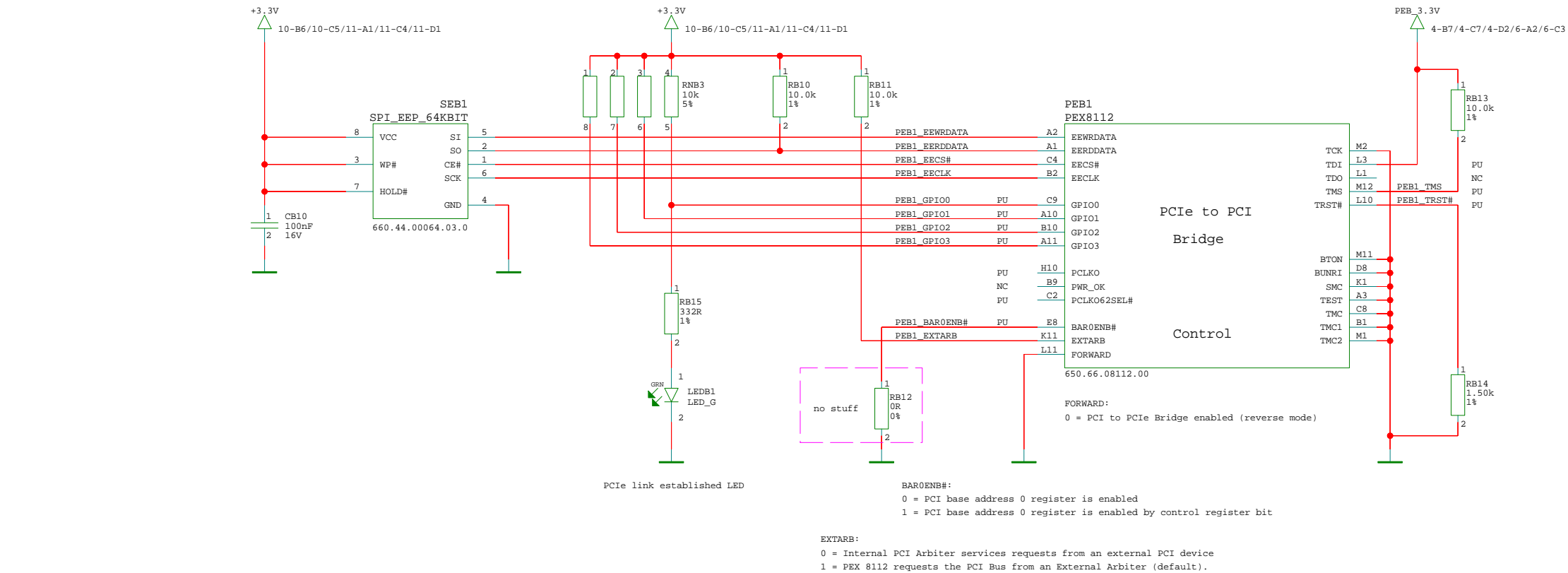
Board	CUE-BALLAD	Rev. 0.0
Sheet Name	02: J1 CompactPCI - PCI	
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optional straight +5V design:  
3.3V, +-12V from CPCI backplane not in use

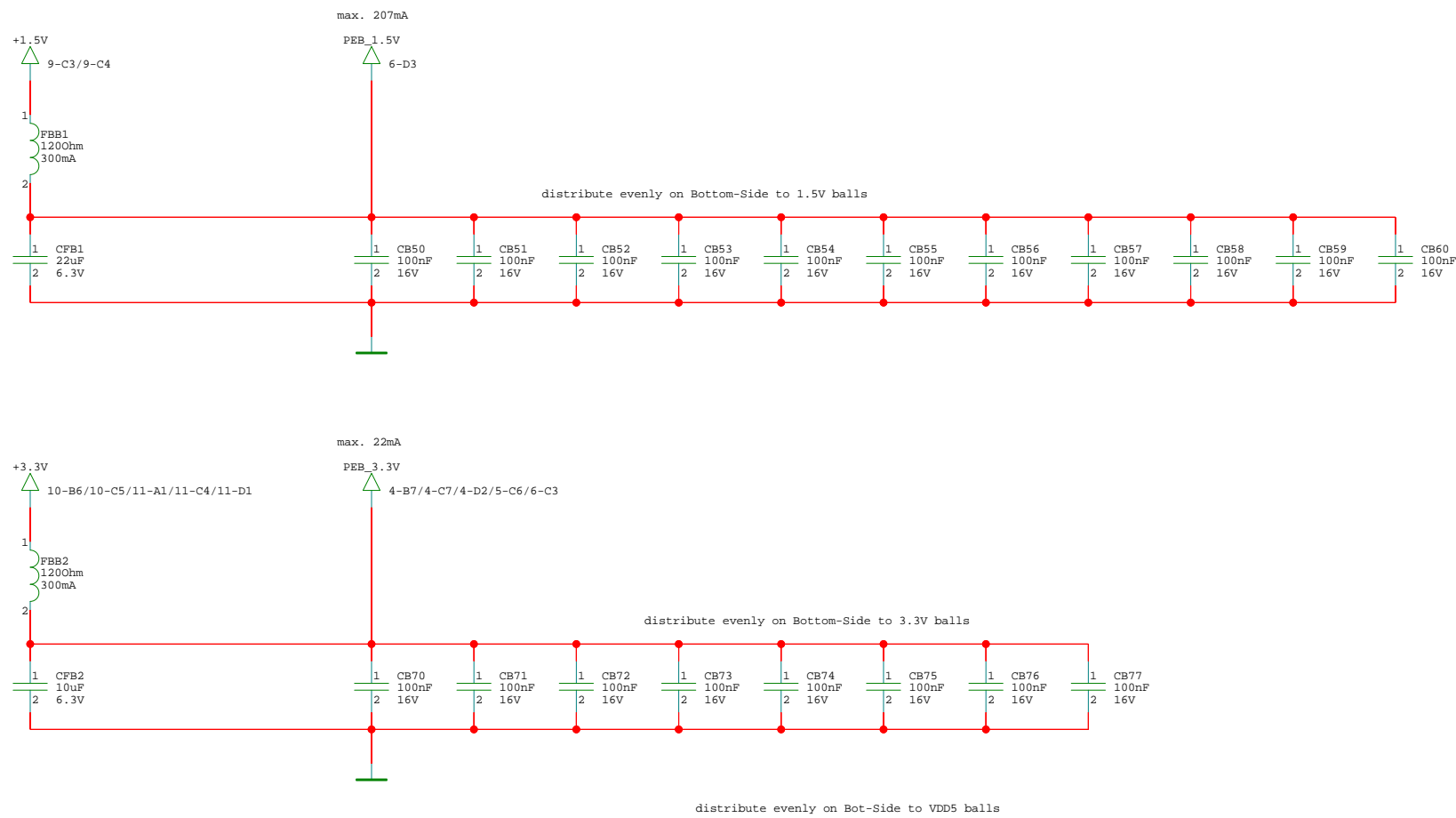


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Sheet Name	03: J1 CompactPCI - Power&GND		
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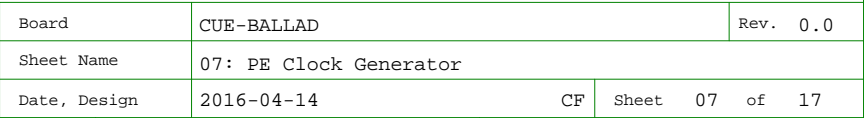


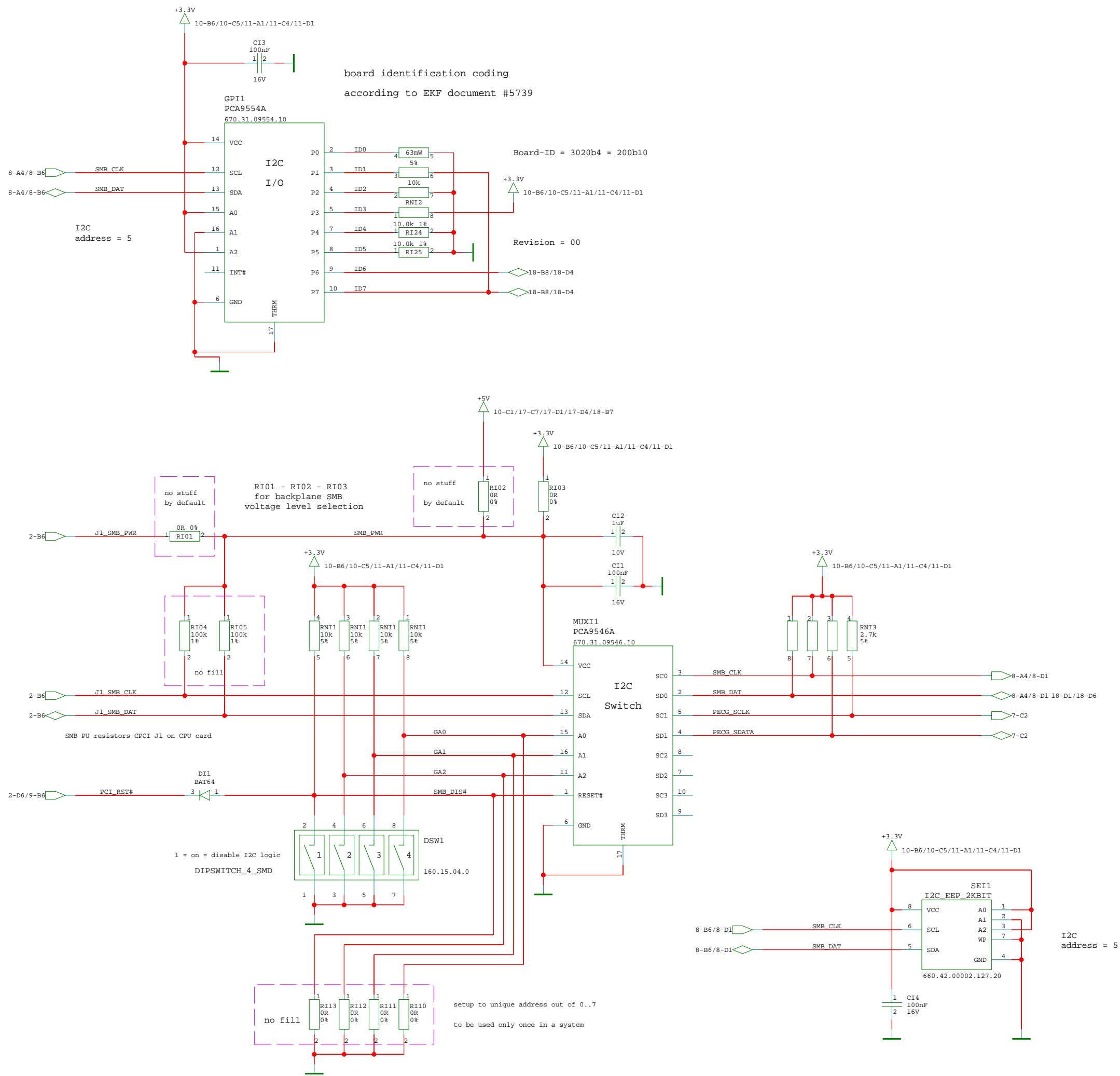


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Sheet Name	05: PE Brigde PEX8112 - Control		
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Sheet Name	06: PE Bridge PEX8112 - Power&GND	
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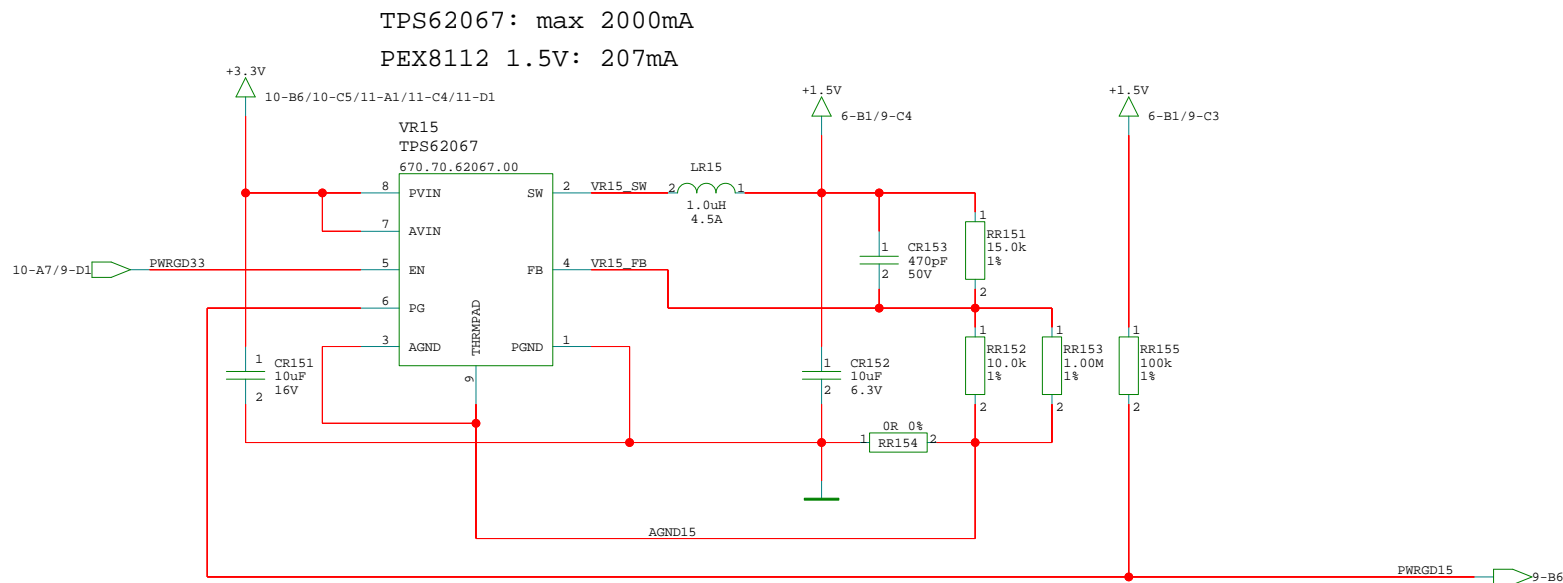
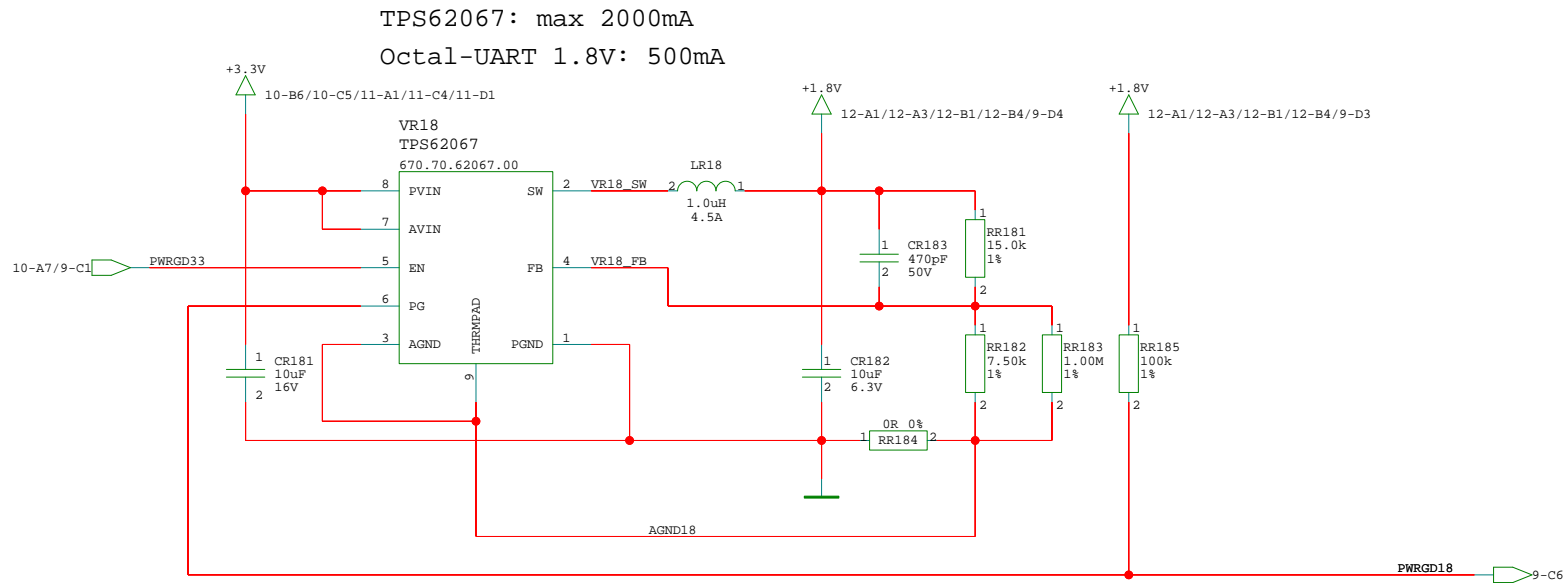




for board versions without  
Hardware ID & SMBus logic  
do not stuff all components  
on this scm sheet

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Sheet Name	08: I2C Logic		
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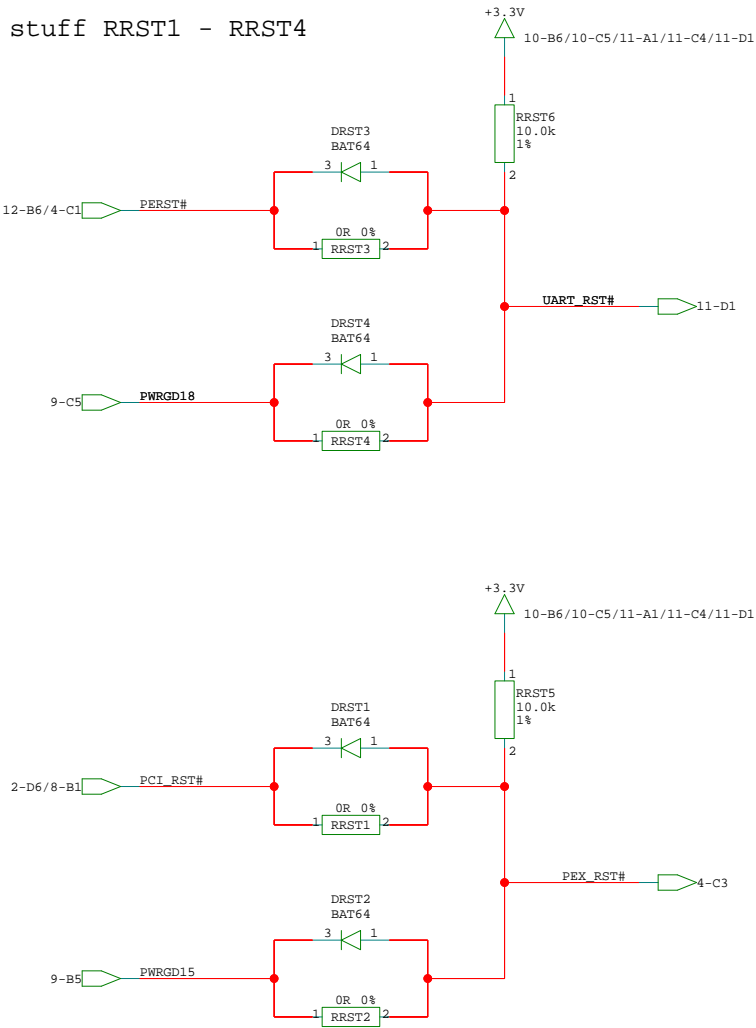
Power Sequencing PEX8112:

PWRGD33 => enVR15

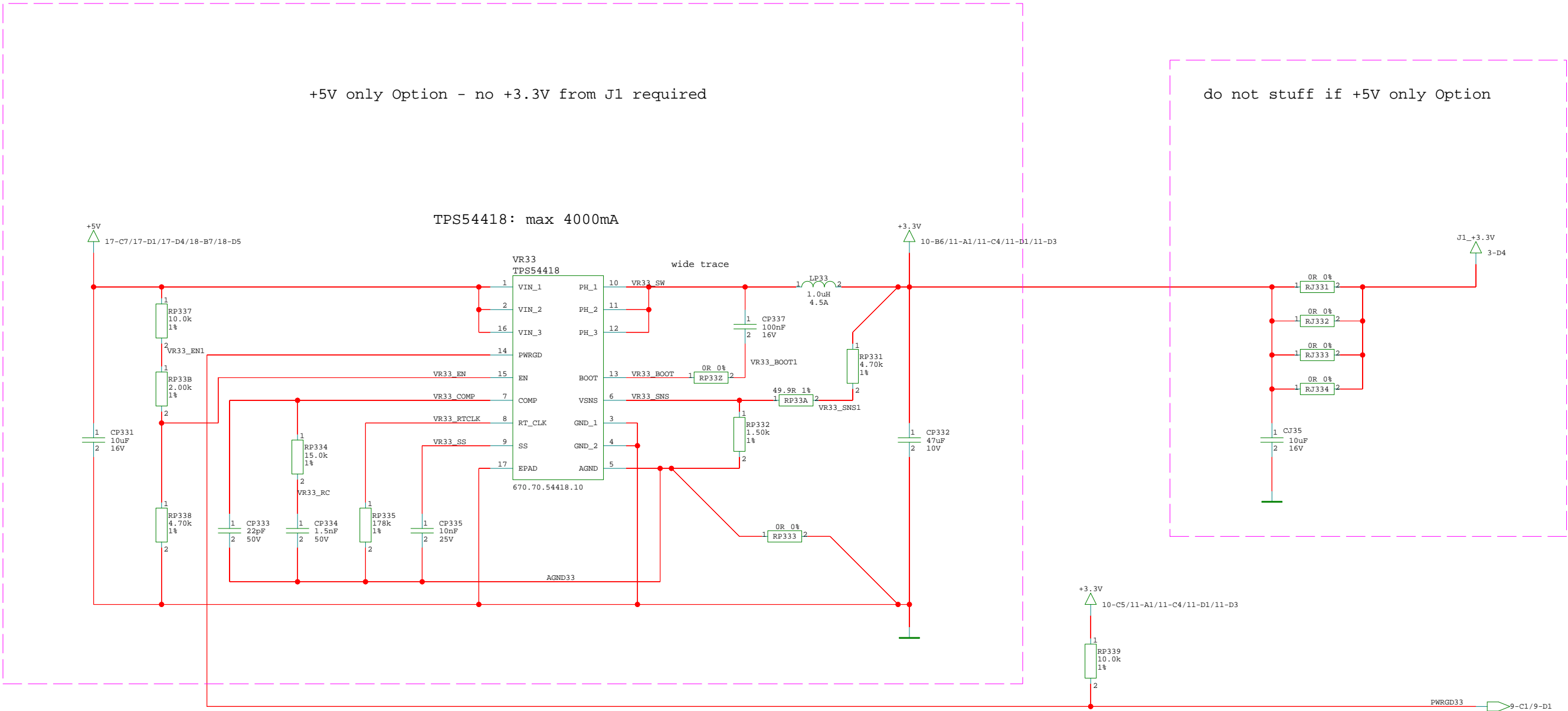
Power Sequencing UART:

PWRGD33 => enVR18

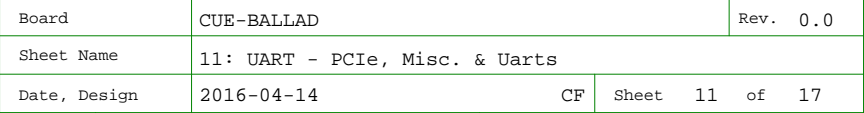
do not stuff RRST1 - RRST4

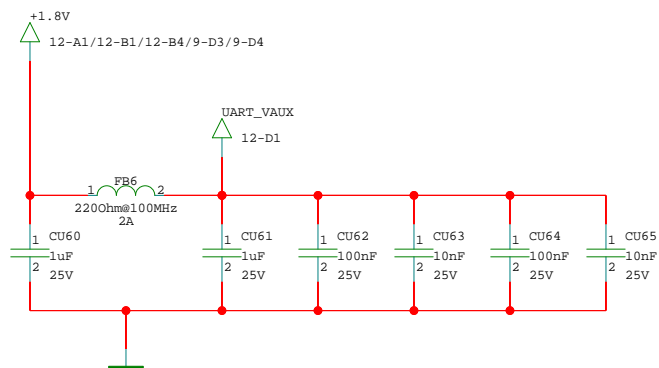


PEX8112_3.3V:	22mA
OSC_25MHz_3.3V:	2x8mA
ICS9194_3.3V:	150mA
UART_3.3V:	273mA
ADM2682_3.3V:	4x260mA
Logic_3.3V:	16x0.05mA
Logic_3.3V:	4x0.5mA
+1.5V/0.25A-DC/DC:	134mA
+1.8V/0.6A-DC/DC:	385mA
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Summe_3.3V:	2023mA

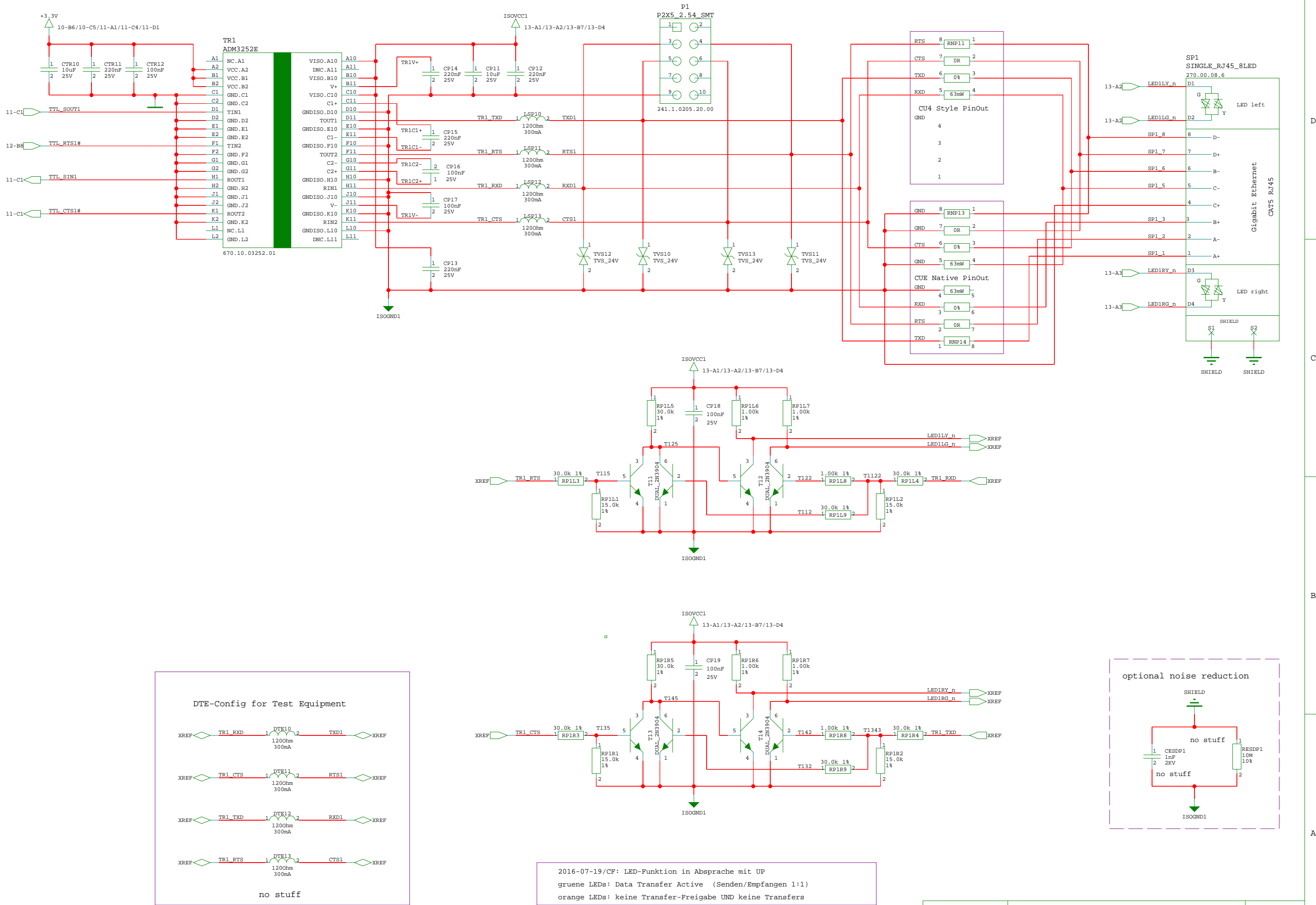


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Sheet Name	10: Single Power Supply Option		
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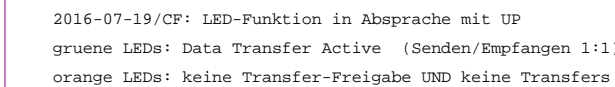




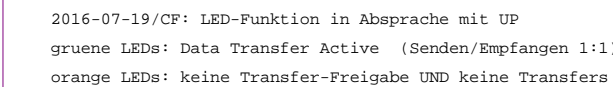
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Sheet Name	12: UART - Power	
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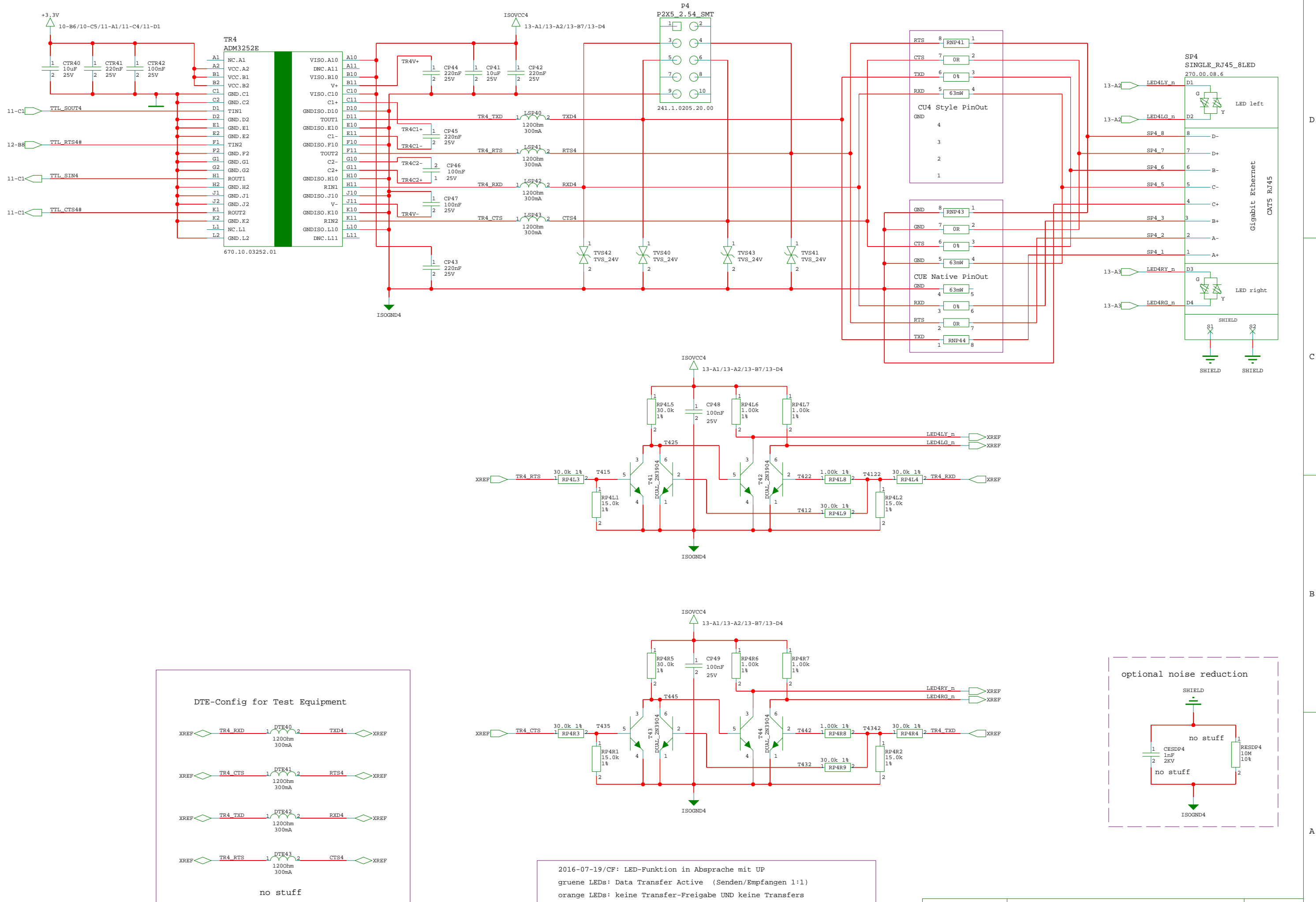
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Sheet Name	13: Serial Port 1		
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1	2	3	4	5	6	7	8
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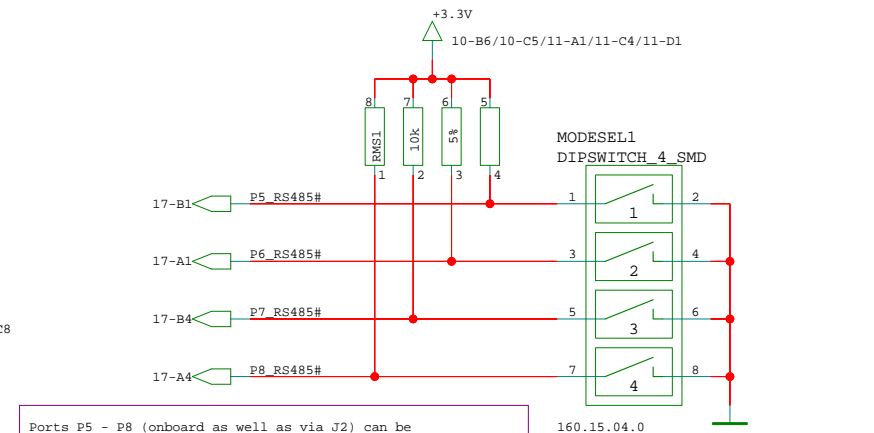
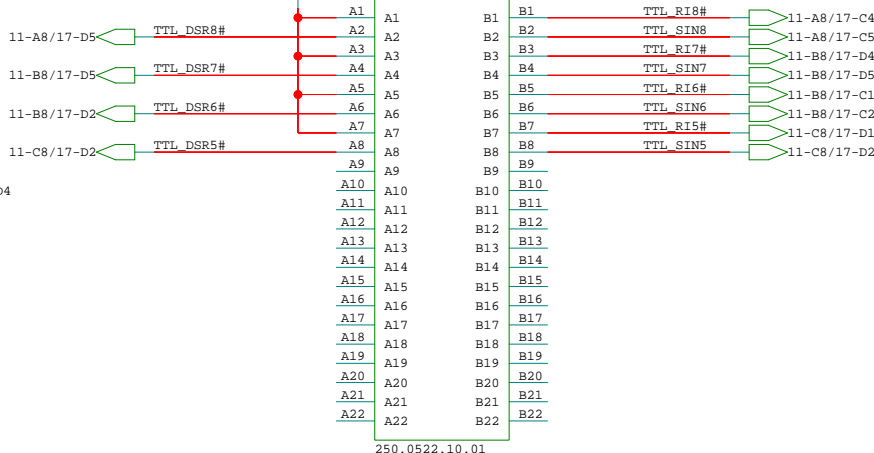
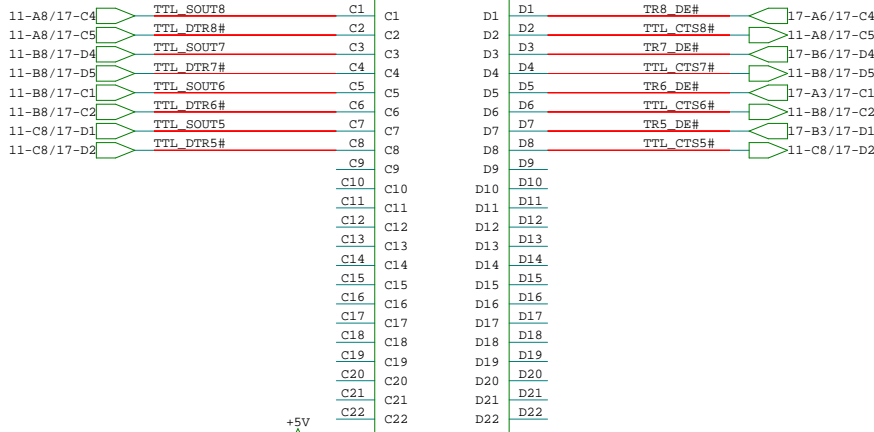
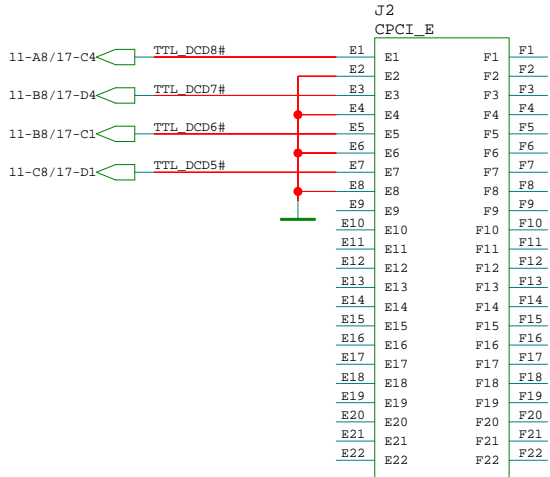
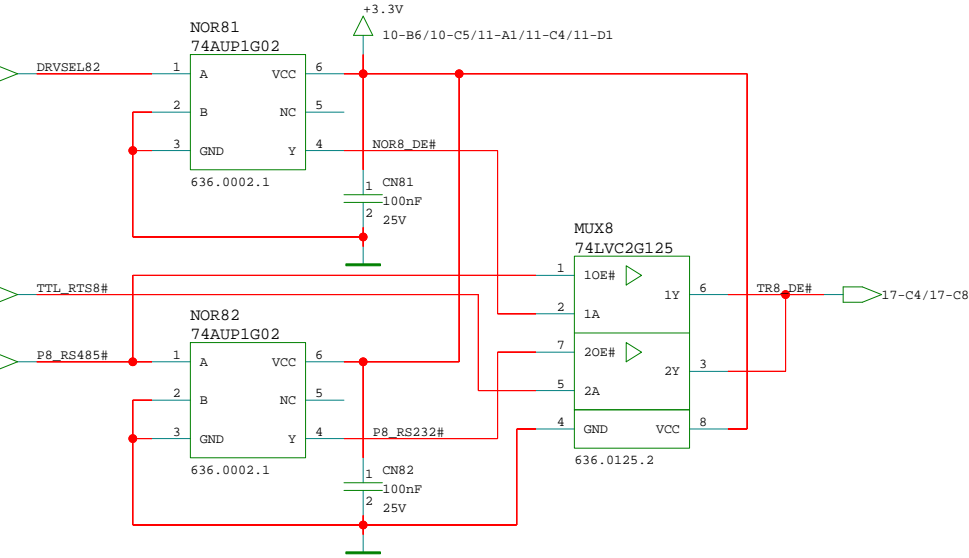
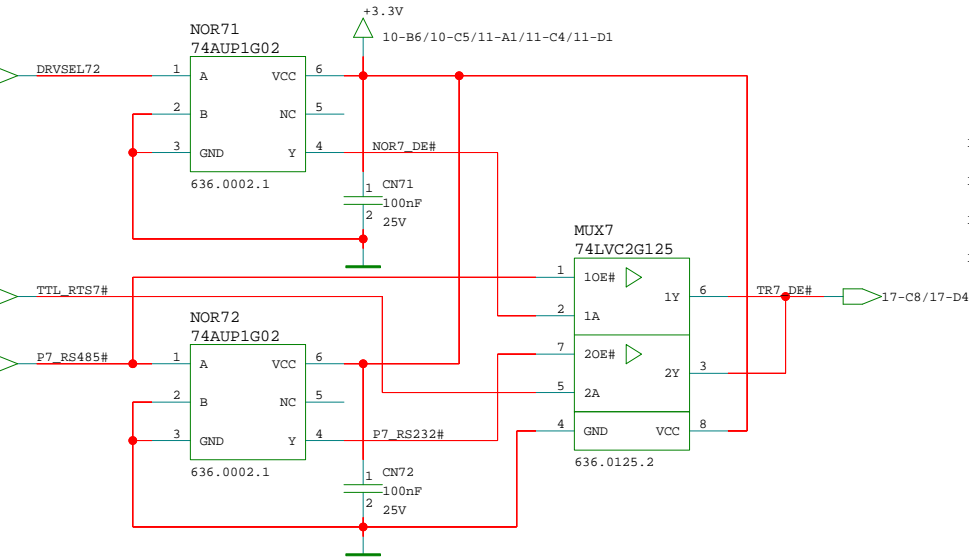
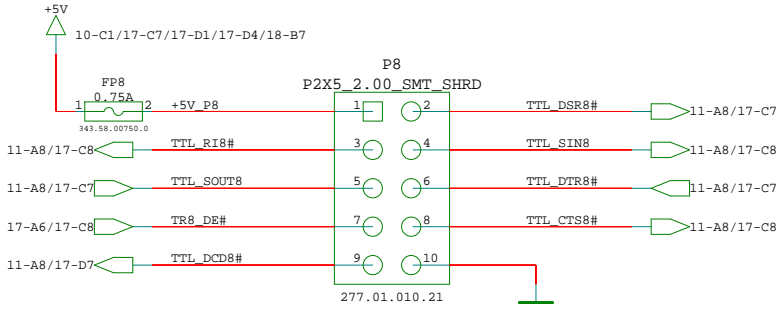
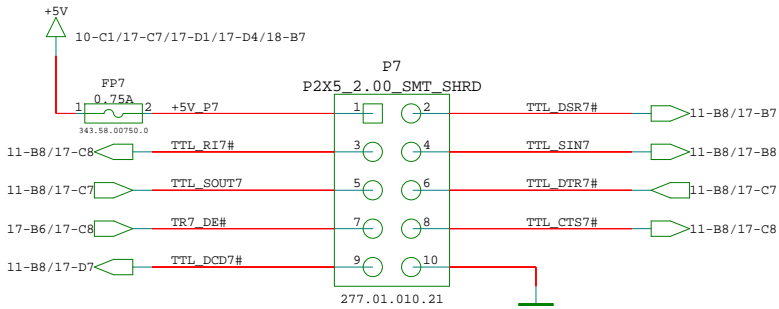
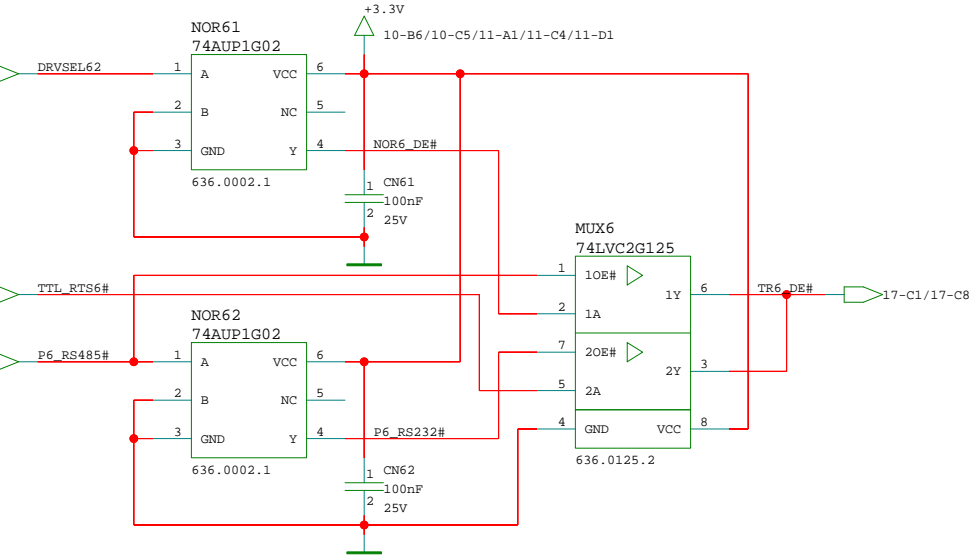
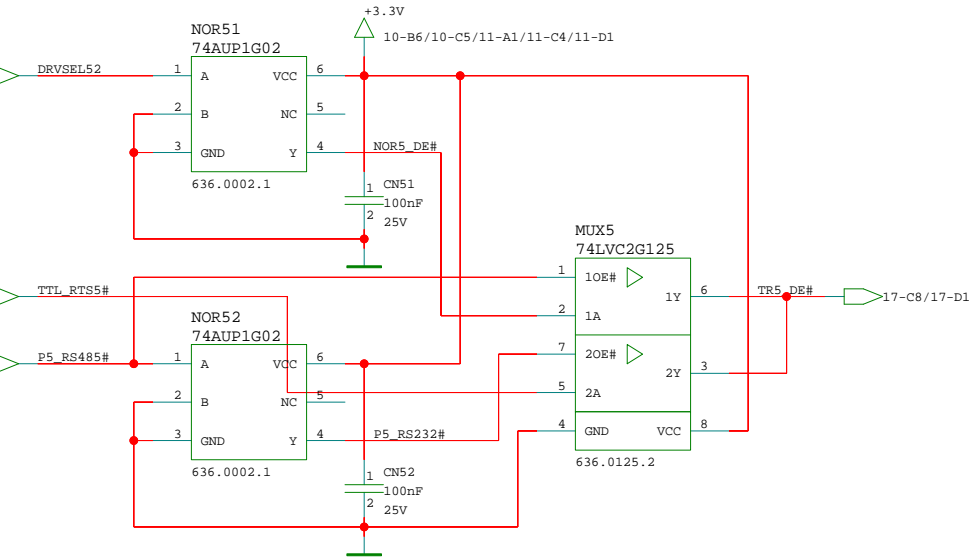
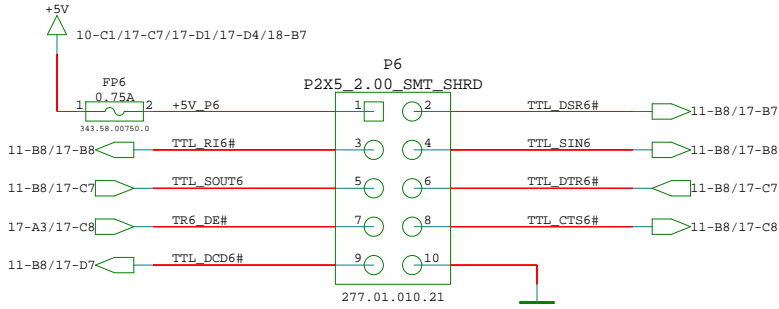
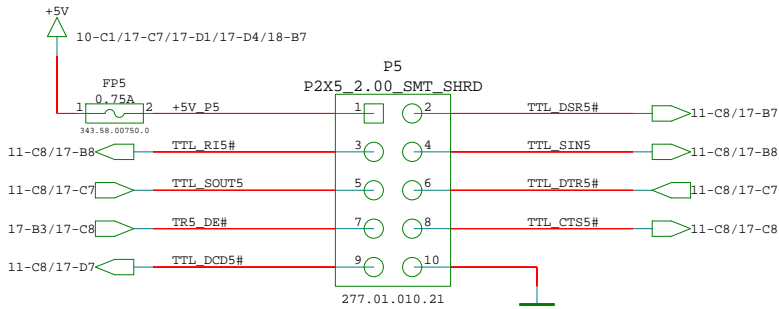


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Ports P5 - P8 (onboard as well as via J2) can be configured individually for RS485 (ON) or RS232 (OFF)

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