

Differential Pairs

Conductor Width (W)

0.16 mm

Conductor Spacing (S)

0.13 mm

Conductor Height (H)

0.196 mm

Applied Voltage

1 Volts

Coupled Length

2.54 mm

Signal Risetime

1 ns

Target Zdiff

100 Ohms

+/- Tolerance = 10%

Target Zdiff Plus

112.500 Ohms

Target Zdiff Minus

87.500 Ohms

Formula Restrictions:

$0.1 < W/H < 3.0$

$0.1 < S/H < 3.0$

Differential Protocol

PCIe Gen2

Send to Via Calculator

Zdifferential

98.919 Ohms

Zo

66.293 Ohms

Zodd

49.459 Ohms

Zeven

88.856 Ohms

? Help

Kb (Term)

0.1454

dB

-16.747

NEXT Voltage

0.0044 V

Kb' (Unterm)

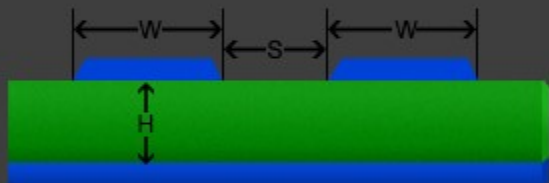
0.2848

dB

-10.908

NEXT Voltage

0.0087 V



Options

Base Copper Weight

- ☐ 9um
- ☒ 18um
- ☐ 35um
- ☐ 53um
- ☐ 70um
- ☐ 88um
- ☐ 106um
- ☐ 142um
- ☐ 178um

Plating Thickness

- ☐ Bare PCB
- ☐ 18um
- ☒ 35um
- ☐ 53um
- ☐ 70um
- ☐ 88um
- ☐ 106um

Differential Layer

- ☒ Edge Cpld Ext
- ☐ Edge Cpld Int Sym
- ☐ Edge Cpld Int Asym
- ☐ Edge Cpld Embed
- ☐ Broad Cpld Shld
- ☐ Broad Cpld NShld

Information

Total Copper Thickness
53 um

W/H = 0.816

S/H = 0.663

Lsat = 83.06 mm



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