2023 Digital IC Design Homework 5

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| NAME |  | | | |
| Student ID |  | | | |
| **Simulation Result** | | | | |
| Functional simulation | | Completed / Fail | Gate-level simulation | Completed / Fail |
| (your functional sim result) | | | (your gate-level sim result) | |
| **Evaluation Results** | | | | |
| test1.png | |  | test2.png |  |
| test3.png | |  | test4.png |  |
| test5.png | |  | test6.png |  |
| **Description of your design** | | | | |
|  | | | | |

*Scoring = average PSNR of the six test images*

**\* PSNR of all interpolation results should meet at least the baseline.**