

## Intel 64 & IA-32 架构

### 1. CR0 控制处理器的工作模式和分页

*enc pawn temp 加密局典当临时工*

PE protected mode enable  
MP monitor coprocessor  
EM emulation  
TS task switched  
PG paging  
AM alignment mask  
WP write protect  
NW not-write through  
ET extension type  
NE numeric error  
CD cache disable

### 2. CR4 处理器在保护模式下的行为

*VMOSX 虚拟机操作系统扩展*

VME virtual 8086 mode extensions  
VMXE virtual machine extension enable  
OSFXSR operating system support for FXSAVE and FXRSTOR instructions  
OSXMMEXCEPT operating system support for unmasked SIMD floating-point exceptions  
OSXSAVE xsave and processor extended state enable

*GC sack P 垃圾回收器抢夺优先权*

PGE page global enabled  
PCID PCID enable  
PSE page size extension  
PAE physical address extension  
PCE performance-monitoring counter enable  
PKE protection key enable

*tmfd smaxe 他爸妈的 sm 斧子*

TSD time stamp disable  
MCE machine check exception  
DE debugging extensions  
FSGSBASE enables the instructions RDFSBASE \*  
WRFSBASE \*  
SMAP supervisor mode access prevention enable  
SMXE safer mode extensions enable  
SMEP supervisor mode execution prevention enable

### 3. EFER 扩展特性使能寄存器

*LMSEA 爱情海(Love Marriage sea)*

LMSLE long mode segment limit enable  
LME long mode enable

LMA long mode active

*f-scent 腐败味道*

SCE system call extension  
SVME secure virtual machine enable  
NXE No-execute enable  
TCE translation cache extension  
FFXSR fast fxsave and fxstore