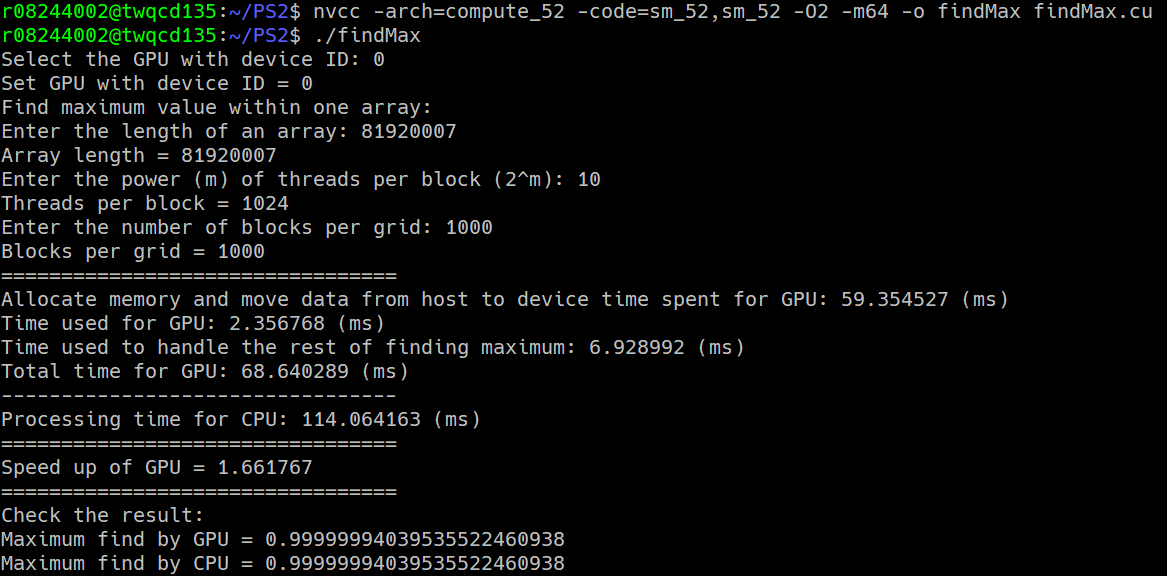
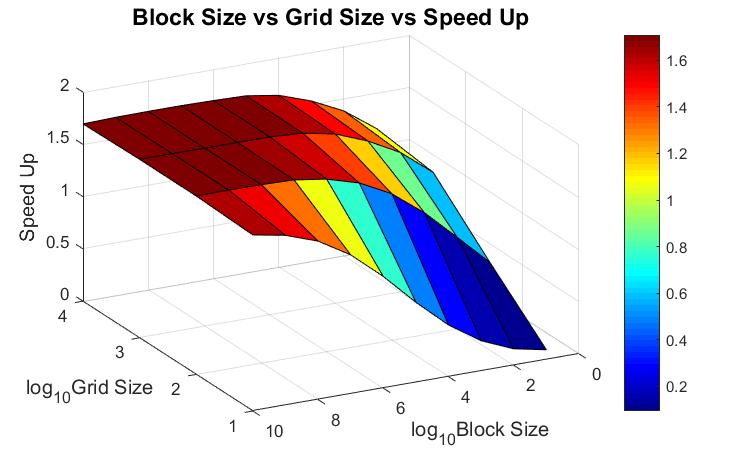
**CUDA Parallel Programming Problem Set2**

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* **Finding maximum value in an array**

1. **Result**

Slightly change the code from *vecAdd.cu*, and save it as *findMax.cu*. Which gives:

In order to optimize the block size and grid size, loop through block size from and grid size from , save the code in *findMax\_Optimize.cu*. Then plot the output with MATLAB.

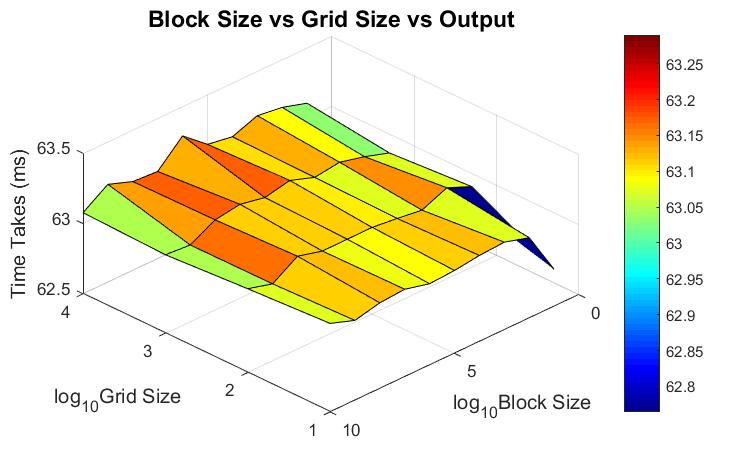
1. **Discussion**
2. From the figure, we can see that GPU with proper block size and grid size greatly speed up the process. The bigger the grid size and block size are, the more it speeds up.

There are some causes that affect this result, discuss in later section:

|  |  |  |  |
| --- | --- | --- | --- |
| ↑：the more ↓：the less | | ↑：faster ↓：slower | |
| Grid size | ↓ | Output | ↑ |
| Block size | ↑ | Parallel reduction | ↓ |
| Block size × Grid size | ↑ | Loop through all the array | ↑ |

1. From the point of view of moving data between host and device, the input data is basically the same for all of them, but the output data length is depending on the grid size. The bigger the grid size is, the slower it is.

We can see that it slightly tilts according the grid size, but it doesn’t affect the efficiency that much.



1. From the point of view of processing parallel reduction, the bigger the block size is, the more time it takes with the same grid size. But from the point of view of looping through all the elements in the array, the more the threads (block size × grid size) are, the less time it needs to loop through, which means faster.

Since looping through all the elements require accessing the global memory, and parallel reduction requires accessing the shared memory, the looping time dominant the results.

So we can see that with the same grid size, the more the block size is, the less time it takes.

