6872 Foundations of Electronics Lecture 11: Operational Amplifier (Op-Amp)

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v. 2020

Abstract

- ► Op-Amp Basics
- ► Circuits
 - ► Inverting Amplifier
 - ► Summing Amplifier
 - ► Non-Inverting Amplifier
 - ► Unit Gain Amplifiers
 - ► Integrator
 - Differentiator
- ► Op-Amp Specifications
- ► Application Examples

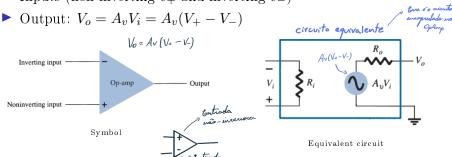
Servores agricolas on que eniceis mento bencos san grado, pora amplificar bo.

Op Amp - Idealijado para ter amplificação ideal: - ganho infinito - banda de amplificação infinita - injedância de entrada infinita lo Não comone potincia da fonte lo Sunciona em beirmina, accounto - impedâncio de soido igual a zero

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Op-Amp Basics

- ▶ Op-Amp is an ideal amplifier having:
 - Infinite (open-loop) voltage gain A_v (actual: few thousands to tens of thousands)
 - ► Infinite bandwidth (actual: MHz to tens of MHz)
 - Infinite input impedance R_i (actual: few megohms)
 - **>** Zero output impedance R_o (actual: less than 100 Ω)
- ▶ Inputs (non-inverting v_+ and inverting v_-)



inversora

Inverting Amplifier - Amplificator inversor

Assuming $R_i = \infty$ and $R_o = 0$:

$$i = \frac{\frac{V_i}{V_1 - V_-}}{R_1} = -\frac{V_o - V_-}{R_f} \quad \underbrace{\frac{V_i - V_-}{R_f}}_{if} + \underbrace{\frac{V_o - V_-}{R_f}}_{if} = 0$$

$$V_o = A_v(V_+ - V_-) = -A_vV_- \to V_- = -\frac{V_o}{A_v}$$



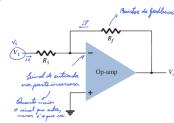
$$V_{-} = -\frac{V_{o}}{A_{-}} \approx 0$$
 (virtual ground)

An o gambo de terrão

$$i = \frac{V_1}{R_1} = -\frac{V_o}{R_f}$$

$$V_{-}=-rac{1}{A_{v}}pprox 0 ext{ (virtual ground)}$$
 $i=rac{V_{1}}{R_{1}}=-rac{V_{o}}{R_{f}}$
Lo Gruendo Vo tende azero.

 $V_{o}=-rac{R_{f}}{R_{1}}V_{1}$



Output voltage:

$$V_o = -\frac{R_f}{R_1} V_1$$

Example: If $R_1 = 100 \text{ k}\Omega$ and $R_f = 500 \text{ k}\Omega$ in the above figure, what output voltage results for an input of $V_1 = 2 \text{ V}$?

$$V_o = -\frac{R_f}{R_1}V_1 = -\frac{500\text{k}}{100\text{k}} \times 2 = -10 \text{ V}$$

Summing Amplifier - Muphificador sourador

Assuming
$$R_i = \infty$$
 and $R_o = 0$:
$$i = \frac{V_1 - V_-}{R_1} + \dots + \frac{V_n - V_-}{R_n} = -\frac{V_o - V_-}{R_f} \underbrace{\downarrow \begin{matrix} V_i & V_i \\ \downarrow & \downarrow & V_i \end{matrix}}_{polyage of substitute of the experiment of the experime$$

▶ If $A_n \gg 1$

$$\frac{V_0 - V_-}{R_B} = \frac{V_0 - V_-}{R_1} + \dots + \frac{V_0 - V_-}{R_0}$$

$$\text{Dund} \qquad V_0 = -R_5 \left(\sum_{i=1}^{N} V_{i,i-1} \right)$$

Output voltage:

$$V_{-}=-rac{V_{o}}{A_{v}}pprox 0 ext{ (virtual ground)}$$

$$i = \frac{V_1}{R_1} + \dots + \frac{V_n}{R_n} = -\frac{V_o}{R_n}$$

$$V_o = -R_f \left(\frac{V_1}{R_1} + \dots + \frac{V_n}{R_n} \right)$$

Example: If $R_f = 1 \text{ M}\Omega$ and $V_1 = -2 \text{ V}$, $V_2 = +3 \text{ V}$, $V_3 = +1 \text{ V}$, $R_1 = 200 \text{ k}\Omega$, $R_2 = 500 \text{ k}\Omega$, $R_3 = 1 \text{ M}\Omega$ in the above figure, what is the output voltage?

$$V_o = -R_f \left(\frac{V_1}{R_1} + \frac{V_2}{R_2} + \frac{V_3}{R_3} \right) = -1M \left(\frac{-2}{200k} + \frac{3}{500k} + \frac{1}{1M} \right) = 3 \text{ V}$$

Non-Inverting Amplifier

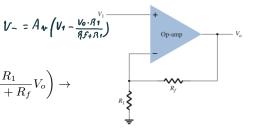
Amp. vão - inversor

Assuming $R_i = \infty$ and $R_o = 0$:

$$V_+ = V_1 \text{ and } V_- = \frac{R_1}{R_1 + R_f} V_o \label{eq:Vpotential}$$

$$V_o = A_v(V_+ - V_-) = A_v \left(V_1 - \frac{R_1}{R_1 + R_f} V_o \right) \to$$

$$V_o = \frac{V_1}{1/A_v + R_1/(R_1 + R_f)}$$



▶ If $A_v \gg 1$

$$V_o = \frac{R_1 + R_f}{R_1} V_1$$

Output voltage:

$$V_o = \frac{R_1 + R_f}{R_1} V_1 = \left(1 + \frac{R_f}{R_1}\right) V_1$$

Example: If $R_1 = 100$ kΩ and $R_f = 500$ kΩ in the above figure, what output voltage results for an input of $V_1 = 2$ V?

$$V_o = \left(1 + \frac{R_f}{R_1}\right) V_1 = \left(1 + \frac{500 \text{k}}{100 \text{k}}\right) \times 2 = 12 \text{ V}$$



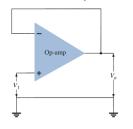
Unit Gain Follower and Unit Gain Inverter

Lo Amp. com ganho unitário

Por que uno one ne não inrock nem amplifico (gambo unitais)? Porque en porso quan index uma poeção do circuito

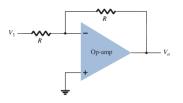
- ▶ Unit Gain Follower
 - Non-inverting amplifier with unit gain:

$$V_o = V_1$$



- Unit Gain Inverter
 - ► Inverting amplifier with unit gain:

$$V_o = -V_1$$

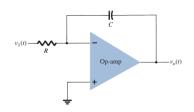


6 o que ma integral no granula

Remembering that $V_{-}=0$:

$$i = \frac{v_1}{R} = -C\frac{dv_o}{dt}$$

$$v_o(t) = -\frac{1}{RC} \int_0^t v_1(\tau) d\tau$$



- ▶ Note: integration operation sums the area under a waveform over a period of time
- ▶ If a fixed voltage is applied as input, the output voltage grows linearly, producing a ramp voltage
- ▶ The output voltage ramp is opposite in polarity to the input voltage and is multiplied by 1/RC
- ▶ If $v_1 = 1 \text{ V}$, $R = 1 \text{ M}\Omega$ and $C = 1 \mu\text{F}$, the output ramp voltage grows -1 V/s
- If $v_1 = 1$ V, R = 100 kΩ and C = 1 μF, the output ramp voltage grows -10 V/s

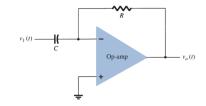


Differentiator La contidar aceleração com o tempo

▶ Remembering that $V_{-} = 0$:

$$i = C\frac{dv_1}{dt} = -\frac{v_o}{R}$$

$$v_o(t) = -RC\frac{dv_1}{dt}$$



▶ Note: differentiation operation gives the rate at which the input waveform changes with respect to time

Specifications and Parameters

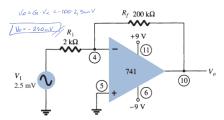
Input offset voltage
Offset - VI neurania para que Vo=0

- ► Input Offset Voltage: differential DC voltage required between the inputs to make the output zero
 - ▶ Idealy, the op-amp output should be 0 V when the input is 0 V; however there is some offset voltage at the output
 - Typical values range from mV down to μ V
- Input Offset Current: is equal to the difference between the input bias current at the non-inverting terminal minus the input bias current at the inverting
 - ▶ Idealy, input bias currents are zero and therefore the input offset current is also zero
 - Typical values range from nA (BJT Op-Amps) down to pA (MOSFET Op-Amps)
- ▶ Open-Loop Gain: gain obtained when no feedback is used in the circuit
- ▶ Input Impedance: given as common mode input impedance (static) and differential input impedance (dynamic with V and I)
- Dutput Impedance: decreases with feedback
- ▶ Frequency Response: given as bandwidth or gain bandwidth product
- Slew-Rate: maximum rate at which amplifier output can change in volts per microsecond of tupe que low para a timis ranion

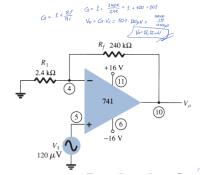
- Constant-Gain Multiplier
- ► Inverting Circuit
 - Voltage Gain: $G = -\frac{R_f}{R_1}$
 - Example: $G = \frac{-200 \text{k}}{2 \text{k}} = -100$ $V_o = GV_i = -100 \times 2.5 \text{m} = -250 \text{ mV}$

$$G_1 = -\frac{Rf}{A_1}$$

$$G = \frac{200\%}{24} = -100$$



- ▶ Non-Inverting Circuit
 - ▶ Voltage Gain: $G = 1 + \frac{R_f}{R_1}$
 - Example: $G = 1 + \frac{240 \text{k}}{2.4 \text{k}} = 101$ $V_o = GV_i = 101 \times 120 \mu = 12.1 \text{ mV}$



► Multiple-Stage Multiplier

- Voltage Gain: $G = G_1 G_2 \dots G_n$
- Example: $G = G_1G_2G_3 = \left(1 + \frac{470k}{4.3k}\right) \left(-\frac{470k}{33k}\right) \left(-\frac{470k}{33k}\right) = 22.4 \times 10^3$ $V_o = GV_i = 22.2 \times 10^3 \times 80\mu = 1.79 \text{ V}$

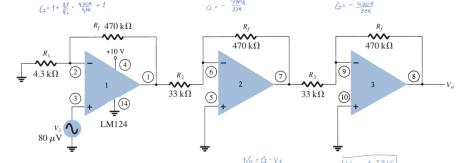
$$C_1 = G_1 \cdot G_2 \cdot G_3$$

$$\left(1 + \frac{470}{4/3}\right) \cdot \frac{470}{33} \cdot \frac{470}{53} = 22, 4 \cdot 10^3$$

$$G = 1 + \frac{32}{3} \cdot \frac{470}{492} + 1$$

$$G = -\frac{470}{392}$$

$$G = \frac{470}{392}$$



Vo = 22,4.103.80 MV

Voltage Summing

► Output Voltage:

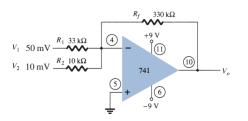
$$V_o = -\left(\frac{R_f}{R_1}V_1 + \frac{R_f}{R_2}V_2 + \dots + \frac{R_f}{R_n}V_n\right)$$

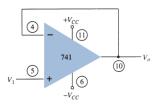
Example:

$$V_o = -\left(\frac{330k}{33k}50m + \frac{330k}{10k}10m\right) = 830 \text{ mV}$$

► Voltage Buffer

Output Voltage:

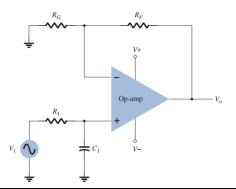


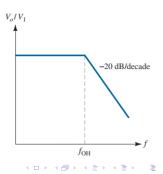


Low-Pass Filter

Low-Pass Fine: $G = 1 + \frac{R_F}{R_G}$

Cutoff Frequency: $f_{OH} =$ $\overline{2\pi R_1 C_1}$

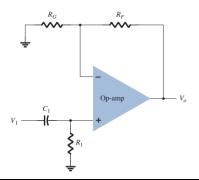


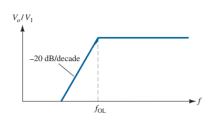


► High-Pass Filter

Voltage Gain: $G = 1 + \frac{R_F}{R_G}$

• Cutoff Frequency: $f_{OL} = \frac{1}{2\pi R_1 C_1}$





► Band-Pass Filter

