CS Ranka N1 Pinout Map
CSW.cx - TCS 2020

Preliminary r 2.23

Resources

4	Serial
2	SPI
3	I2C
1	CAN
10	Analog

Main Pinout

USB

			Name (FAR)	Physical	NRST		VUSB	Physical	Name (FAR)		
			Vin	V	R		V	G	GND		
			GND	G			V	3V3			
			3V3	V		DBG		62	PB9		TX1
RTC			PC13	2				61	PB8		RX1
A0		TX4	PA0	14				59	PB7	RX1	SDA1
A1		RX4	PA1	15				58	PB6	TX1	SCL1
A2		TX2	PA2	16				41	PA8	CK1	SCL3
A3		RX2	PA3	17				40	PC9		SDA3
DAC1	A4	CS1	CK2	PA4	20			36	PB15		MOSI2
DAC2	A5	CK1		PA5	21			35	PB14	RTS3	MISO2
	A6	MISO1		PA6	22			34	PB13	CTS3	CK2
	A7	MOSI1		PA7	23			33	PB12	CK3	CS2
	A8			PB0	25			29	PB11	RX3	SDA2
	A9			PB1	26			28	PB10	TX3	SCL2

! (DAC1/DAC2)

OTG HS (MOSI2/MISO2)

Bottom row: 3 | G | 39 | 38 | 37
3V3 | GND | PC8 | PC7 | PC6
CK6 | RX6 | TX6

DBG

		GND	G	3	3V3	
46	SWIO	IO	CK	SWCK	49
55	JTDO	O	I	JTDI	50
56	NRST	R	B	Boot0	60

Flash Control

F-HD	PD2	54

Flash

			On board Flash
50	PA15	CS3	
51	PC10	CK3	
52	PC11	MISO3	
53	PC12	MOSI3	

USB

D-	35	PB14	HS	Optional
	44	PA11	FS	Default
D+	36	PB15	HS	Optional
	45	PA12	FS	Default

Breakout

BTN	PB5	57	27	PB2	LED
	PA10	43	11	PC3	
	PA9	42	10	PC2	
	PC4	24	9	PC1	
	PC14	3	8	PC0	
	PC15	4	VB	VBAT	