STM32F730R8T6 Resource Map
CSW.cx - TCS 2020

Pin	Name/FAR	)Functi(	I/O	Misc	Timer [TIM]	Serial
1	VBAT	RTC				
2	PC13	RTC	5VT	RTC_TAMP1/TS/OUT		
3	PC14-OSC32_IN		5VT			
4	C15-OSC32_OUT		5VT			
5	PH0-OSC_IN		5VT			
6	PH1-OSC_OUT		5VT			
7	NRST	RST				
8	PC0		5VT			
9	PC1		5VT	TRACED0		
10	PC2		5VT			
11	PC3		5VT			
12	VSSA	GND				
13	VREF+	S?				
14	PA0-WKUP		5VT	WKUP1	2_CH1/ETR, 5_CH1, 8_ETR	USART2_CTS, UART4_TX
15	PA1		5VT		2_CH2, 5_CH2	USART2_RTS, UART4_RX
16	PA2		5VT	WKUP2	2_CH3, 5_CH3, 9_CH1	USART2_TX
17	PA3		5VT			USART2_RX
18	VSS	GND				
19	VDD	3V3				
20	PA4		ADC			USART2_CK
21	PA5		ADC		2_CH1/2_ETR, 8_CH1N	
22	PA6		5VT		1 8_BKIN, 3 13_CH1	
23	PA7		5VT		1 8_CH1N, 3_CH2, 14_CH1	
24	PC4		5VT			
25	PB0		5VT		1 8_CH2N, 3_CH3	UART4_CTS
26	PB1		5VT		1 8_CH3N, 2_CH4	
27	PB2		5VT			
28	PB10		5VTf		2_CH3	USART3_TX
29	PB11		5VTf		2_CH4	USART3_RX
30	VCAP_1	S?				
31	VSS	GND				
32	VDD	3V3				
33	PB12		5VT		1_BKIN	USART3_CK
34	PB13		5VT		1_CH1N	USART3_CTS
35	PB14		5VT		1 8_CH2N, 12_CH1	USART3_RTS
36	PB15		5VT	RTC_REFIN	1 8_CH3N, 12_CH2	
37	PC6		5VT		3 8_CH1,	USART6_TX
38	PC7		5VT		3 8_CH2	USART6_RX
39	PC8		5VT	TRACED1	3 8_CH3	UART5_RTS, USART6_CK

40	PC9	5VTf	MCO2	3 8_CH4	UART5_CTS
41	PA8	5VTf	MCO1	1_CH1, 8_BKIN2	USART1_CK
42	PA9	5VT		1_CH2	USART1_TX
43	PA10	5VT		1_CH3	USART1_RX
44	PA11	5VT		1_CH4	USART1_CTS
45	PA12	5VT		1_ETR	USART1_RTS
46	PA13	5VT	JTMS-SWDIO		
47	VSS	GND			
48	VDD	3V3			
49	PA14	5VT	JTCK-SWCLK		
50	PA15	5VT	JTDI	2_CH1/ETR	UART4_RTS
51	PC10	5VT			USART3_TX, UART4_TX
52	PC11	5VT			USART3_RX, UART4_RX
53	PC12	5VT	TRACED3		USART3_CK, UART5_TX
54	PD2	5VT	TRACED2	3_ETR	UART5_RX
55	PB3	5VT	JTDO/TRACESWO	2_CH2	
56	PB4	5VT	NJTRST	3_CH1	
57	PB5	5VT		3_CH2	
58	PB6	5VTf		4_CH1	USART1_TX
59	PB7	5VTf		4_CH2	USART1_RX
60	BOOT	B			
61	PB8	5VTf		4_CH3, 10_CH1	
62	PB9	5VTf		4_CH4, 11_CH1	
63	VSS	GND			
64	VDD	3V3			

Resources

[SPI]	[I2C]	[CAN]	[SAI]	Analog [ADC]	USB [OTG_]
			2_FS_B	123_IN10	HS_ULPI_STP
2_MOSI/I2S2_SD			1_SD_A	123_IN11	
2_MISO				123_IN12	HS_ULPI_DIR
2_MOSI/I2S2_SD				123_IN13	HS_ULPI_NXT
			2_SD_B	123_IN0	
QUAD_BK1_IO3			2_MCK_B	123_IN1	
			2_SCK_B	123_IN2	
				123_IN3	HS_ULPI_D0
13_NSS/I2S13_WS				12_IN4, DAC1	HS_SOF
1_SCK/I2S1_CK				12_IN5, DAC2	HS_ULPI_CK
1_MISO				12_IN6	
1_MOSI/I2S1_SD				12_IN7	
I2S1_MCK				12_IN14	
				12_IN8	HS_ULPI_D1
				12_IN9	HS_ULPI_D2
3_MOSI/I2S3_SD, QUAD_CLK			1_SD_A		
2_SCK/I2S2_CK	2_SCL				HS_ULPI_D3
	2_SDA				HS_ULPI_D4
2_NSS/I2S2_WS	2_SMBA				HS_ULPI_D5, ID
2_SCK/I2S2_CK					HS_ULPI_D6, VBUS
2_MISO					HS_DM
2_MOSI/I2S2_SD					HS_DP
I2S2_MCK					
I2S3_MCK					

QUAD_BK1_IO0, I2S_CKIN	3_SDA		FS_SOF
	3_SCL		FS_VBUS
2_SCK/I2S2_CK	3_SMBA		FS_ID
		1_RX	FS_DM
		1_TX 2_FS_B	FS_DP

1 3_NSS/I2S1 3_WS
3_SCK/I2S3_CK, QUAD_BK1_IO1
3_MISO, QUAD_BK2_NCS
3_MOSI/I2S3_SD

1 3_SCK/I2S1 3_CK
1 3_MISO, 2_NSS/I2S2_WS
1 3_MOSI/I2S1 3_SD   1_SMBA   HS_ULPI_D7
QUAD_BK1_NCS   1_SCL
                     1_SDA

                     1_SDA   1_RX
2_NSS/I2S2_WS   1_TX