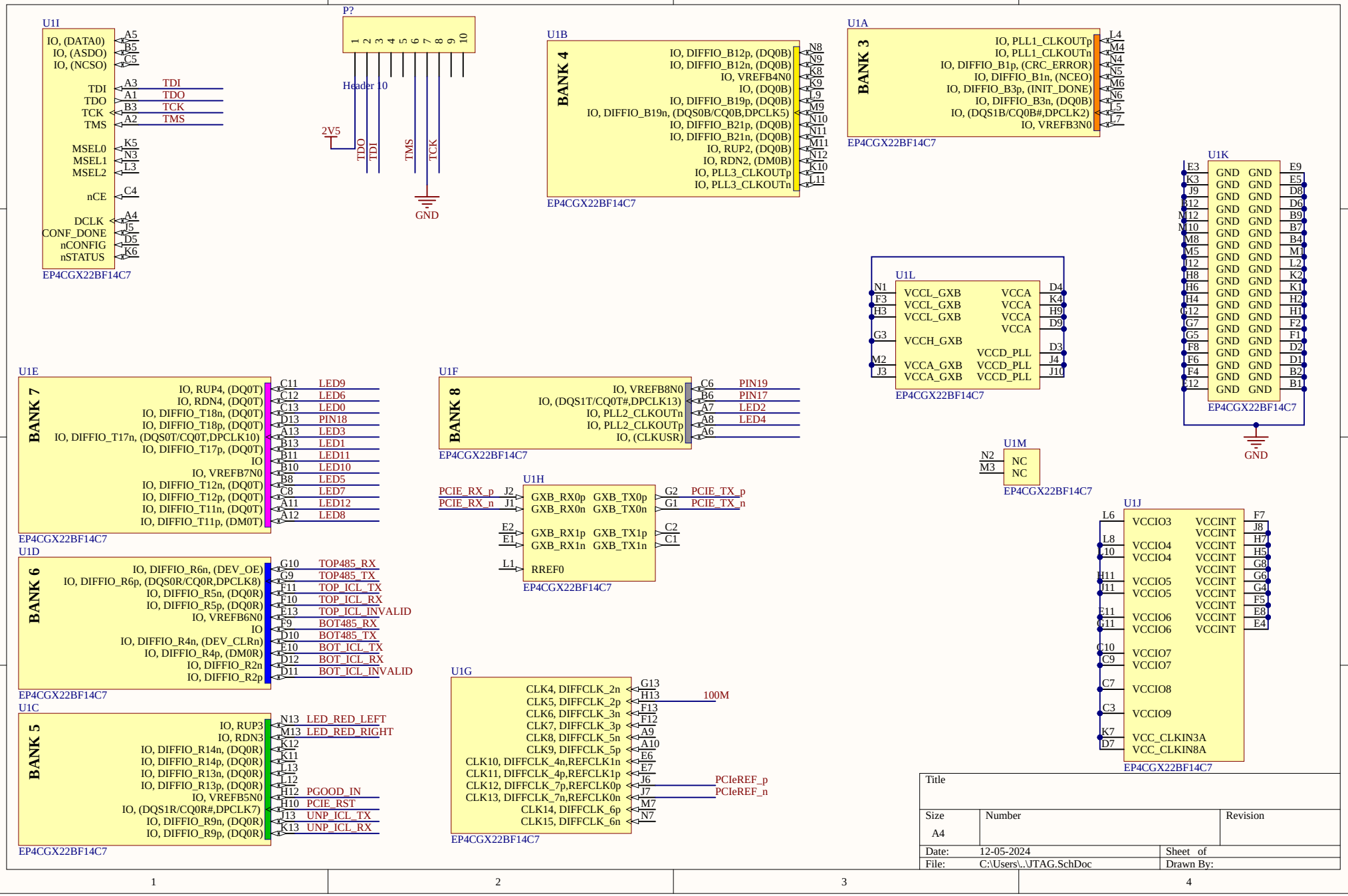
U1I
IO, (DATA0) A5
IO, (ASDO) B5
IO, (NCSO) C5
TDI A3 TDI
TDO A1 TDO
TCK B3 TCK
TMS A2 TMS
MSEL0 K5
MSEL1 N3
MSEL2 L3
nCE C4
DCLK A4
CONF_DONE J5
nCONFIG D5
nSTATUS K6
EP4CGX22BF14C7
P?
Header 10
2V5
TDO
TDI
TMS
TCK
GND
U1B
BANK 4
IO, DIFFIO_B12p, (DQ0B) N8
IO, DIFFIO_B12n, (DQ0B) N9
IO, VREFB4N0 K8
IO, (DQ0B) K9
IO, DIFFIO_B19p, (DQ0B) L9
IO, DIFFIO_B19n, (DQS0B/CQ0B,DPCLK5) M9
IO, DIFFIO_B21p, (DQ0B) N10
IO, DIFFIO_B21n, (DQ0B) N11
IO, RUP2, (DQ0B) M11
IO, RDN2, (DM0B) N12
IO, PLL3_CLKOUTp K10
IO, PLL3_CLKOUTn L11
EP4CGX22BF14C7
U1A
BANK 3
IO, PLL1_CLKOUTp L4
IO, PLL1_CLKOUTn M4
IO, DIFFIO_B1p, (CRC_ERROR) N4
IO, DIFFIO_B1n, (NCEO) N5
IO, DIFFIO_B3p, (INIT_DONE) M6
IO, DIFFIO_B3n, (DQ0B) N6
IO, (DQS1B/CQ0B#,DPCLK2) L5
IO, VREFB3N0 L7
EP4CGX22BF14C7
U1K
GND
EP4CGX22BF14C7
U1L
VCCL_GXB
VCCH_GXB
VCCA_GXB
VCCA
VCCD_PLL
EP4CGX22BF14C7
U1E
BANK 7
IO, RUP4, (DQ0T) C11 LED9
IO, RDN4, (DQ0T) C12 LED6
IO, DIFFIO_T18n, (DQ0T) C13 LED0
IO, DIFFIO_T18p, (DQ0T) D13 PIN18
IO, DIFFIO_T17n, (DQS0T/CQ0T,DPCLK10) A13 LED3
IO, DIFFIO_T17p, (DQ0T) B13 LED1
IO B11 LED11
IO, VREFB7N0 B10 LED10
IO, DIFFIO_T12n, (DQ0T) B8 LED5
IO, DIFFIO_T12p, (DQ0T) C8 LED7
IO, DIFFIO_T11n, (DQ0T) A11 LED12
IO, DIFFIO_T11p, (DM0T) A12 LED8
EP4CGX22BF14C7
U1F
BANK 8
IO, VREFB8N0 C6 PIN19
IO, (DQS1T/CQ0T#,DPCLK13) B6 PIN17
IO, PLL2_CLKOUTn A7 LED2
IO, PLL2_CLKOUTp A8 LED4
IO, (CLKUSR) A6
EP4CGX22BF14C7
U1M
NC
EP4CGX22BF14C7
U1H
PCIE_RX_p J2
PCIE_RX_n J1
GXB_RX0p GXB_TX0p G2 PCIE_TX_p
GXB_RX0n GXB_TX0n G1 PCIE_TX_n
GXB_RX1p GXB_TX1p C2
GXB_RX1n GXB_TX1n C1
RREF0
EP4CGX22BF14C7
U1D
BANK 6
IO, DIFFIO_R6n, (DEV_OE) G10 TOP485_RX
IO, DIFFIO_R6p, (DQS0R/CQ0R,DPCLK8) G9 TOP485_TX
IO, DIFFIO_R5n, (DQ0R) F11 TOP_ICL_TX
IO, DIFFIO_R5p, (DQ0R) F10 TOP_ICL_RX
IO, VREFB6N0 E13 TOP_ICL_INVALID
IO F9 BOT485_RX
IO, DIFFIO_R4n, (DEV_CLRn) D10 BOT485_TX
IO, DIFFIO_R4p, (DM0R) E10 BOT_ICL_TX
IO, DIFFIO_R2n D12 BOT_ICL_RX
IO, DIFFIO_R2p D11 BOT_ICL_INVALID
EP4CGX22BF14C7
U1J
VCCIO3
VCCIO4
VCCIO5
VCCIO6
VCCIO7
VCCIO8
VCCIO9
VCC_CLKIN3A
VCC_CLKIN8A
VCCINT
EP4CGX22BF14C7
U1C
BANK 5
IO, RUP3 N13 LED_RED_LEFT
IO, RDN3 M13 LED_RED_RIGHT
IO, DIFFIO_R14n, (DQ0R) K12
IO, DIFFIO_R14p, (DQ0R) K11
IO, DIFFIO_R13n, (DQ0R) L13
IO, DIFFIO_R13p, (DQ0R) L12
IO, VREFB5N0 H12 PGOOD_IN
IO, (DQS1R/CQ0R#,DPCLK7) H10 PCIE_RST
IO, DIFFIO_R9n, (DQ0R) J13 UNP_ICL_TX
IO, DIFFIO_R9p, (DQ0R) K13 UNP_ICL_RX
EP4CGX22BF14C7
U1G
CLK4, DIFFCLK_2n G13
CLK5, DIFFCLK_2p H13 100M
CLK6, DIFFCLK_3n F13
CLK7, DIFFCLK_3p F12
CLK8, DIFFCLK_5n A9
CLK9, DIFFCLK_5p A10
CLK10, DIFFCLK_4n,REFCLK1n E6
CLK11, DIFFCLK_4p,REFCLK1p E7
CLK12, DIFFCLK_7p,REFCLK0p J6 PCIeREF_p
CLK13, DIFFCLK_7n,REFCLK0n J7 PCIeREF_n
CLK14, DIFFCLK_6p M7
CLK15, DIFFCLK_6n N7
EP4CGX22BF14C7
Title
Size A4
Number
Revision
Date: 12-05-2024
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File: C:\Users\..\JTAG.SchDoc
Drawn By: