LANGUAGE BASED SECURITY (LBT)

SECURE COMPILATION

Chiara Bodei, Gian-Luigi Ferrari

Lecture May, 13 2024



Outline



Compiler Correctness

Secure compilation

General overview*

How can we trust a compiler is correct?

- •Testing can help discover bugs, but it is not enough
- Disable any optimization: too restrictive
- •So why not formally verify the compiler itself?
 - This is the aim of CompCert for instance, programmed and verified using the Coq proof assistant
- We need formal semantics for the source language and the target language

Ken Thompson

What code can we trust?

...

Can we trust the compiler?
Solution: inspect the compiler

- Question: is this compilation correct?
- •Is an easy or a difficult property to prove?
- •By applying program proof to the compiler itself, we can obtain mathematically strong guarantees that the generated executable code is faithful to the semantics of the source program

Semantic preservation

For all source code S, if the compiler generates machine code C from source S without reporting any compilation error, then C behaves like S

Note that proving the correctness of a compiler is not a new idea

•Is this compilation correct?

```
for(i=0; i < 10; i++)
    printf("%d\n", i);</pre>

printf("42\n");
```

•Is this compilation correct? No

```
for(i=0; i < 10; i++)
    printf("%d\n", i);</pre>

printf("42\n");
```

•Is this compilation correct?

```
int n = some_pt->n;
if (some_pt == NULL)
    // Some code
use (n)

int n = some_pt->n;
use (n)
```

•Is this compilation correct? Not really

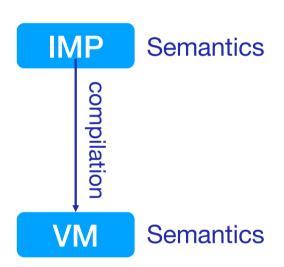
```
int n = some_pt->n;
if (some_pt == NULL)
    // Some code
use (n)

int n = some_pt->n;
use (n)
```

Compiling IMP to a simple virtual machine

We show the basics of verified compilation

- Compiling IMP to a simple virtual machine VM
- Semantic preservation notion
- Verification of an optimizing program transformation (dead store elimination)



IMP: arithmetic expressions

C-like imperative language with a notion of store a := n | x | a0 + a1 | a0 - a1 | a0 * a1

Abstract syntax

```
Definition ident := string.
Inductive aexp : Type :=
 CONST (n: Z) (* a constant, or *) [e_1 - e_2] s = [e_1] s - [e_2] s
 VAR (x: ident) (* a variable, or *) ...
 PLUS (a1: aexp) (a2: aexp) (* a sum, or *)
 MINUS (al: aexp) (a2: aexp) (* a difference *)
```

[Denotational] **Semantics**

```
\llbracket x \rrbracket s = s(x)
       [n]s = n
[e_1 + e_2] s = [e_1] s + [e_2] s
```

IMP: boolean expressions

C-like imperative language with a notion of store $b:= true \mid false \mid a1 = a2 \mid a1 \le a2 \mid not b \mid b1$ and b2

[Denotational] **Semantics**

Abstract syntax

```
Definition ident := string.
Inductive bexp : Type :=
|BTrue
| BFalse
| BEq (a1 a2 : aexp)
| BNeq (a1 a2 : aexp)
| BLe (a1 a2 : aexp)
| BGt (a1 a2 : aexp)
| BAnd (b1 b2 : bexp)
```

...

IMP: commands

C-like imperative language with a notion of store c:= skip | x=a | c0; c1 | if b then c1 else c2| while b do c

Abstract syntax

```
Inductive com := | CSkip |
CAss (x: string) (a: aexp)|
CSeq (c1 c2: com) |
CIf (b: bexp) (c1 c2: com)|
CWhile (b: bexp) (c: com)
```

[Big-step operational] **Semantics**

$$\begin{array}{lll} & \text{skip/s} \Rightarrow \text{s} & \text{$x := a/s \Rightarrow s[x \leftarrow \llbracket a \rrbracket \, s]$} \\ & \frac{c_1/s \Rightarrow s_1 \quad c_2/s_1 \Rightarrow s_2}{c_1; \, c_2/s \Rightarrow s_2} & \frac{c_1/s \Rightarrow s' \text{ if } \llbracket b \rrbracket \, s = \text{true}}{c_2/s \Rightarrow s' \text{ if } \llbracket b \rrbracket \, s = \text{false}} \\ & \frac{c_2/s \Rightarrow s' \text{ if } \llbracket b \rrbracket \, s = \text{false}}{\text{if b then c_1 else $c_2/s \Rightarrow s'$}} \\ & \frac{\llbracket b \rrbracket \, s = \text{false}}{\text{while b do c done/s} \Rightarrow s} \\ & \frac{\llbracket b \rrbracket \, s = \text{true} \quad c/s \Rightarrow s_1 \quad \text{while b do c done/s} \Rightarrow s_2}{\text{while b do c done/s} \Rightarrow s_2} \end{array}$$

The IMP virtual machine VM

Components of the machine:

- •The code C: a list of instructions.
- •The program counter pc: an integer, giving the position of the currently-executing instruction in C.
- •The store st: a mapping from variable names to integer values.
- •The stack σ: a list of integer values (to store intermediate results temporarily)

Digression on Stack Machines

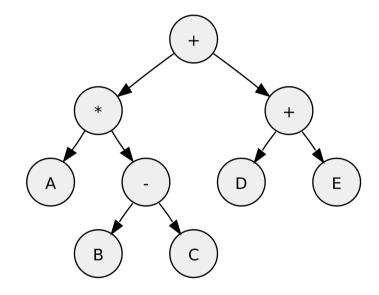
- Stack machine is one type of CPU organizations, e.g., the Java Virtual Machine is stack-based
- Stacks represent a natural and suitable tool to be used in processing wellstructured code
- Machines with stacks are also required to compile and to do it efficiently
- Stacks can be used to support expression evaluation, subroutine return address storage, dynamically allocated local variable storage, and subroutine parameter passing

Digression on Stack Machines (cont.)

- The instruction set carries out most ALU actions that work only on the expression stack, not on data registers or main memory cells
- Arithmetic expression

$$A*(B-C)+(D+E)$$

Reverse "polish" or post-fix notation A B C - * D E + +



This can be very convenient for executing high-level languages because most arithmetic expressions can be easily translated into postfix notation

Digression on Stack Machines (cont.)

$$10 * (5-3) + (2+4)$$
 becomes

	STACK
push	
10	10
push 5	5 10
push	
3	3 5 10
subtract	2 10
multiply	20
push 2	2 20
push 2	4 2 20
add	6 20
add	26

The IMP virtual machine VM

- Translation to a stack-based expression language with a program counter, a store and a stack
- Instruction set

• • •

- The code for an arithmetic expression a executes in sequence and deposits the value of a at the top of the stack, e.g.:
- •1 + 2 is translated into Iconst 1 :: Iconst 2 :: Iadd :: nil

The IMP virtual machine VM (cont.)

A transition relation represents the execution of one instruction.

```
Definition stack := list Z.

Definition store := ident \rightarrow Z.

Definition config := (Z * stack * store).

Inductive transition (C: code): config -> Prop := |
trans_const: \forall pc stack s n, instr_at C pc = Some(Iconst n)
\rightarrow transition C (pc, stack, s) (pc + 1, n :: stack, s)

...

Intially: pc = 0, initial store, empty stack

Finally: pc points to a lhalt instruction, empty stack
```

Example of compilation

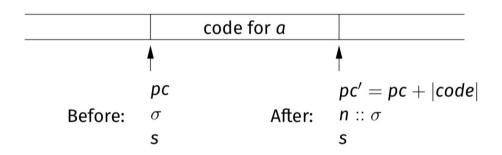
```
 x = x+1 \text{ is translated into} 
 Ivar(x):: Iconst 1 :: Iadd :: Isetvar(x):: nil 
 stack \mid \epsilon \qquad | 12 \qquad | 13 \qquad | \epsilon \qquad | 12 \qquad | 13 \qquad | \epsilon \qquad | 12 \qquad | 13 \qquad | 12 \qquad | 13 \qquad | 13 \qquad | 13 \qquad | 14 \qquad |
```

At each instruction the program counter is incremented by one, unless the instruction prescribes a branch

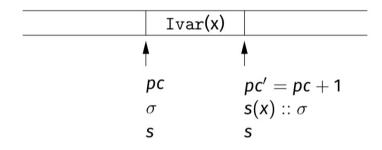
Compilation of arithmetic expressions

Compilation is just translation to "reverse Polish notation"

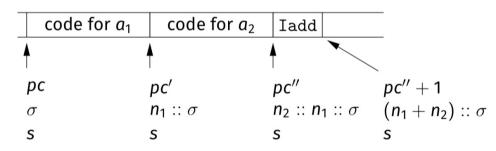
General contract: if a evaluates to n in store s,



Base case: if a = x,



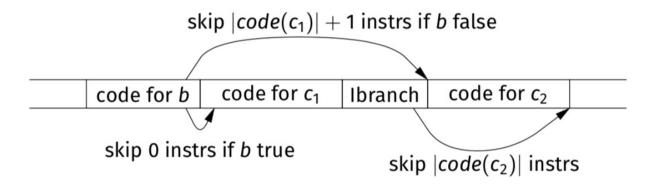
decomposition: if $a = a_1 + a_2$,



Similarly for Boolean expressions

Compilation of if then else

Code for IFTHENELSE $b c_1 c_2$:



Similarly for the other commands

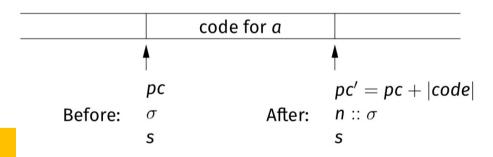
Verifying the compilation of expressions

Proof: a simple induction on the structure of a.

The generated code must behave as prescribed by the semantics of the source program:

if a evaluates to n in the store, then in the translation n ends up on the stack and vice versa

General contract: if a evaluates to n in store s,



This is not enough to conclude that the compiler is correct!

Correctness, formally

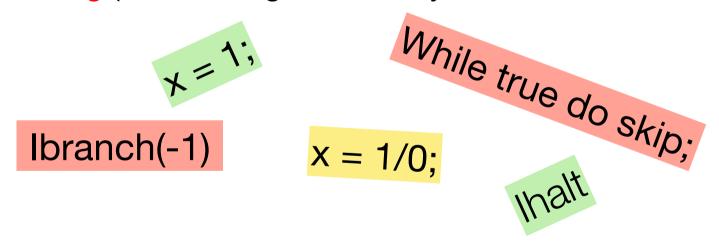
Formally, a compiler is a function $\llbracket \cdot \rrbracket_T^S$ that translates programs s written in a source language S into programs t written in a target language T

What does it mean that a compiler is correct? It means that the compiled code behaves as the source code

Correctness

A program may:

- Terminate and return a result value
- Diverge (= indefinitely run, e.g. infinite loop)
- •Go wrong (= crash, e.g. division by zero, or invalid memory access)



The previous approach only works with normal terminating programs

Semantic Preservation

What should be preserved?

Observable behaviors

What should be preserved?

Answer: observable behaviors $\mathcal{B}(s)$

- Hence, observable behaviors are normal termination or abnormal termination and divergence
- Inputs and outputs can be observed as well

Observable behaviors

We define the behaviour of a source code in terms of the set of observable actions, e.g., I/O actions, memory operations, ...

For example, in terms of traces, composed by strings of observable actions

For an imperative language with I/O: add a trace of input-output operations performed during execution.

```
x := 1; \quad x := 2; \qquad \approx \qquad \qquad x := 2;
(trace: \epsilon) \qquad (trace: \epsilon)
print(1); print(2); \qquad \not\approx \qquad print(2);
(trace: out(1).out(2)) \qquad (trace: out(2))
```

Semantic Preservation (cont.)

A correct compilation is semantics preserving if the generated code behaves as prescribed by the semantics of the source

Preservation

A correct compilation is semantics preserving if the generated code behaves as prescribed by the semantics of the source

How to characterize preservation?

Answer: we need to compare the behaviors of two programs, by using simulations and program equivalences

Two programs are equivalent when they behave the same, even if they are different (same semantics and possibly different syntax)

Digression on equivalences

Suppose to have two tea/coffee machines:

- •M₁ allows you to:
 - ∘Put a coin
 - Choose to press the tea or the coffee button
 - After providing you with the required beverage, the machine is again waiting for requests
- M₂ allows you to:
 - ∘Put a coin
 - When a tea or coffee button is pressed, non-deterministically delivers tea or coffee

٥...

•Do M₁ and M₂ behave the same?

Digression on equivalences

What does it mean that two machines "are the same" for an observer?

Intuitively, if you do something with one machine, you must be able to do the same with the other, and on the two states which the machines evolve to, the same is again true

Bisimulation (observation equivalence)

How to characterize preservation?

Answer: simulations and program equivalences

Definition (Bisimulation)

The source program S and the compiled program C have exactly the same observable behaviors (they are equivalent).

Every possible behavior of S is a possible behavior of C

Every possible behavior of C is a possible behavior of S

$$\mathcal{B}([[s]]) = \mathcal{B}(s)$$

Very strong notion of semantic preservation: often too strong in practice

Forward Simulation

Definition (Forward Simulation)

Every possible behavior of the source program S is a possible behavior of the compiled program C

$$\mathcal{B}([[s]]) \subseteq \mathcal{B}(s)$$



Our compilation: if the source code terminates (with the same final store)/diverges, then the compiled code terminates/diverges

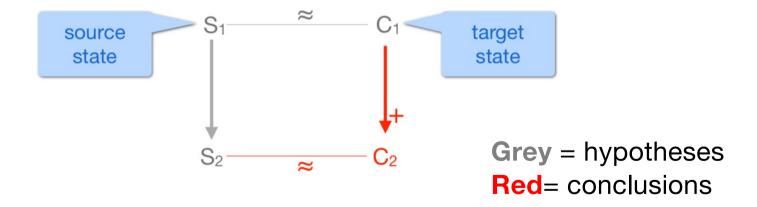
But, what if the compiled code C has more behaviors (maybe udesirable) than the source S, e.g., what if C can terminate or go wrong?

Simulation diagrams

Behaviors are defined in terms of sequences of transitions.

Forward simulation can be proved as follows:

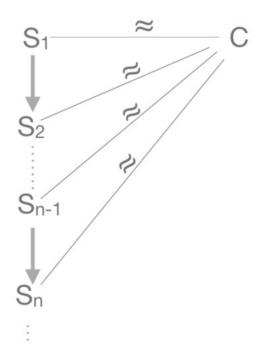
- show that every transition in S is simulated by some transitions in C
- while preserving an invariant ≈ between the states of S and C



For instance, for the translation of x = x+1 it works

Infinite stuttering

- •The source program diverges but the compiled code can terminate normally or by going wrong
- This denotes an incorrect optimization of a diverging program, e.g. compiling (while true skip) into skip
- To solve this problem, we need continuationbased small-step semantics of IMP



Reducing non-determinism during compilation

C does not guarantee the order of evaluation of the operands of the + operator

```
int x = 0;
int f(void) { x = x + 1; return x; }
int g(void) { x = x - 1; return x; }
int y = f() + g();
```

f()+ g() can be evaluated in two ways

- •It returns 1, if f() is called first
- •It returns -1, if g() is called first

This is a form of non-determinism

The compiler chooses one behaviour*: hence the compiled code has fewer behaviours. Forward simulation and bisimulation fail

^{*}This is a form of refinement

Reducing non-determinism in concurrent setting

```
Shared Counter
class C {
 private int counter;
 void inc() { synchronized (this) {
 counter++; }
 int get() {synchronized(this){
         return counter;}
 void set(int val) { synchronized (this) {
 counter= val }
Cc = new C(0);
```

There is a common sub-expression that can be eliminated

```
THREAD 1
:
aval = c.get() +1:
bval = c.get()+1;
:
```

```
THREAD 2
:
c.set(1);
:
```

If c is initially 0, (aval,bval) can range in {(1,1), (1,2), (2,2)}

Reducing non-determinism in concurrent setting

```
Shared Counter
class C {
 private int counter;
 void inc() { synchronized (this) {
 counter++; }
 int get() {synchronized(this){
         return counter;}
 void set(int val) { synchronized (this) {
 counter= val }
Cc = new C(0);
```

```
THREAD 1
:
aval = c.get() +1:
bval = aval;
:
```

```
THREAD 2
:
c.set(1);
:
```

If c is initially 0, (aval,bval) can range in {(1,1), (1,2), (2,2)}

Backward Simulation (a.k.a. refinement)

Definition (Backward Simulation)

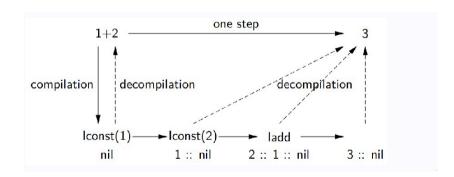
Every possible behavior of the compiled program C is a possible behavior of the source program S. C may have fewer behaviors than S

$$\mathcal{B}([[s]]) \supseteq \mathcal{B}(s)$$

- Our compilation: if the compiled code terminates/diverges, then the source code terminates/diverges
- •Backward simulation can be proved similarly to forward simulation, but simulates transitions of C by transitions of S.
- Backward simulation suffices to show the preservation of properties established by source-level verification

Backward Simulation (a.k.a. refinement)

- •Difficult to prove: all steps that the compiled code can take can be traced back to steps the source program can take
- •Problematic: one single step may be translated into many steps (e.g., 1+2 is one step in IMP, but several instructions in the VM)



Solution: introduce a **decompilation**, defined on all intermediate states, and that is left-inverse of compilation (comp of decomp = id)

Fwd simulation + determinism = bisimulation

A language is deterministic if every program has only one observable behavior

Lemma

If the target language is deterministic, forward simulation implies backward simulation and therefore bisimulation

Our compilation: if the source code terminates/diverges, then the compiled code terminates/diverges

Once solved the stuttering issue, all IMP programs, terminating or not, are correctly compiled

Should "going wrong" behaviors be preserved?

As we know, compilers usually "optimize away" going-wrong behaviors, maybe under the assumption that there are none, as in C

There are weaker simulation properties, by restricting ourselves to the cases in which the computations do not go wrong ("invoke undefined behavior")

Safe forward simulation: any behavior of the source program S other than "going wrong" is a possible behavior of the compiled code C

Safe backward simulation: for any behavior b of the compiled code C, the source program S can either have behavior b or go wrong

A program is safe when it either terminates or diverges

Optimizations

Optimizations automatically transform the programmer-supplied code into equivalent code that should be more efficient

For instance, DSE is a form of compilation from initial programs to programs where dead store have been removed

Back to dead store elimination

DSE is a form of **compilation** from initial programs to programs where dead store have been removed

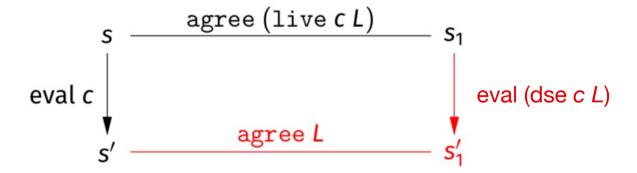
Liveness can be indeed seen as an information flow property

Semantic preservation for dead store elimination (DSE) relates executions of c and c in absence of dead stores:

Assume initial states agree on live variables "before" c: Then, the two executions terminate on final states that agree on live variables "after" c

Fwd simulation for dead store elimination

Forward simulation: the execution of c after removal of dead stores simulates the execution of c while preserving the agree relation between the stores



Real verified compilers

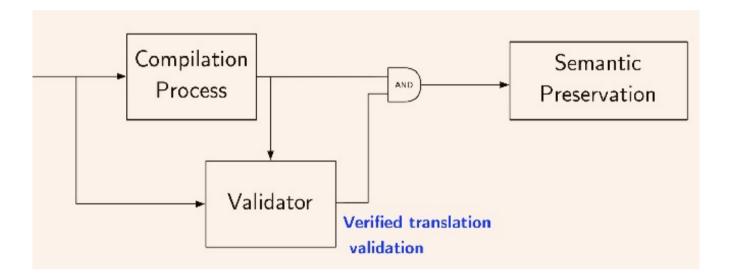
CompCert: fully written in Coq, it compiles C in many real-world architectures. It presents mechanized proofs of correctness

CakeML: compiles a subset of Standard ML. It uses HOL4 for mechanized proofs

Alternative approaches: Translation validation

Considering just a single run of the compiler at the time on the single

program



It takes the source and compiled programs as input and checks whether the source program semantics is preserved in the compiled one

Beyond whole programs

- Many real-world programs are partial, i.e. they are not written as a whole by programmers
- Partial programs are made "full" by linking with a context
- •Contexts model external definitions from standard libraries, code written by third parties, external components, ...

Issue: All the above cannot deal with partial programs In case of partial programs resort to prove compiler correctness without the whole-program restriction

Separate compilation correctness

- Separately compile the source context and the source program and link them together
- A separate compiler independently translates a programs components in a way that preserves correctness of the program as a whole
- Compile the partial source program
- Compile the source context with the same compiler
- Link them together
- Correctness of the result is guaranteed

Compositional compilation correctness

- Aim: to guarantee correct compilation of components
- •Compile the source program, choose a target context that correctly implements the source one and then link them together
 - Compile the partial program
 - Choose a target context that correctly implements the source one
 - Link them together
- Correctness of the result is guaranteed!
- •This variant is stronger, more useful and more difficult to obtain
- Note that in Separate Compilation linking is only defined with linking contexts produced by the same compiler

Bibliography

- •Xavier Leroy. *Proving the correctness of a compiler*. EUTypes Summer School 2019
- •Sandrine Blazy. *Verified compilation An introduction to CompCert*. OPLSS, Eugene, 2023-07-05

End