

# Electronics Systems (938II)

Lecture 0 – Teacher intro and contacts, course intro



### My contacts and reception info

- Luca Crocetti
  - Assistant Professor at the Department of Information Engineering (University of Pisa)



■ E-mail <u>luca.crocetti@unipi.it</u>

■ Teams <u>a026770@unipi.it</u>

Website <a href="https://people.unipi.it/luca\_crocetti/en/">https://people.unipi.it/luca\_crocetti/en/</a>





## My contacts and reception info

- Luca Crocetti
  - Student reception:
    - Booking via e-mail (or Teams)
    - Remote reception (via Teams) Mainly
    - Physical reception Possible, but to be scheduled a few days in advance



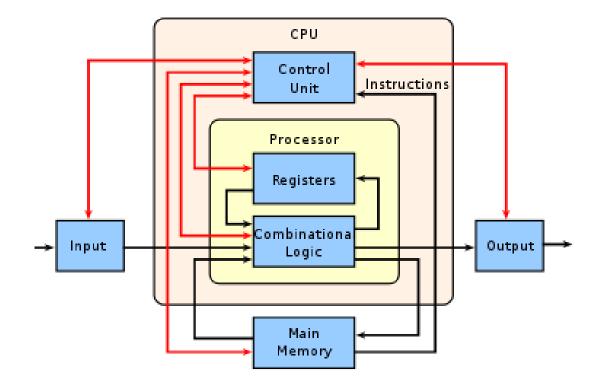
- Exam:
  - Oral (only)
  - About 1h ( $^{\sim}30' + ^{\sim}30'$ )
    - Electronics Systems: → ~30'
      - Me → ~ 15'
      - Prof. Baronti  $\rightarrow$  ~ 15′
    - Telecommunication Technologies → ~30'
      - Prof. Luise  $\rightarrow$  ~ 15′
      - Prof. Bacci →  $^{\sim}$  15'



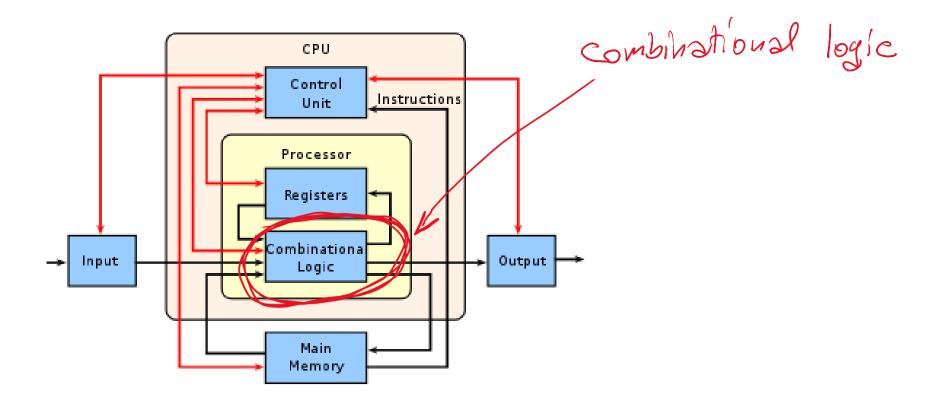
Course content (overview) – Module: Electronics Systems

A journey to understand the main characteristics and components of modern electronic digital systems

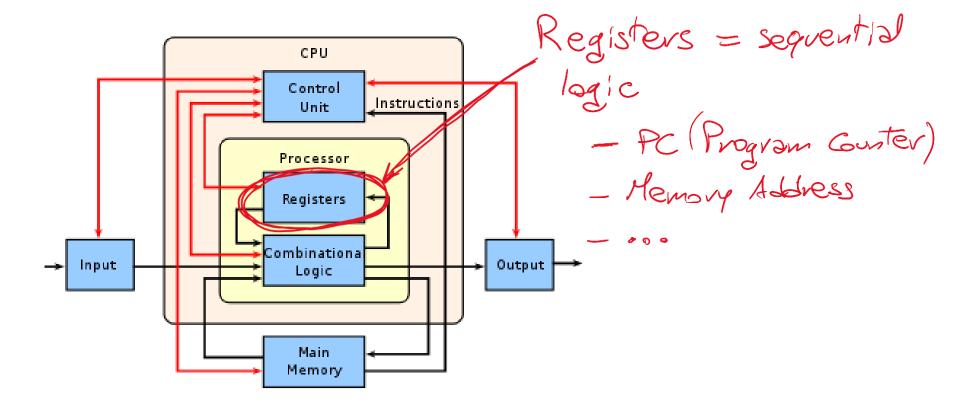




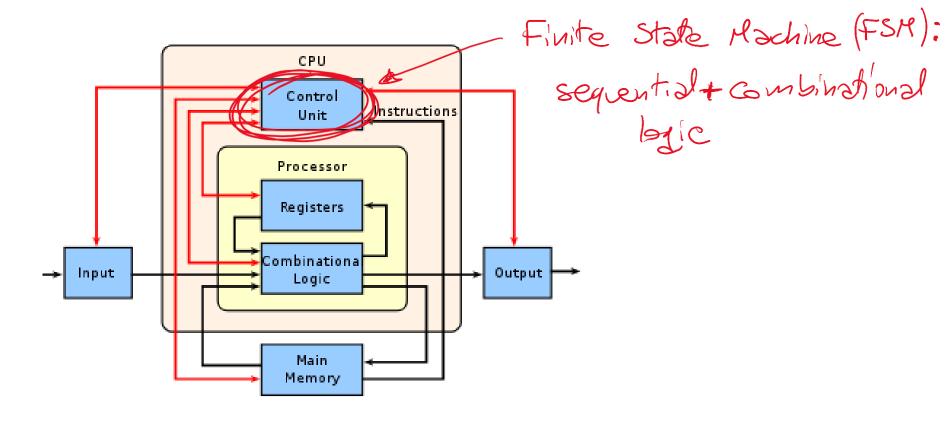




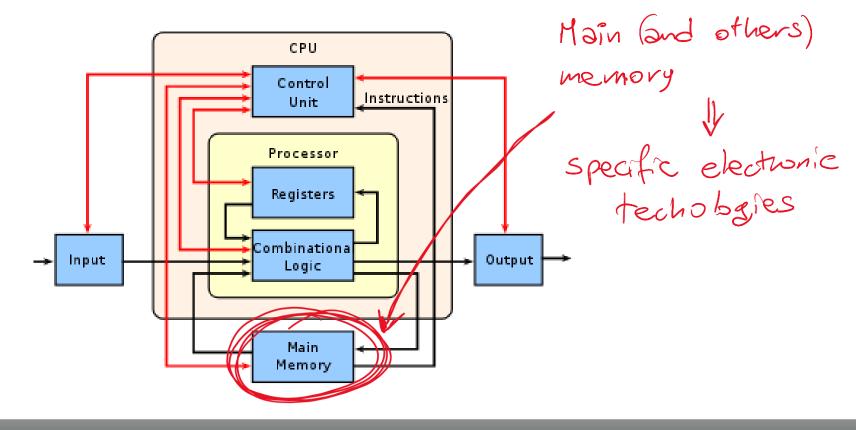




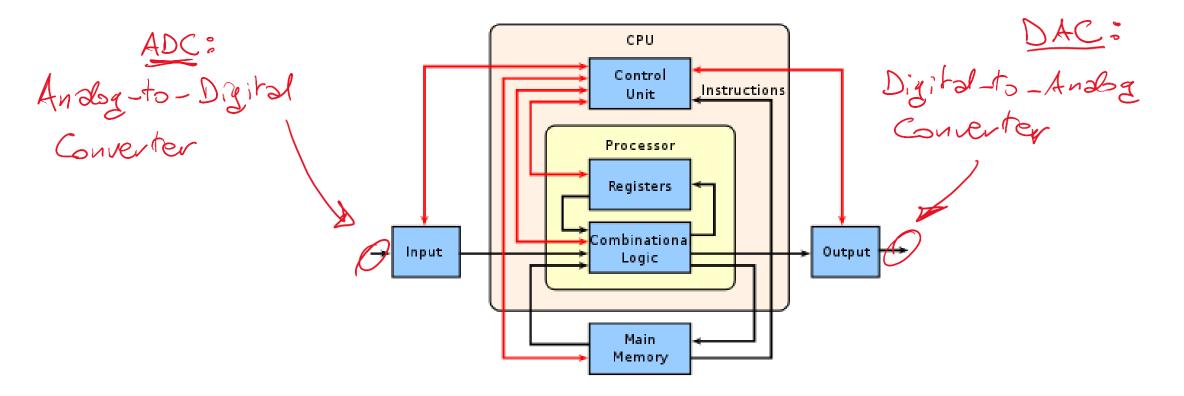














Course content (overview) – Module: Electronics Systems

Basic principles: CMOS technology and digital data representation (F. Baronti)

Combinational and Sequential logic elements (L. Crocetti)

Finite State Machine (L. Crocetti)

Memories (L. Crocetti)

Programmable Logic Devices (L. Crocetti)

• ADC, DAC (F. Baronti)

• CPU Organisation (F. Baronti)



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# Thank you for your attention

Luca Crocetti (luca.crocetti@unipi.it)