

全実験結果報告

チェーンレス型多層合意における 待ち時間の実験的評価

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1 誤植と訂正

投稿原稿 https://ken.ieice.org/ken/form/index.php?tgs_regid=0eb9c92b15b4a42e85c88e1tgid=IEICE-NS&now=20220830150252 の図 10 のデータの表示に誤植がありました .
誤りの図を図??に修正後の図を図??に記載します .

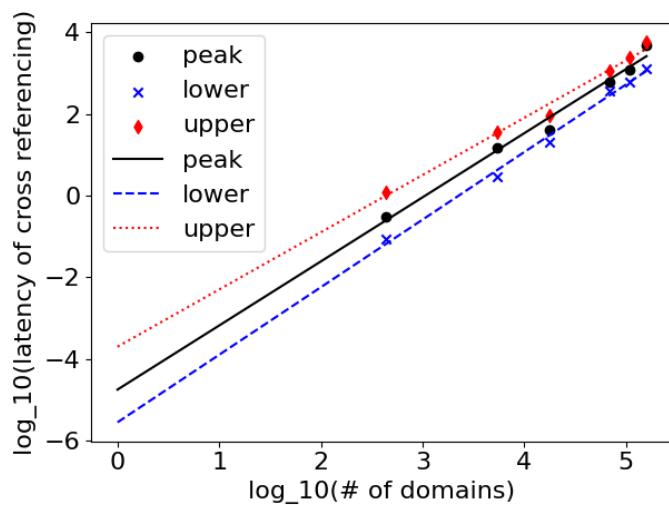


図 1 ブロック不生成間隔 0.5 秒の統計図（誤：訂正前）

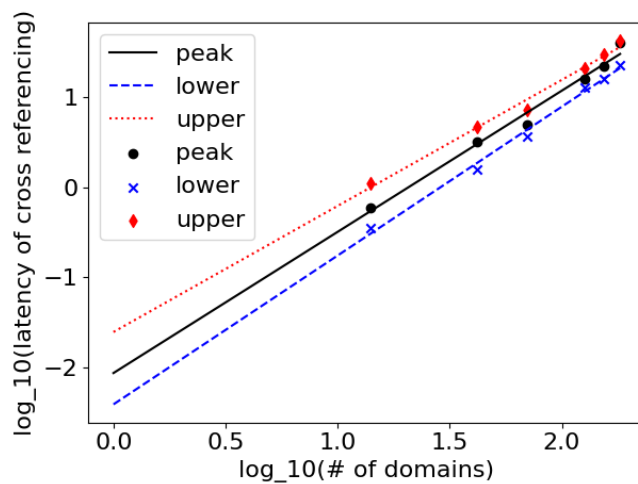


図 2 （正：訂正版）ブロック不生成間隔 0.5 秒の統計図

2 付録: 実験結果

実験結果の大半を以下に示す. 紙面の都合上掲載できなかった全ての実験結果を以下順に記載していく.

2.1 PC7

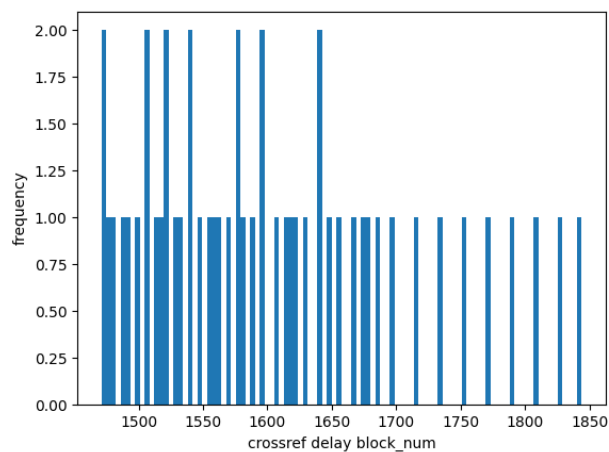


図 3 42D PC7ALL DIF1 INT05 histogram-delay blocknum

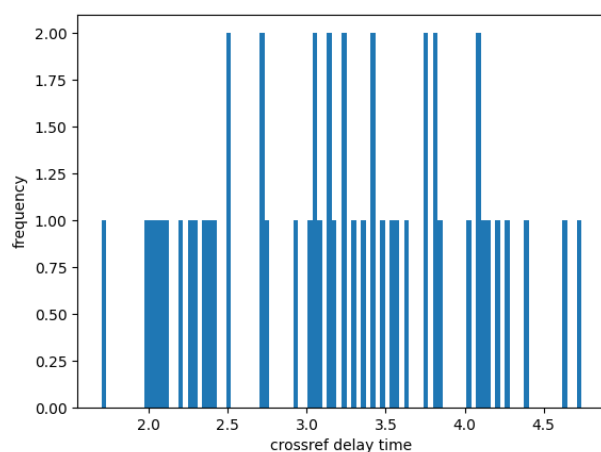


図 4 42D PC7ALL DIF1 INT05 histogram-delay time

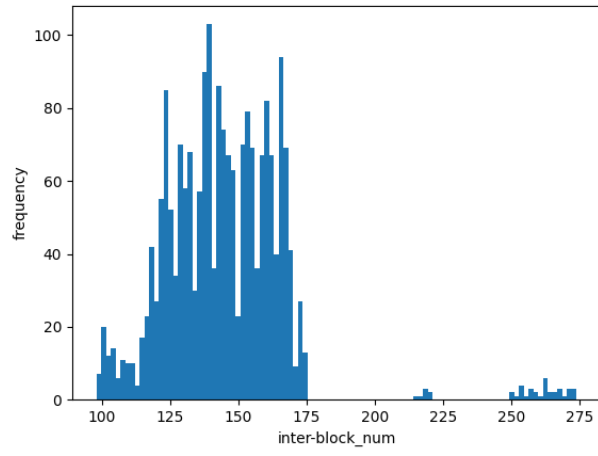


图 5 42D PC7ALL DIF1 INT05 histogram-inter blocknum

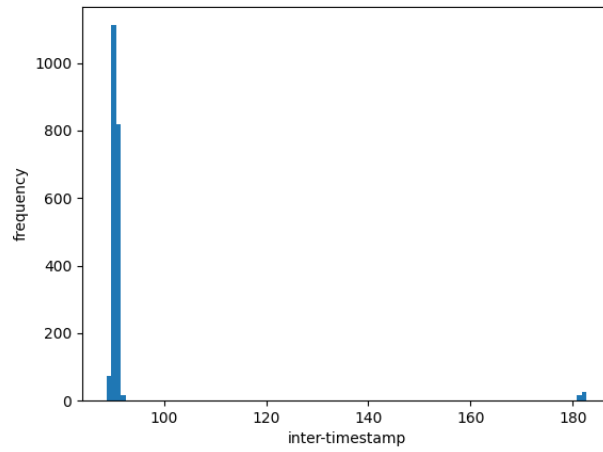


图 6 42D PC7ALL DIF1 INT05 histogram-inter timestamp

2.2 PC9

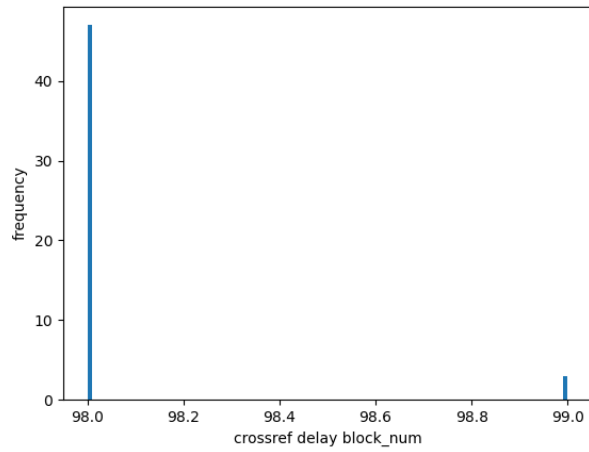


图 7 42D PC7 DIF1 INT5 histogram-delay blocknum

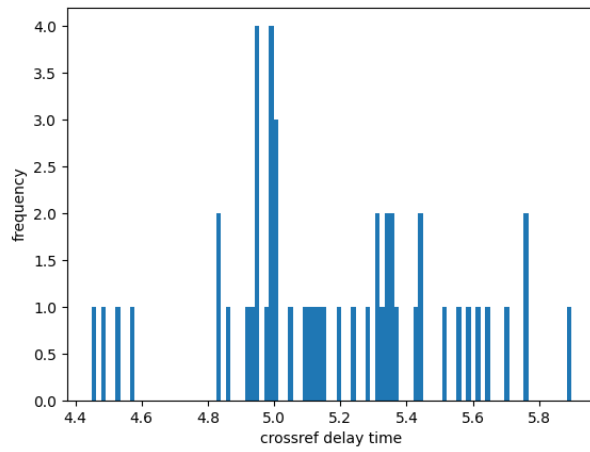
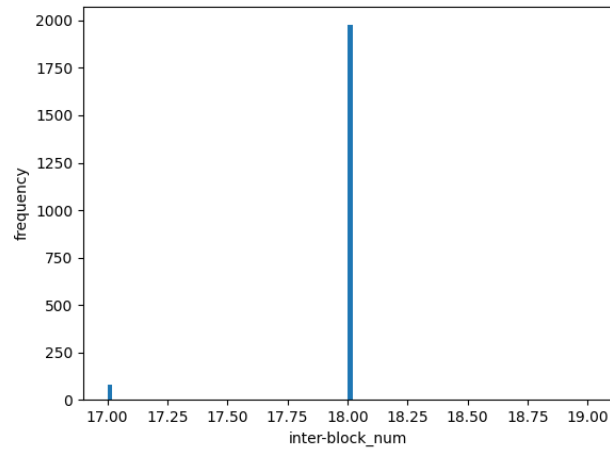
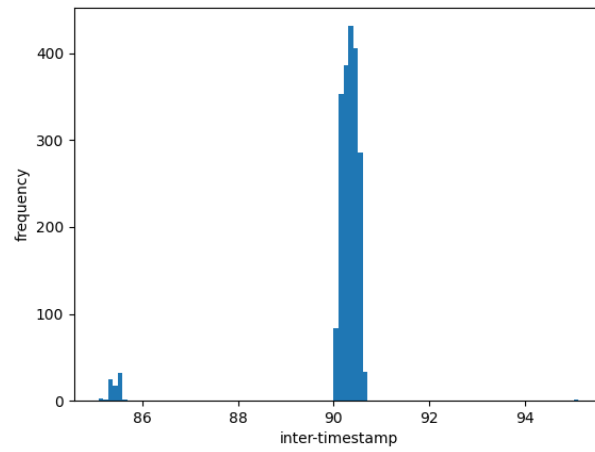


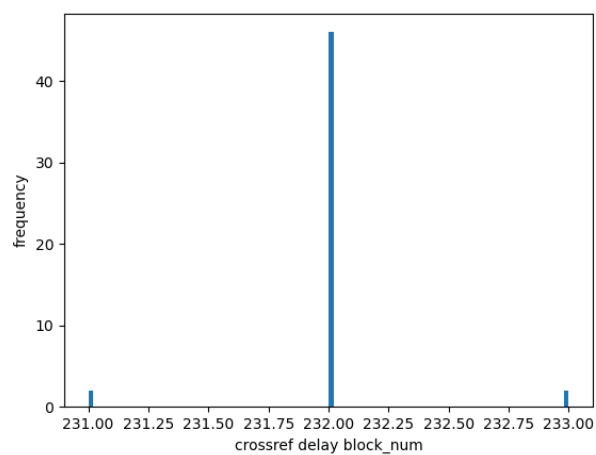
图 8 42D PC7 DIF1 INT5 histogram-delay time



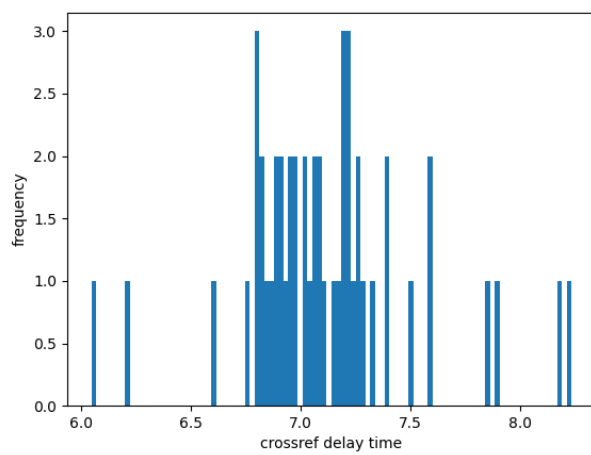
☒ 9 42D PC7 DIF1 INT5 histogram-inter blocknum



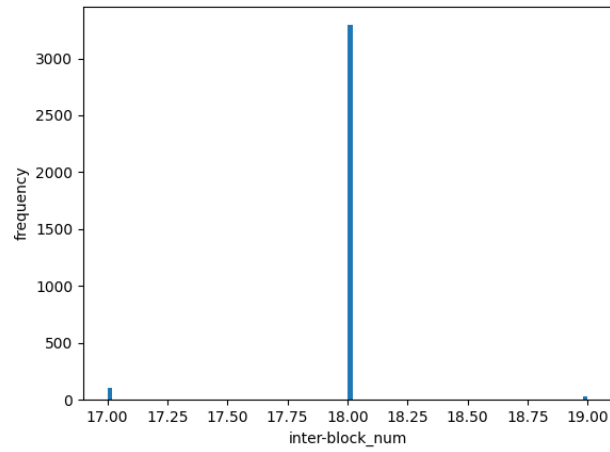
☒ 10 42D PC7 DIF1 INT5 histogram-inter timestamp



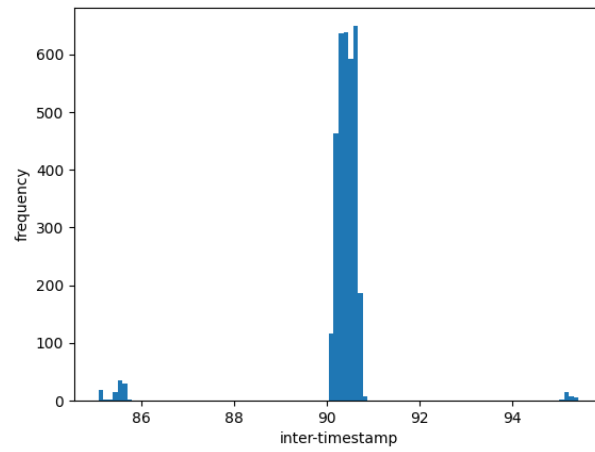
☒ 11 70D PC7ALL DIF1 INT5 histogram-delay blocknum



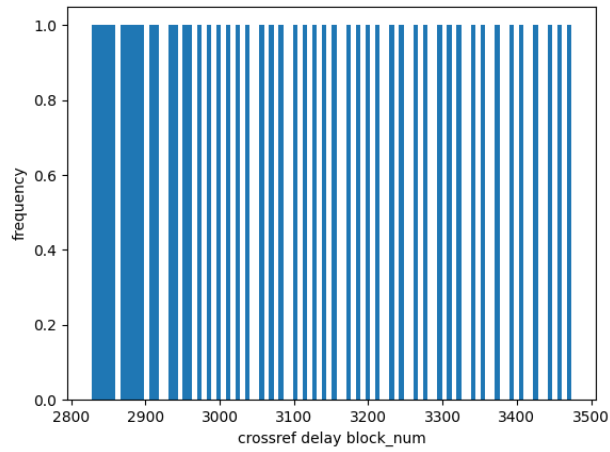
☒ 12 70D PC7ALL DIF1 INT5 histogram-delay time



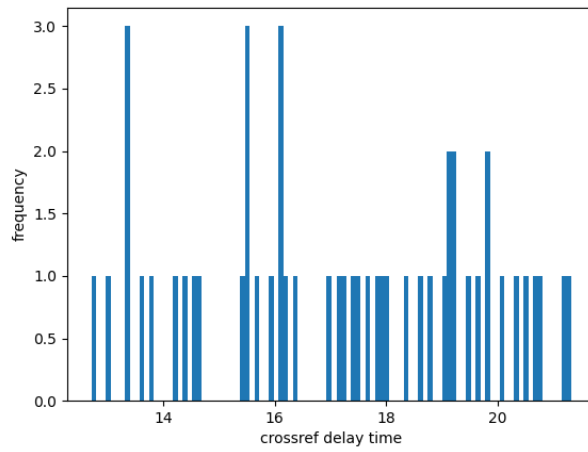
☒ 13 70D PC7ALL DIF1 INT5 histogram-inter blocknum



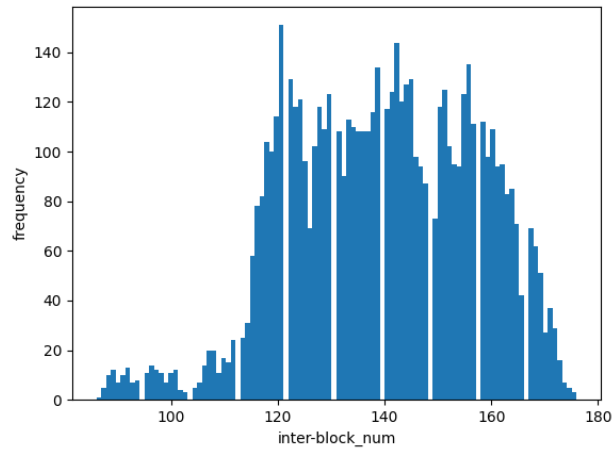
☒ 14 70D PC7ALL DIF1 INT5 histogram-inter timestamp



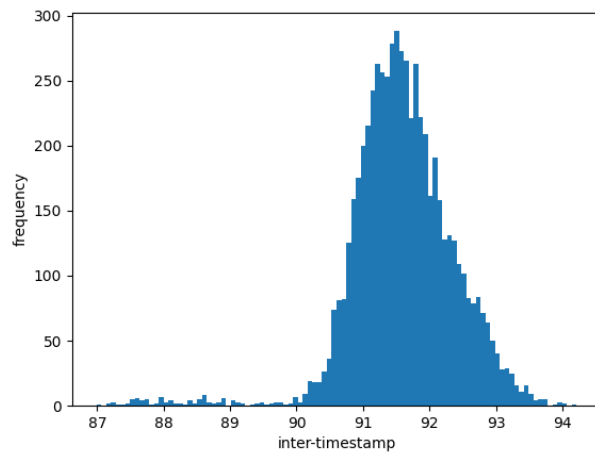
☒ 15 126D PC7ALL DIF1 INT05 histogram-delay blocknum



☒ 16 126D PC7ALL DIF1 INT05 histogram-delay time



☒ 17 126D PC7ALL DIF1 INT05 histogram-inter blocknum



☒ 18 126D PC7ALL DIF1 INT05 histogram-inter timestamp

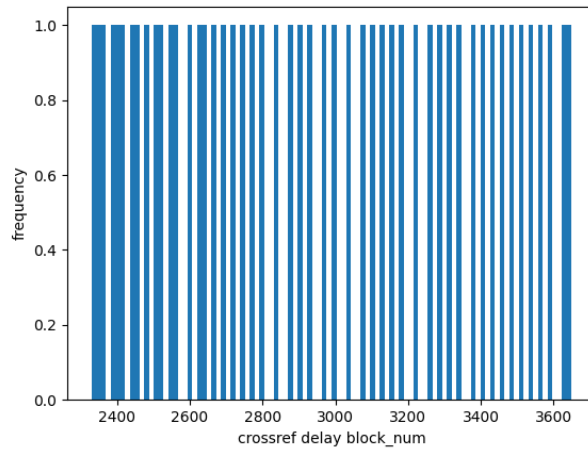


图 19 154D PC7ALL DIF1 INT05 histogram-delay blocknum

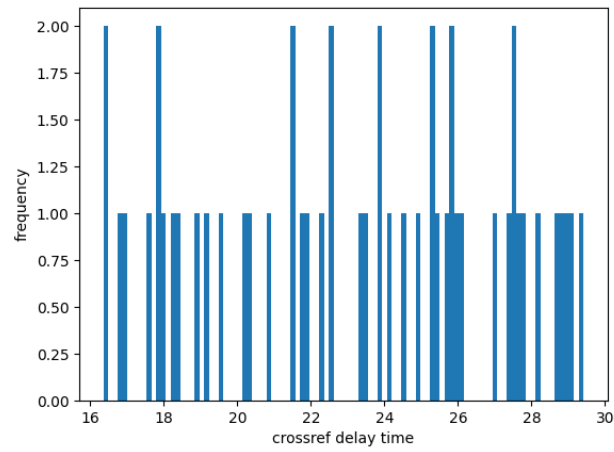
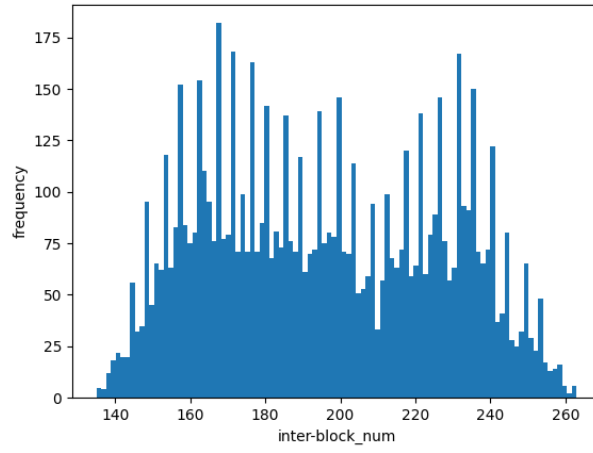
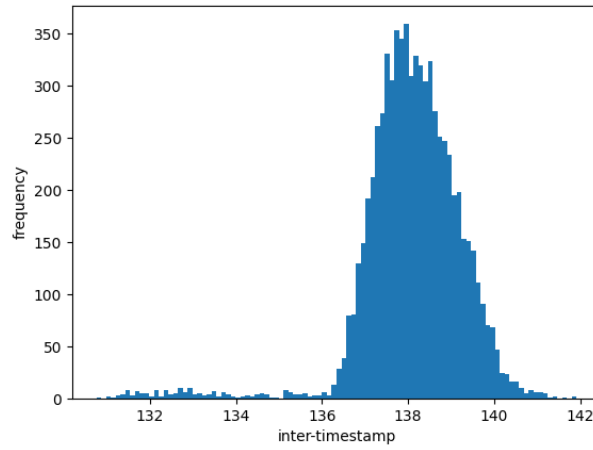


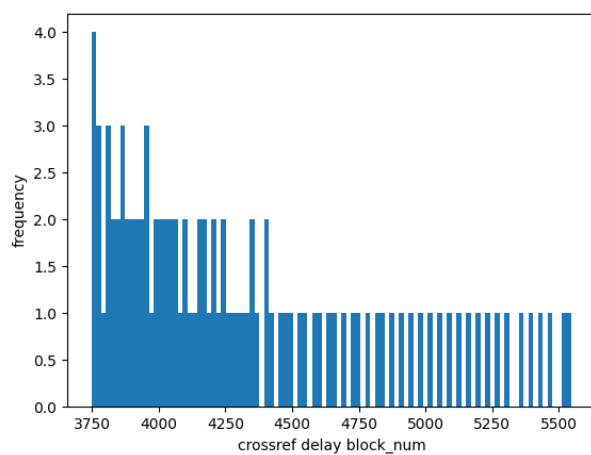
图 20 154D PC7ALL DIF1 INT05 histogram-delay time



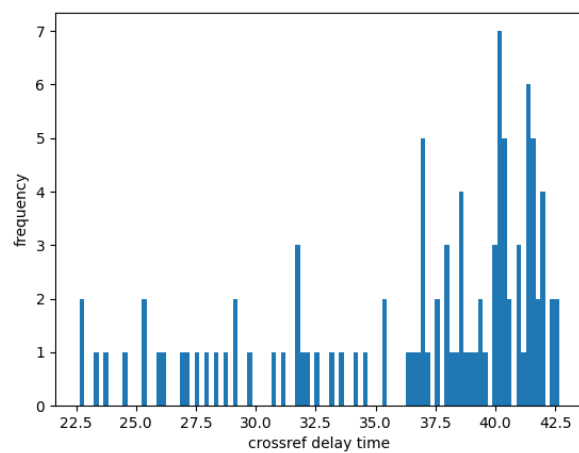
21 154D PC7ALL DIF1 INT05 histogram-inter blocknum



22 154D PC7ALL DIF1 INT05 histogram-inter timestamp



23 182D PC7ALL DIF1 INT05 histogram-inter timestamp



24 182D PC7ALL DIF1 INT05 histogram-inter timestamp

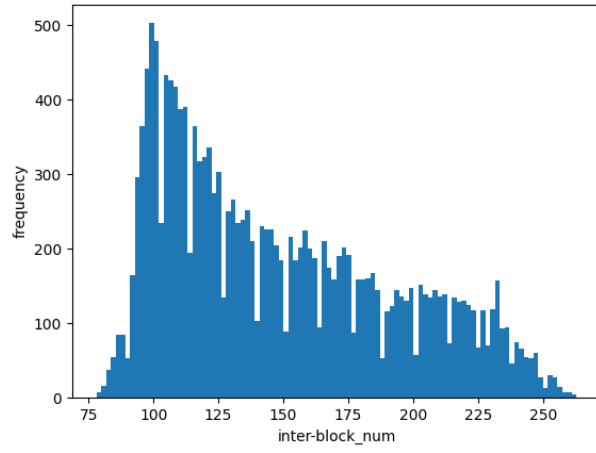


图 25 182D PC7ALL DIF1 INT05 histogram-inter timestamp

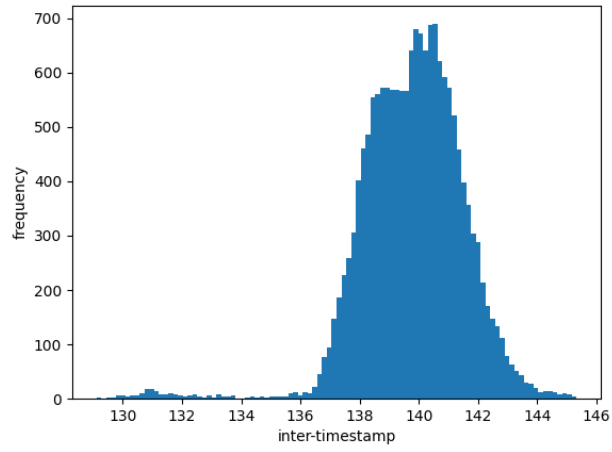
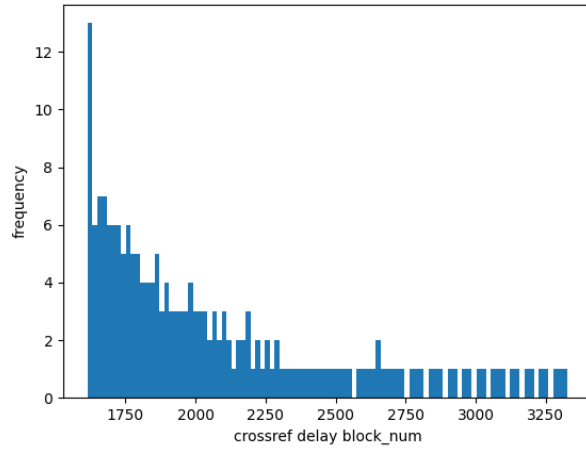
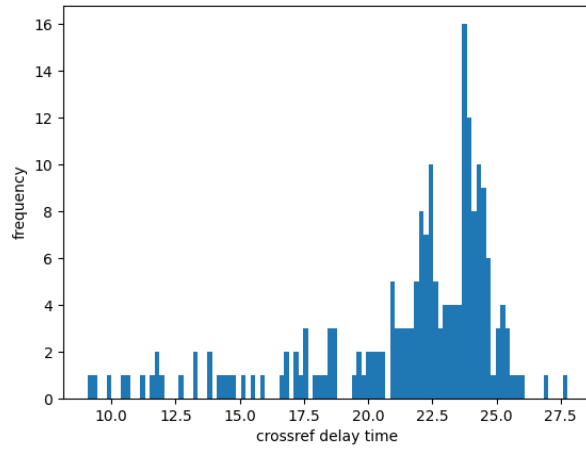


图 26 182D PC7ALL DIF1 INT05 histogram-inter timestamp



27 99D PC9ALL DIF1 INT05 histogram-inter timestamp



28 99D PC9ALL DIF1 INT05 histogram-inter timestamp

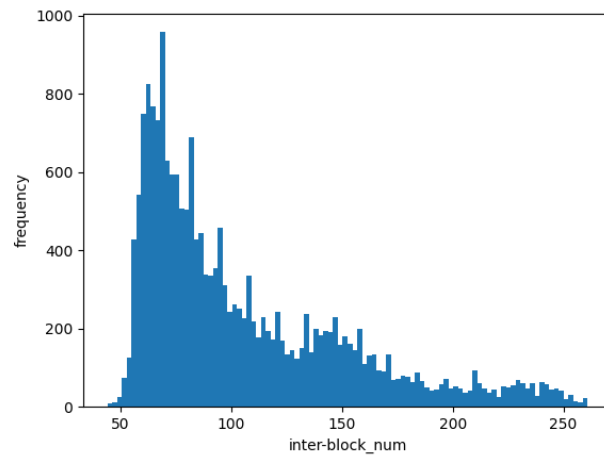


图 29 99D PC9ALL DIF1 INT05 histogram-inter timestamp

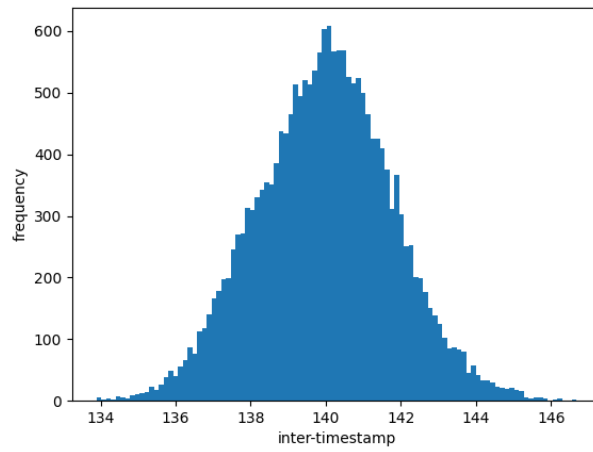
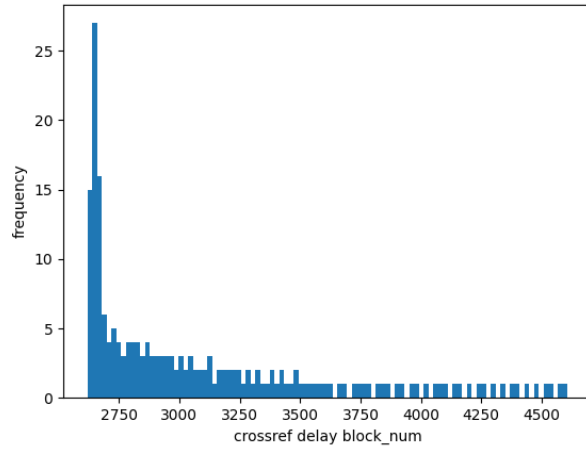
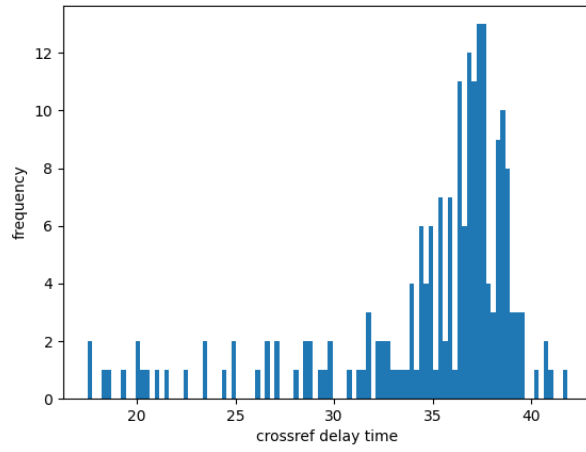


图 30 99D PC9ALL DIF1 INT05 histogram-inter timestamp



31 135D PC9ALL DIF1 INT05 histogram-inter timestamp



32 135D PC9ALL DIF1 INT05 histogram-inter timestamp

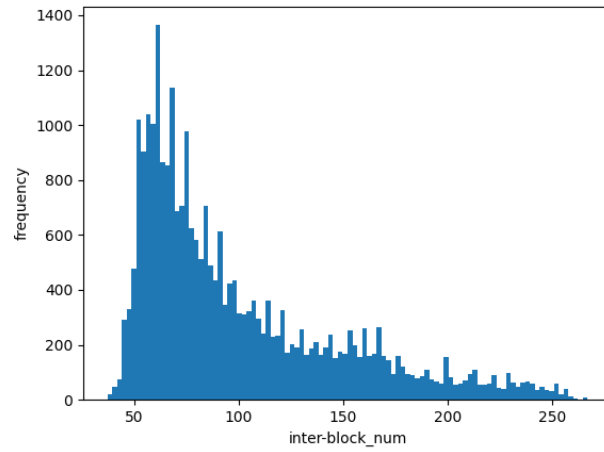


图 33 135D PC9ALL DIF1 INT05 histogram-inter timestamp

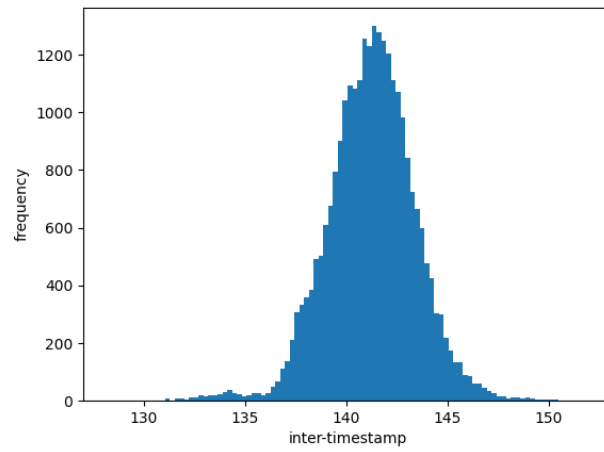
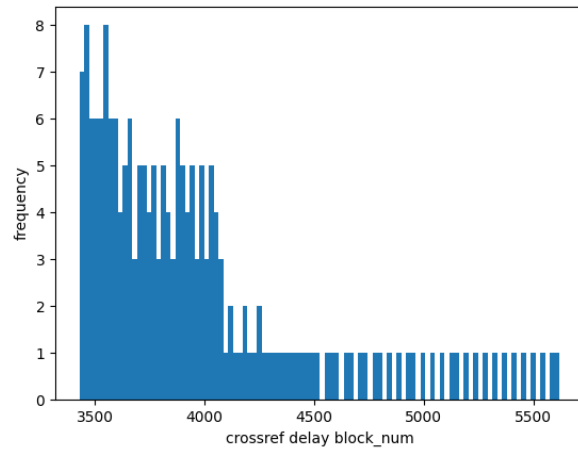
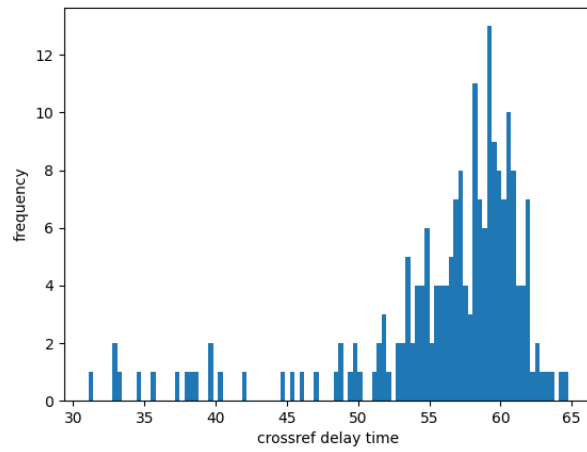


图 34 135D PC9ALL DIF1 INT05 histogram-inter timestamp



35 180D PC9ALL DIF1 INT05 histogram-inter timestamp



36 180D PC9ALL DIF1 INT05 histogram-inter timestamp

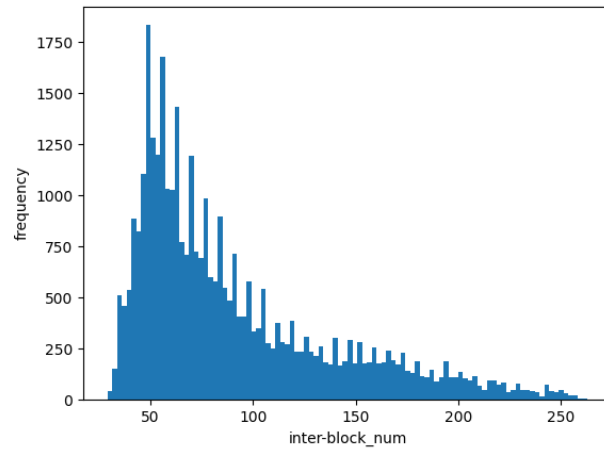


图 37 180D PC9ALL DIF1 INT05 histogram-inter timestamp

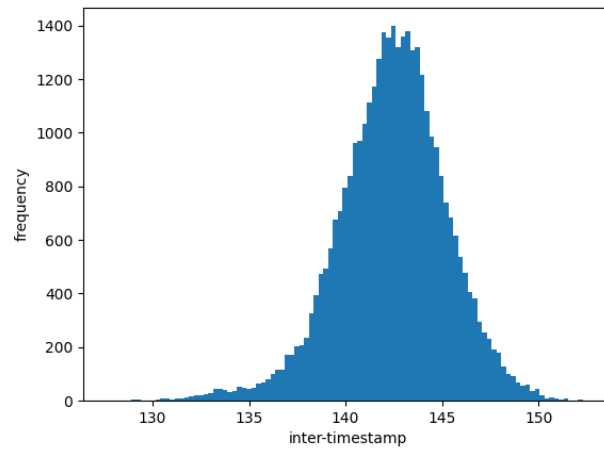


图 38 180D PC9ALL DIF1 INT05 histogram-inter timestamp

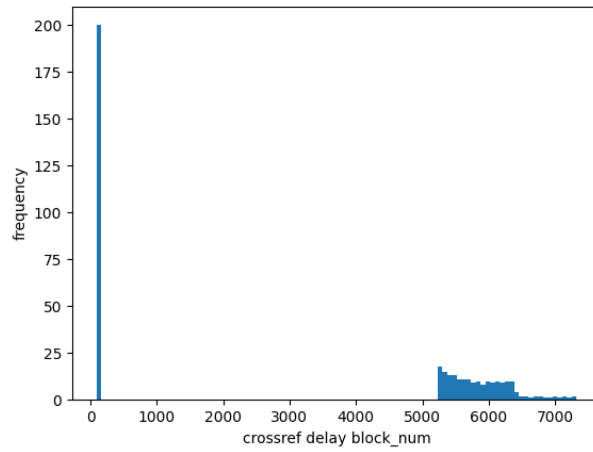


图 39 216D PC9ALL DIF1 INT05 histogram-inter timestamp

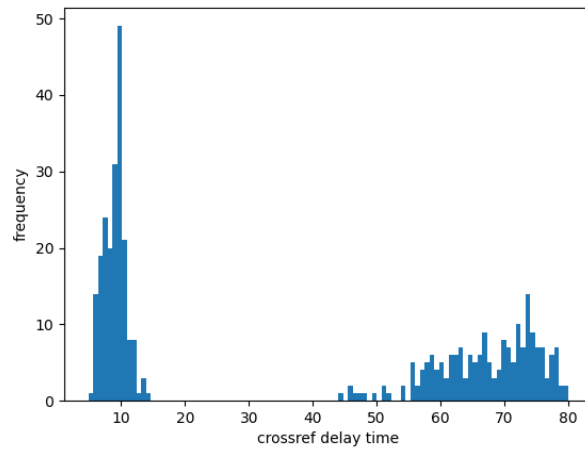


图 40 216D PC9ALL DIF1 INT05 histogram-inter timestamp

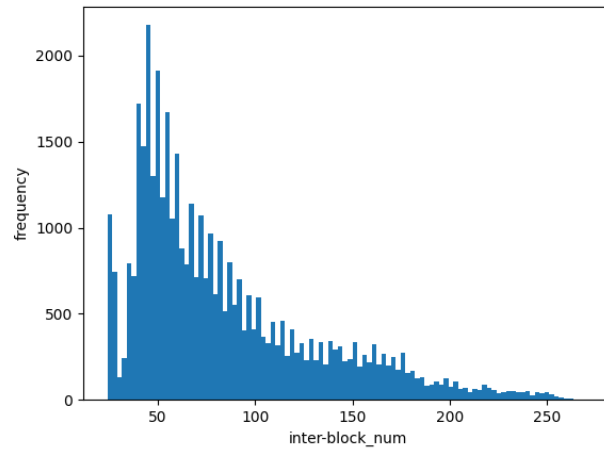


图 41 216D PC9ALL DIF1 INT05 histogram-inter timestamp

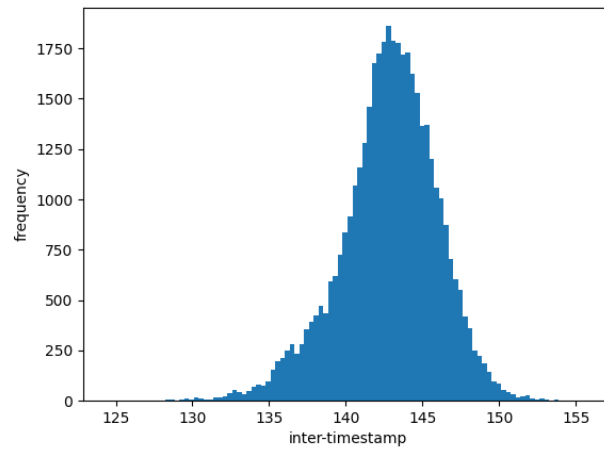
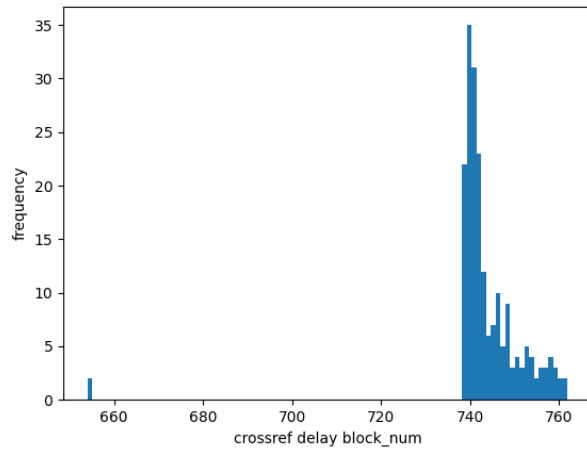
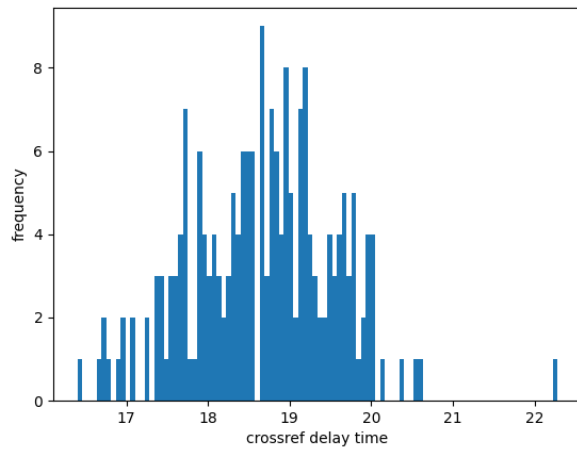


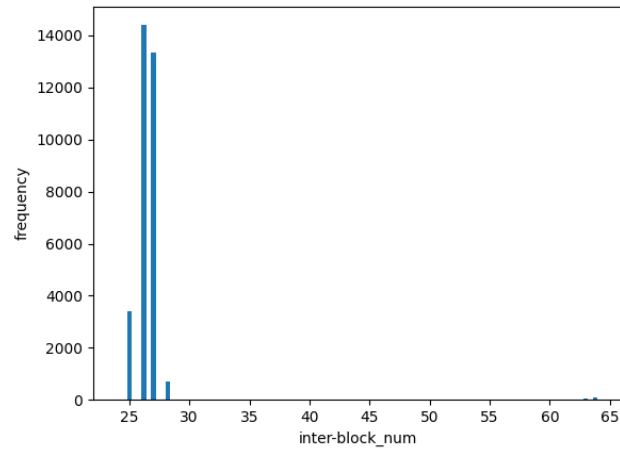
图 42 216D PC9ALL DIF1 INT05 histogram-inter timestamp



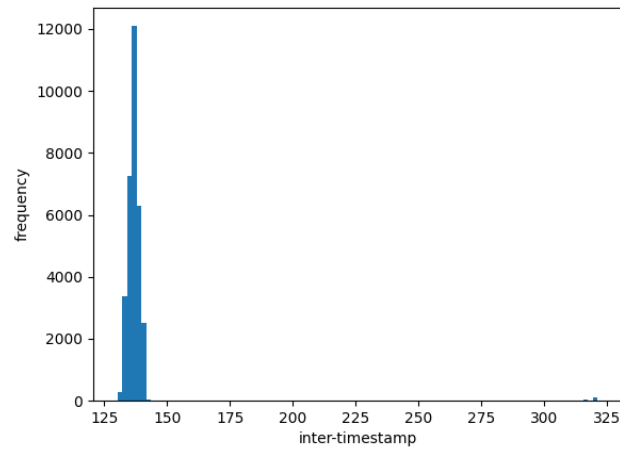
43 180D PC9 DIF1 INT5 histogram-inter timestamp



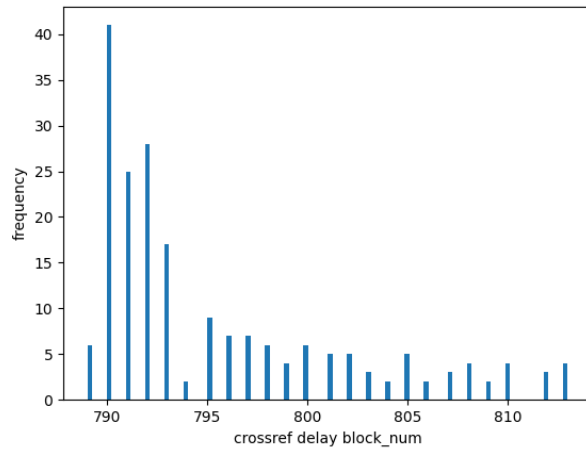
44 180D PC9 DIF1 INT5 histogram-inter timestamp



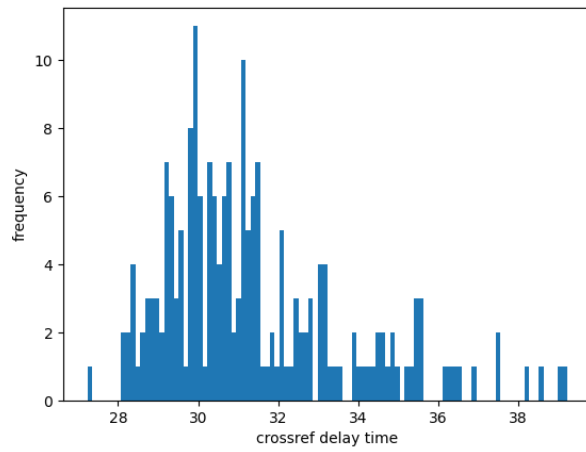
45 180D PC9 DIF1 INT5 histogram-inter timestamp



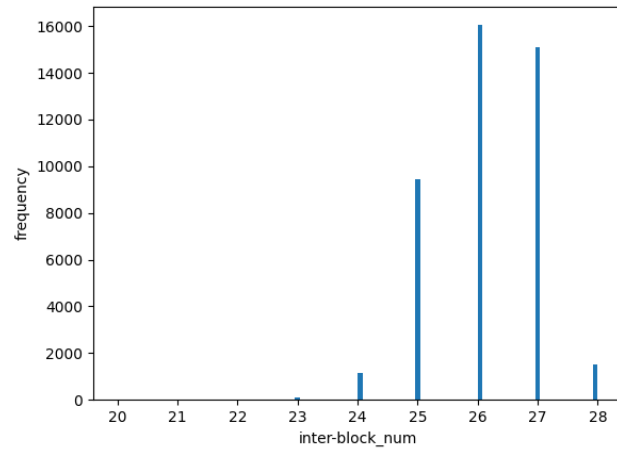
46 180D PC9 DIF1 INT5 histogram-inter timestamp



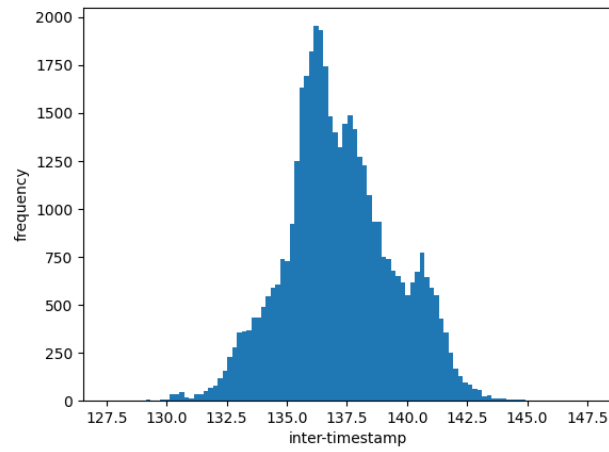
47 218D PC9 DIF1 INT5 histogram-inter timestamp



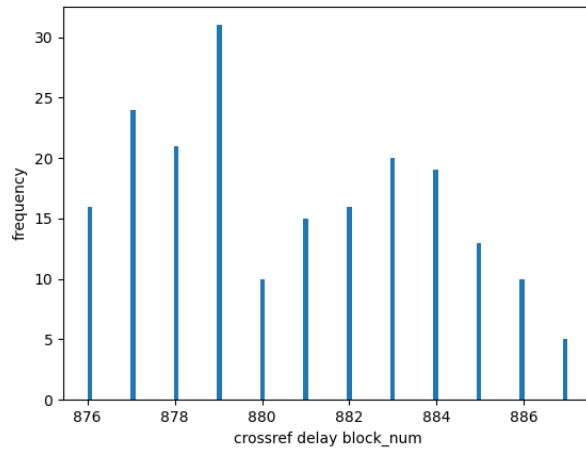
48 218D PC9 DIF1 INT5 histogram-inter timestamp



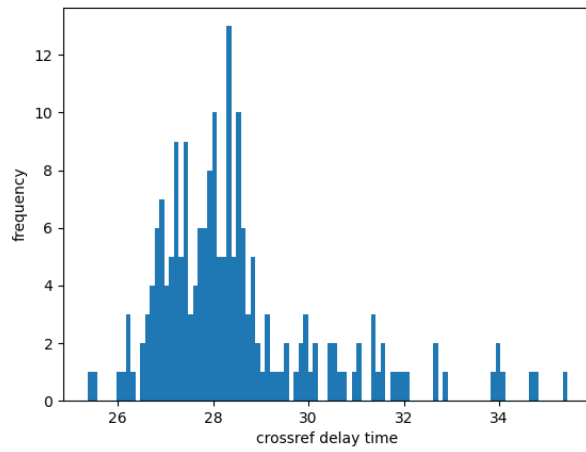
☒ 49 218D PC9 DIF1 INT5 histogram-inter timestamp



☒ 50 218D PC9 DIF1 INT5 histogram-inter timestamp



51 218D PC9 DIF1 INT5 histogram-inter timestamp



52 218D PC9 DIF1 INT5 histogram-inter timestamp

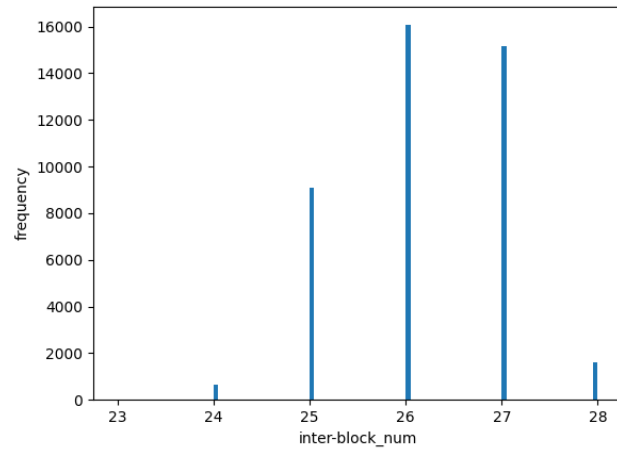


图 53 218D PC9 DIF1 INT5 histogram-inter timestamp

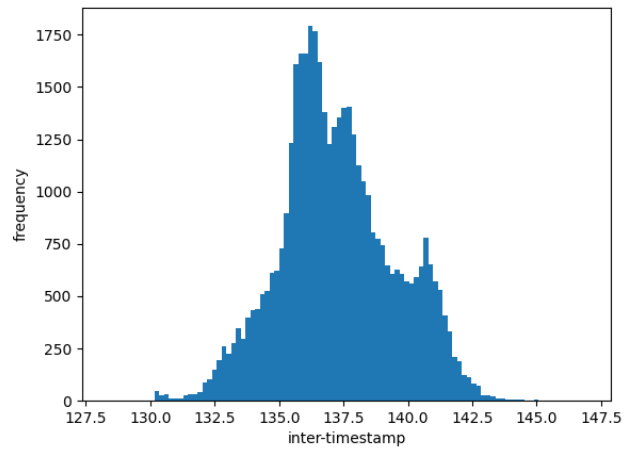


图 54 218D PC9 DIF1 INT5 histogram-inter timestamp

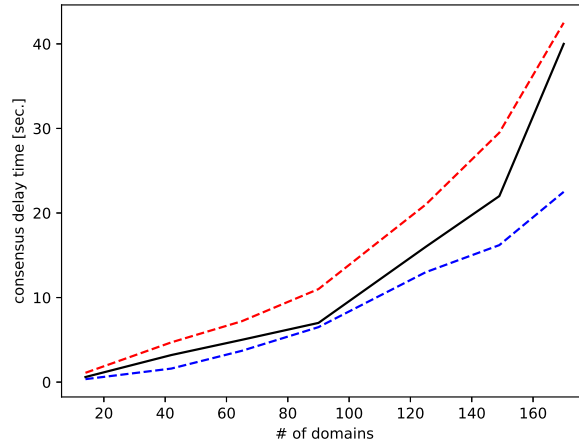


図 55 consensus delay time tendency PC7ALL INT05

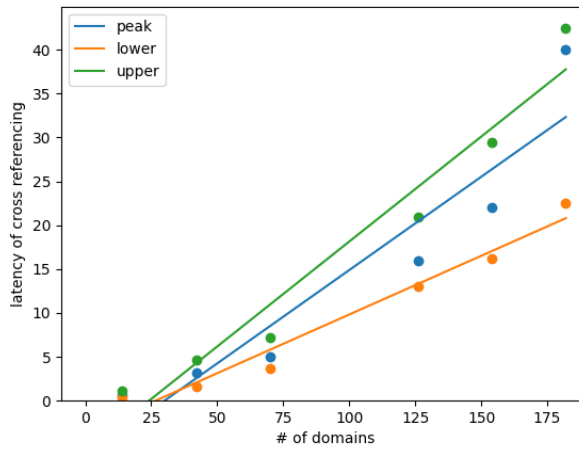


図 56 consensus delay time tendency PC7ALL INT5

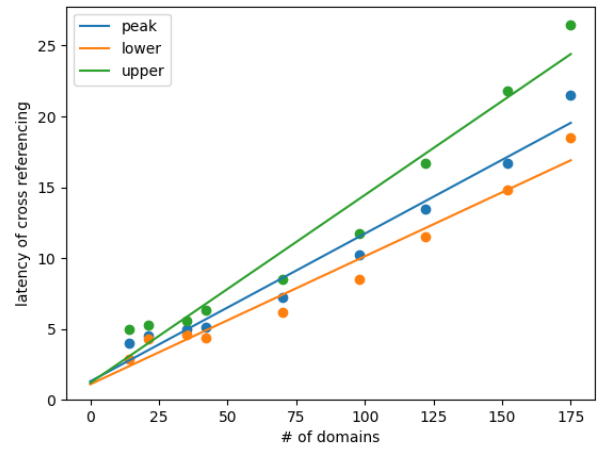


Figure 57 consensus delay time tendency PC7ALL INT5

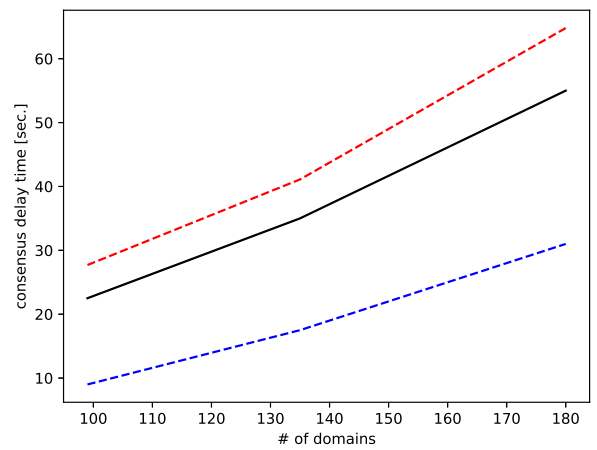


Figure 58 consensus delay time tendency PC9 INT05

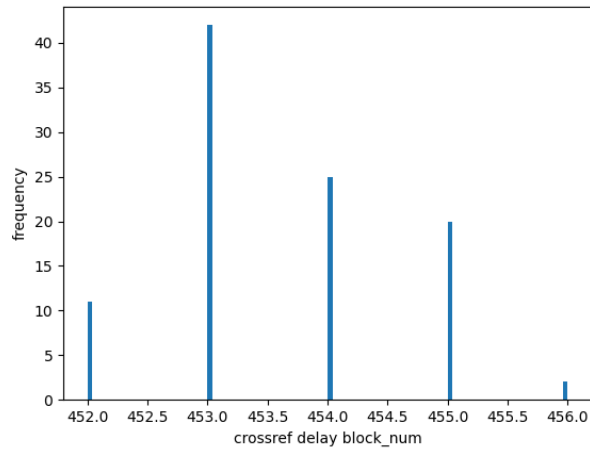


图 59 126D PC7ALL DIF1 INT5 histogram-delay blocknum

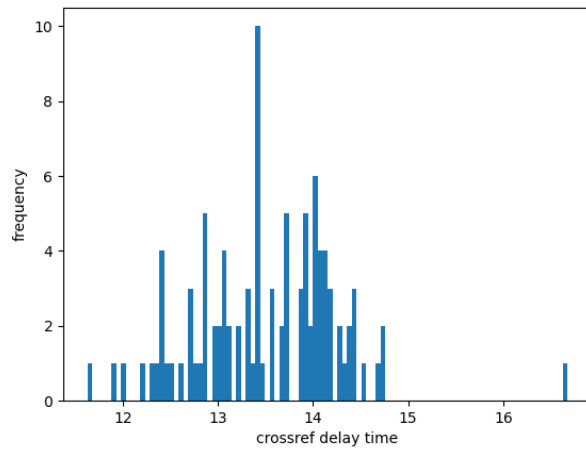


图 60 126D PC7ALL DIF1 INT5 histogram-inter timestamp

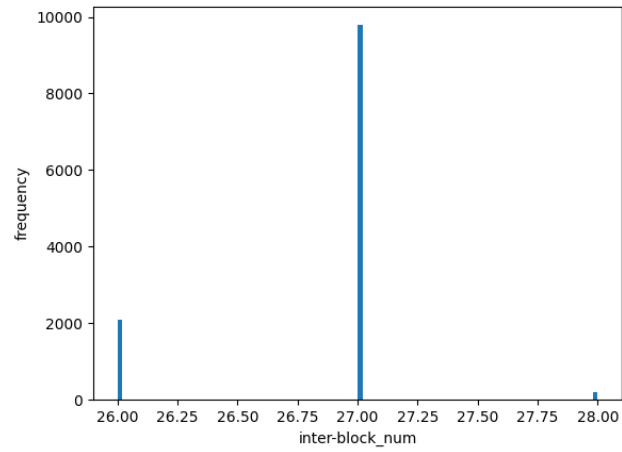


图 61 126D PC7ALL DIF1 INT5 histogram-inter timestamp

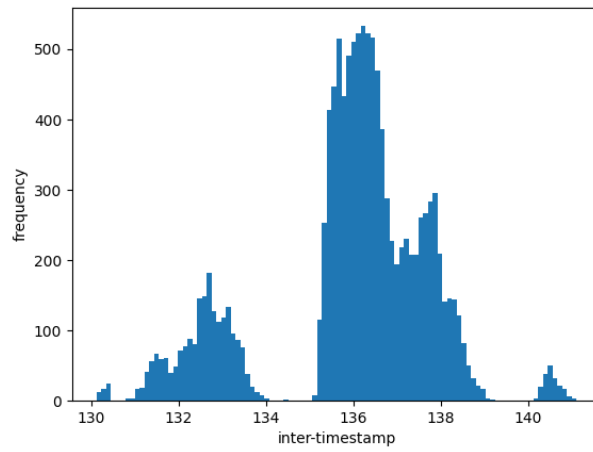


图 62 126D PC7ALL DIF1 INT5 histogram-inter timestamp

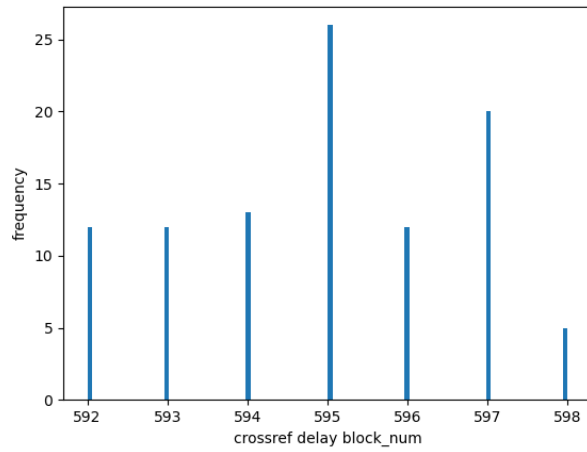


图 63 154D PC7ALL DIF1 INT5 histogram-delay blocknum

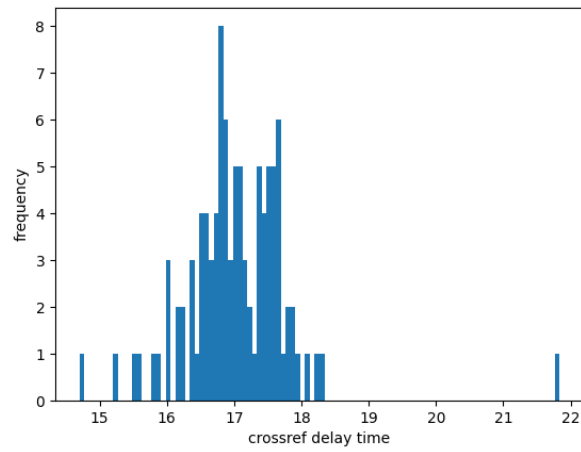


图 64 154D PC7ALL DIF1 INT5 histogram-inter timestamp

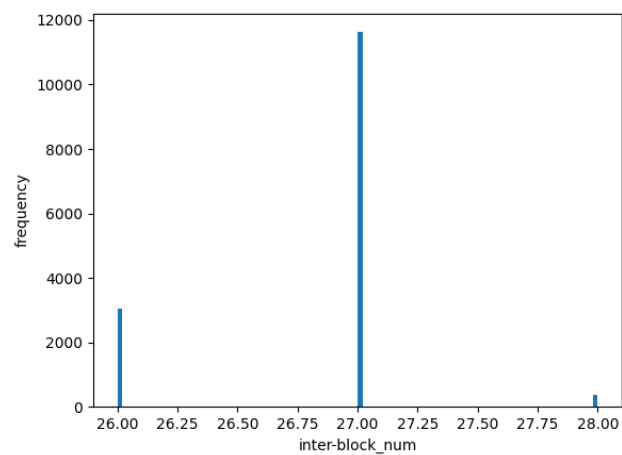


图 65 154D PC7ALL DIF1 INT5 histogram-inter timestamp

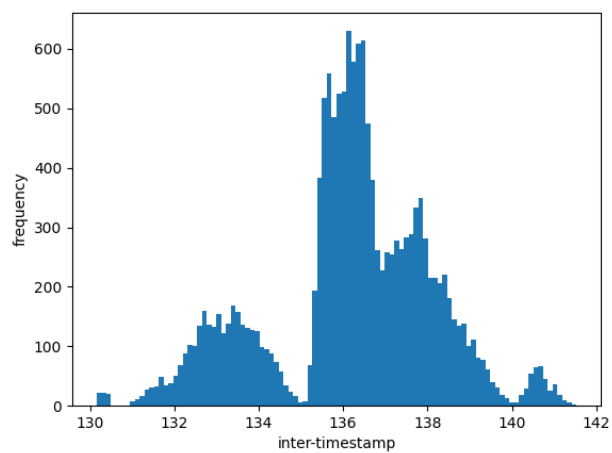


图 66 154D PC7ALL DIF1 INT5 histogram-inter timestamp

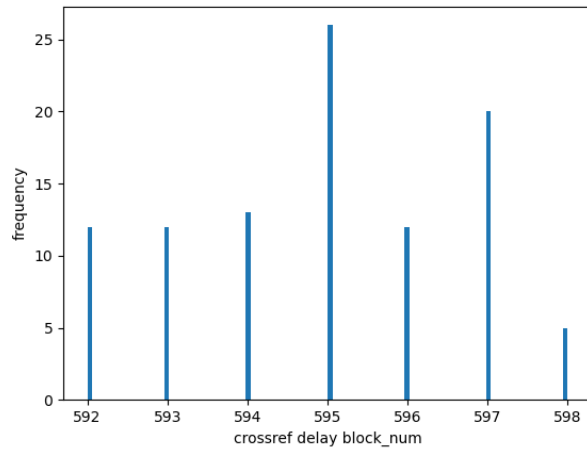


图 67 154D PC7ALL DIF1 INT5 histogram-delay blocknum

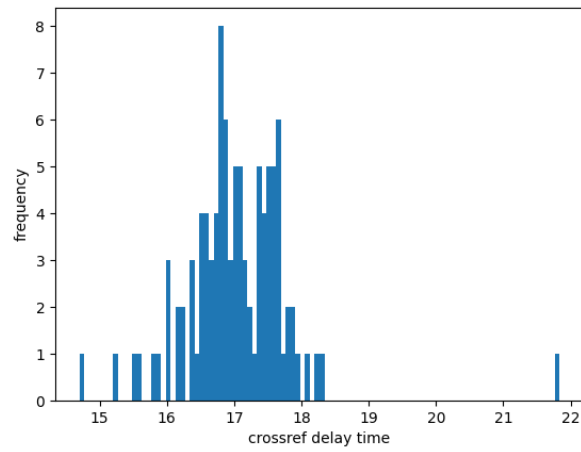


图 68 154D PC7ALL DIF1 INT5 histogram-inter timestamp

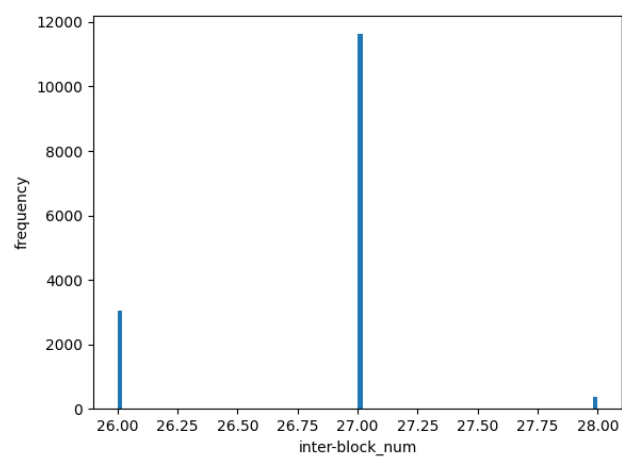


图 69 154D PC7ALL DIF1 INT5 histogram-inter timestamp

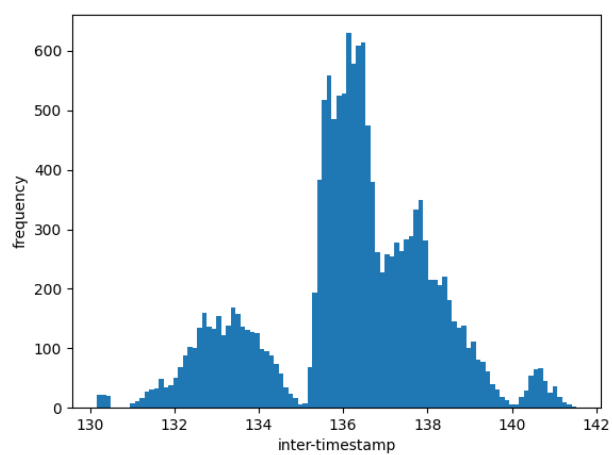
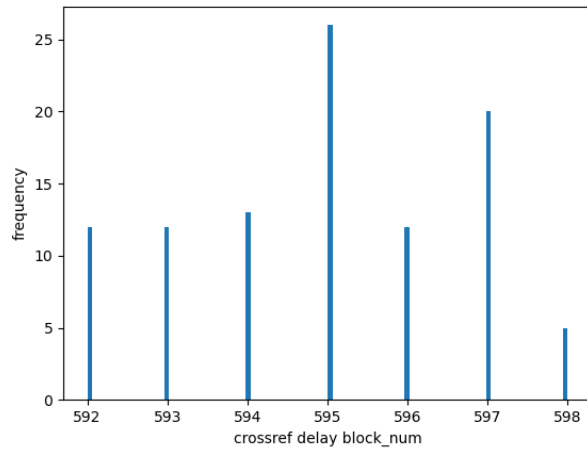
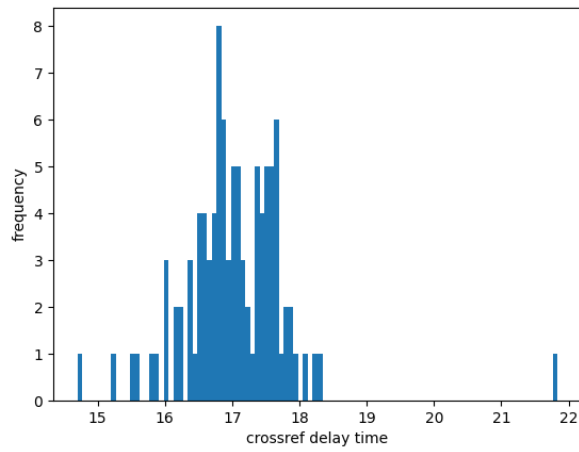


图 70 154D PC7ALL DIF1 INT5 histogram-inter timestamp



☒ 71 154D PC7ALL DIF1 INT5 histogram-delay blocknum



☒ 72 154D PC7ALL DIF1 INT5 histogram-delay time

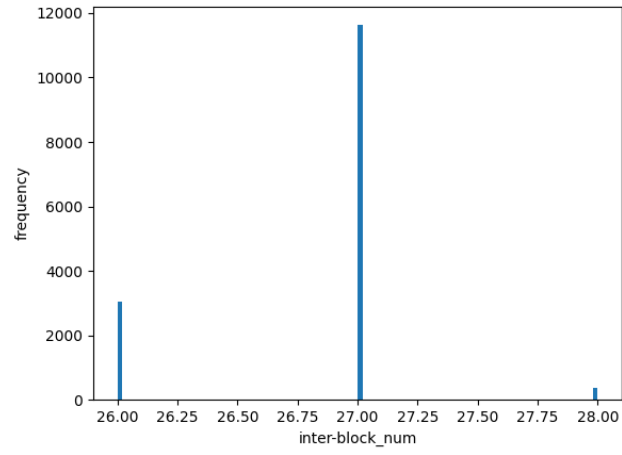


图 73 154D PC7ALL DIF1 INT5 histogram-inter blocknum

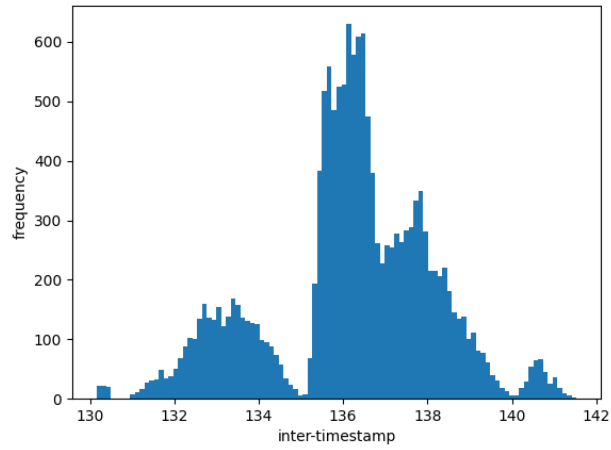


图 74 154D PC7ALL DIF1 INT5 histogram-inter timestamp

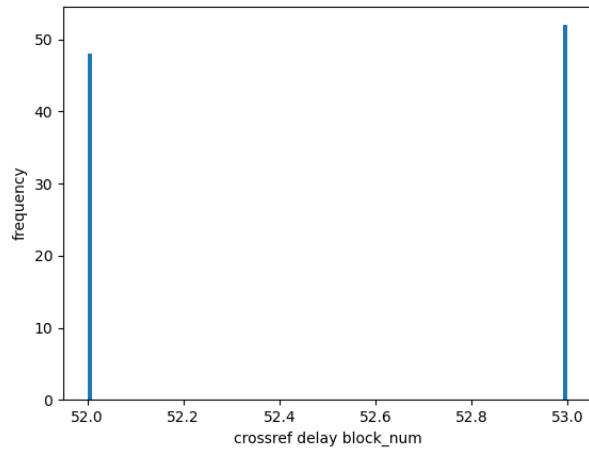


图 75 154D PC7ALL DIF1 INT5 histogram-delay blocknum

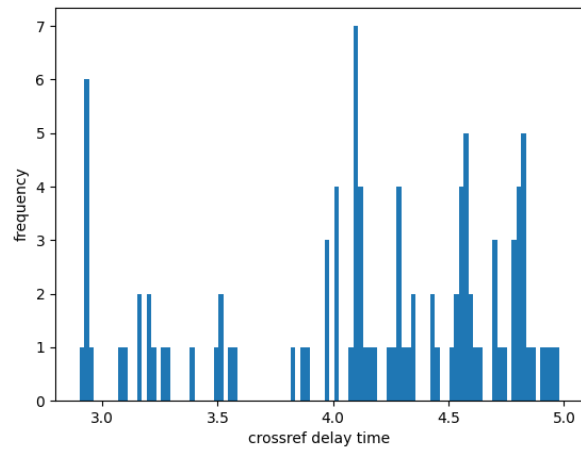
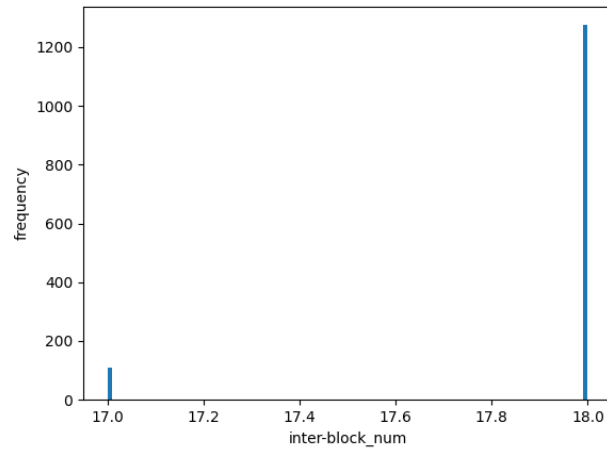
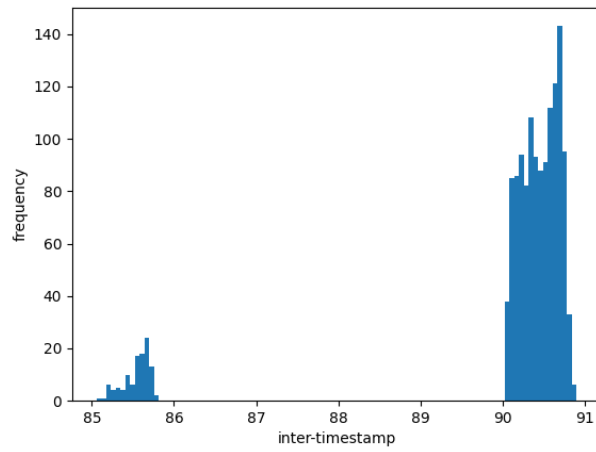


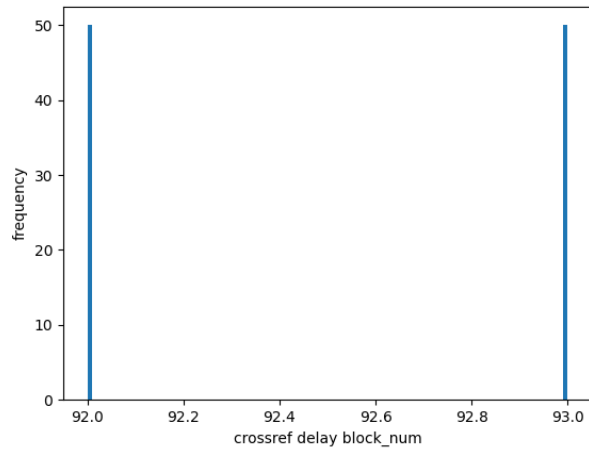
图 76 14D PC7ALL DIF1 INT5 histogram-delay time



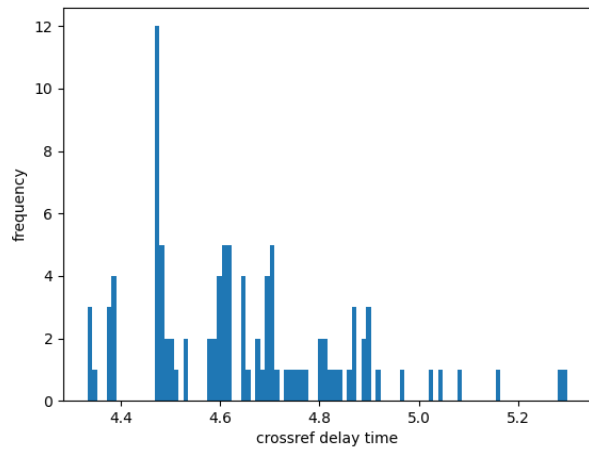
77 14D PC7ALL DIF1 INT5 histogram-inter blocknum



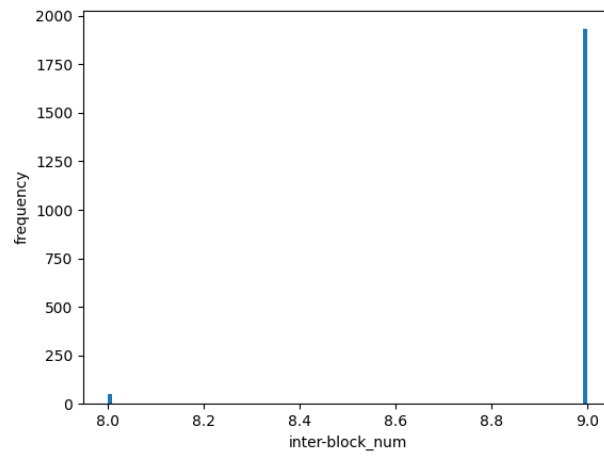
78 14D PC7ALL DIF1 INT5 histogram-inter timestamp



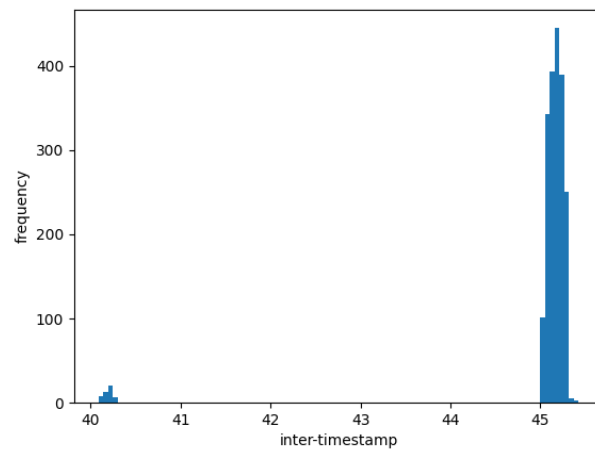
☒ 79 21D PC7ALL DIF1 INT5 histogram-delay blocknum



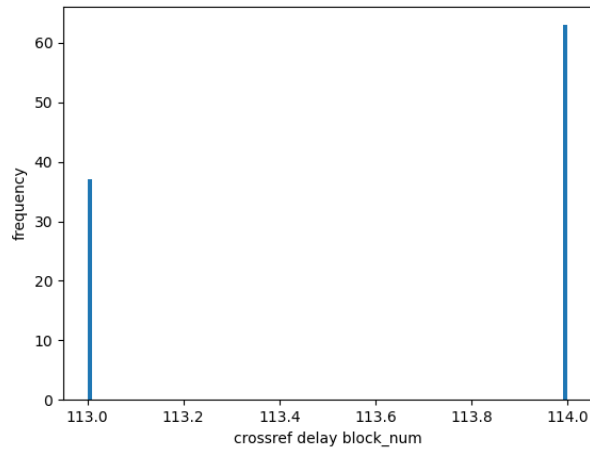
☒ 80 21D PC7ALL DIF1 INT5 histogram-delay time



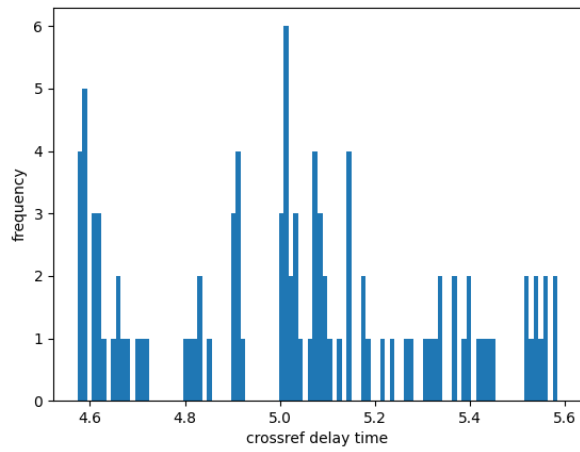
81 21D PC7ALL DIF1 INT5 histogram-inter blocknum



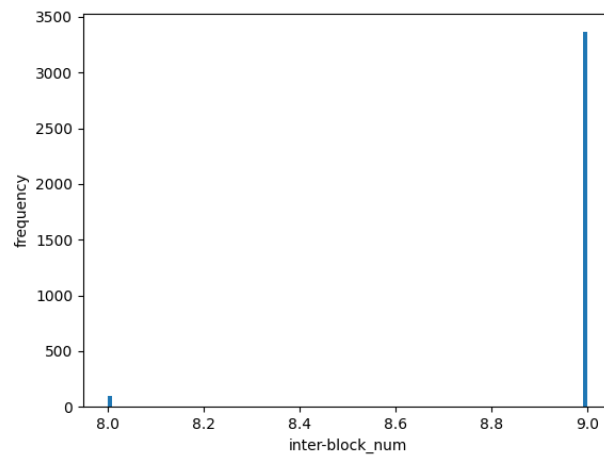
82 21D PC7ALL DIF1 INT5 histogram-inter timestamp



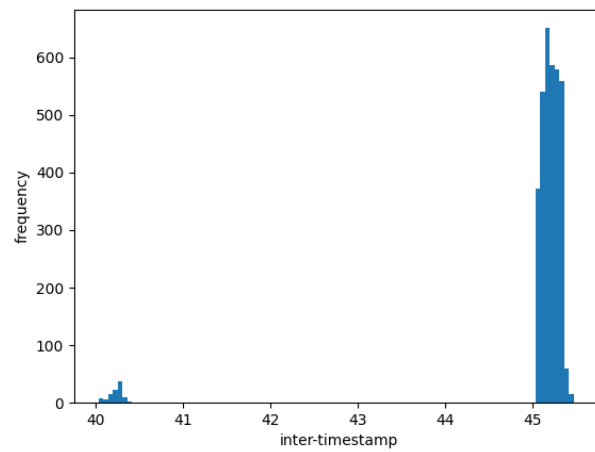
83 35D PC7ALL DIF1 INT5 histogram-delay blocknum



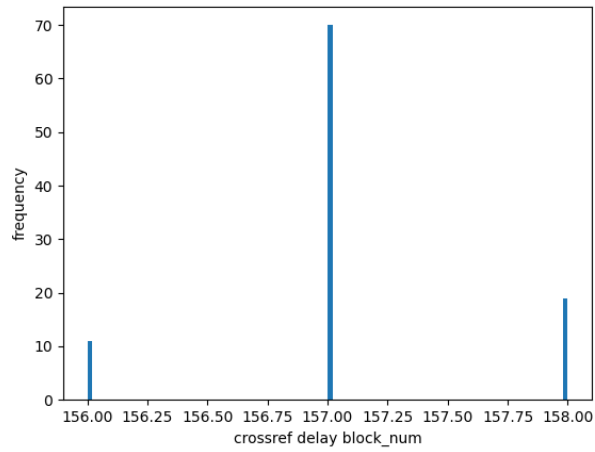
84 35D PC7ALL DIF1 INT5 histogram-delay time



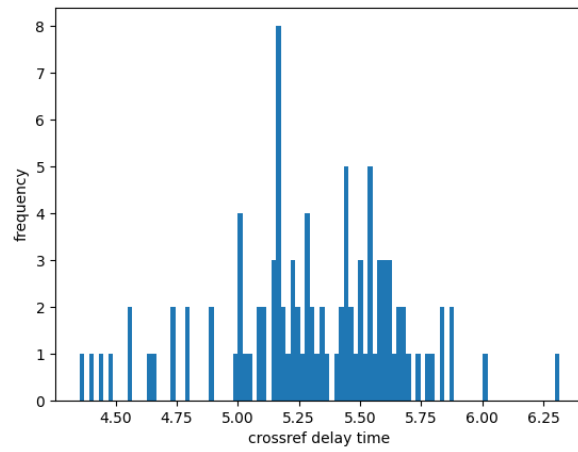
85 35D PC7ALL DIF1 INT5 histogram-inter blocknum



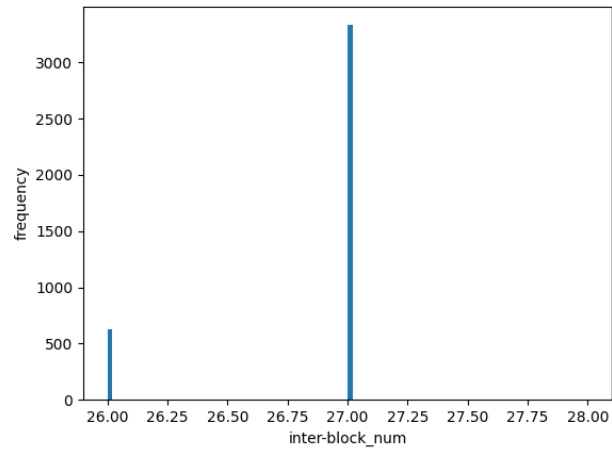
86 35D PC7ALL DIF1 INT5 histogram-inter timestamp



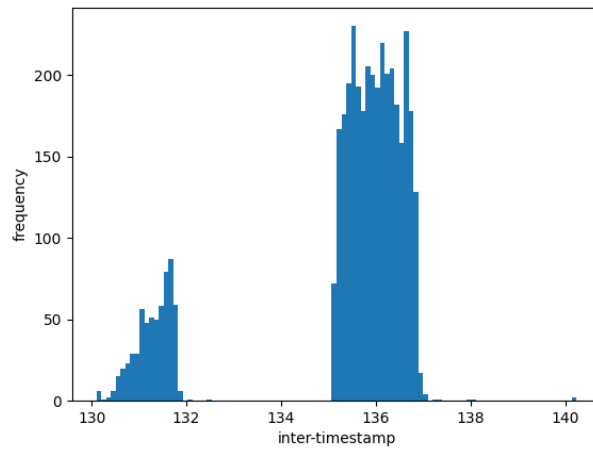
☒ 87 42D PC7ALL DIF1 INT5 histogram-delay blocknum



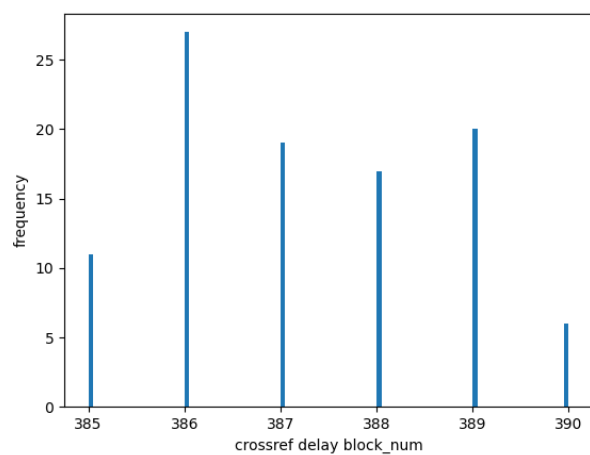
☒ 88 42D PC7ALL DIF1 INT5 histogram-delay time



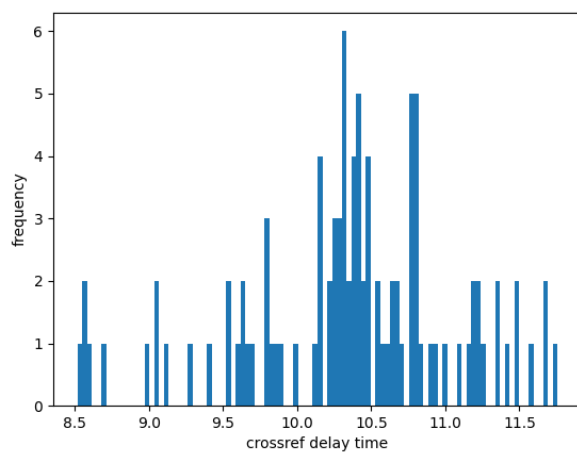
89 42D PC7ALL DIF1 INT5 histogram-inter blocknum



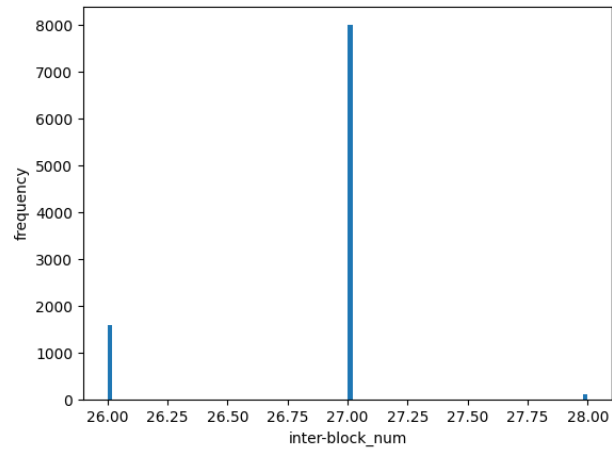
90 42D PC7ALL DIF1 INT5 histogram-inter timestamp



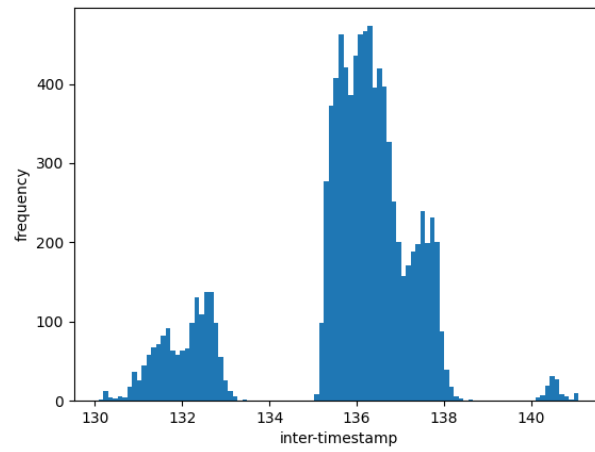
91 98D 1012 PC7ALL DIF1 INT5 histogram-delay blocknum



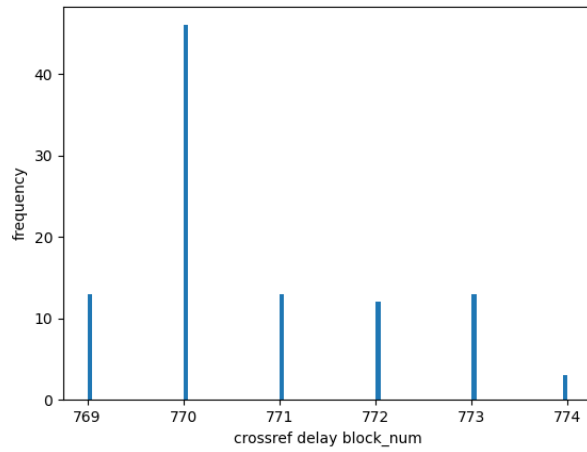
92 98D 1012 PC7ALL DIF1 INT5 histogram-delay time



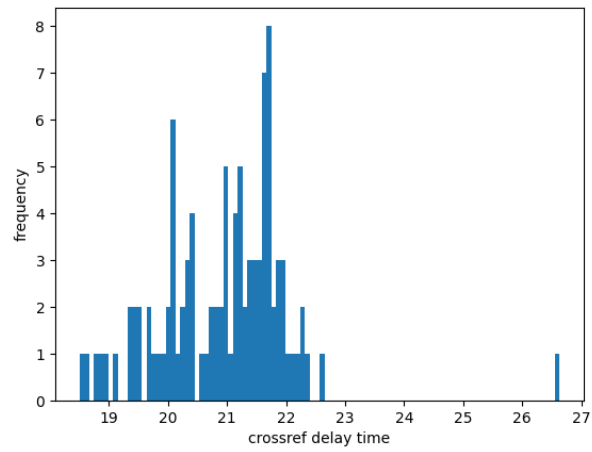
93 98D 1012 PC7ALL DIF1 INT5 histogram-inter blocknum



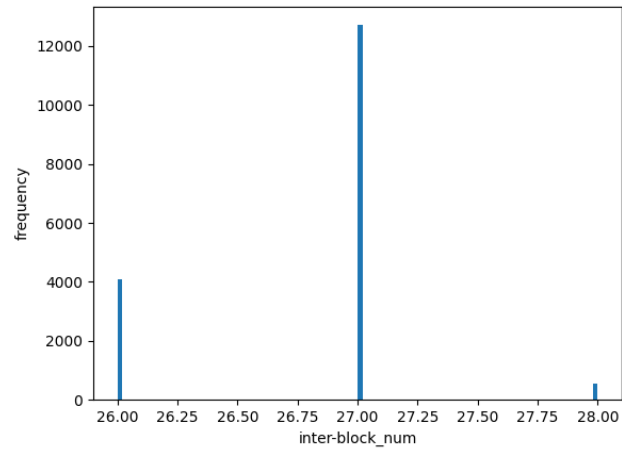
94 98D 1012 PC7ALL DIF1 INT5 histogram-inter timestamp



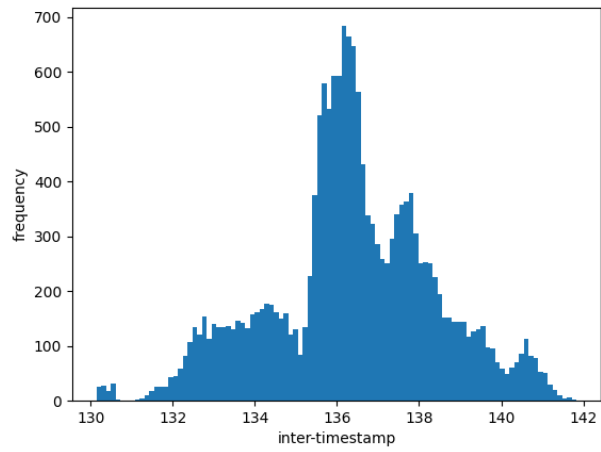
☒ 95 182D PC7ALL DIF1 INT5 histogram-delay blocknum



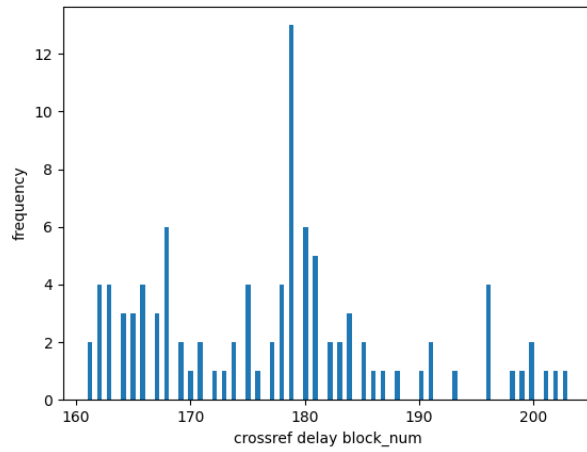
☒ 96 182D PC7ALL DIF1 INT5 histogram-delay time



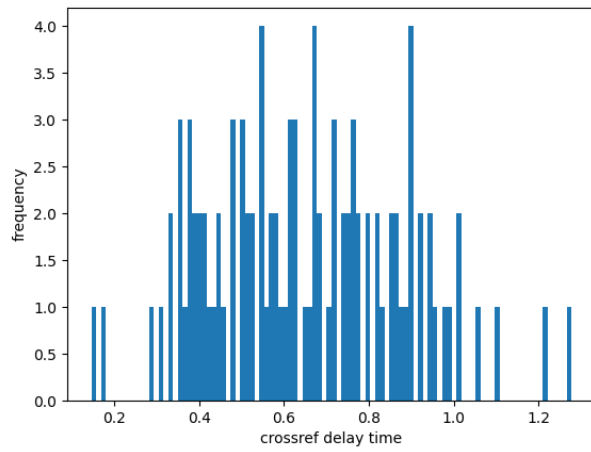
97 182D PC7ALL DIF1 INT5 histogram-inter blocknum



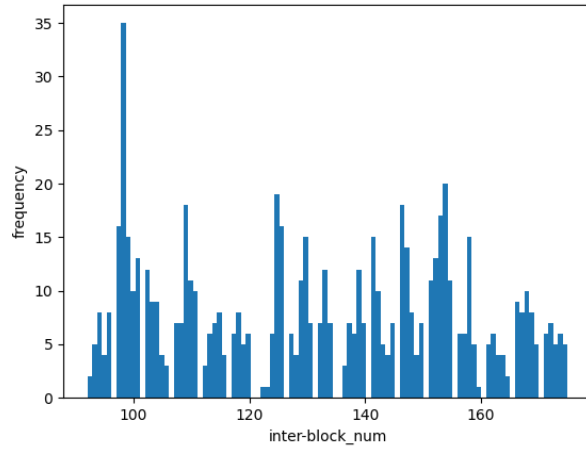
98 182D PC7ALL DIF1 INT5 histogram-inter timestamp



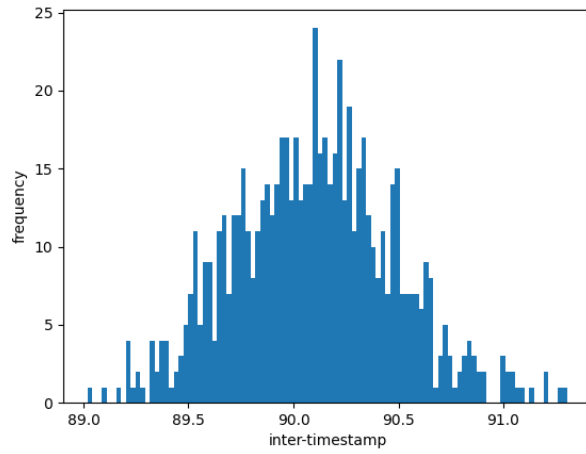
99 7D PC7ALL DIF1 INT05 histogram-delay blocknum



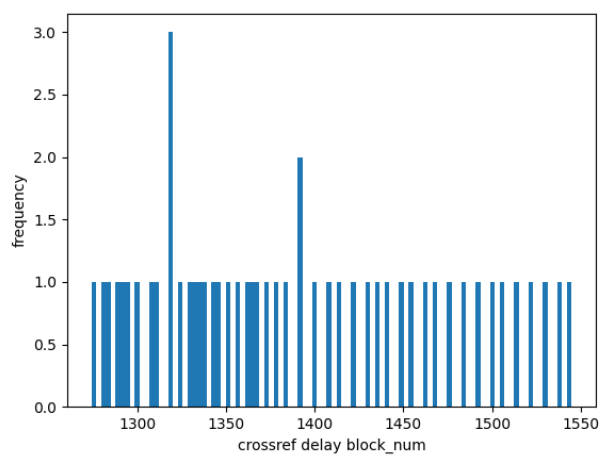
100 7D PC7ALL DIF1 INT05 histogram-delay time



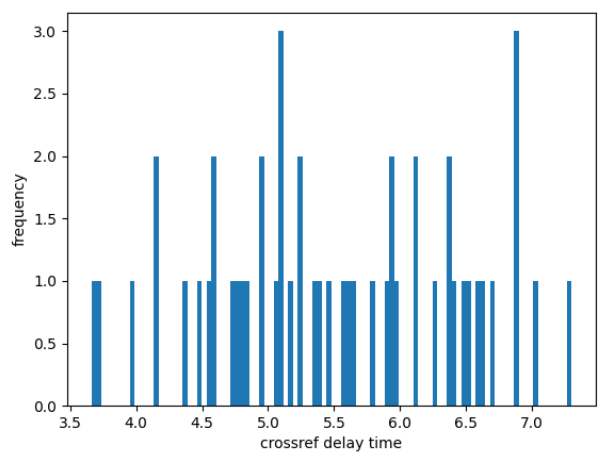
101 7D PC7ALL DIF1 INT05 histogram-inter blocknum



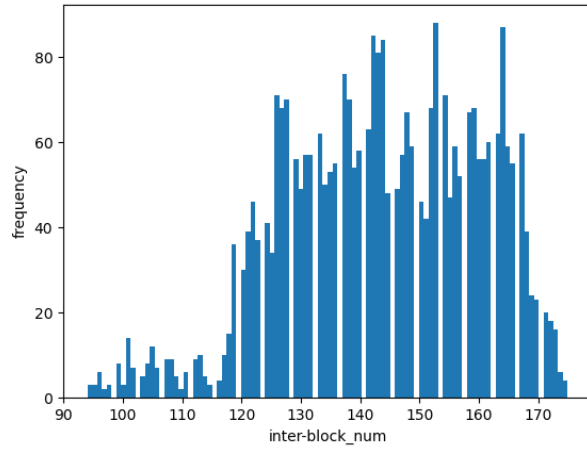
102 7D PC7ALL DIF1 INT05 histogram-inter timestamp



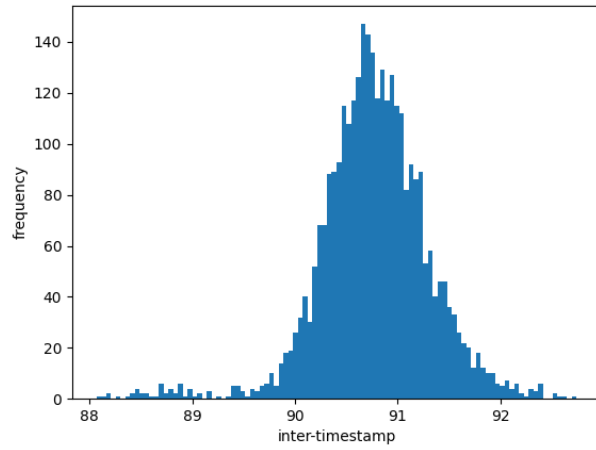
103 70D PC7ALL DIF1 INT05 histogram-delay blocknum



104 70D PC7ALL DIF1 INT05 histogram-delay time



☒ 105 70D PC7ALL DIF1 INT05 histogram-inter blocknum



☒ 106 70D PC7ALL DIF1 INT05 histogram-inter timestamp