

抜粋実験結果報告

チェーンレス型多層合意の安全性と セキュリティに関する考察

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概要

本紙では、報告原稿の紙面の都合上掲載できなかった実験結果を羅列する形で掲載する。

目次

1	付録: 実験結果	1
2	1129-70D-PC7ALL-DIF1-INT30	17
3	1201-70D-PC7ALL-DIF1-INT30	32
4	1202-1-70D-PC7ALL-DIF1-INT1-30	48
5	1202-2-70D-PC7ALL-DIF1-INT1-30	55
6	1203-1-70D-PC7ALL-DIF1-INT1-30	75
7	1203-2-70D-PC7ALL-DIF1-INT1-30	94
8	1204-1-70D-PC7ALL-DIF1-INT1-30	114
9	1205-1-70D-PC7ALL-DIF1-INT1-30	133
10	1205-2-70D-PC7ALL-DIF1-INT1-30	153
11	1206-1-70D-PC7ALL-DIF1-INT1-30	173
12	1206-2-70D-PC7ALL-DIF1-INT1-30	189
13	1207-70D-PC7ALL-DIF1-INT1-30	206
14	1208-75D-PC3ALL-DIF1-INT1-30	224
15	1212-1-70D-PC7ALL-DIF1-INT1-30	229
16	1213-1-70D-PC7ALL-DIF1-INT1-30	260

17	1213-2-75D-PC3ALL-DIF1-INT1-30	279
18	1214-1-75D-PC3ALL-DIF1-INT1-30	298
19	1215-1-75D-PC3ALL-DIF1-INT1-30	317
20	1215-2-75D-PC3ALL-DIF1-INT1-30	336
21	1216-1-75D-PC3ALL-DIF1-INT1-30	355
22	1216-2-75D-PC3ALL-DIF1-INT1-30	374
23	1217-2-75D-PC3ALL-DIF1-INT1-30	393
24	1218-1-75D-PC3ALL-DIF1-INT1-30	412
25	1220-1-75D-PC3ALL-DIF1-INT1-30	431

1 付録: 実験結果

報告原稿にて紙面の都合上掲載できなかった全ての実験結果を以降に示す。

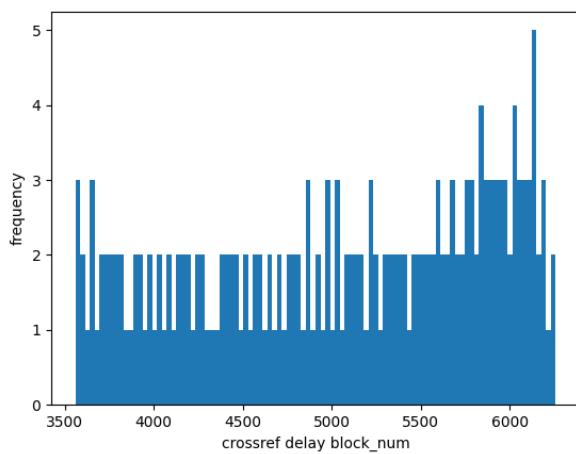


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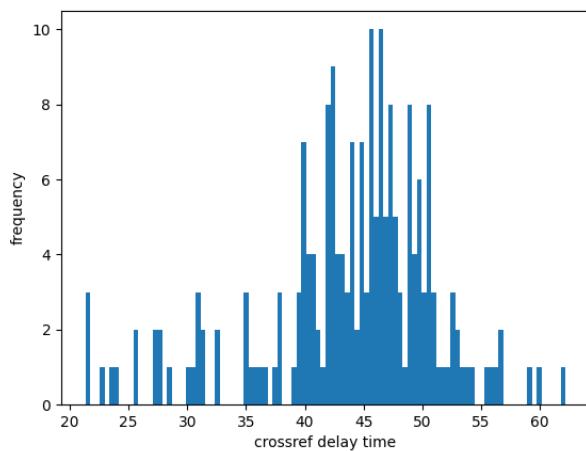


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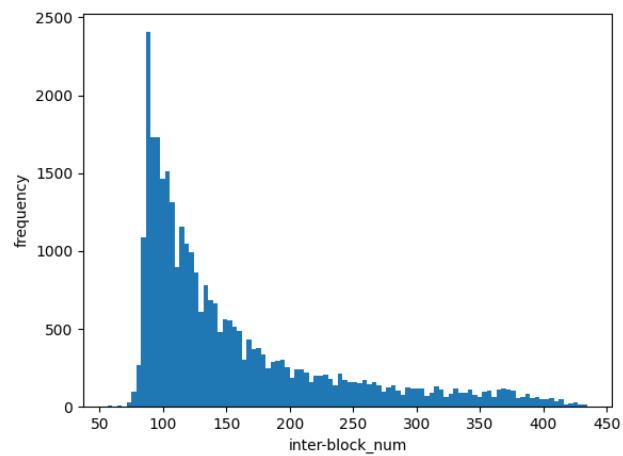


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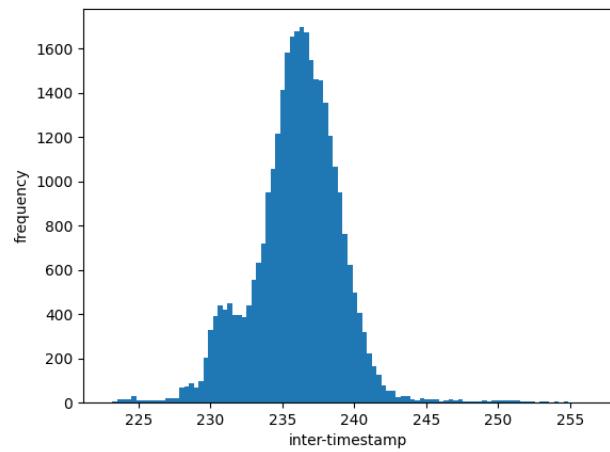


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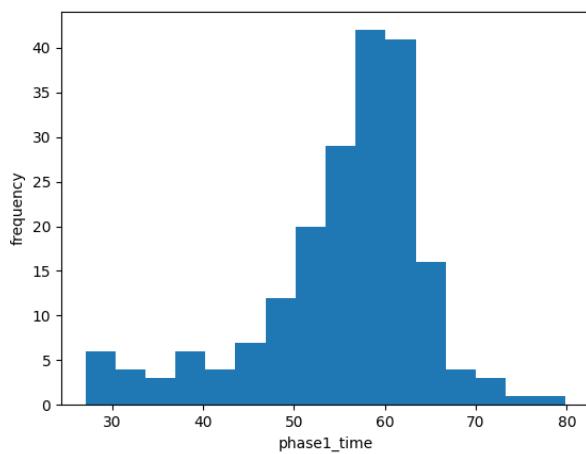


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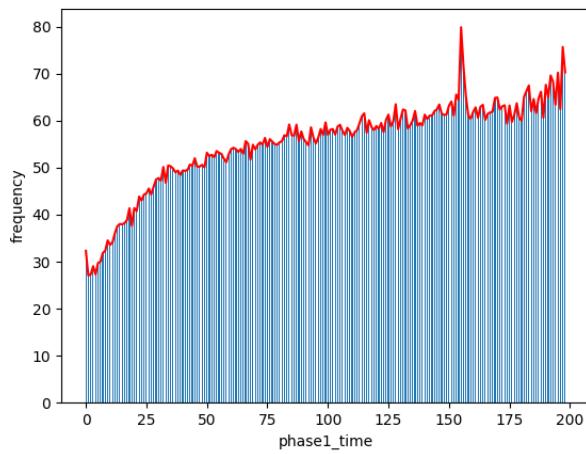


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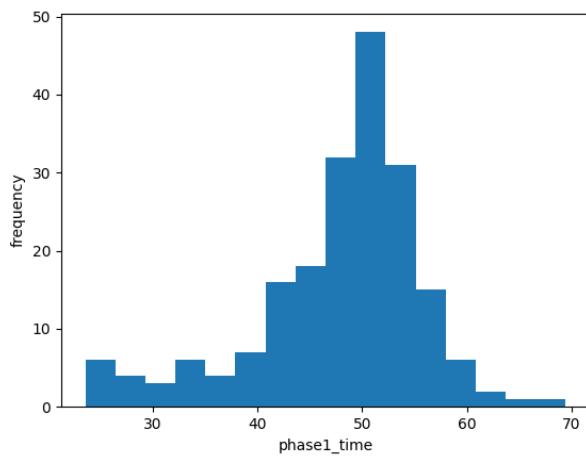


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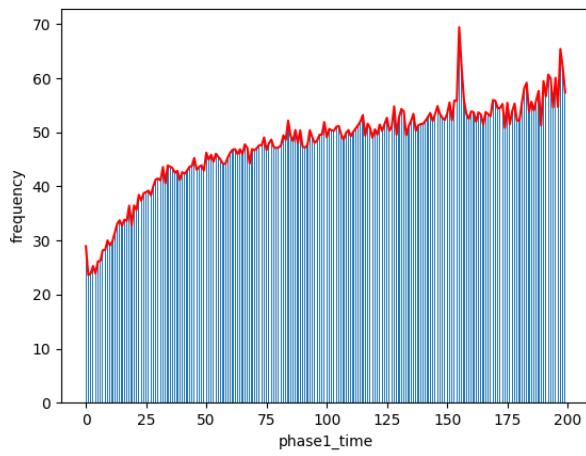


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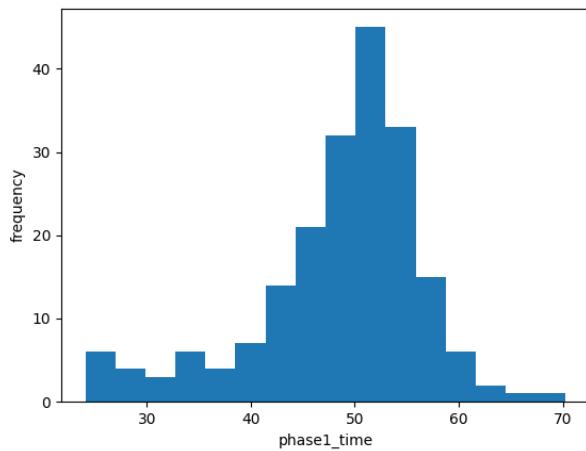


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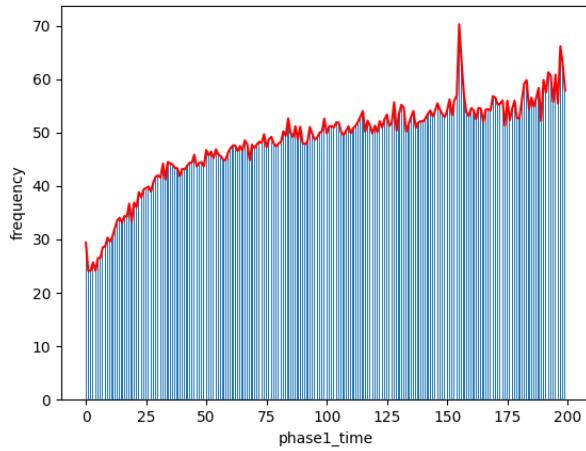


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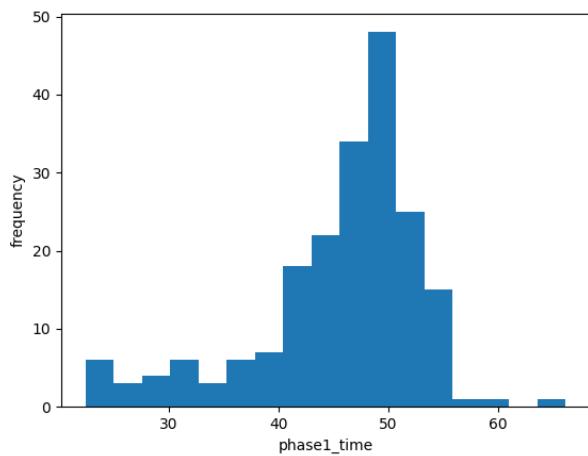


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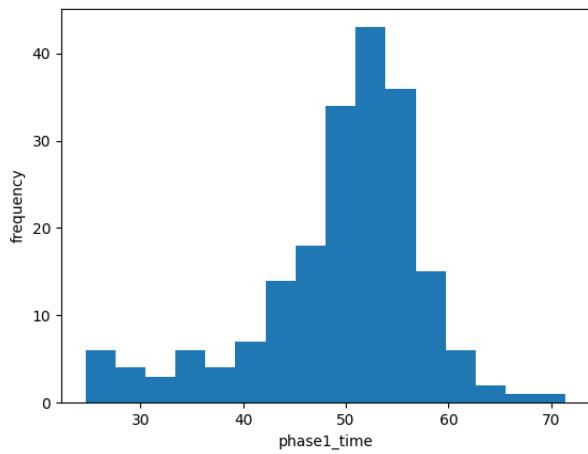


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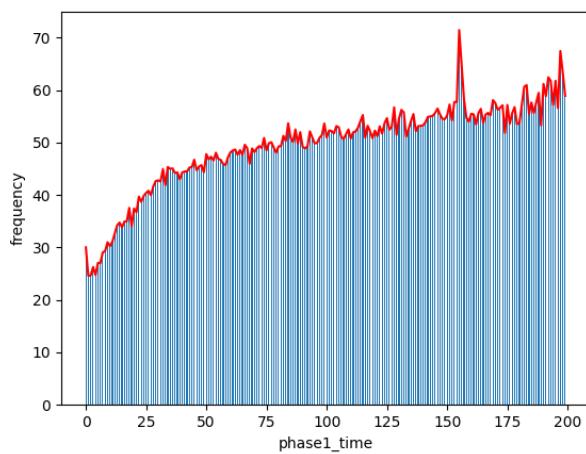


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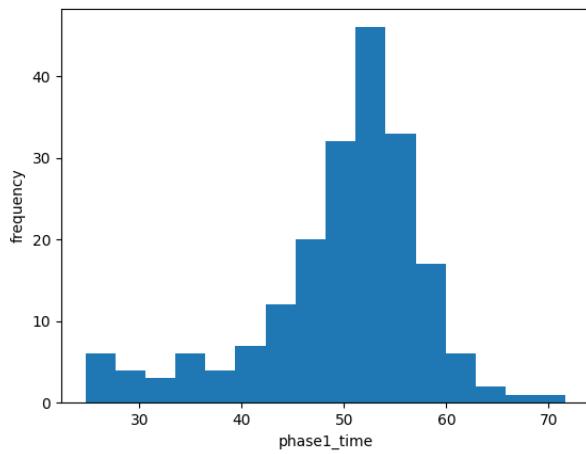


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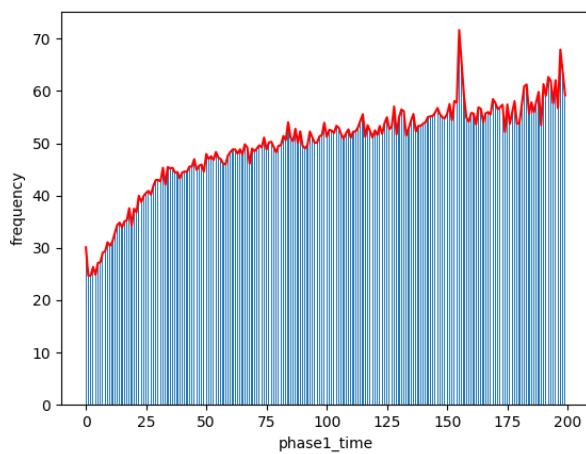


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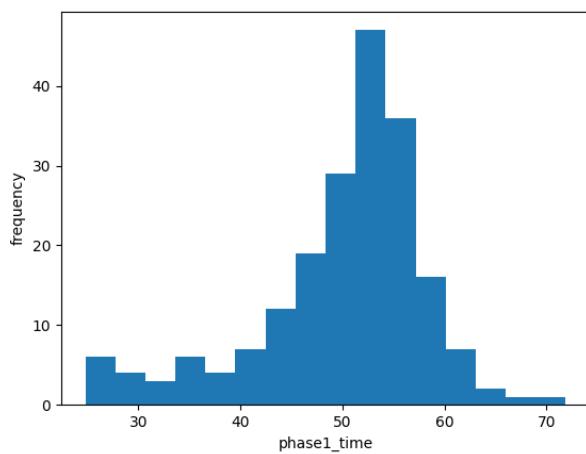


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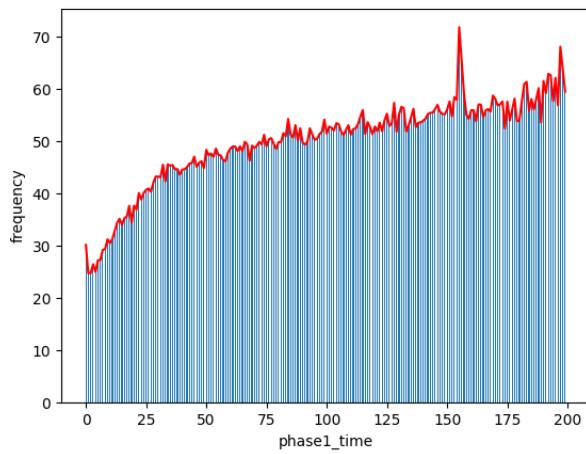


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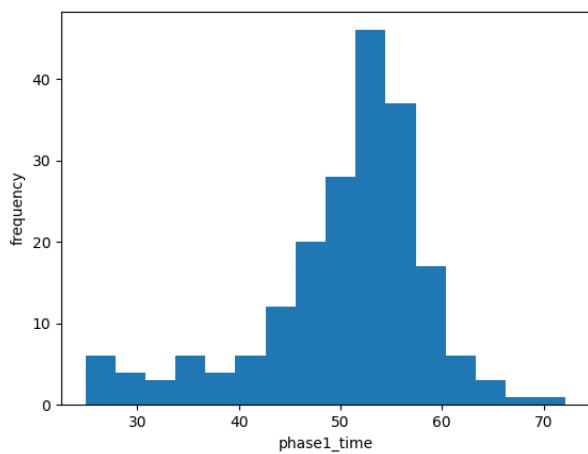


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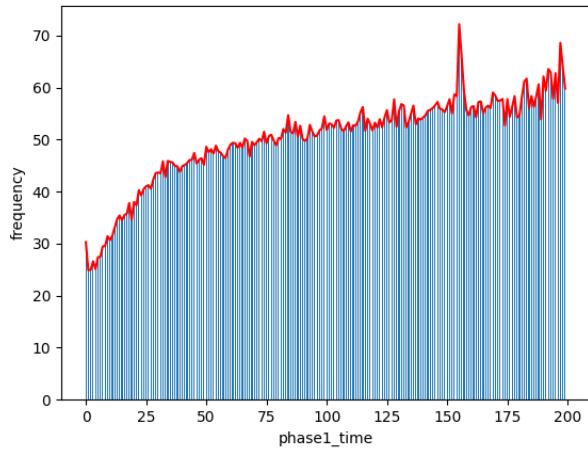


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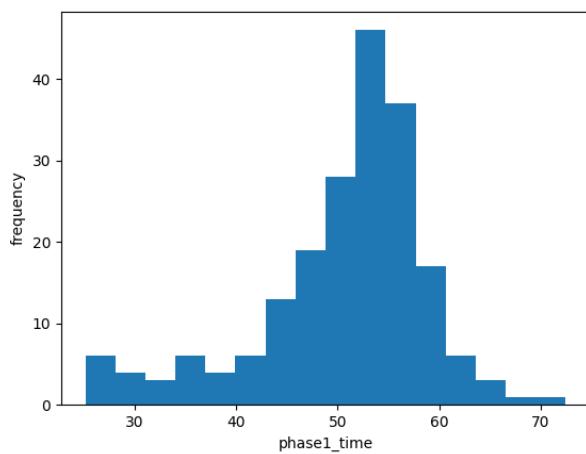


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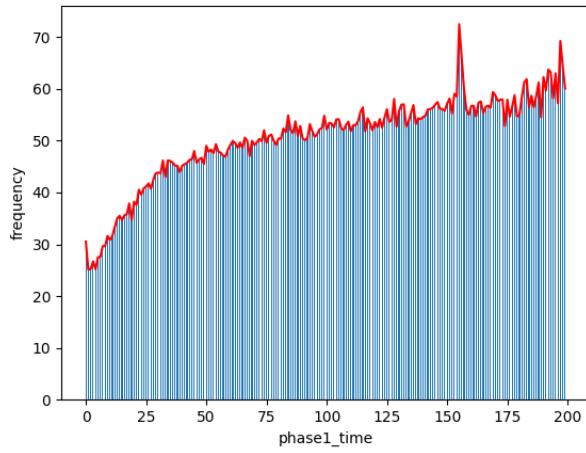


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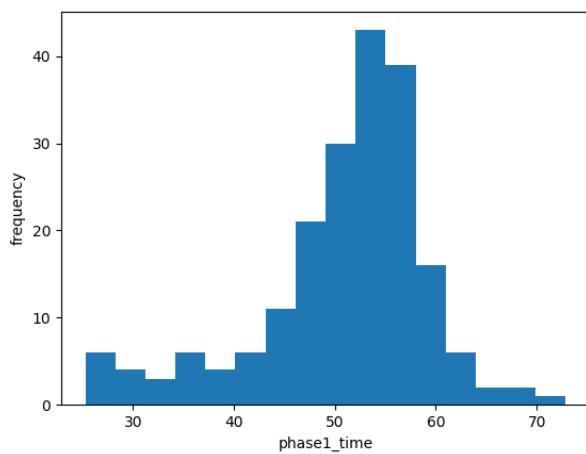


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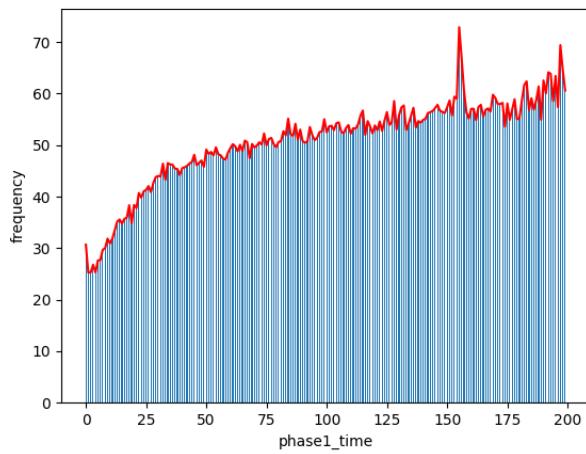


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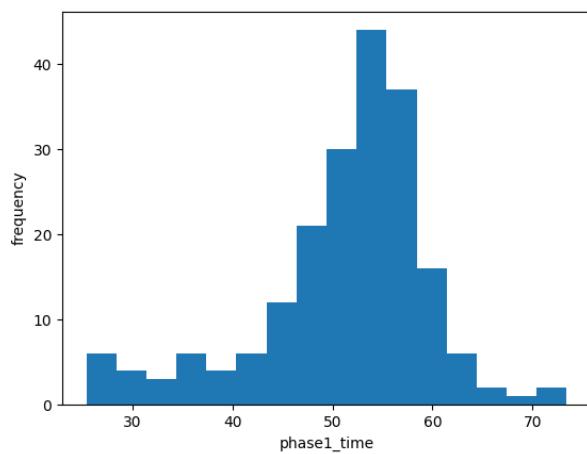


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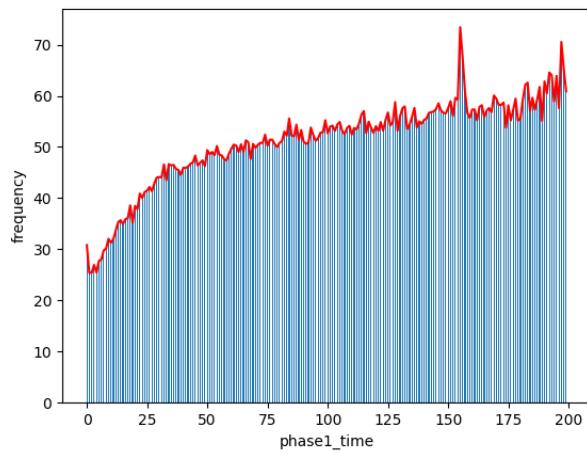


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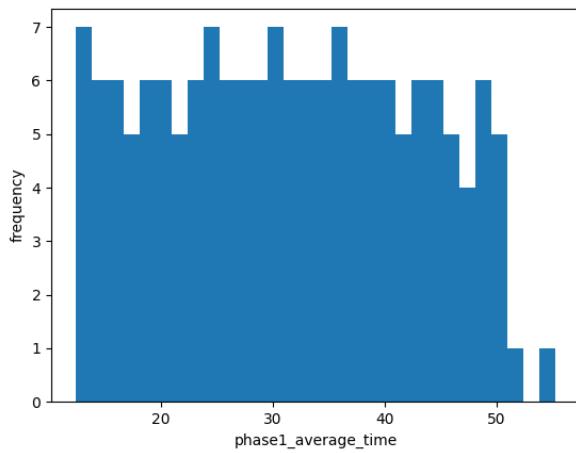


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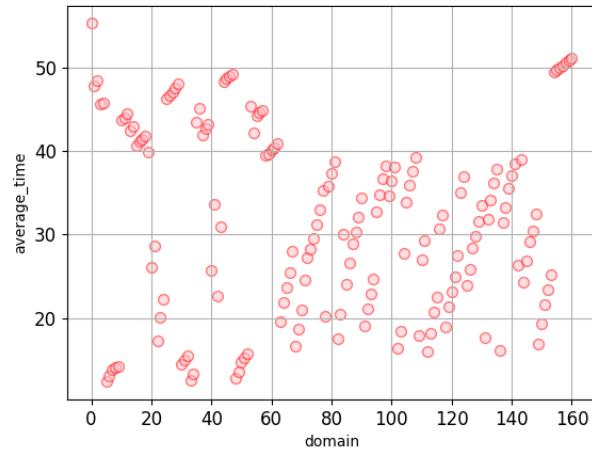


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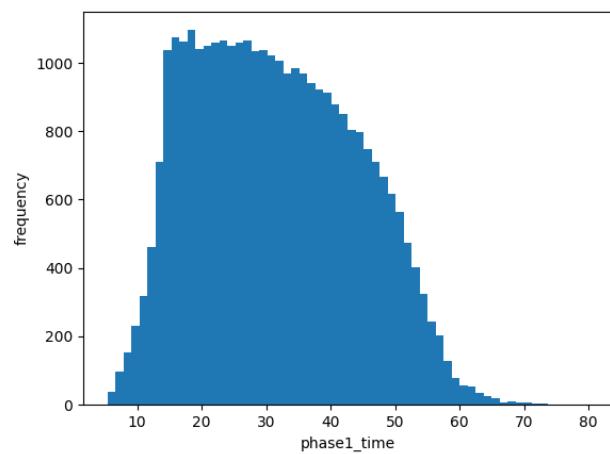


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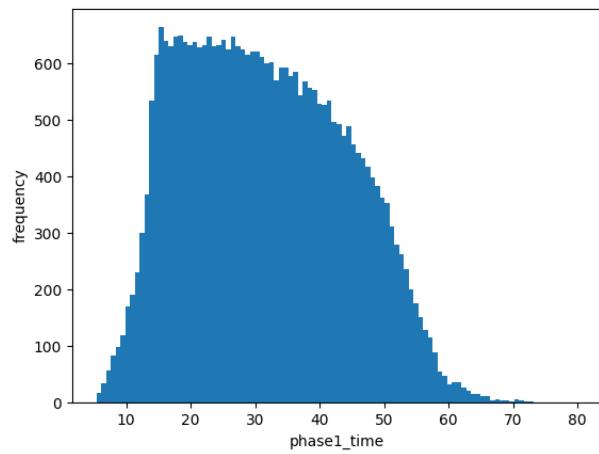


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INT05analyze-phase1-time-bins100.eps

2 1129-70D-PC7ALL-DIF1-INT30

2 章にて報告する条件を表 1 に示す.

表 1 設定値の一覧

設定項目	設定値
起動ノード数 (ドメイン数 D)	70 [ノード]
履歴交差間隔	90 [秒]
履歴交差回数	200 [回]
ブロック不生成間隔	0.5[秒]
ブロック生成難易度	1 [0x]
PC 内同時起動台数	7 [台]
使用台数	10 [台]
ブロック不生成間隔 : 30 秒	70 [ノード]
ブロック不生成間隔 : 1 秒	0 [ノード]

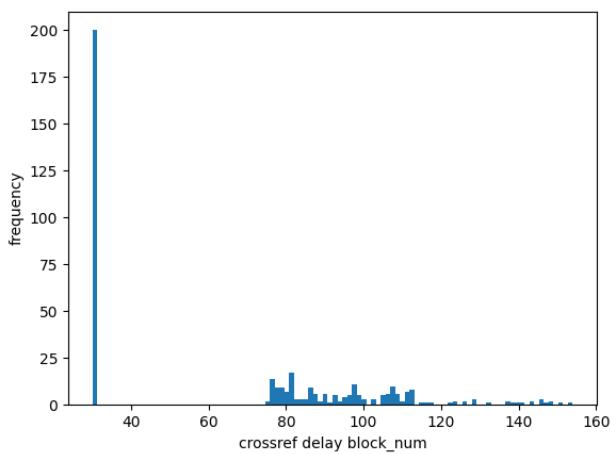


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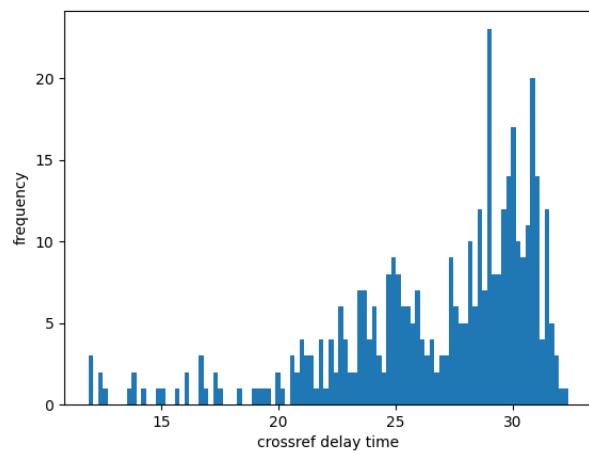


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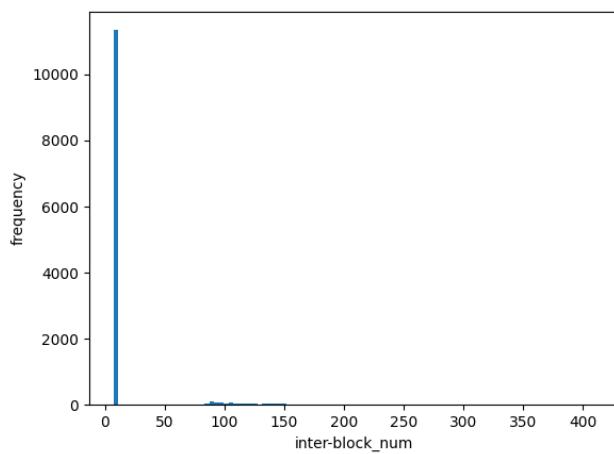


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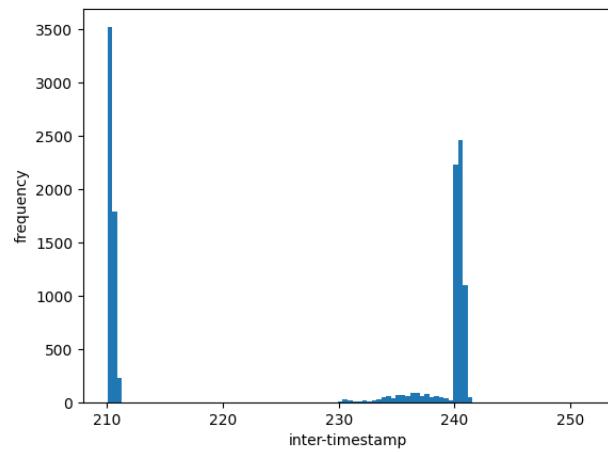


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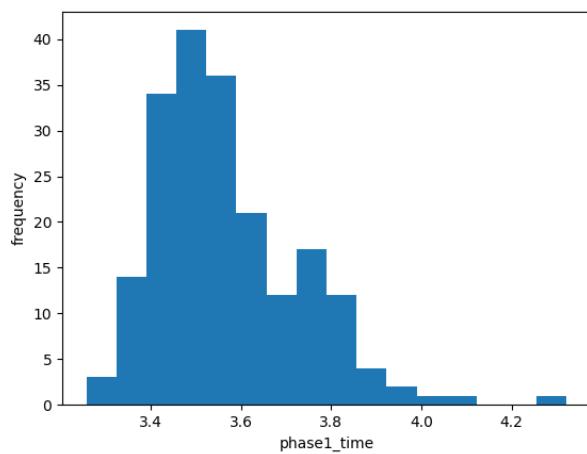


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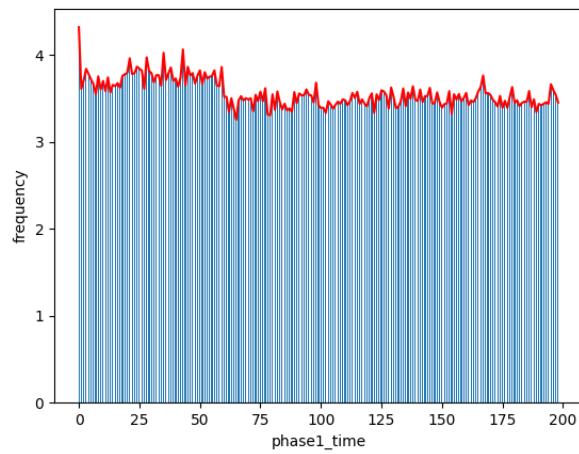


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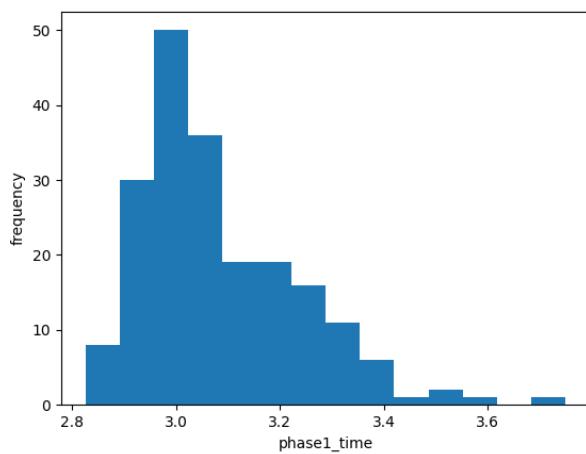


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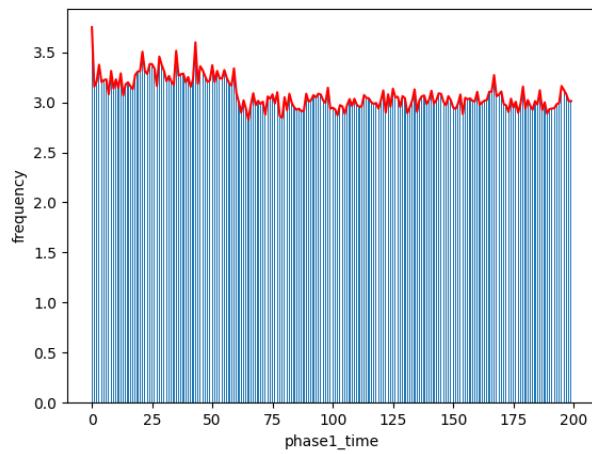


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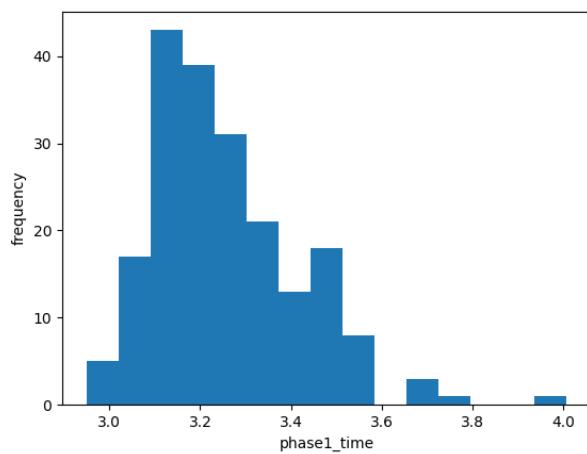


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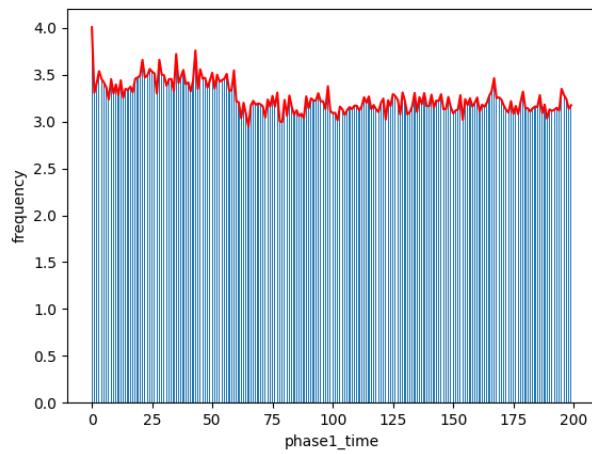


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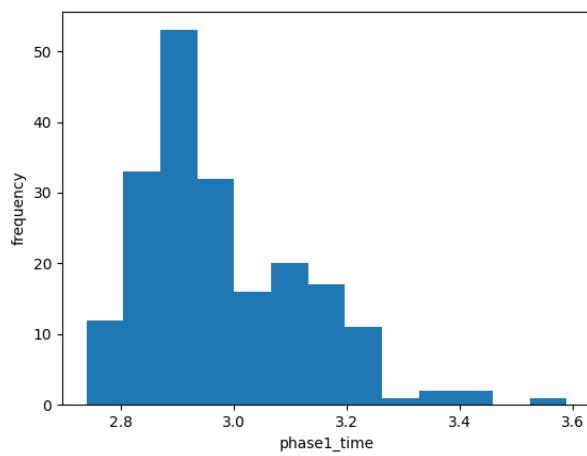


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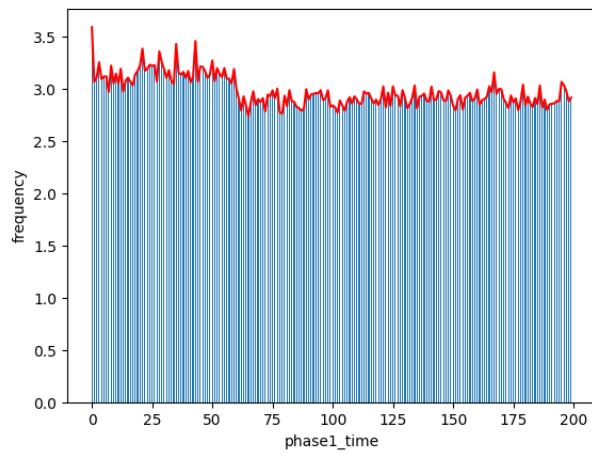


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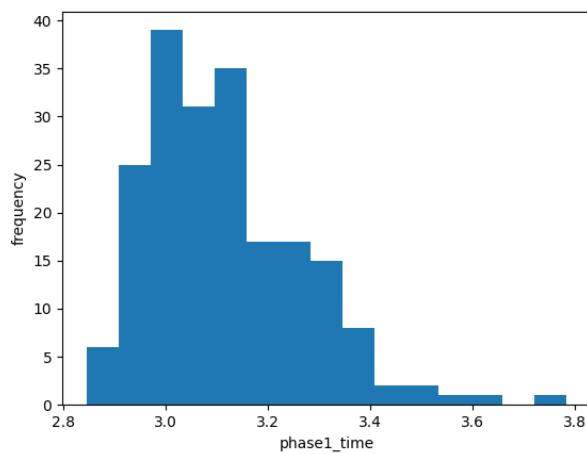


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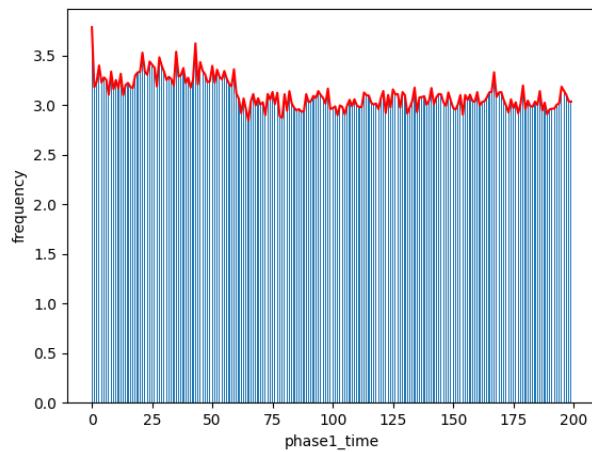


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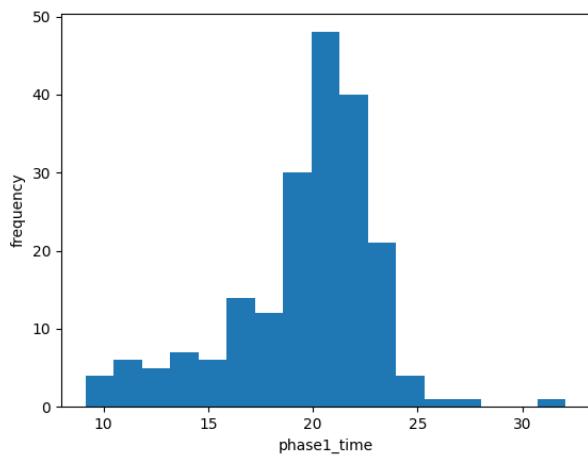


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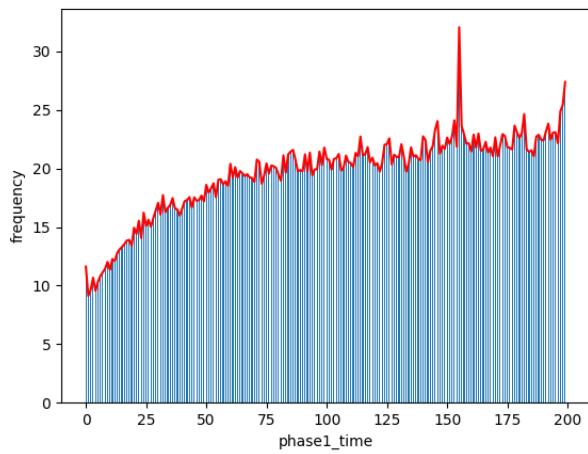


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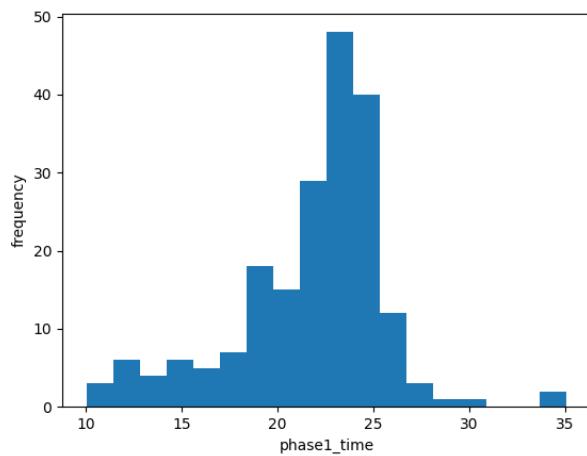


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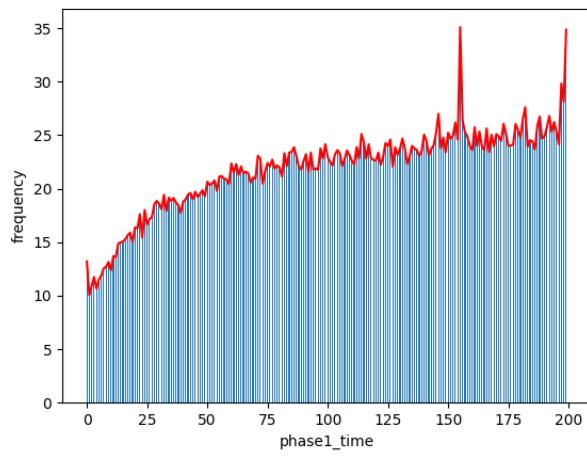


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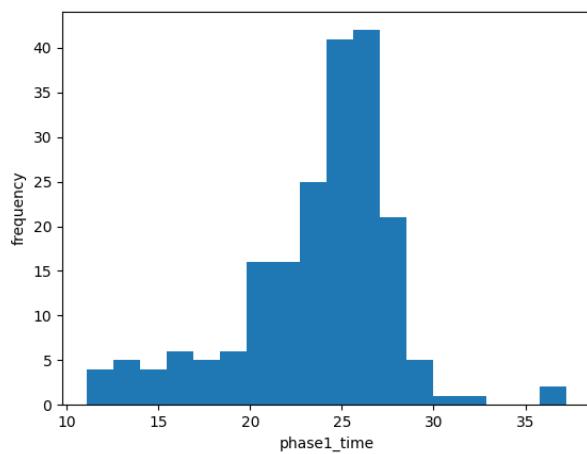


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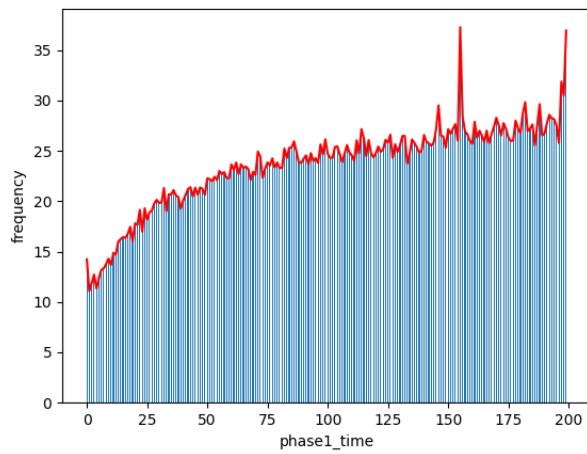


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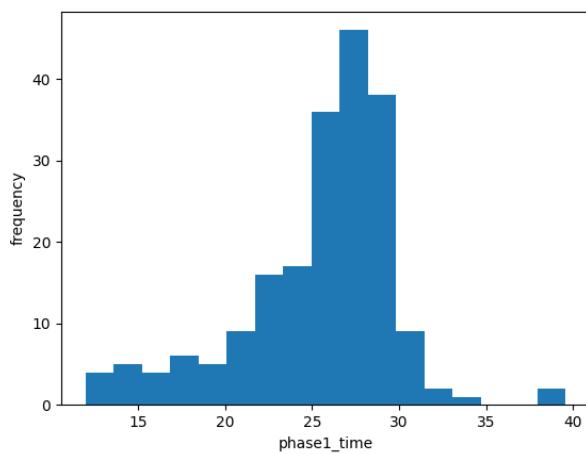


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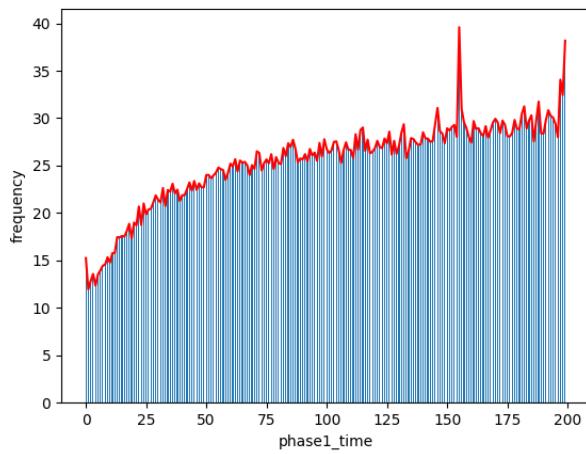


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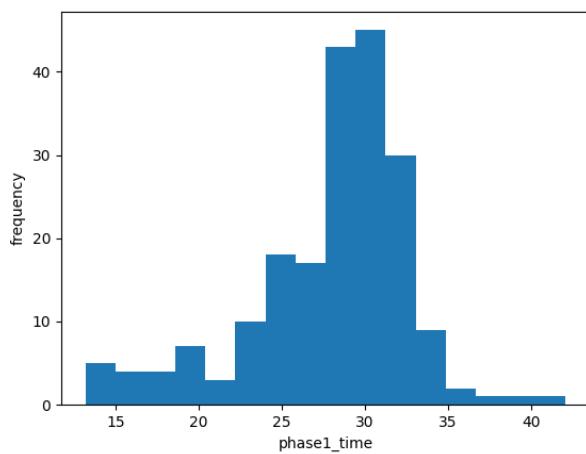


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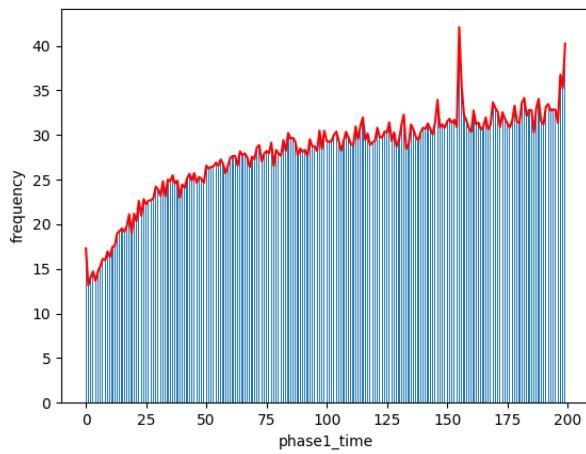


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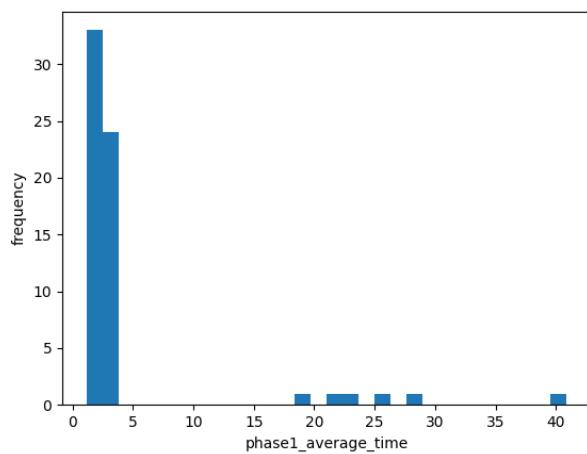


図 54 pht/experiment-win/1129/phase1/1129-70D-PC7ALL-DIF1-
INT30analyze-phase1-average-time.eps

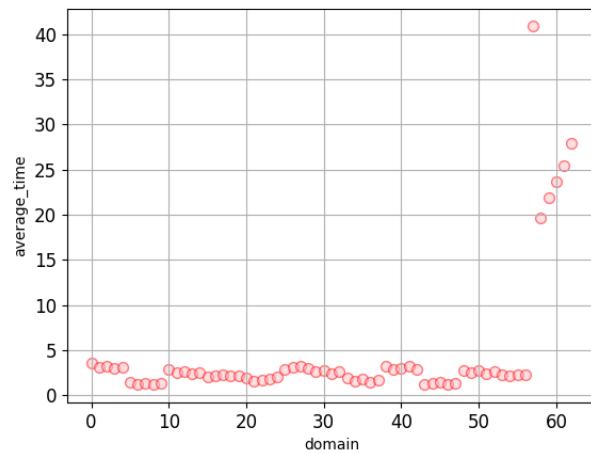


図 55 pht/experiment-win/1129/phase1/1129-70D-PC7ALL-DIF1-
INT30analyze-phase1-average-time-scatter.eps

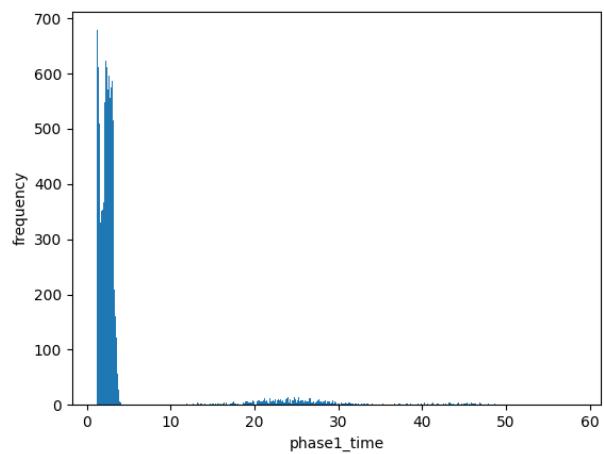


図 56 pht/experiment-win/1129/phase1/1129-70D-PC7ALL-DIF1-
INT30analyze-phase1-time-auto.eps

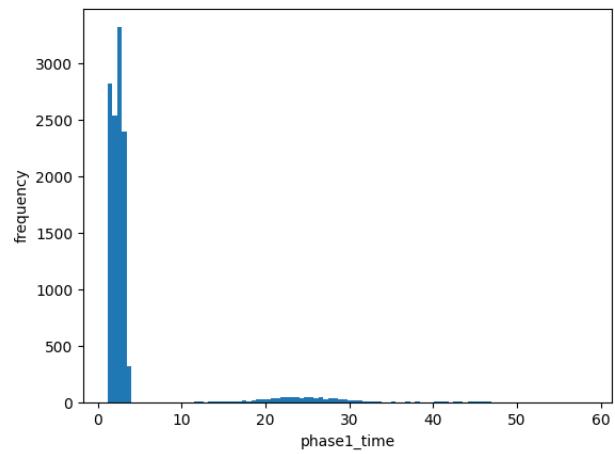


図 57 pht/experiment-win/1129/phase1/1129-70D-PC7ALL-DIF1-
INT30analyze-phase1-time-bins100.eps

3 1201-70D-PC7ALL-DIF1-INT30

3 章にて報告する条件を表 2 に示す.

表 2 設定値の一覧

設定項目	設定値
起動ノード数 (ドメイン数 D)	70 [ノード]
履歴交差間隔	90 [秒]
履歴交差回数	200 [回]
ブロック不生成間隔	0.5[秒]
ブロック生成難易度	1 [0x]
PC 内同時起動台数	7 [台]
使用台数	10 [台]
ブロック不生成間隔 : 30 秒	70 [ノード]
ブロック不生成間隔 : 1 秒	0 [ノード]

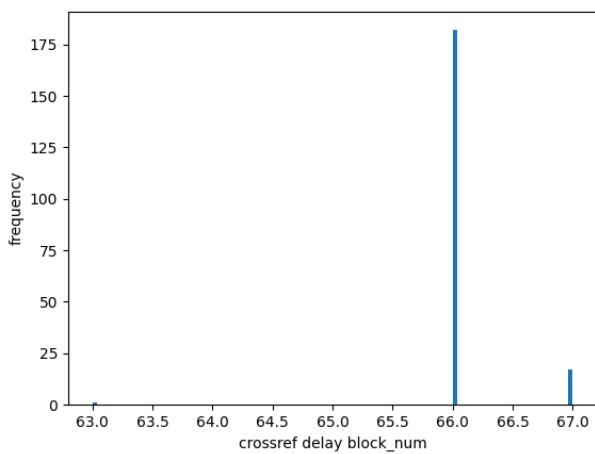


図 58 pht/experiment-win/1201/BC/1201-70D-PC7ALL-DIF1-
INT30histogram-delay-blocknum.eps

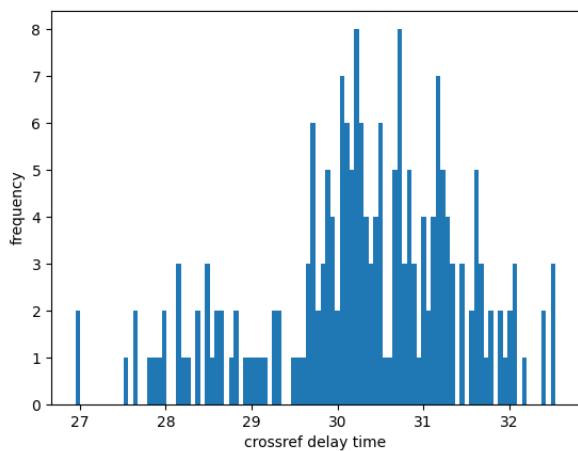


図 59 pht/experiment-win/1201/BC/1201-70D-PC7ALL-DIF1-
INT30histogram-delay-time.eps

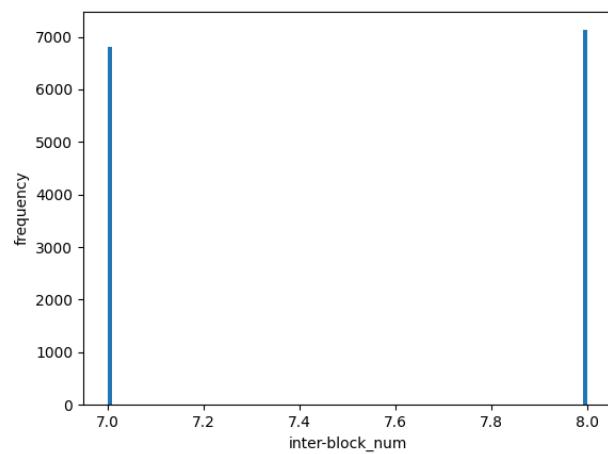


図 60 pht/experiment-win/1201/BC/1201-70D-PC7ALL-DIF1-
INT30histogram-inter-blocknum.eps

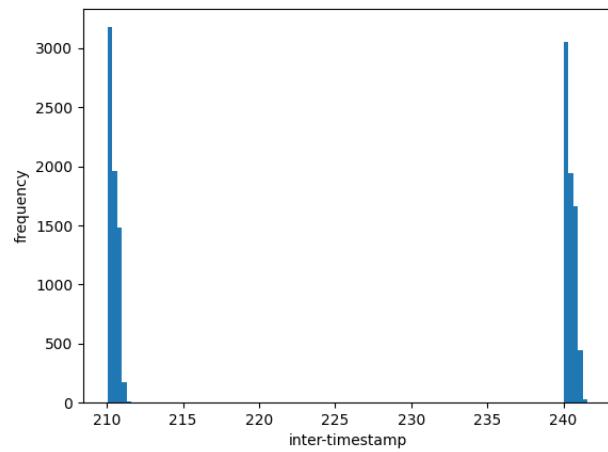


図 61 pht/experiment-win/1201/BC/1201-70D-PC7ALL-DIF1-
INT30histogram-inter-timestamp.eps

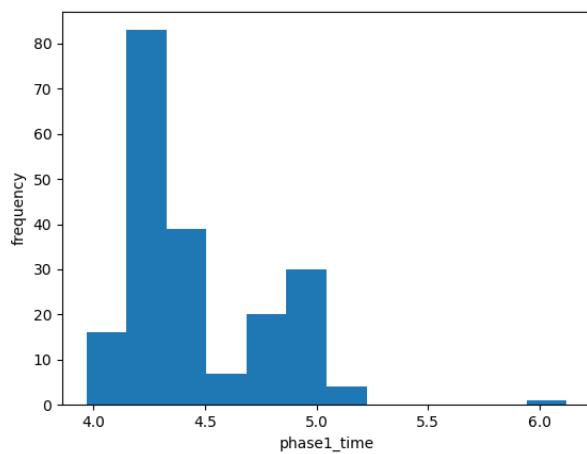


図 62 pht/experiment-win/1201/phase1/1201-70D-PC7ALL-DIF1-
INT30Phase1-time50051.eps

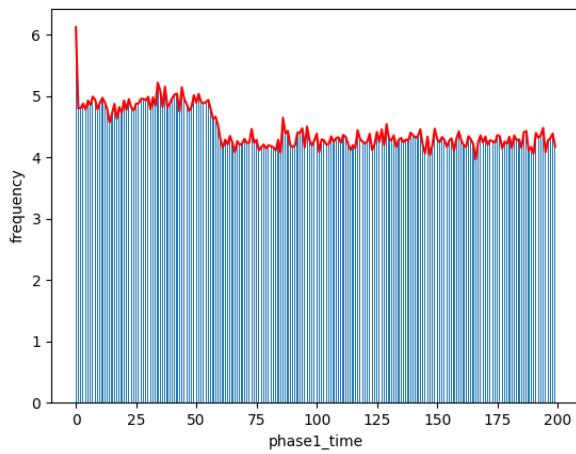


図 63 pht/experiment-win/1201/phase1/1201-70D-PC7ALL-DIF1-
INT30Phase1-time50051-in-chronological-order.eps

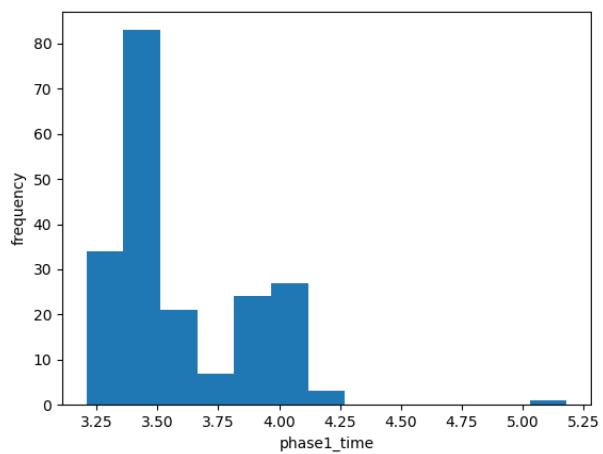


図 64 pht/experiment-win/1201/phase1/1201-70D-PC7ALL-DIF1-
INT30Phase1-time50053.eps

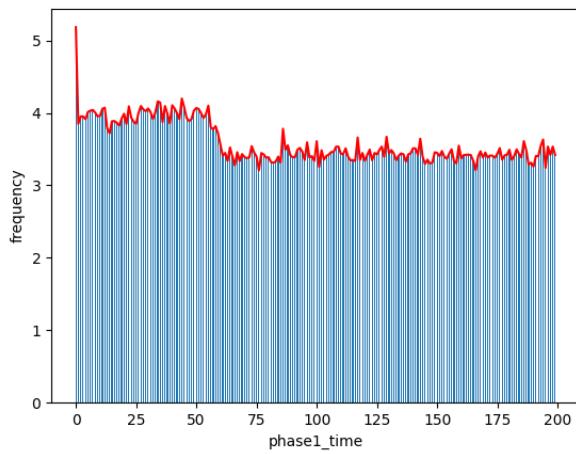


図 65 pht/experiment-win/1201/phase1/1201-70D-PC7ALL-DIF1-
INT30Phase1-time50053-in-chronological-order.eps

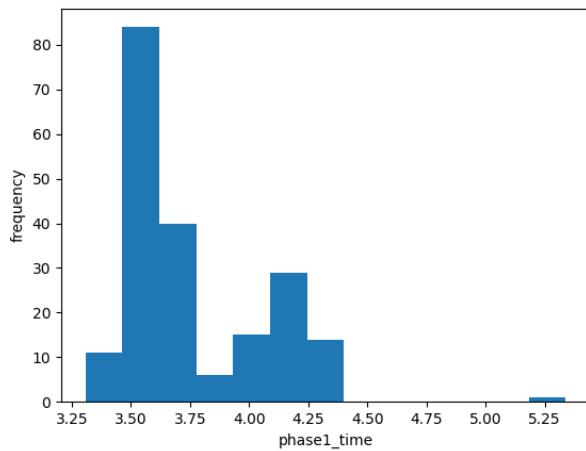


図 66 pht/experiment-win/1201/phase1/1201-70D-PC7ALL-DIF1-
INT30Phase1-time50055.eps

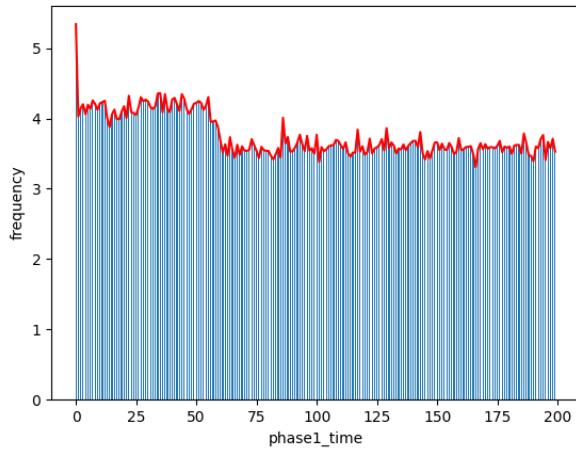


図 67 pht/experiment-win/1201/phase1/1201-70D-PC7ALL-DIF1-
INT30Phase1-time50055-in-chronological-order.eps

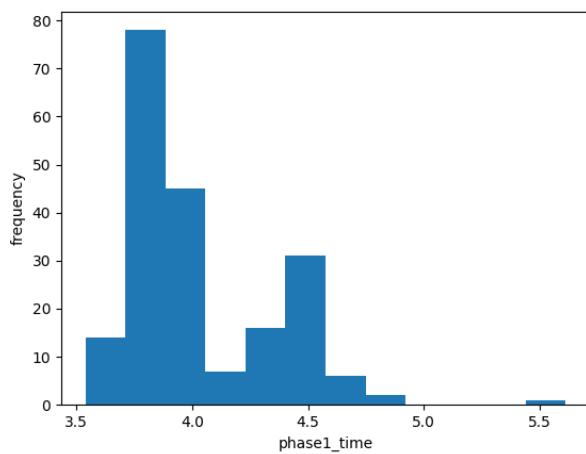


図 68 pht/experiment-win/1201/phase1/1201-70D-PC7ALL-DIF1-
INT30Phase1-time50057.eps

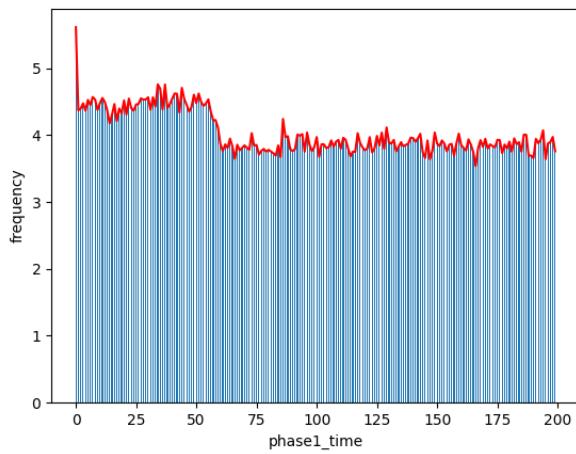


図 69 pht/experiment-win/1201/phase1/1201-70D-PC7ALL-DIF1-
INT30Phase1-time50057-in-chronological-order.eps

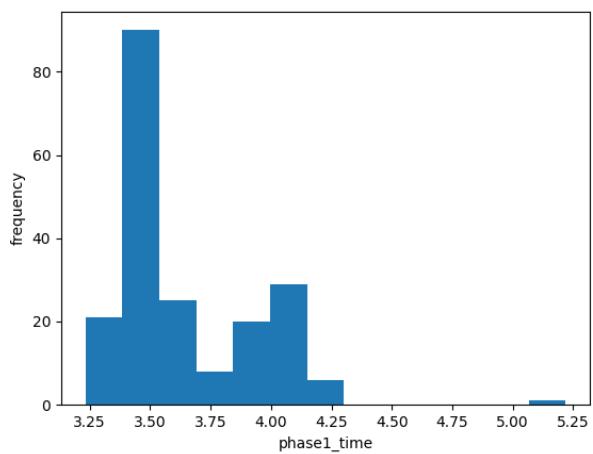


図 70 pht/experiment-win/1201/phase1/1201-70D-PC7ALL-DIF1-
INT30Phase1-time50059.eps

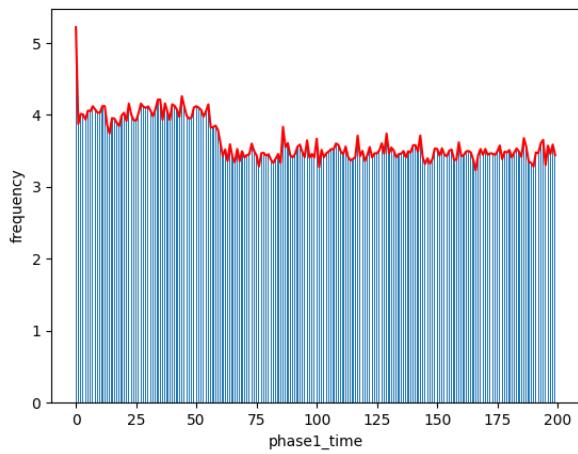


図 71 pht/experiment-win/1201/phase1/1201-70D-PC7ALL-DIF1-
INT30Phase1-time50059-in-chronological-order.eps

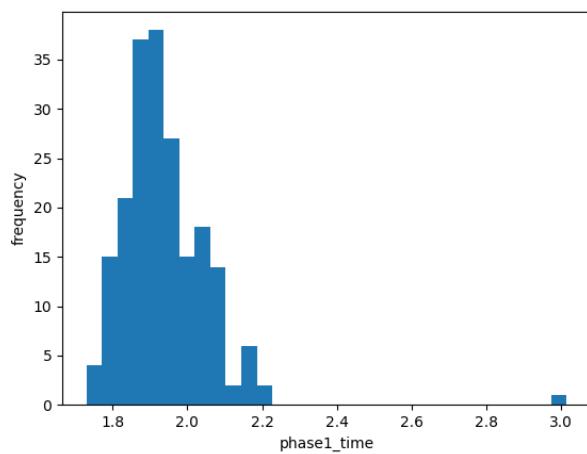


図 72 pht/experiment-win/1201/phase1/1201-70D-PC7ALL-DIF1-
INT30Phase1-time50181.eps

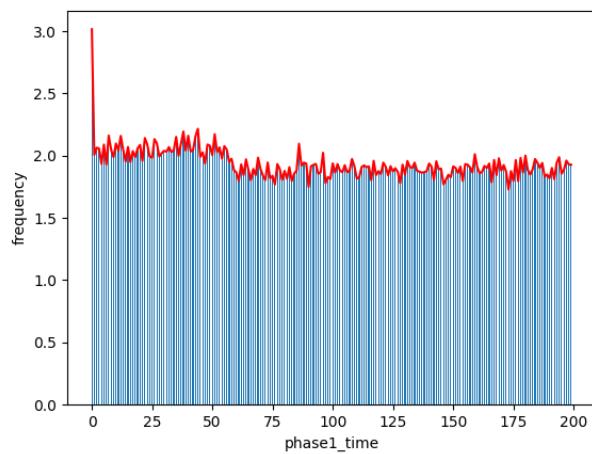


図 73 pht/experiment-win/1201/phase1/1201-70D-PC7ALL-DIF1-
INT30Phase1-time50181-in-chronological-order.eps

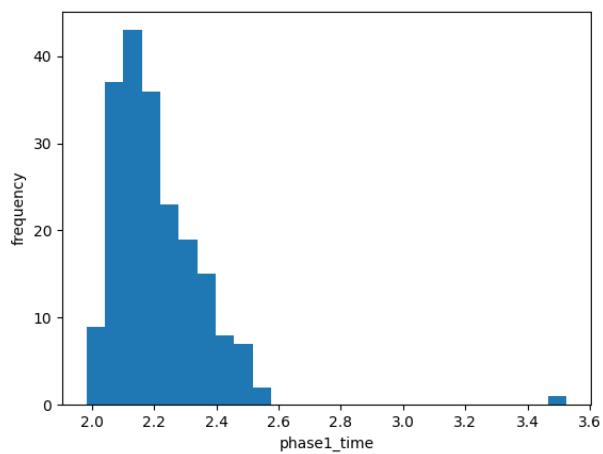


図 74 pht/experiment-win/1201/phase1/1201-70D-PC7ALL-DIF1-
INT30Phase1-time50183.eps

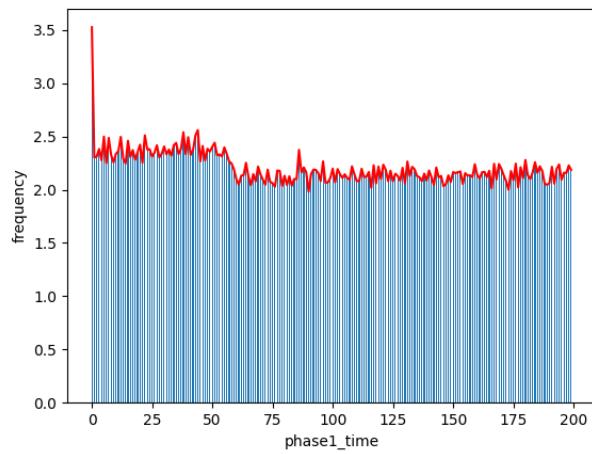


図 75 pht/experiment-win/1201/phase1/1201-70D-PC7ALL-DIF1-
INT30Phase1-time50183-in-chronological-order.eps

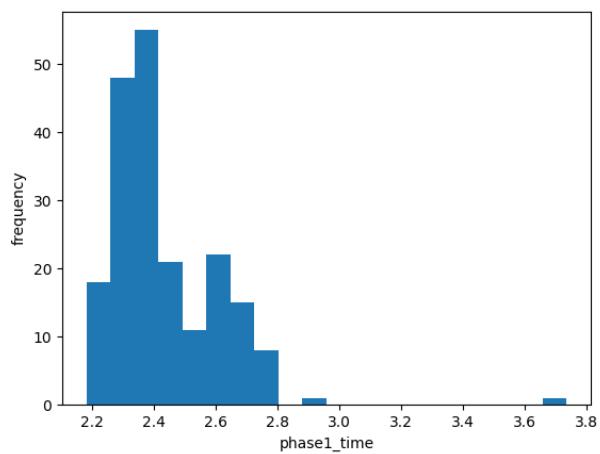


図 76 pht/experiment-win/1201/phase1/1201-70D-PC7ALL-DIF1-
INT30Phase1-time50185.eps

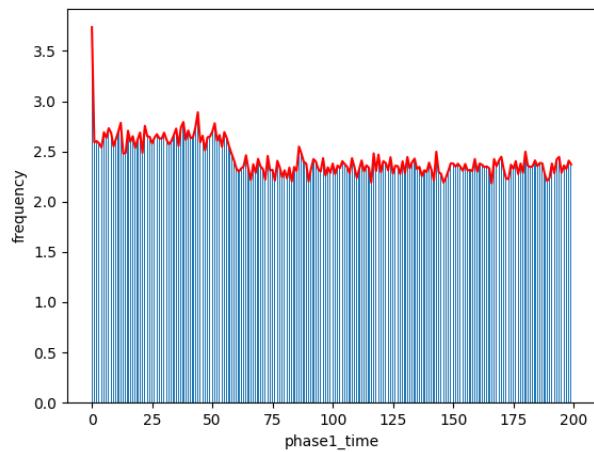


図 77 pht/experiment-win/1201/phase1/1201-70D-PC7ALL-DIF1-
INT30Phase1-time50185-in-chronological-order.eps

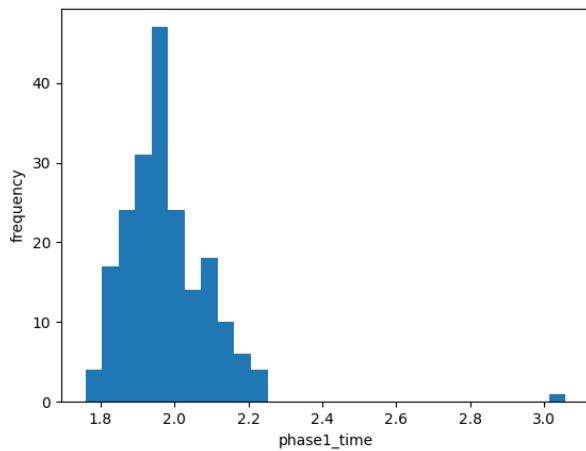


図 78 pht/experiment-win/1201/phase1/1201-70D-PC7ALL-DIF1-
INT30Phase1-time50187.eps

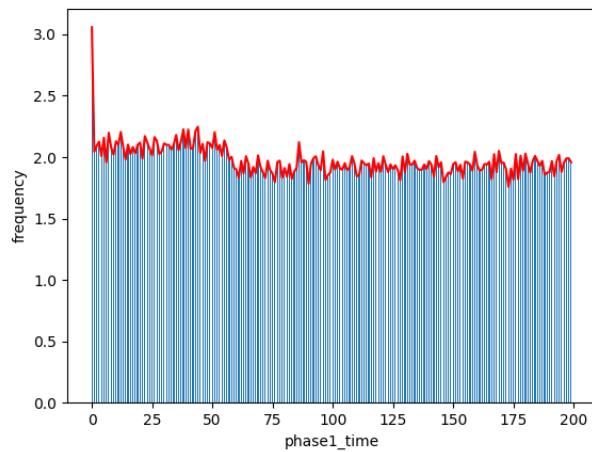


図 79 pht/experiment-win/1201/phase1/1201-70D-PC7ALL-DIF1-
INT30Phase1-time50187-in-chronological-order.eps

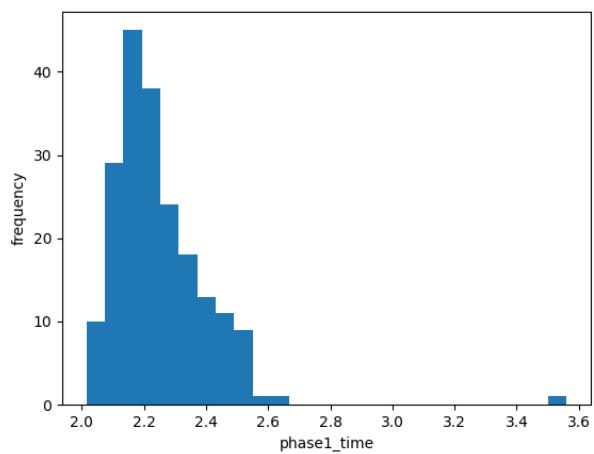


図 80 pht/experiment-win/1201/phase1/1201-70D-PC7ALL-DIF1-
INT30Phase1-time50189.eps

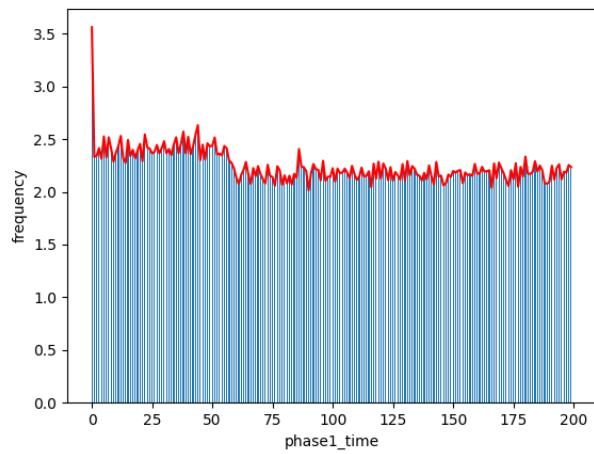


図 81 pht/experiment-win/1201/phase1/1201-70D-PC7ALL-DIF1-
INT30Phase1-time50189-in-chronological-order.eps

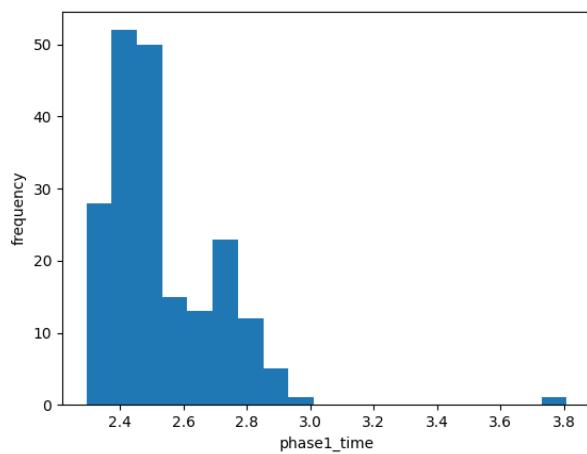


図 82 pht/experiment-win/1201/phase1/1201-70D-PC7ALL-DIF1-
INT30Phase1-time50191.eps

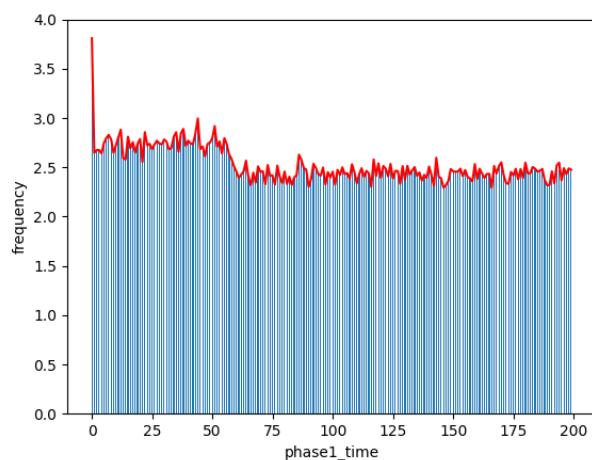


図 83 pht/experiment-win/1201/phase1/1201-70D-PC7ALL-DIF1-
INT30Phase1-time50191-in-chronological-order.eps

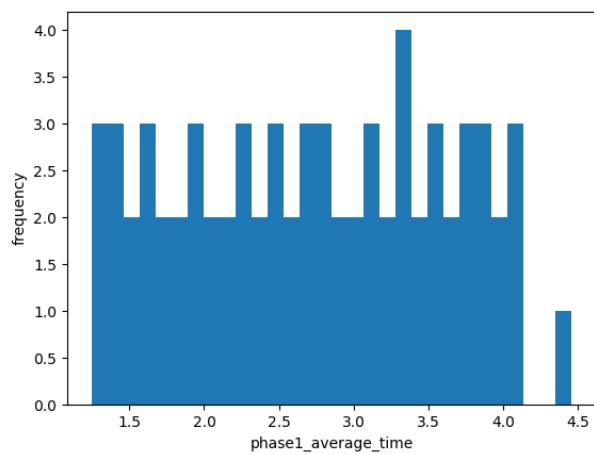


図 84 pht/experiment-win/1201/phase1/1201-70D-PC7ALL-DIF1-
INT30analyze-phase1-average-time.eps

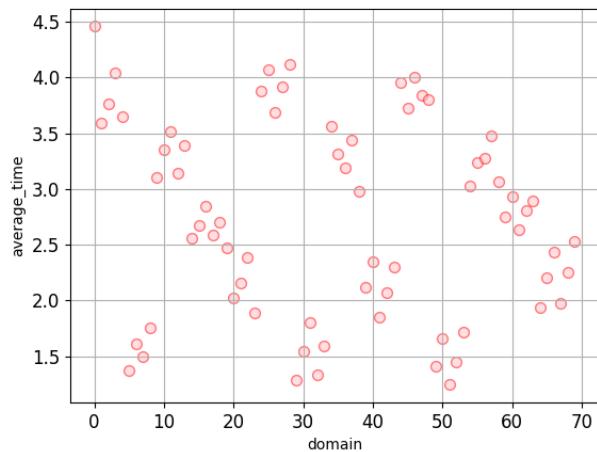


図 85 pht/experiment-win/1201/phase1/1201-70D-PC7ALL-DIF1-
INT30analyze-phase1-average-time-scatter.eps

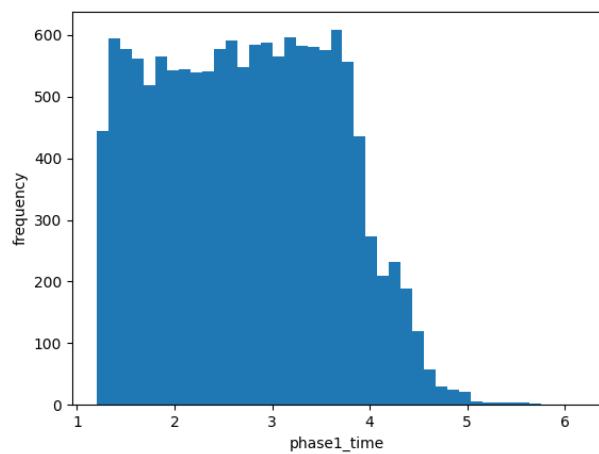


図 86 pht/experiment-win/1201/phase1/1201-70D-PC7ALL-DIF1-
INT30analyze-phase1-time-auto.eps

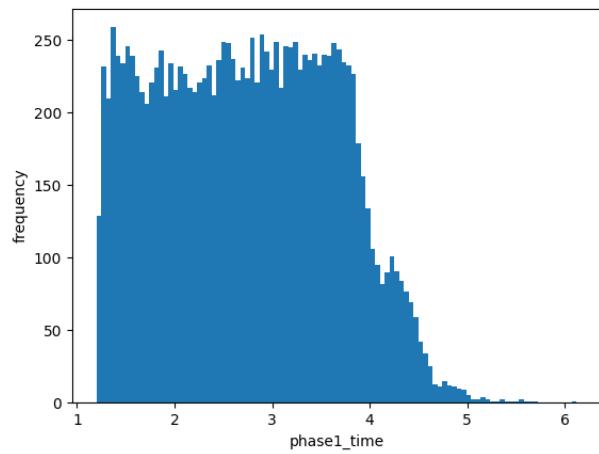


図 87 pht/experiment-win/1201/phase1/1201-70D-PC7ALL-DIF1-
INT30analyze-phase1-time-bins100.eps

4 1202-1-70D-PC7ALL-DIF1-INT1-30

4 章にて報告する条件を表 3 に示す。

表 3 設定値の一覧

設定項目	設定値
起動ノード数 (ドメイン数 D)	70 [ノード]
履歴交差間隔	90 [秒]
履歴交差回数	200 [回]
ブロック不生成間隔	0.5[秒]
ブロック生成難易度	1 [0x]
PC 内同時起動台数	7 [台]
使用台数	10 [台]
ブロック不生成間隔 : 30 秒	70 [ノード]
ブロック不生成間隔 : 1 秒	0 [ノード]

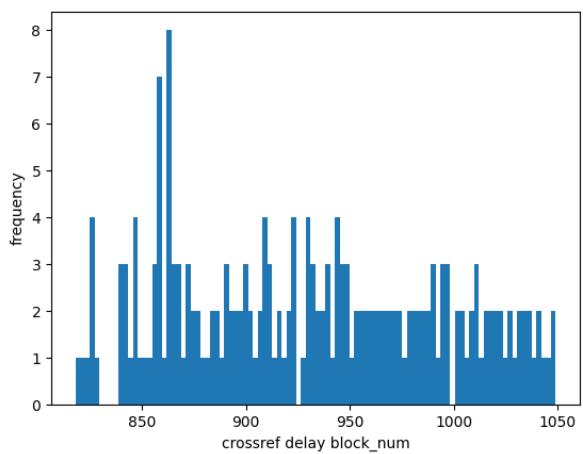


図 88 pht/experiment-win/1202-1/BC/1/1202-1-70D-PC7ALL-DIF1-INT1-30histogram-delay-blocknum.eps

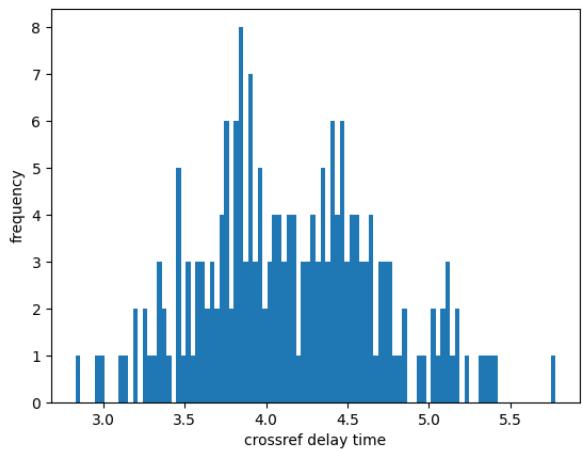


図 89 pht/experiment-win/1202-1/BC/1/1202-1-70D-PC7ALL-DIF1-INT1-30histogram-delay-time.eps

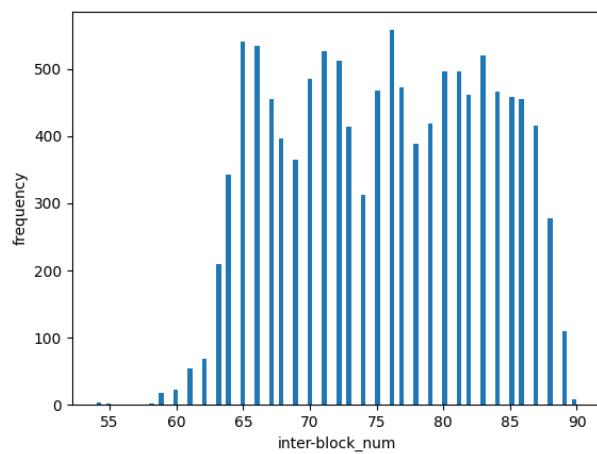


図 90 pht/experiment-win/1202-1/BC/1/1202-1-70D-PC7ALL-DIF1-INT1-30histogram-inter-blocknum.eps

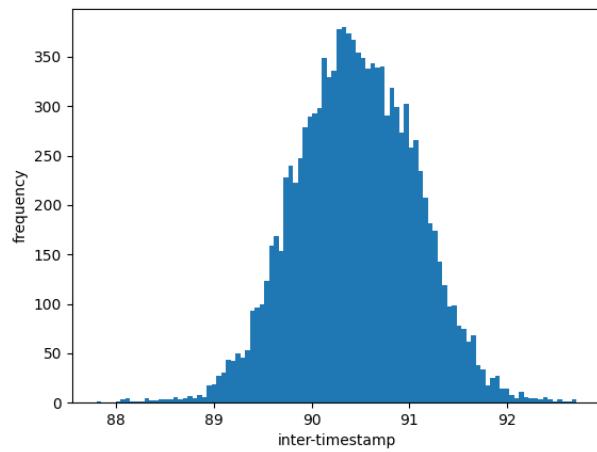


図 91 pht/experiment-win/1202-1/BC/1/1202-1-70D-PC7ALL-DIF1-INT1-30histogram-inter-timestamp.eps

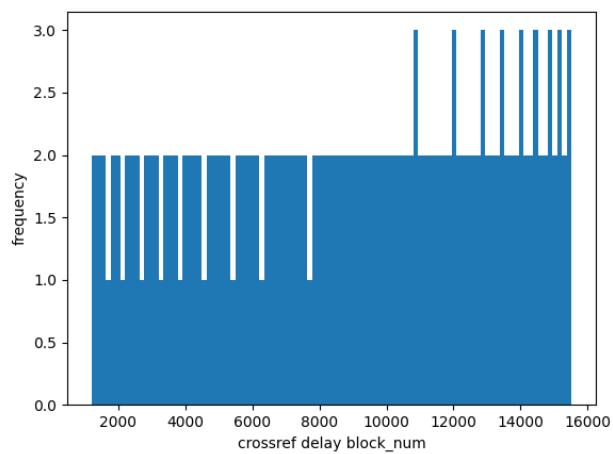


図 92 pht/experiment-win/1202-1/BC/1202-1-70D-PC7ALL-DIF1-INT1-30histogram-delay-blocknum.eps

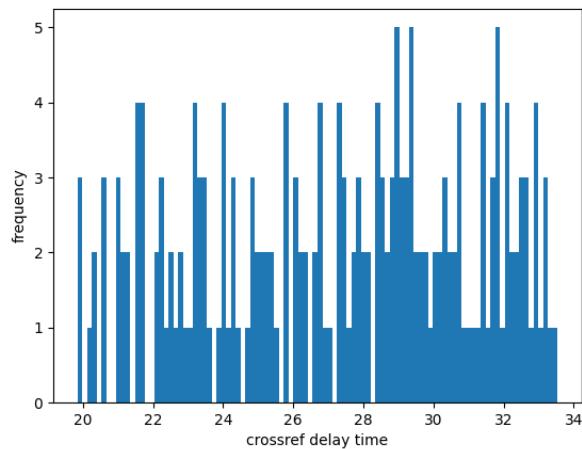


図 93 pht/experiment-win/1202-1/BC/1202-1-70D-PC7ALL-DIF1-INT1-30histogram-delay-time.eps

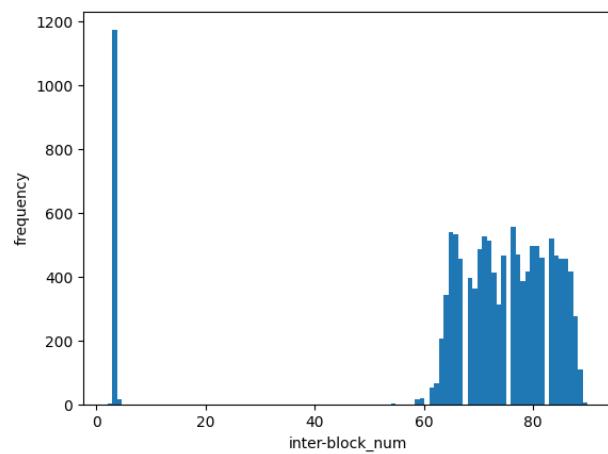


図 94 pht/experiment-win/1202-1/BC/1202-1-70D-PC7ALL-DIF1-INT1-30histogram-inter-blocknum.eps

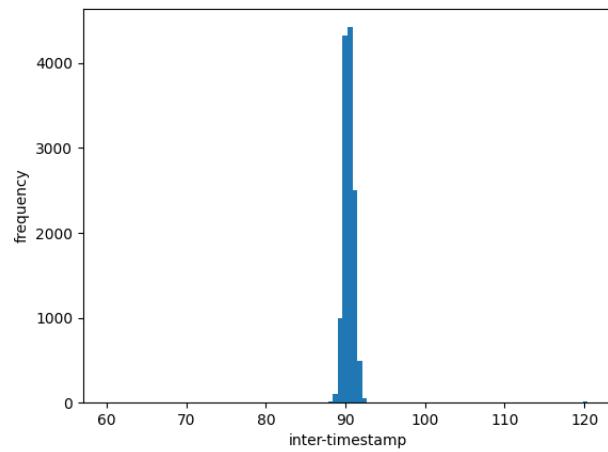


図 95 pht/experiment-win/1202-1/BC/1202-1-70D-PC7ALL-DIF1-INT1-30histogram-inter-timestamp.eps

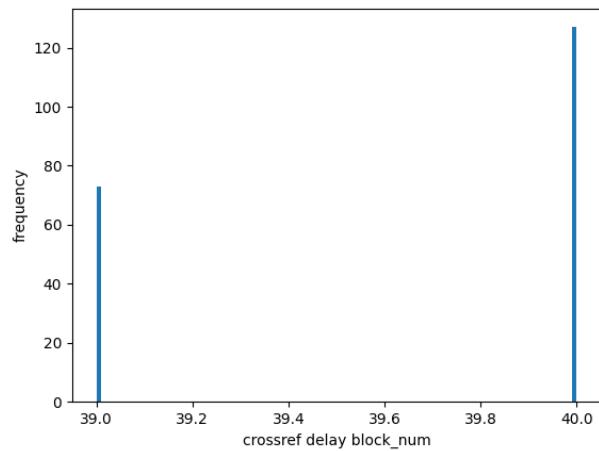


図 96 pht/experiment-win/1202-1/BC/30/1202-1-70D-PC7ALL-DIF1-INT1-30histogram-delay-blocknum.eps

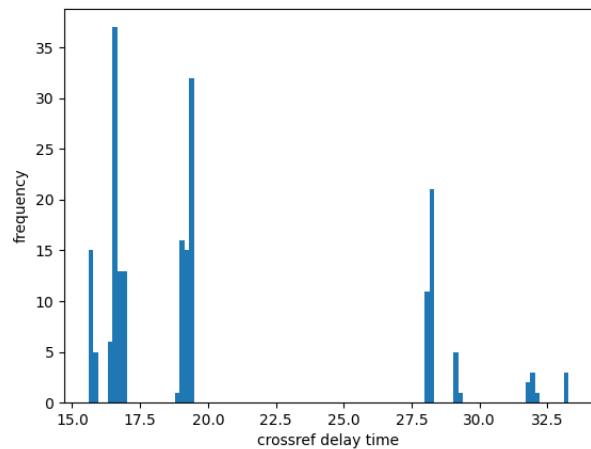


図 97 pht/experiment-win/1202-1/BC/30/1202-1-70D-PC7ALL-DIF1-INT1-30histogram-delay-time.eps

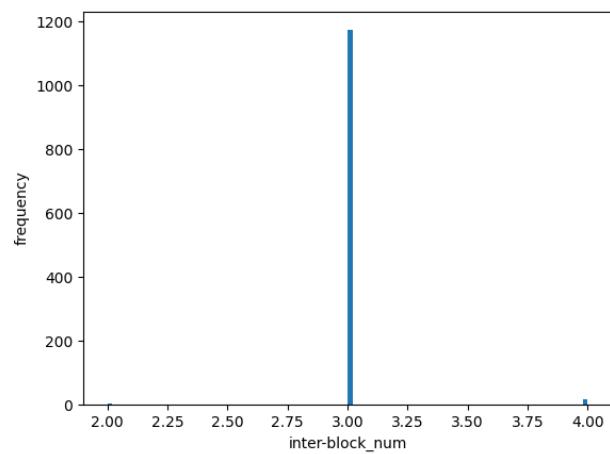


図 98 pht/experiment-win/1202-1/BC/30/1202-1-70D-PC7ALL-DIF1-INT1-30histogram-inter-blocknum.eps

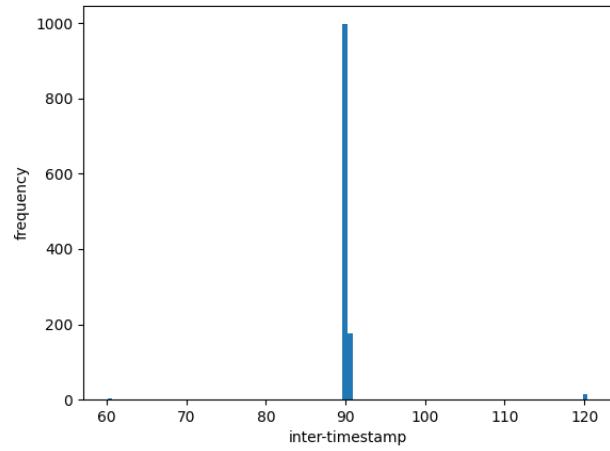


図 99 pht/experiment-win/1202-1/BC/30/1202-1-70D-PC7ALL-DIF1-INT1-30histogram-inter-timestamp.eps

5 1202-2-70D-PC7ALL-DIF1-INT1-30

5 章にて報告する条件を表 4 に示す.

表 4 設定値の一覧

設定項目	設定値
起動ノード数 (ドメイン数 D)	70 [ノード]
履歴交差間隔	90 [秒]
履歴交差回数	200 [回]
ブロック不生成間隔	0.5[秒]
ブロック生成難易度	1 [0x]
PC 内同時起動台数	7 [台]
使用台数	10 [台]
ブロック不生成間隔 : 30 秒	70 [ノード]
ブロック不生成間隔 : 1 秒	0 [ノード]

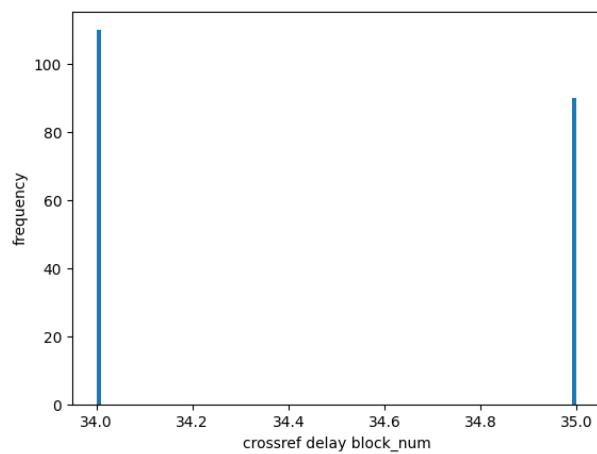


図 100 pht/experiment-win/1202-2/BC/1/1202-2-70D-PC7ALL-DIF1-INT1-30histogram-delay-blocknum.eps

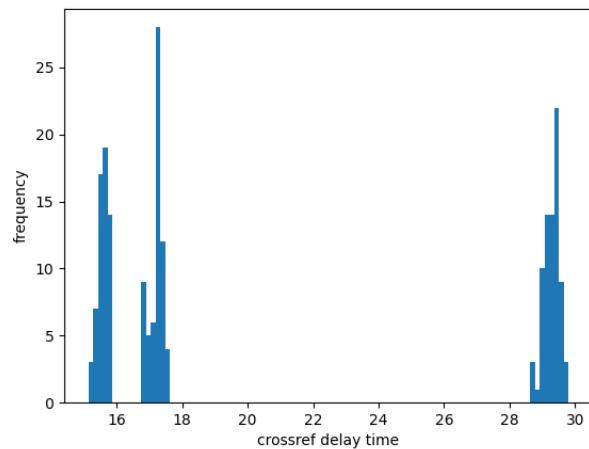


図 101 pht/experiment-win/1202-2/BC/1/1202-2-70D-PC7ALL-DIF1-INT1-30histogram-delay-time.eps

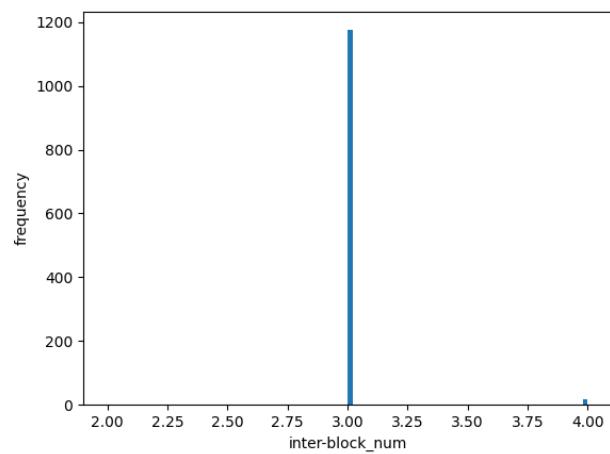


図 102 pht/experiment-win/1202-2/BC/1/1202-2-70D-PC7ALL-DIF1-INT1-30histogram-inter-blocknum.eps

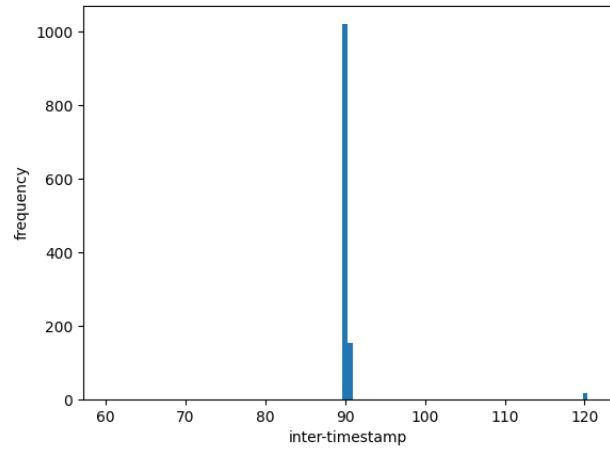


図 103 pht/experiment-win/1202-2/BC/1/1202-2-70D-PC7ALL-DIF1-INT1-30histogram-inter-timestamp.eps

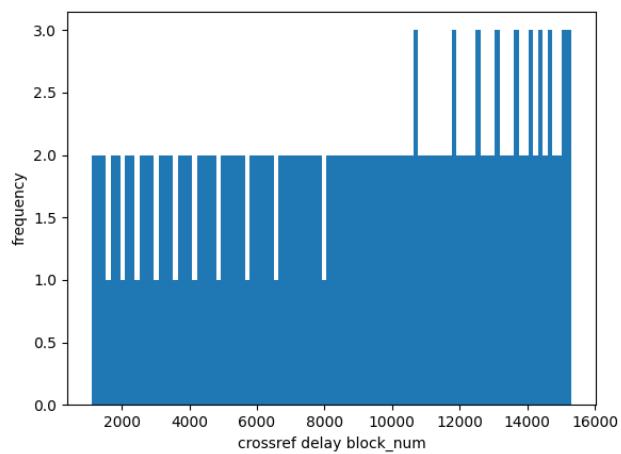


図 104 pht/experiment-win/1202-2/BC/1202-2-70D-PC7ALL-DIF1-INT1-30histogram-delay-blocknum.eps

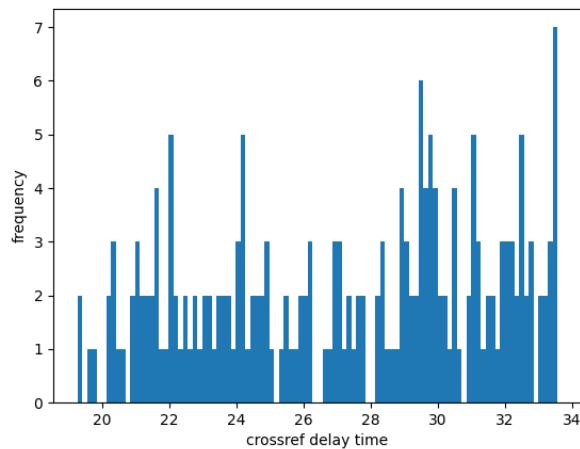


図 105 pht/experiment-win/1202-2/BC/1202-2-70D-PC7ALL-DIF1-INT1-30histogram-delay-time.eps

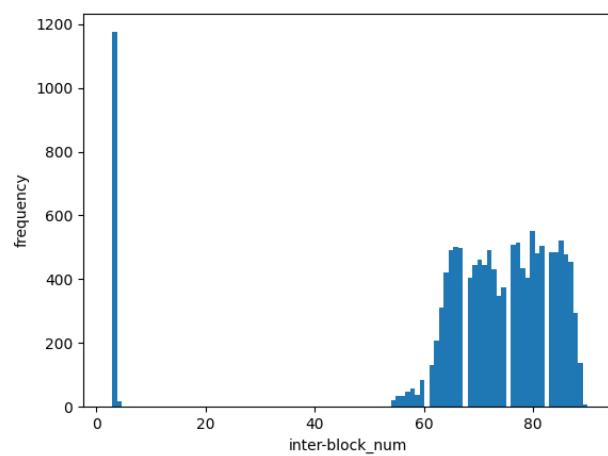


図 106 pht/experiment-win/1202-2/BC/1202-2-70D-PC7ALL-DIF1-INT1-30histogram-inter-blocknum.eps

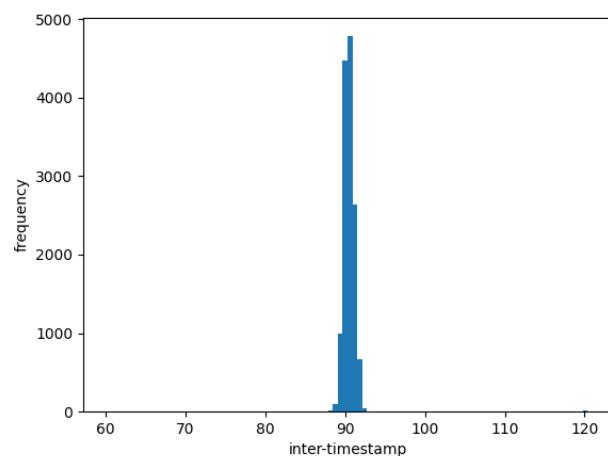


図 107 pht/experiment-win/1202-2/BC/1202-2-70D-PC7ALL-DIF1-INT1-30histogram-inter-timestamp.eps

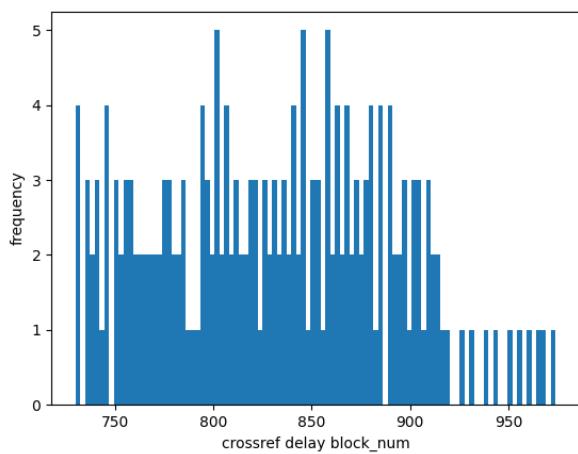


図 108 pht/experiment-win/1202-2/BC/30/1202-2-70D-PC7ALL-DIF1-INT1-30histogram-delay-blocknum.eps

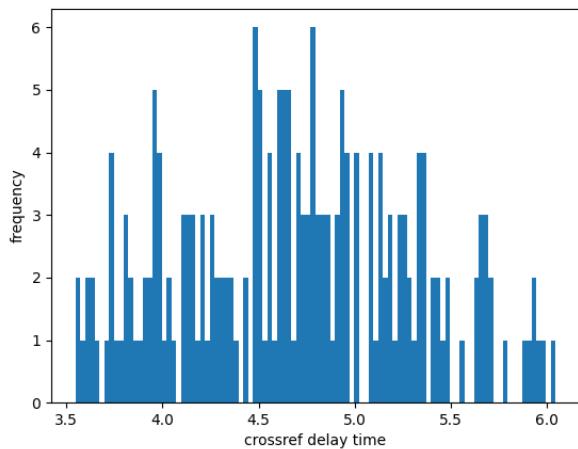


図 109 pht/experiment-win/1202-2/BC/30/1202-2-70D-PC7ALL-DIF1-INT1-30histogram-delay-time.eps

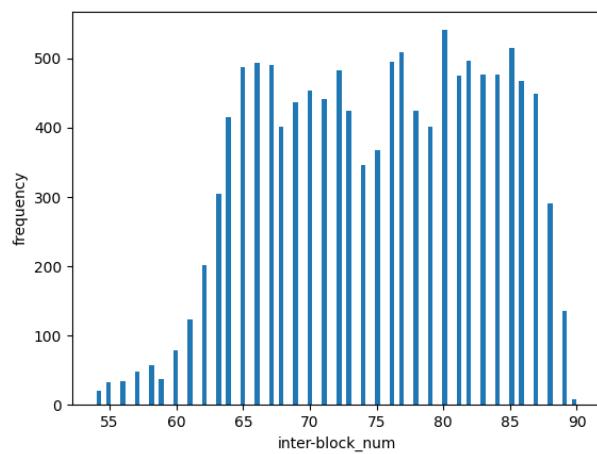


図 110 pht/experiment-win/1202-2/BC/30/1202-2-70D-PC7ALL-DIF1-INT1-30histogram-inter-blocknum.eps

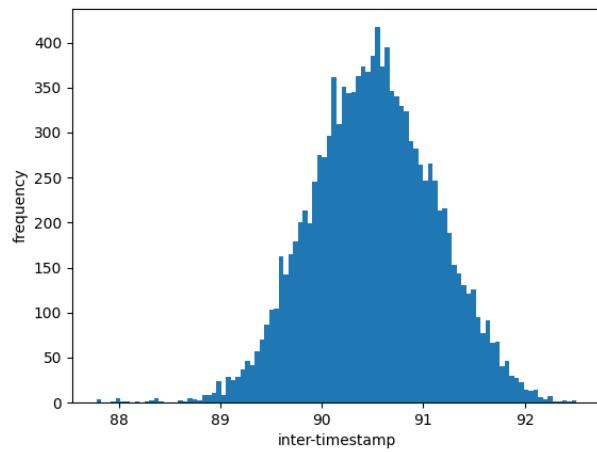


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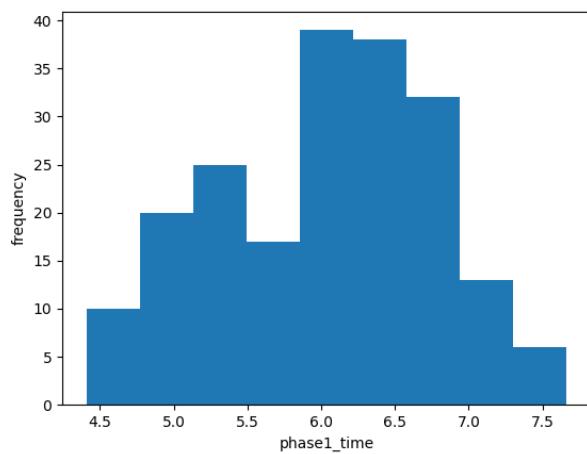


図 112 pht/experiment-win/1202-2/phase1/1202-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50051.eps

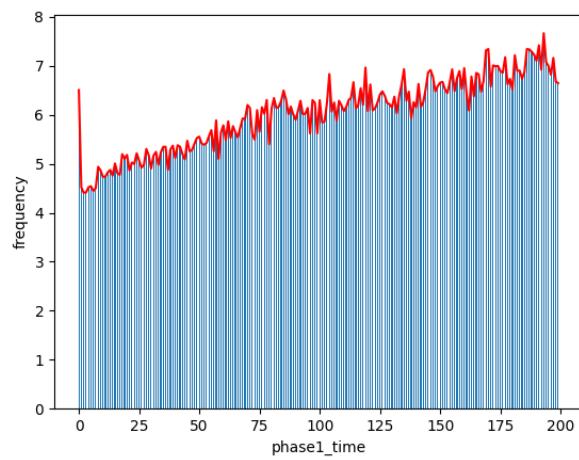


図 113 pht/experiment-win/1202-2/phase1/1202-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50051-in-chronological-order.eps

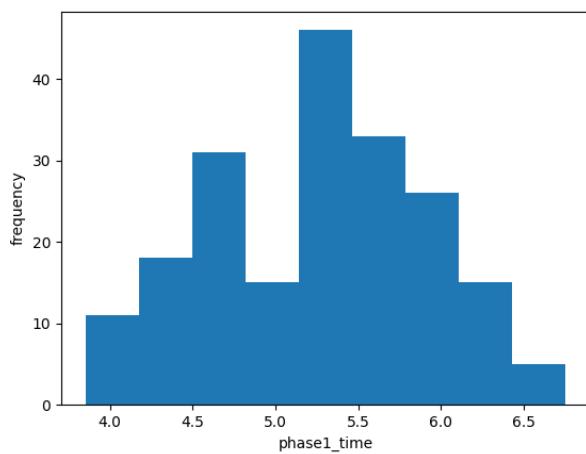


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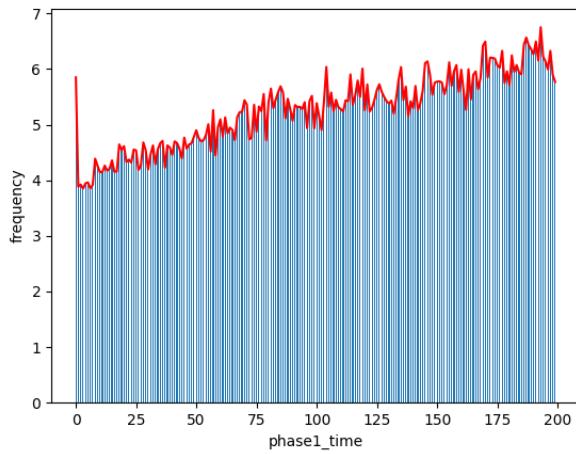


図 115 pht/experiment-win/1202-2/phase1/1202-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50053-in-chronological-order.eps

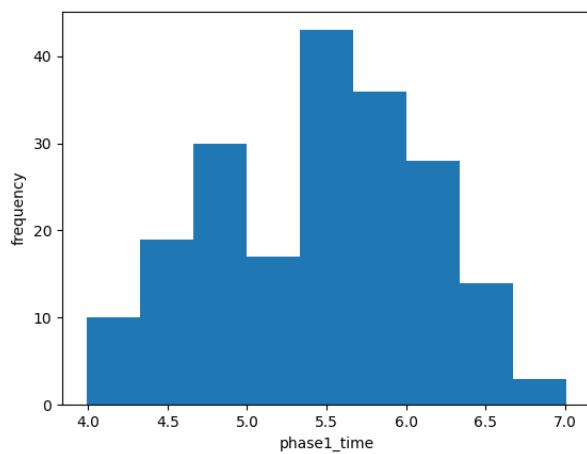


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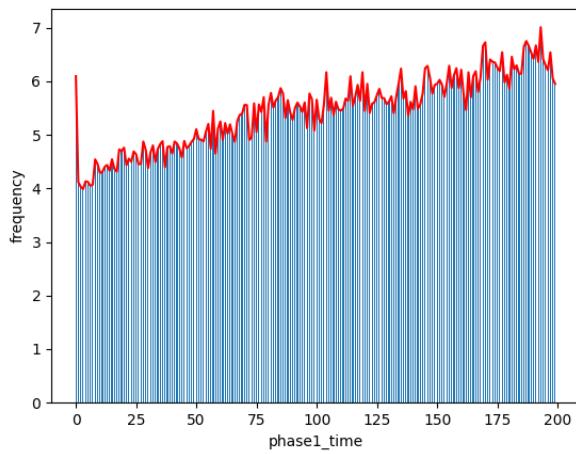


図 117 pht/experiment-win/1202-2/phase1/1202-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50055-in-chronological-order.eps

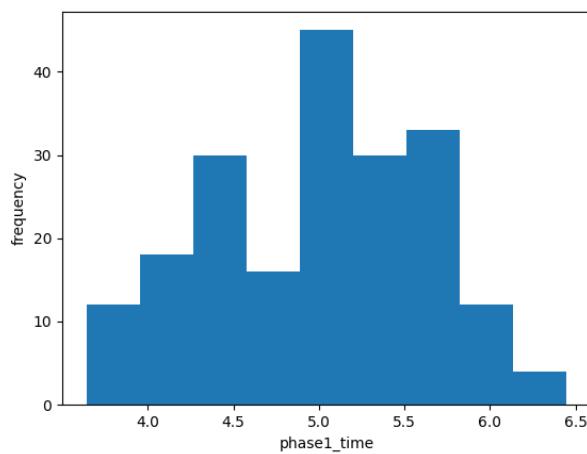


図 118 pht/experiment-win/1202-2/phase1/1202-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50057.eps

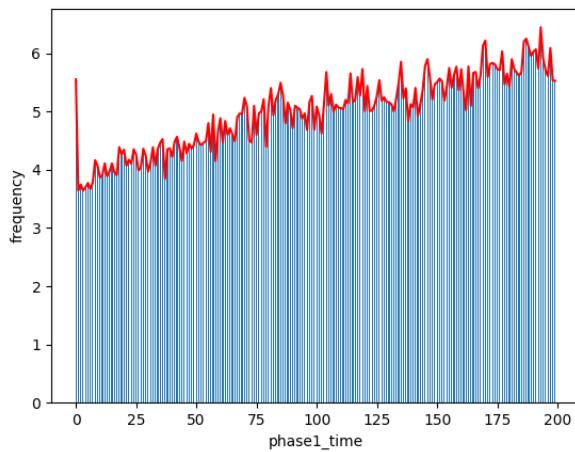


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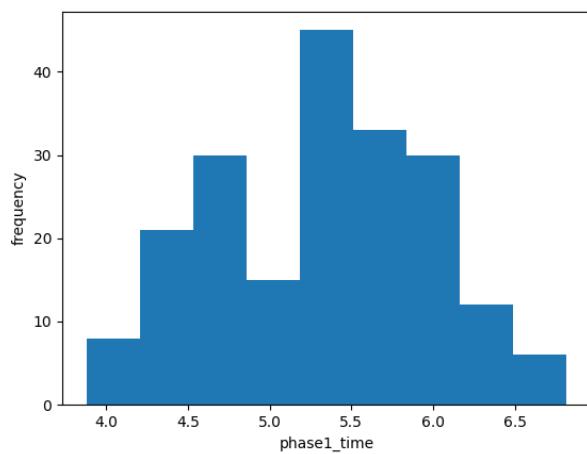


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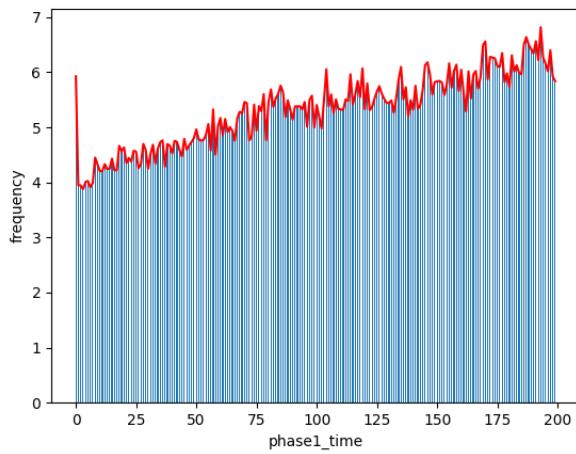


図 121 pht/experiment-win/1202-2/phase1/1202-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50059-in-chronological-order.eps

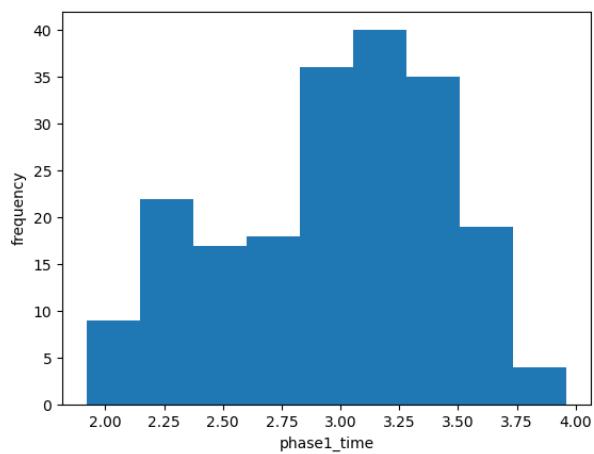


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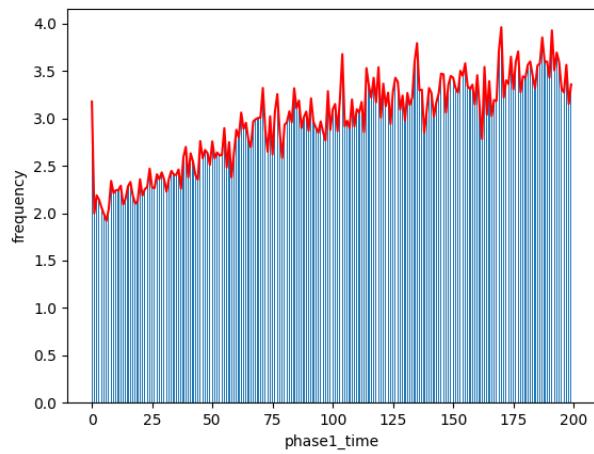


図 123 pht/experiment-win/1202-2/phase1/1202-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50181-in-chronological-order.eps

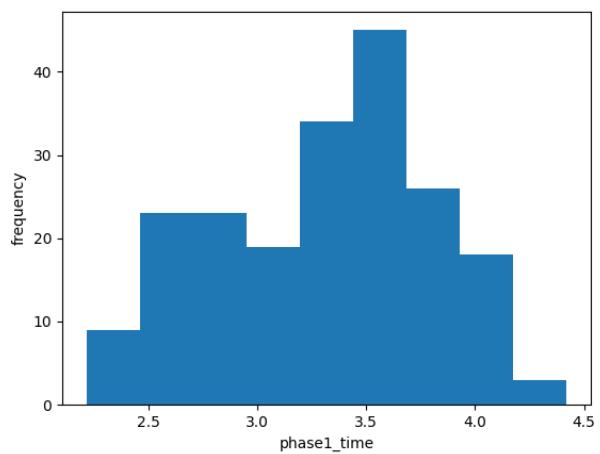
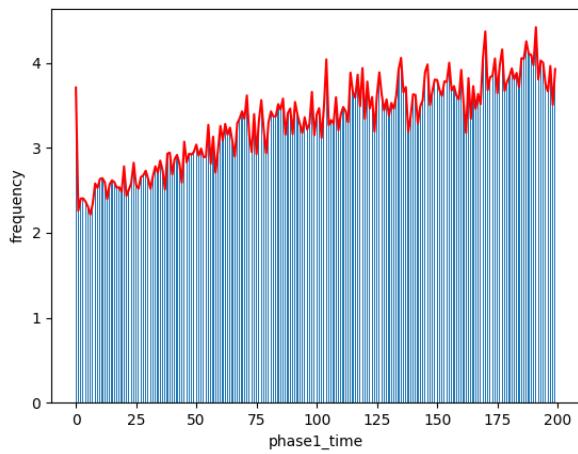


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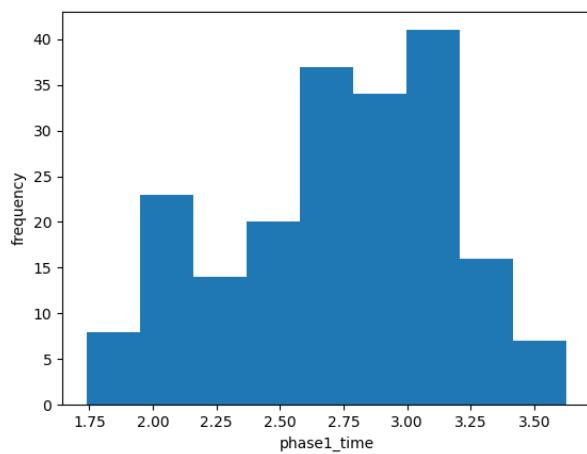


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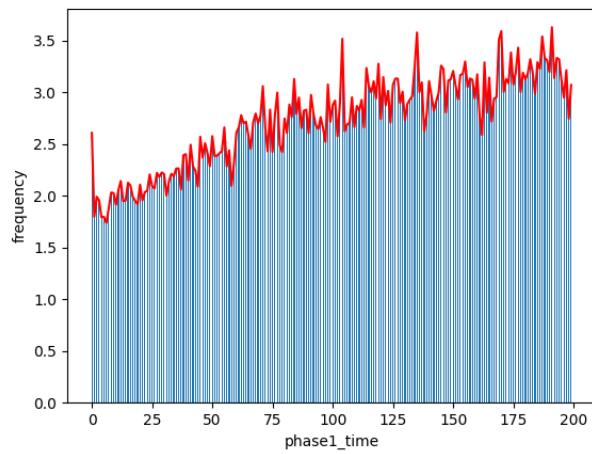


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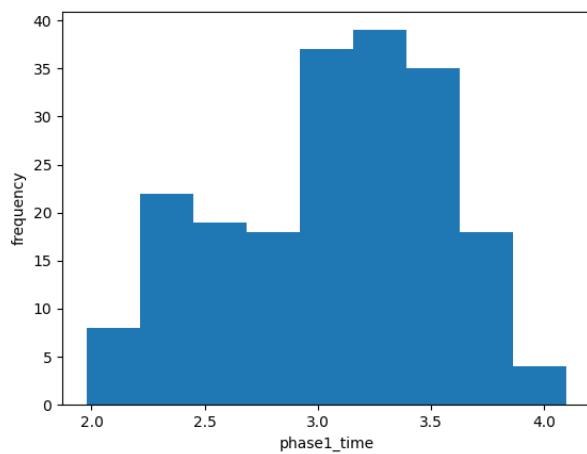


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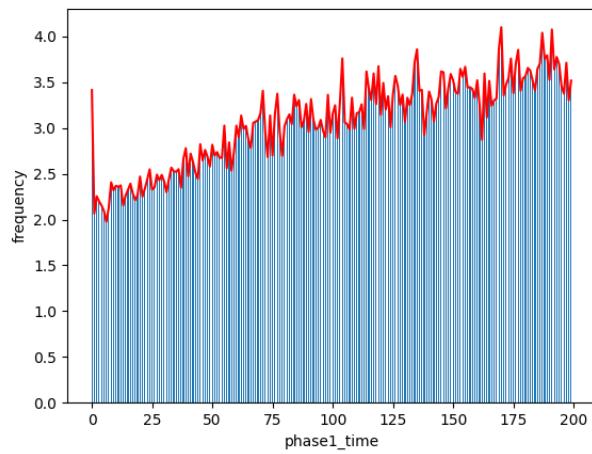


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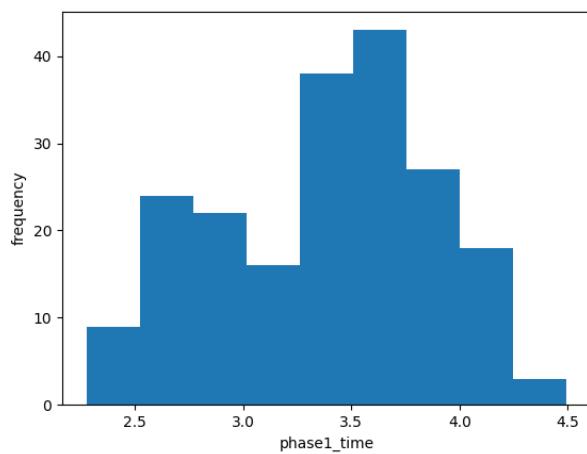


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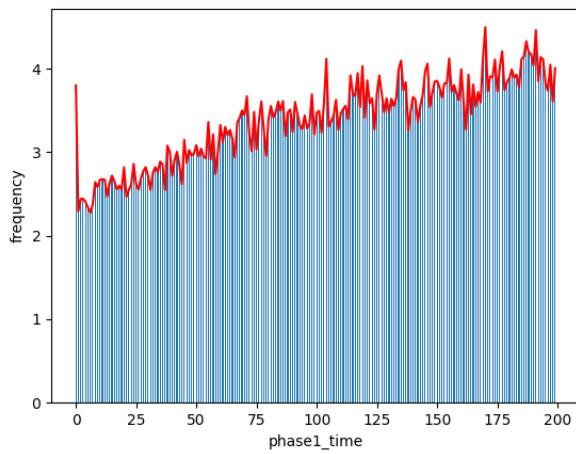


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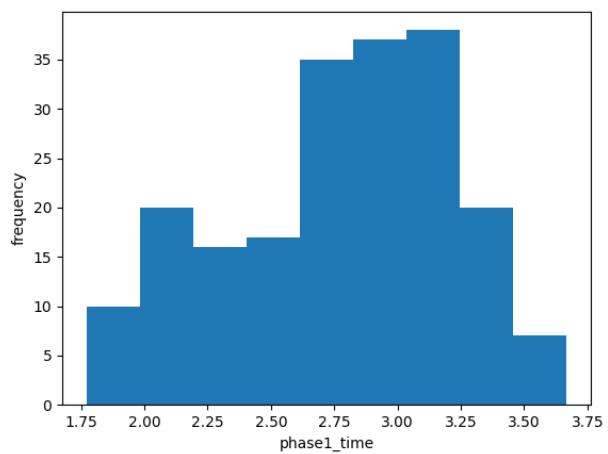


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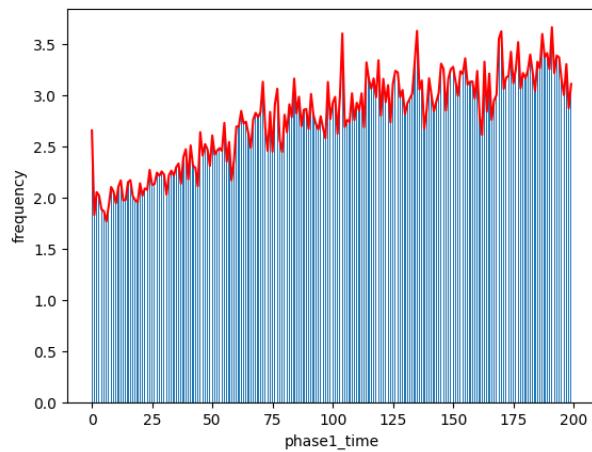


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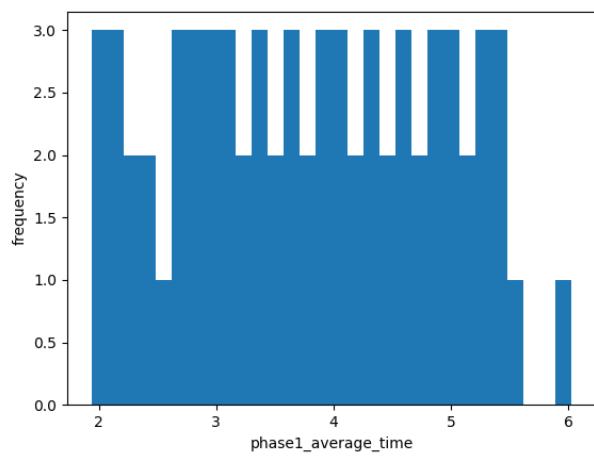


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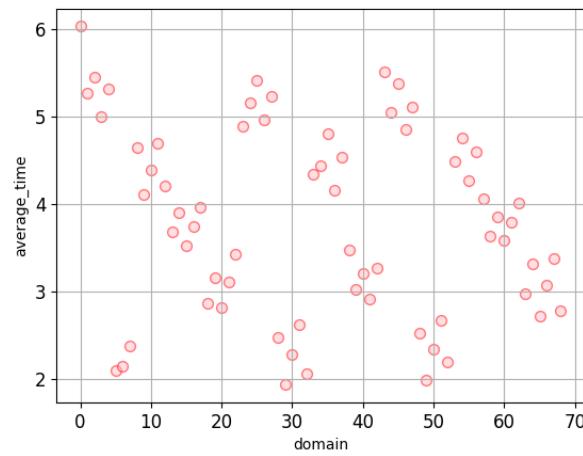


図 135 pht/experiment-win/1202-2/phase1/1202-2-70D-PC7ALL-DIF1-INT1-30analyze-phase1-average-time-scatter.eps

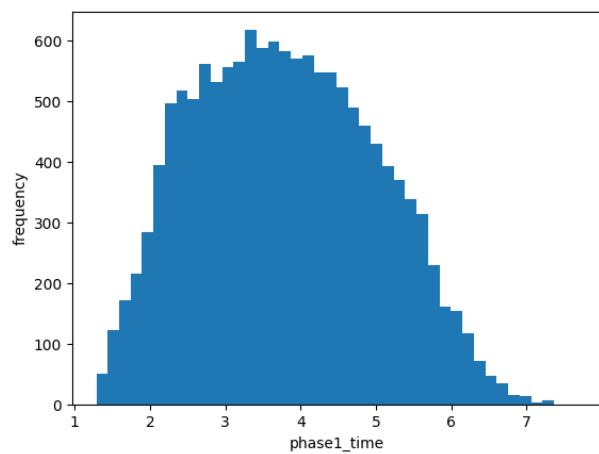


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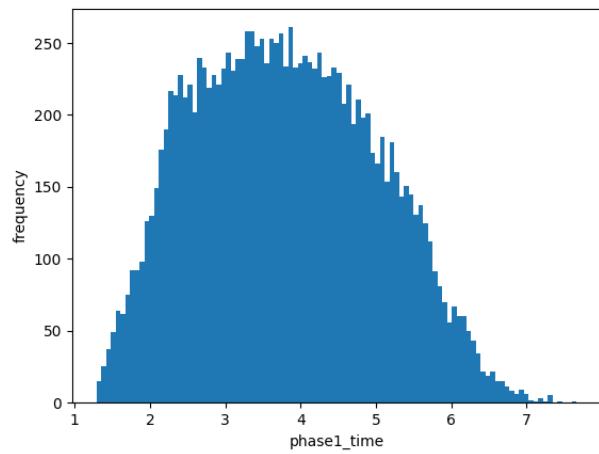


図 137 pht/experiment-win/1202-2/phase1/1202-2-70D-PC7ALL-DIF1-INT1-30analyze-phase1-time-bins100.eps

6 1203-1-70D-PC7ALL-DIF1-INT1-30

6 章にて報告する条件を表 5 に示す。

表 5 設定値の一覧

設定項目	設定値
起動ノード数 (ドメイン数 D)	70 [ノード]
履歴交差間隔	90 [秒]
履歴交差回数	200 [回]
ブロック不生成間隔	0.5[秒]
ブロック生成難易度	1 [0x]
PC 内同時起動台数	7 [台]
使用台数	10 [台]
ブロック不生成間隔 : 30 秒	70 [ノード]
ブロック不生成間隔 : 1 秒	0 [ノード]

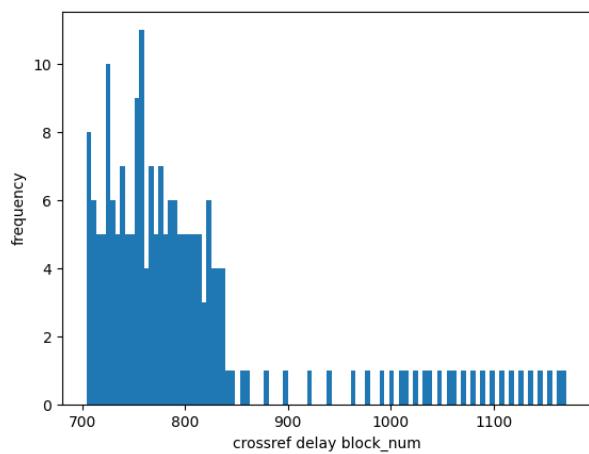


図 138 pht/experiment-win/1203-1/BC/1/1203-1-70D-PC7ALL-DIF1-INT1-30histogram-delay-blocknum.eps

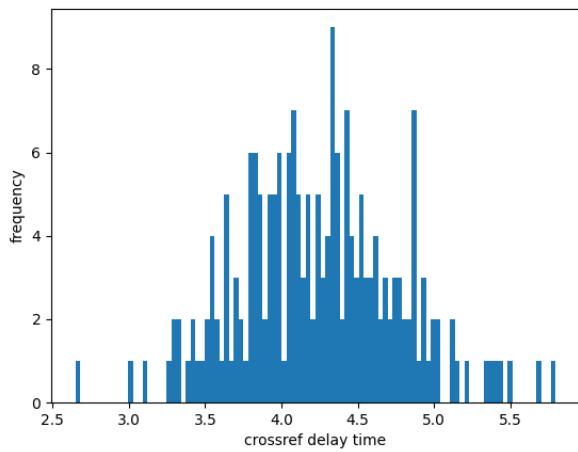


図 139 pht/experiment-win/1203-1/BC/1/1203-1-70D-PC7ALL-DIF1-INT1-30histogram-delay-time.eps

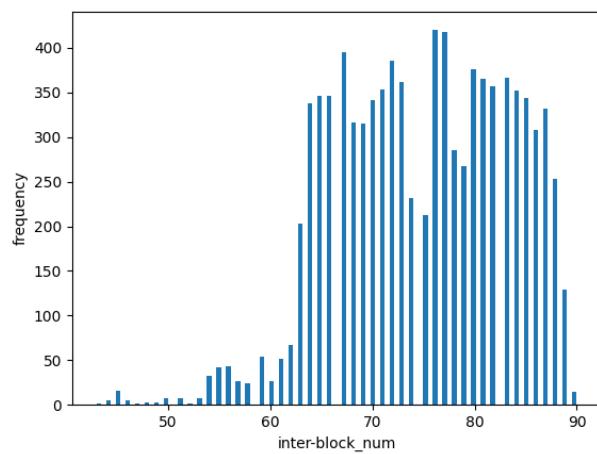


図 140 pht/experiment-win/1203-1/BC/1/1203-1-70D-PC7ALL-DIF1-INT1-30histogram-inter-blocknum.eps

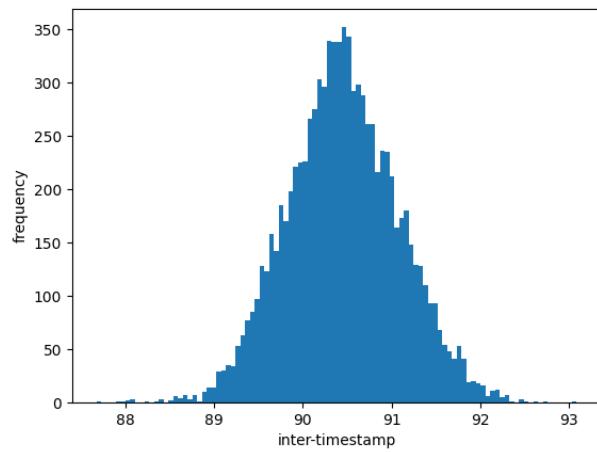


図 141 pht/experiment-win/1203-1/BC/1/1203-1-70D-PC7ALL-DIF1-INT1-30histogram-inter-timestamp.eps

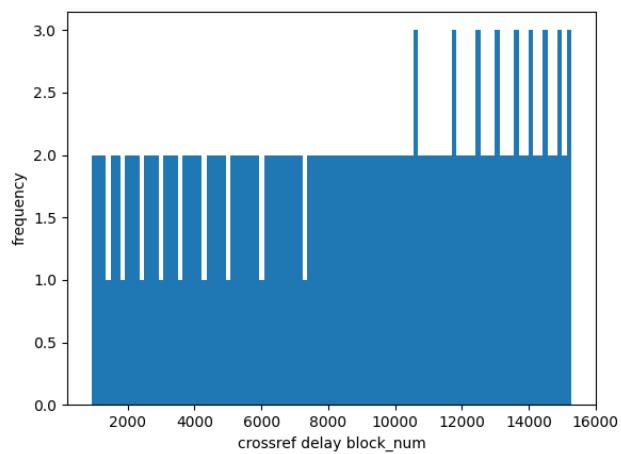


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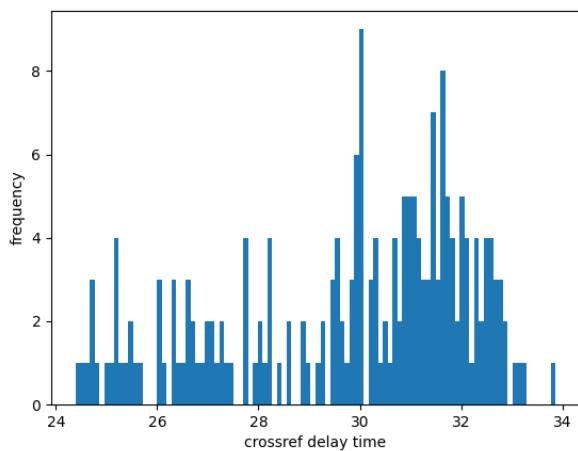


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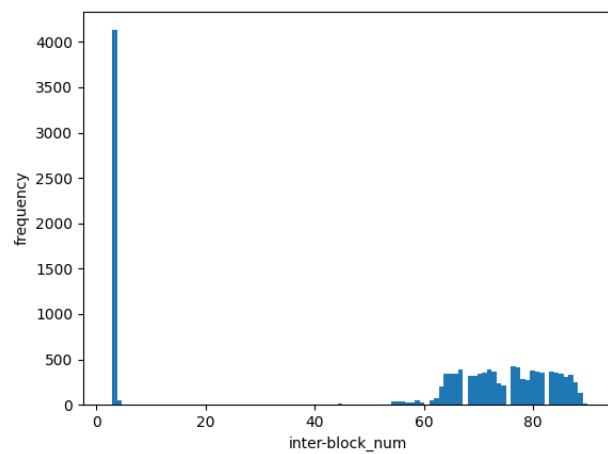


図 144 pht/experiment-win/1203-1/BC/1203-1-70D-PC7ALL-DIF1-INT1-30histogram-inter-blocknum.eps

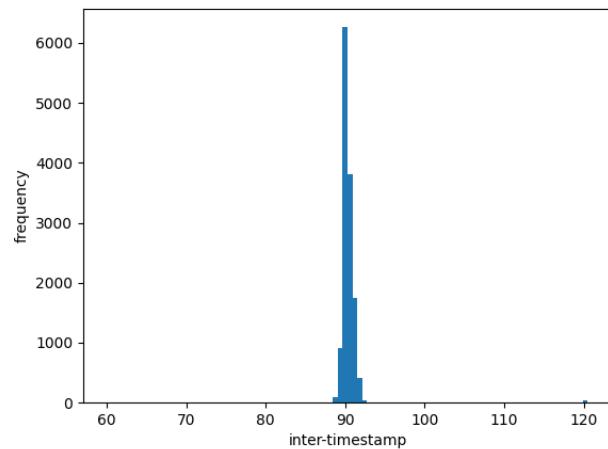


図 145 pht/experiment-win/1203-1/BC/1203-1-70D-PC7ALL-DIF1-INT1-30histogram-inter-timestamp.eps

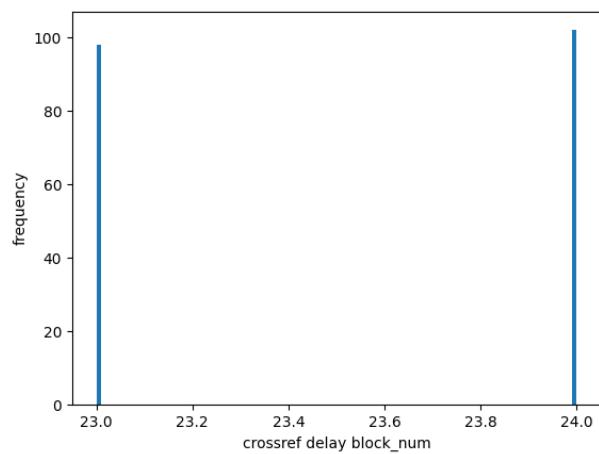


図 146 pht/experiment-win/1203-1/BC/30/1203-1-70D-PC7ALL-DIF1-INT1-30histogram-delay-blocknum.eps

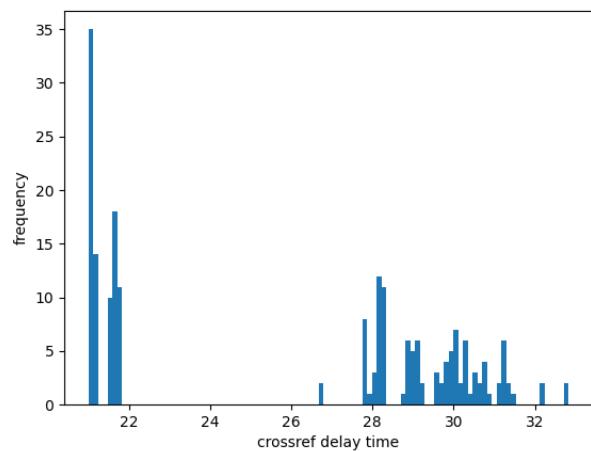


図 147 pht/experiment-win/1203-1/BC/30/1203-1-70D-PC7ALL-DIF1-INT1-30histogram-delay-time.eps

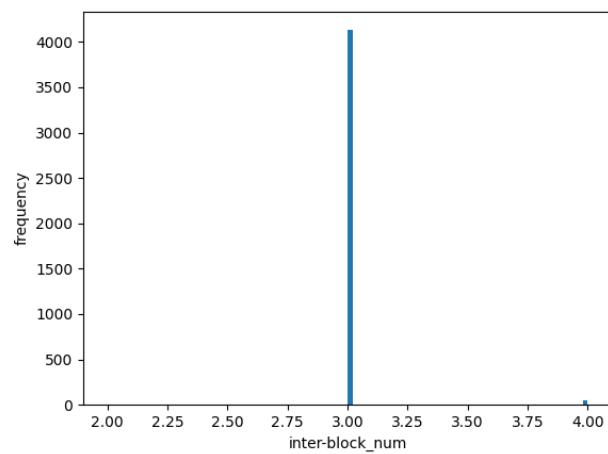


図 148 pht/experiment-win/1203-1/BC/30/1203-1-70D-PC7ALL-DIF1-INT1-30histogram-inter-blocknum.eps

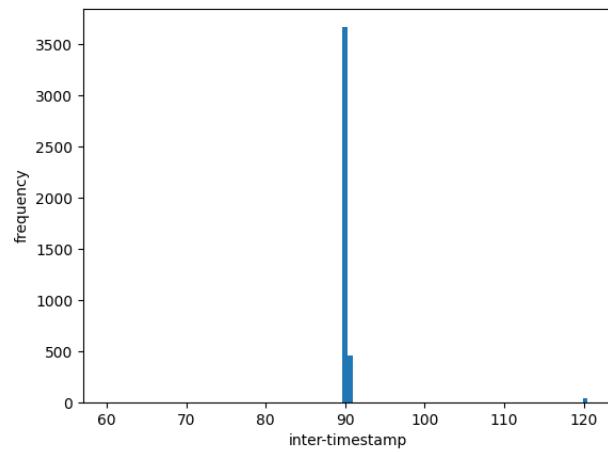


図 149 pht/experiment-win/1203-1/BC/30/1203-1-70D-PC7ALL-DIF1-INT1-30histogram-inter-timestamp.eps

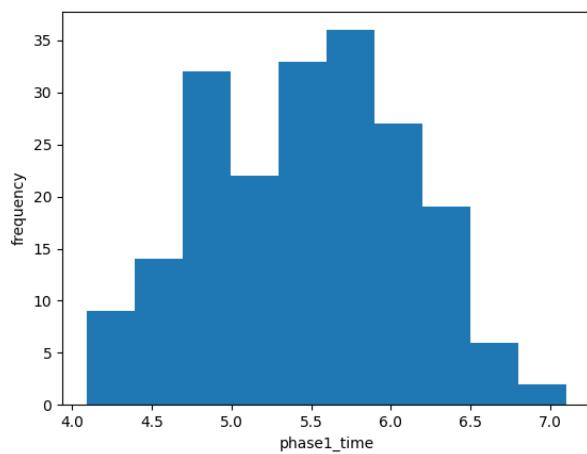


図 150 pht/experiment-win/1203-1/phase1/1203-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50051.eps

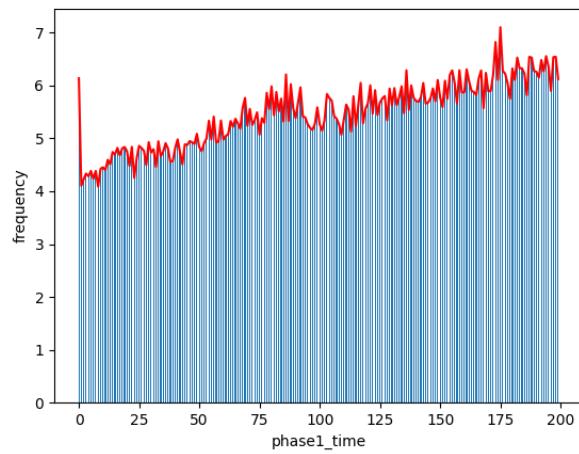


図 151 pht/experiment-win/1203-1/phase1/1203-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50051-in-chronological-order.eps

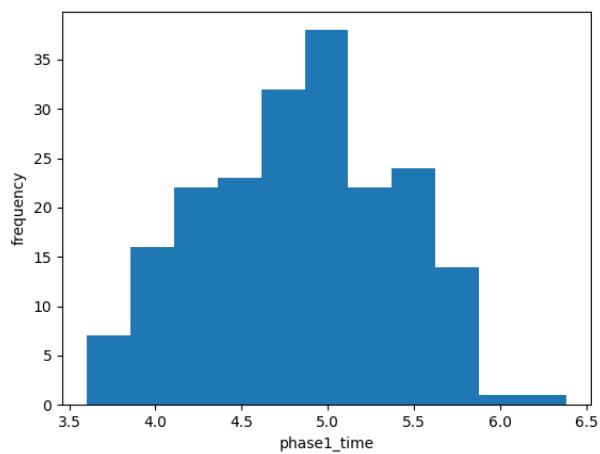


図 152 pht/experiment-win/1203-1/phase1/1203-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50053.eps

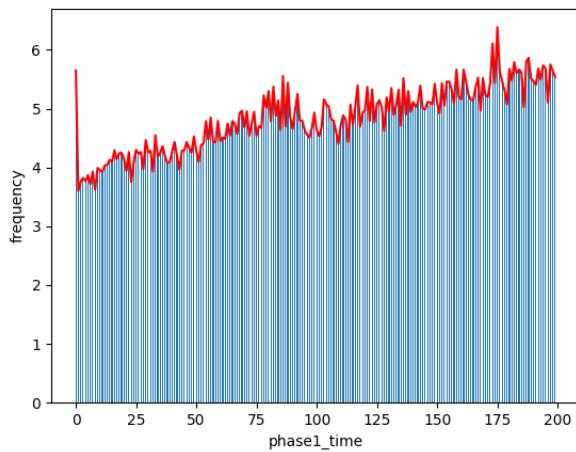


図 153 pht/experiment-win/1203-1/phase1/1203-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50053-in-chronological-order.eps

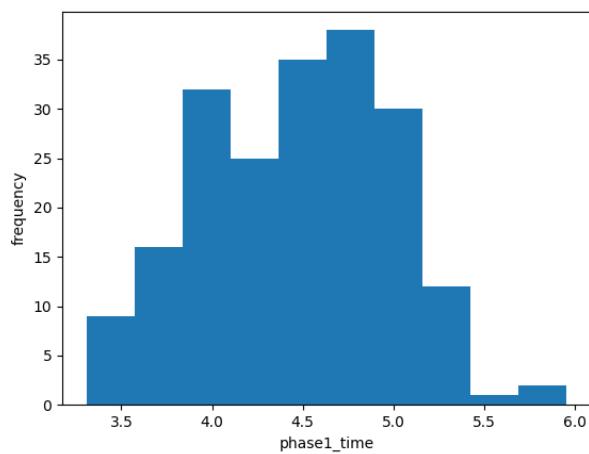


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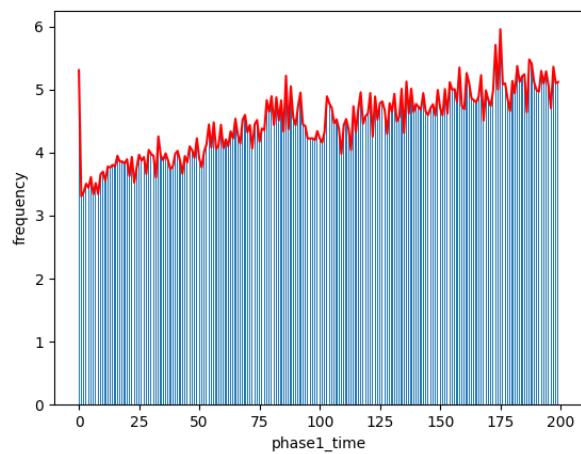


図 155 pht/experiment-win/1203-1/phase1/1203-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50055-in-chronological-order.eps

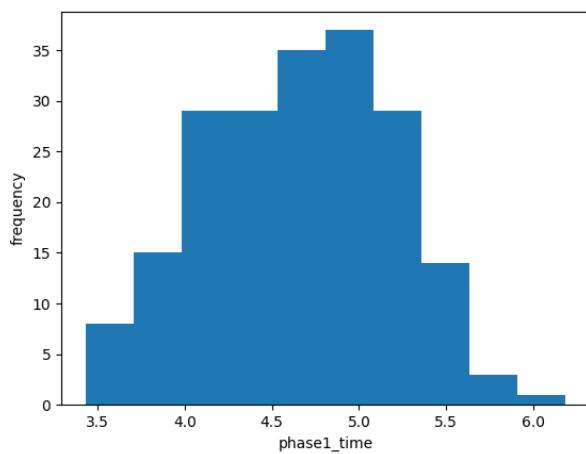


図 156 pht/experiment-win/1203-1/phase1/1203-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50057.eps

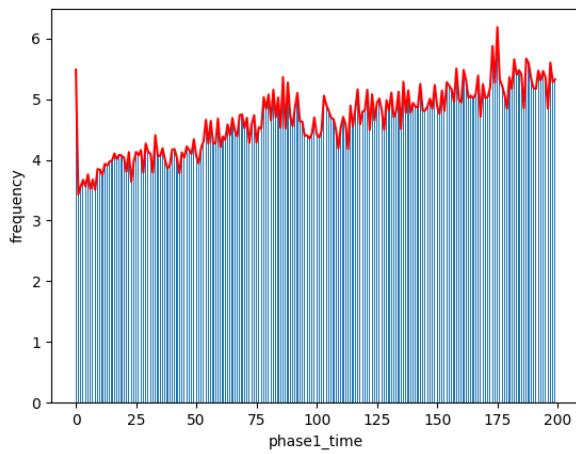


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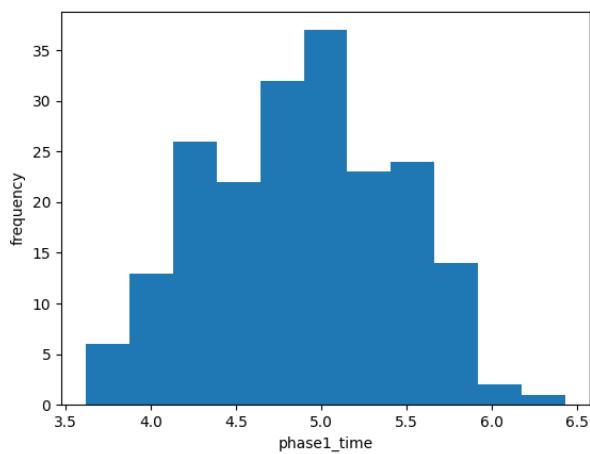


図 158 pht/experiment-win/1203-1/phase1/1203-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50059.eps

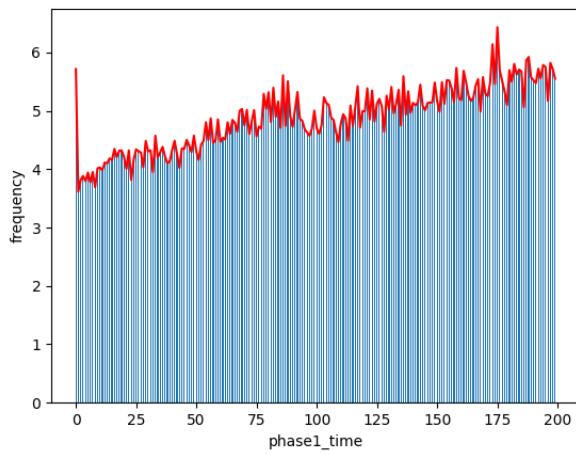


図 159 pht/experiment-win/1203-1/phase1/1203-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50059-in-chronological-order.eps

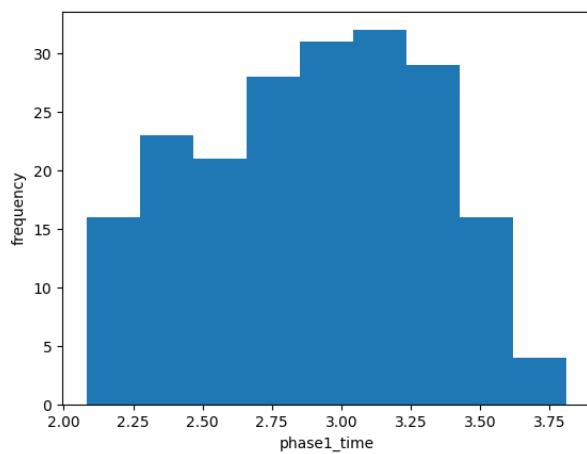


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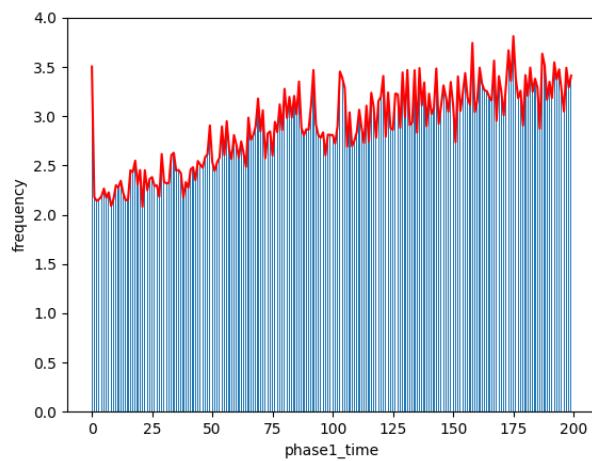


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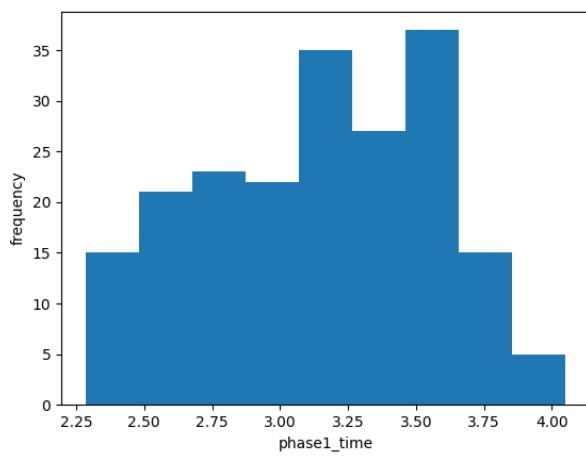


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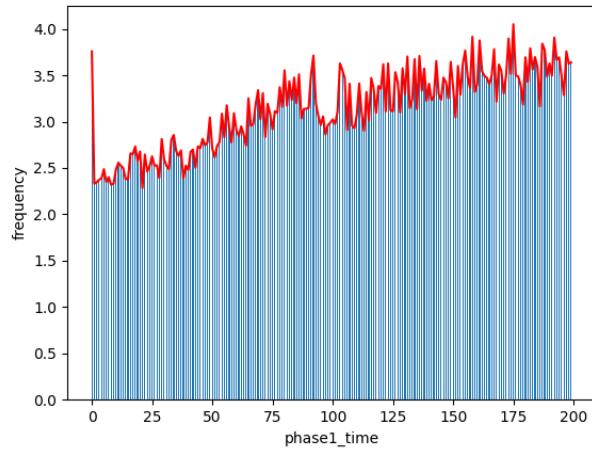


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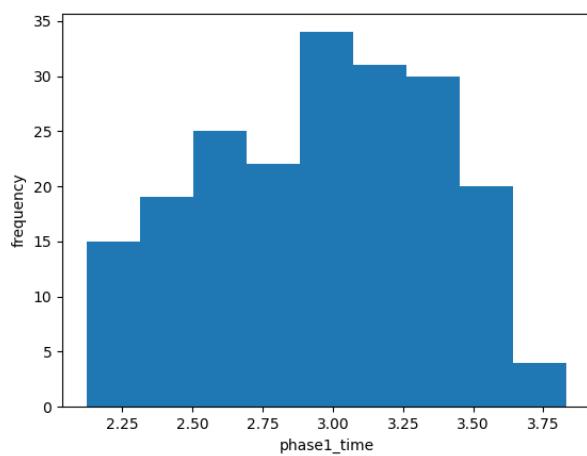


図 164 pht/experiment-win/1203-1/phase1/1203-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50187.eps

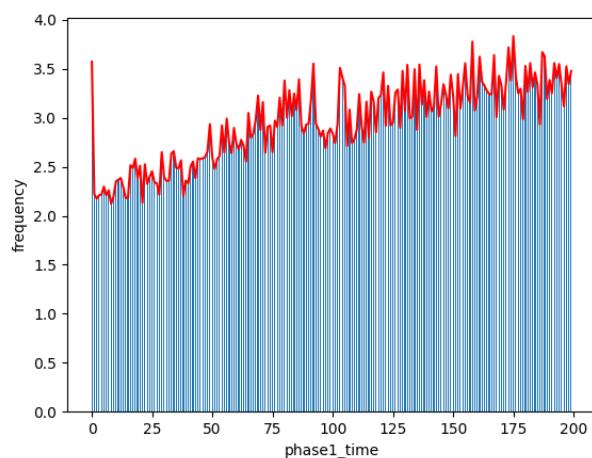


図 165 pht/experiment-win/1203-1/phase1/1203-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50187-in-chronological-order.eps

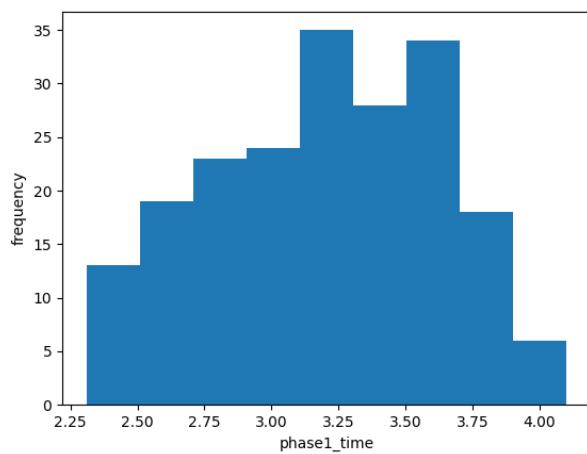


図 166 pht/experiment-win/1203-1/phase1/1203-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50189.eps

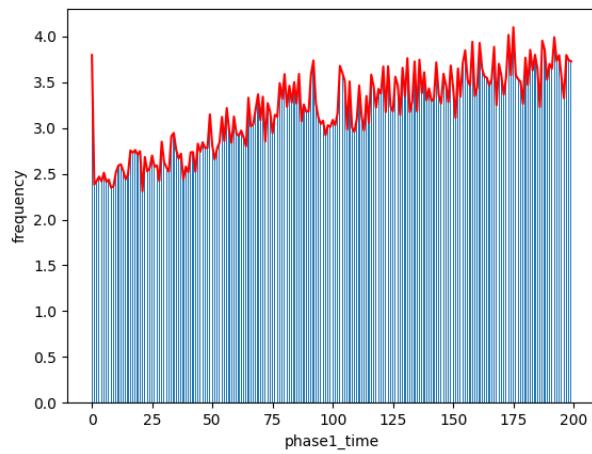


図 167 pht/experiment-win/1203-1/phase1/1203-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50189-in-chronological-order.eps

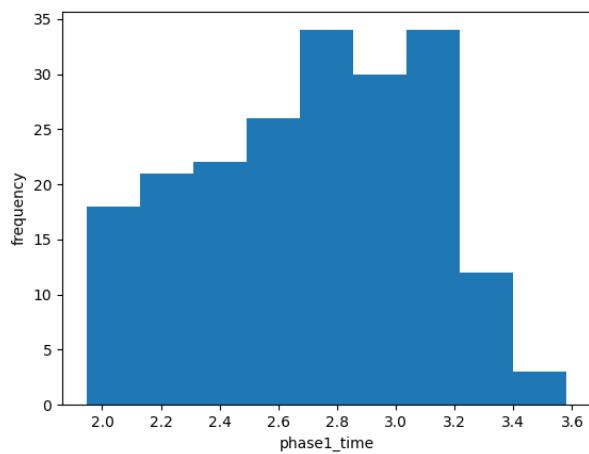


図 168 pht/experiment-win/1203-1/phase1/1203-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50191.eps

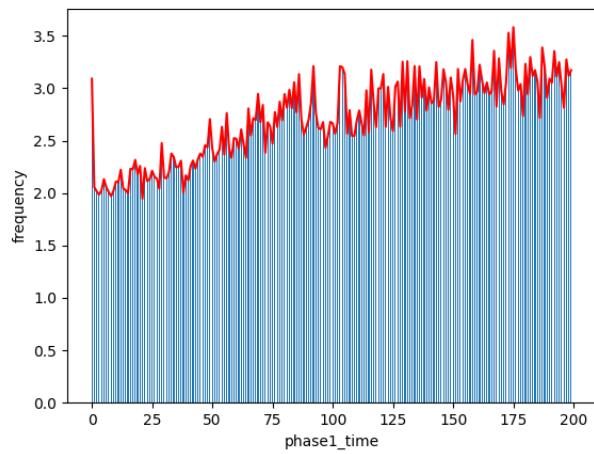


図 169 pht/experiment-win/1203-1/phase1/1203-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50191-in-chronological-order.eps

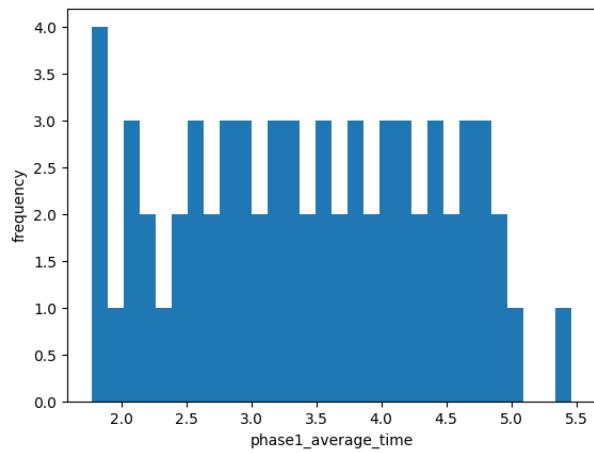


図 170 pht/experiment-win/1203-1/phase1/1203-1-70D-PC7ALL-DIF1-INT1-30analyze-phase1-average-time.eps

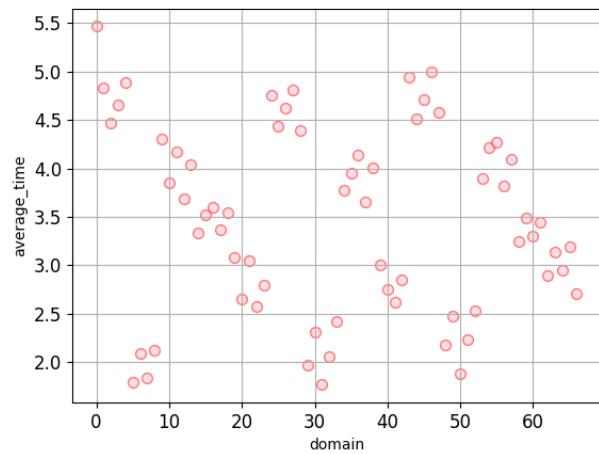


図 171 pht/experiment-win/1203-1/phase1/1203-1-70D-PC7ALL-DIF1-INT1-30analyze-phase1-average-time-scatter.eps

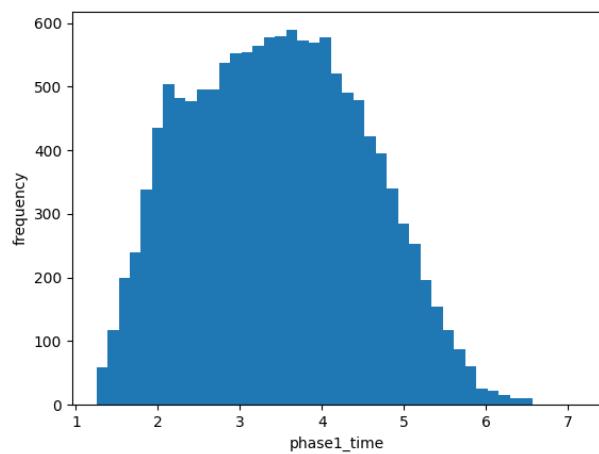


図 172 pht/experiment-win/1203-1/phase1/1203-1-70D-PC7ALL-DIF1-INT1-30analyze-phase1-time-auto.eps

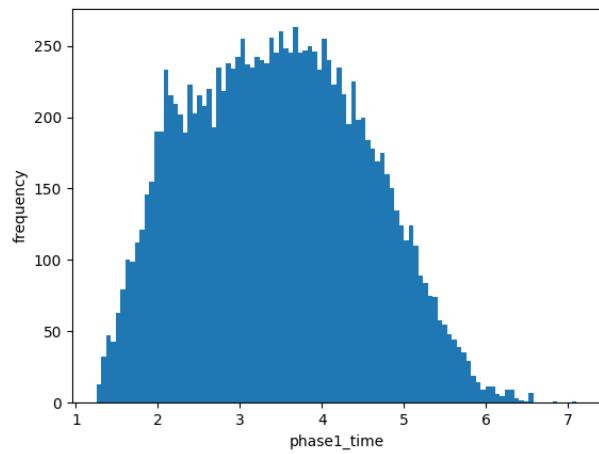


図 173 pht/experiment-win/1203-1/phase1/1203-1-70D-PC7ALL-DIF1-INT1-30analyze-phase1-time-bins100.eps

7 1203-2-70D-PC7ALL-DIF1-INT1-30

7章にて報告する条件を表6に示す。

表6 設定値の一覧

設定項目	設定値
起動ノード数 (ドメイン数 D)	70 [ノード]
履歴交差間隔	90 [秒]
履歴交差回数	200 [回]
ブロック不生成間隔	0.5[秒]
ブロック生成難易度	1 [0x]
PC 内同時起動台数	7 [台]
使用台数	10 [台]
ブロック不生成間隔 : 30 秒	70 [ノード]
ブロック不生成間隔 : 1 秒	0 [ノード]

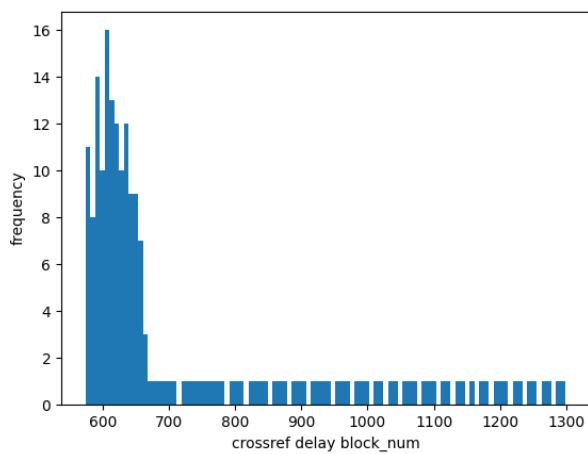


図 174 pht/experiment-win/1203-2/BC/1/1203-2-70D-PC7ALL-DIF1-INT1-30histogram-delay-blocknum.eps

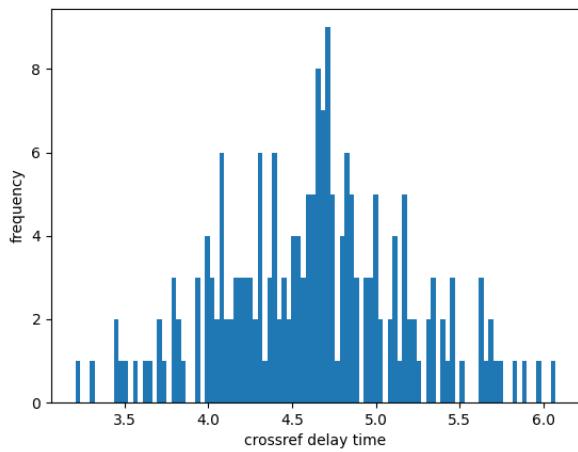


図 175 pht/experiment-win/1203-2/BC/1/1203-2-70D-PC7ALL-DIF1-INT1-30histogram-delay-time.eps

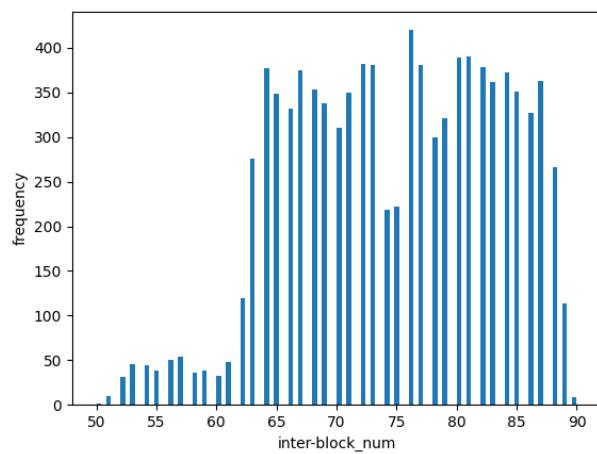


図 176 pht/experiment-win/1203-2/BC/1/1203-2-70D-PC7ALL-DIF1-INT1-30histogram-inter-blocknum.eps

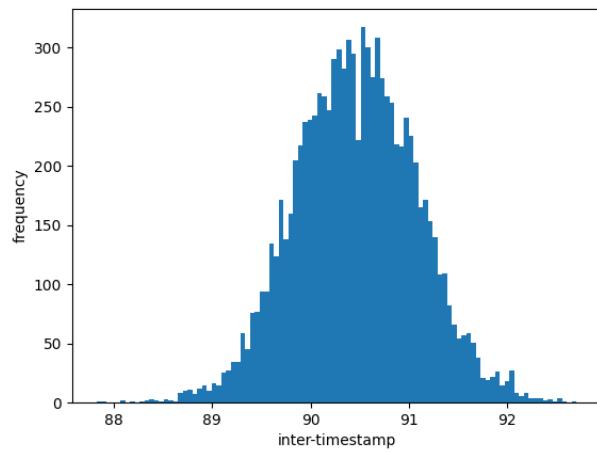


図 177 pht/experiment-win/1203-2/BC/1/1203-2-70D-PC7ALL-DIF1-INT1-30histogram-inter-timestamp.eps

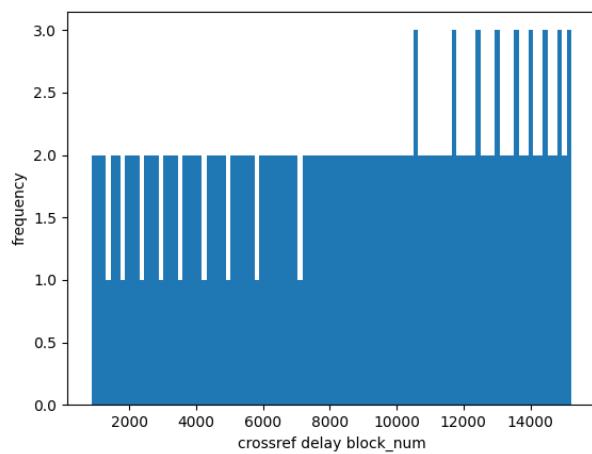


図 178 pht/experiment-win/1203-2/BC/1203-2-70D-PC7ALL-DIF1-INT1-30histogram-delay-blocknum.eps

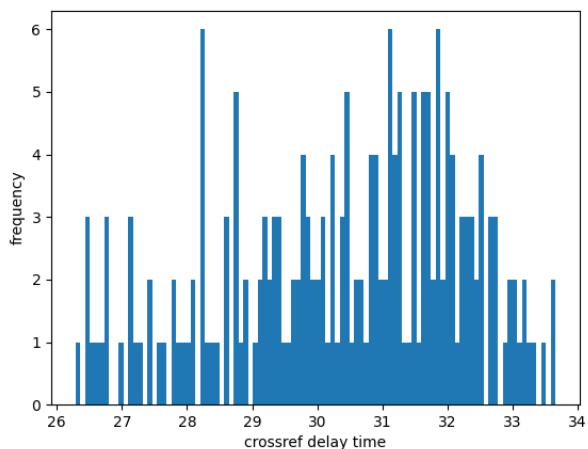


図 179 pht/experiment-win/1203-2/BC/1203-2-70D-PC7ALL-DIF1-INT1-30histogram-delay-time.eps

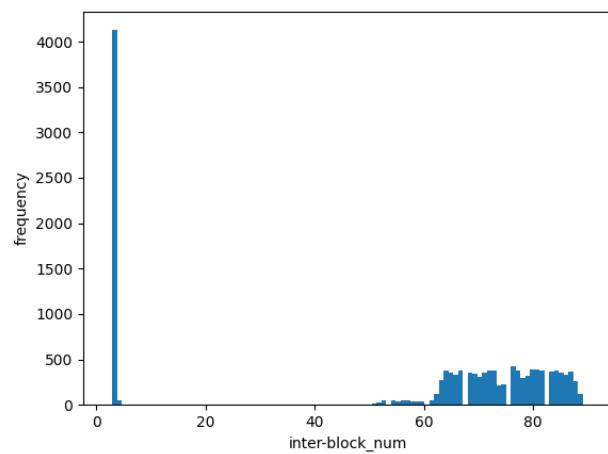


図 180 pht/experiment-win/1203-2/BC/1203-2-70D-PC7ALL-DIF1-INT1-30histogram-inter-blocknum.eps

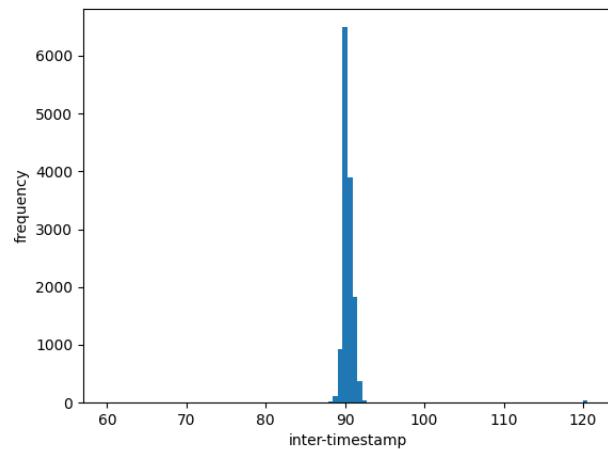


図 181 pht/experiment-win/1203-2/BC/1203-2-70D-PC7ALL-DIF1-INT1-30histogram-inter-timestamp.eps

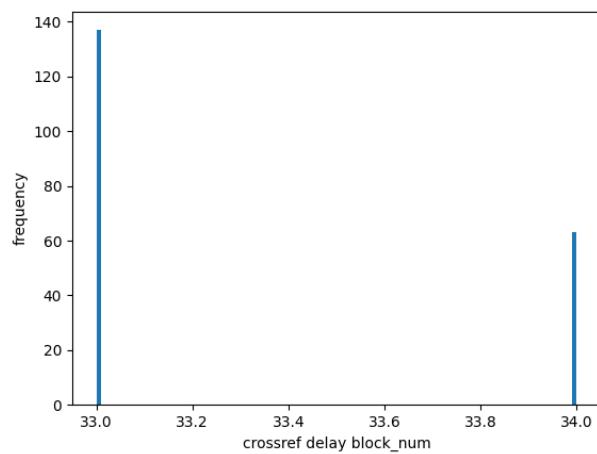


図 182 pht/experiment-win/1203-2/BC/30/1203-2-70D-PC7ALL-DIF1-INT1-30histogram-delay-blocknum.eps

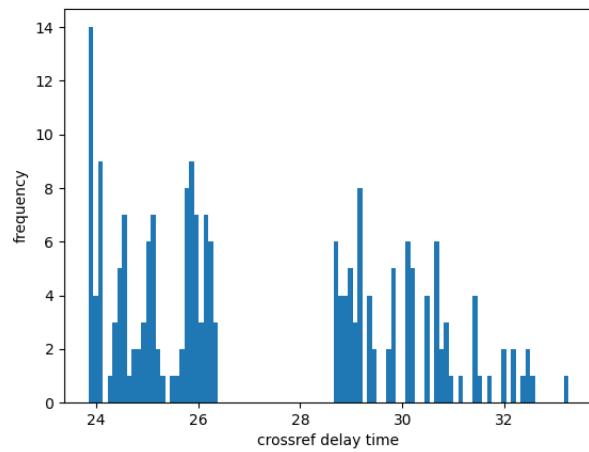


図 183 pht/experiment-win/1203-2/BC/30/1203-2-70D-PC7ALL-DIF1-INT1-30histogram-delay-time.eps

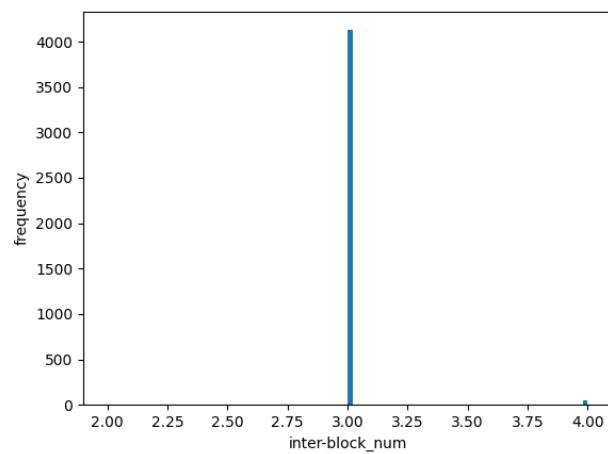


図 184 pht/experiment-win/1203-2/BC/30/1203-2-70D-PC7ALL-DIF1-INT1-30histogram-inter-blocknum.eps

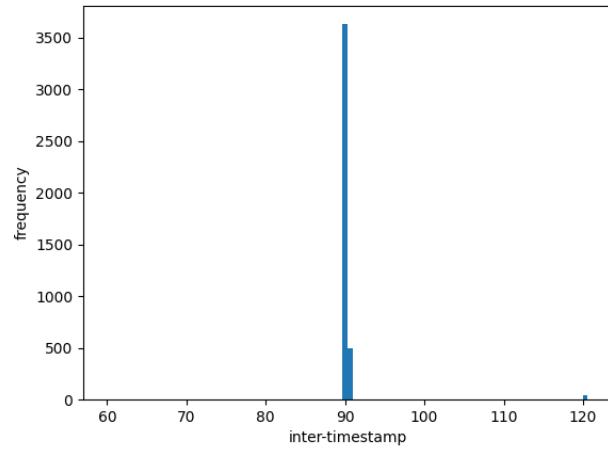


図 185 pht/experiment-win/1203-2/BC/30/1203-2-70D-PC7ALL-DIF1-INT1-30histogram-inter-timestamp.eps

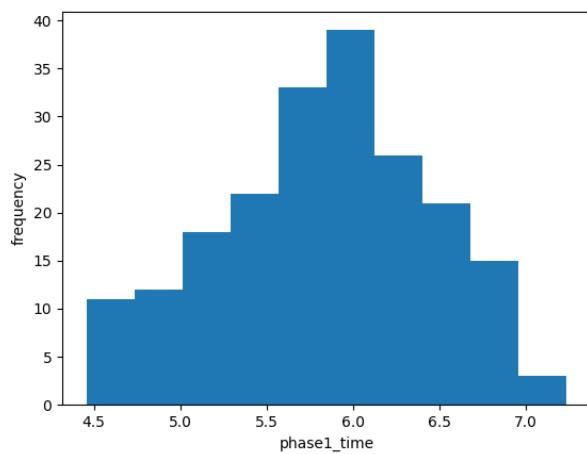


図 186 pht/experiment-win/1203-2/phase1/1203-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50051.eps

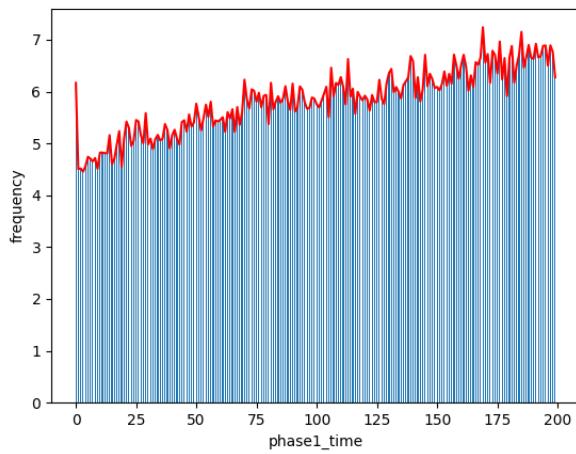


図 187 pht/experiment-win/1203-2/phase1/1203-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50051-in-chronological-order.eps

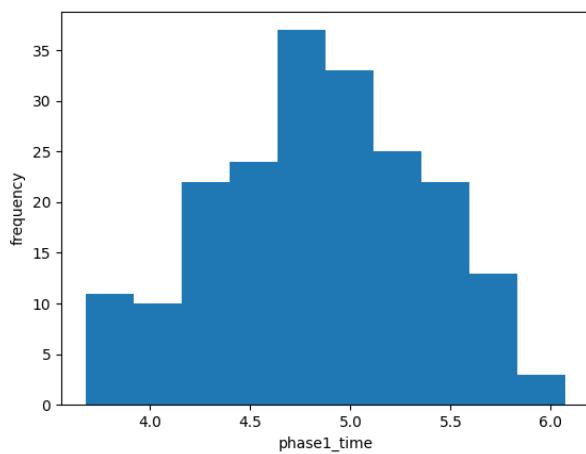


図 188 pht/experiment-win/1203-2/phase1/1203-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50053.eps

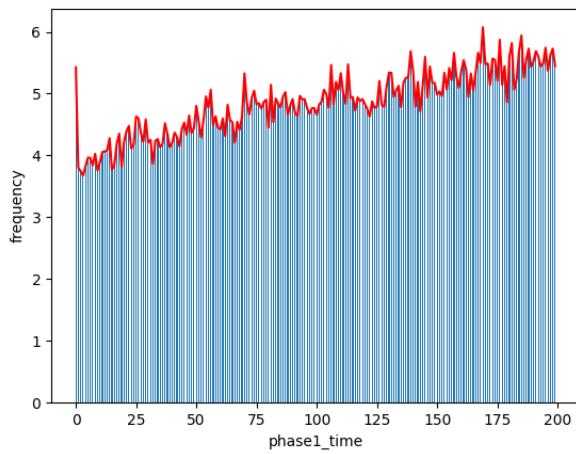


図 189 pht/experiment-win/1203-2/phase1/1203-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50053-in-chronological-order.eps

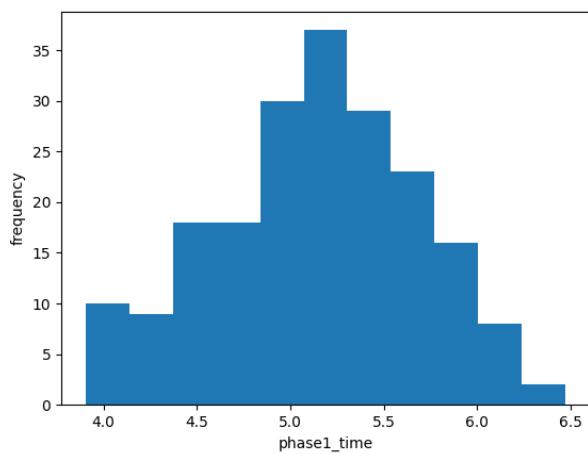


図 190 pht/experiment-win/1203-2/phase1/1203-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50055.eps

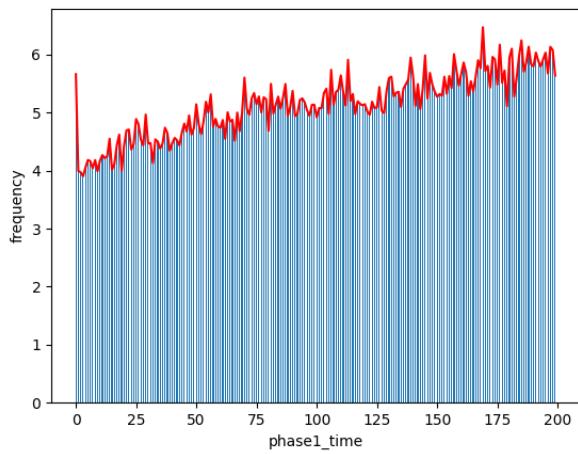


図 191 pht/experiment-win/1203-2/phase1/1203-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50055-in-chronological-order.eps

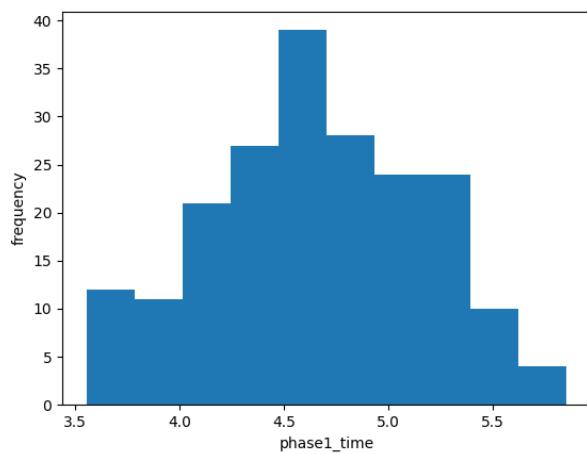


図 192 pht/experiment-win/1203-2/phase1/1203-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50057.eps

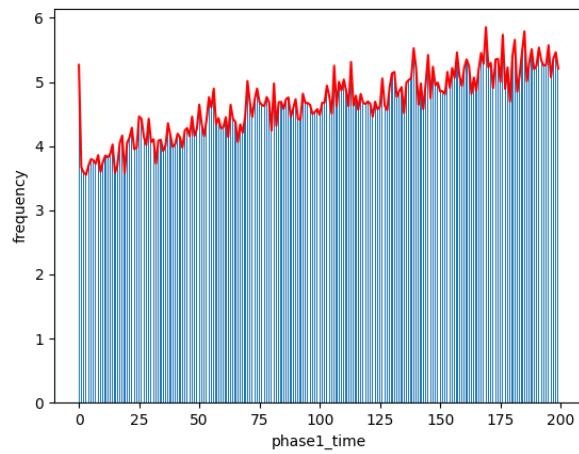


図 193 pht/experiment-win/1203-2/phase1/1203-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50057-in-chronological-order.eps

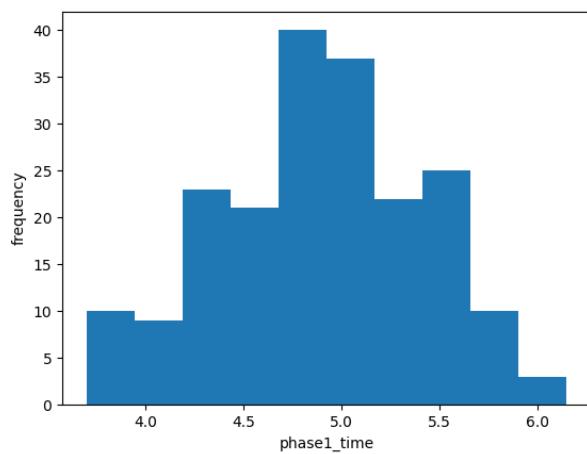


図 194 pht/experiment-win/1203-2/phase1/1203-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50059.eps

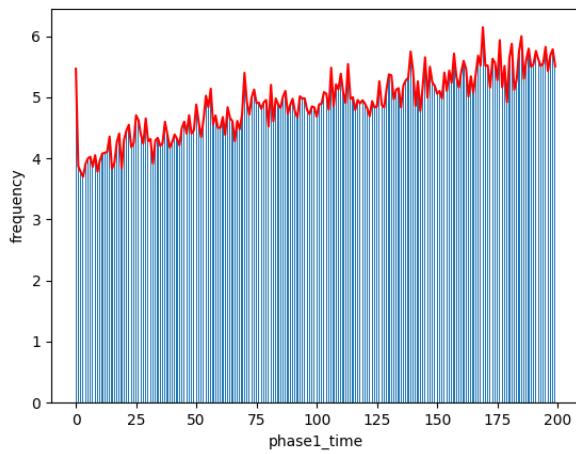


図 195 pht/experiment-win/1203-2/phase1/1203-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50059-in-chronological-order.eps

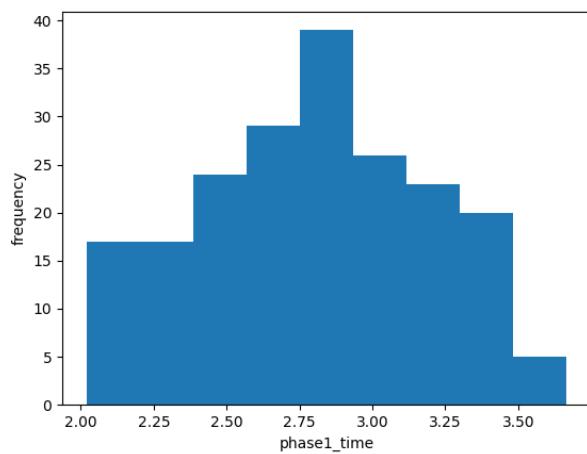


図 196 pht/experiment-win/1203-2/phase1/1203-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50181.eps

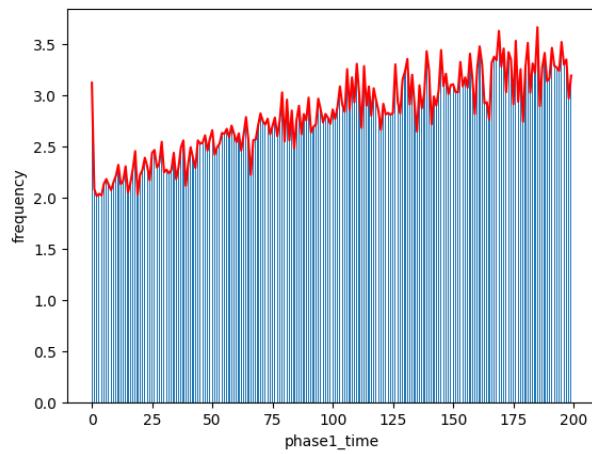


図 197 pht/experiment-win/1203-2/phase1/1203-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50181-in-chronological-order.eps

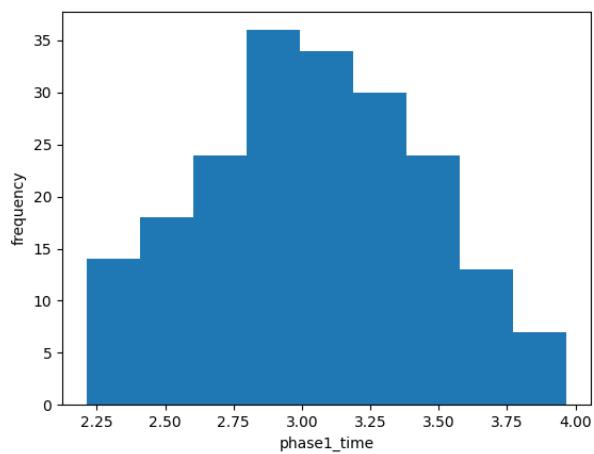


図 198 pht/experiment-win/1203-2/phase1/1203-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50183.eps

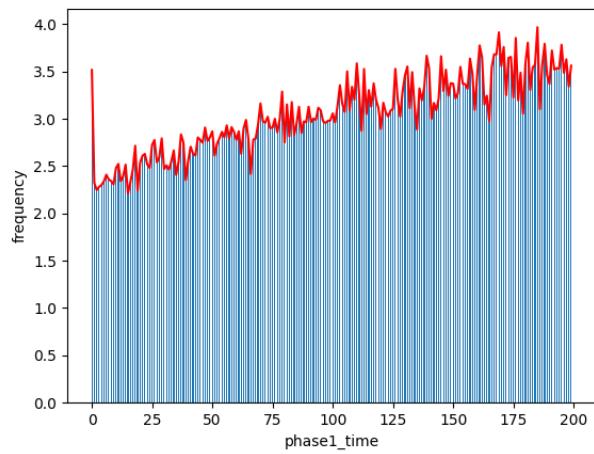


図 199 pht/experiment-win/1203-2/phase1/1203-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50183-in-chronological-order.eps

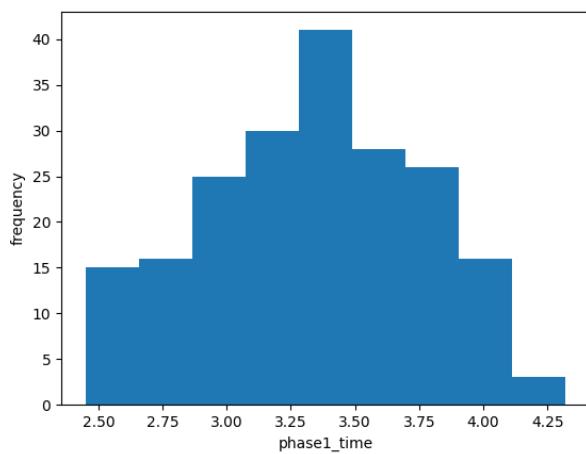


図 200 pht/experiment-win/1203-2/phase1/1203-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50185.eps

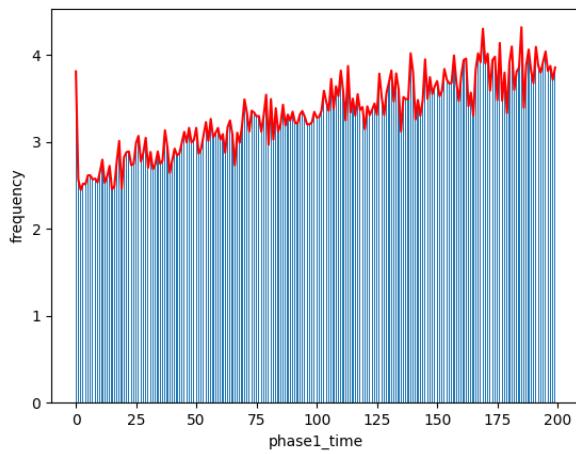


図 201 pht/experiment-win/1203-2/phase1/1203-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50185-in-chronological-order.eps

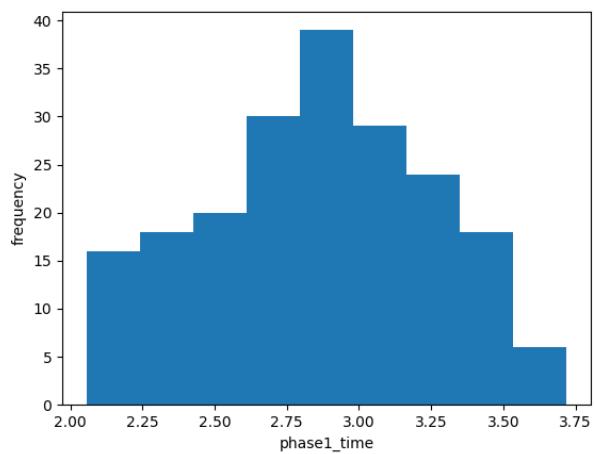


図 202 pht/experiment-win/1203-2/phase1/1203-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50187.eps

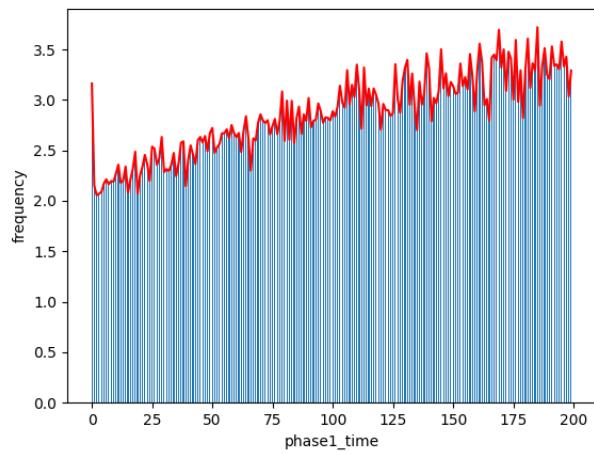


図 203 pht/experiment-win/1203-2/phase1/1203-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50187-in-chronological-order.eps

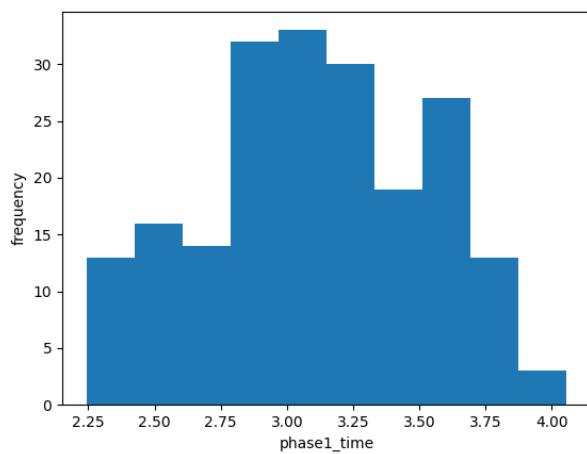


図 204 pht/experiment-win/1203-2/phase1/1203-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50189.eps

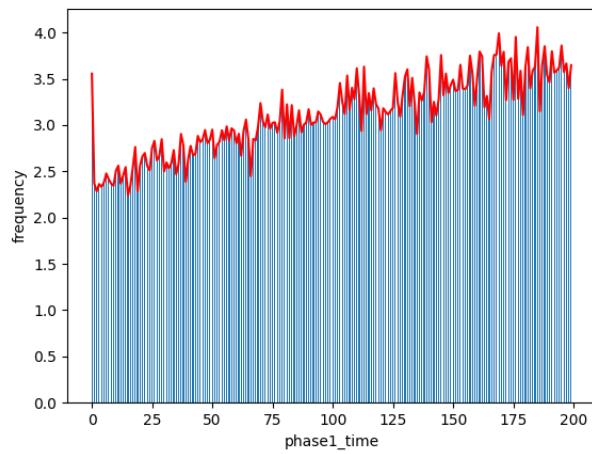


図 205 pht/experiment-win/1203-2/phase1/1203-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50189-in-chronological-order.eps

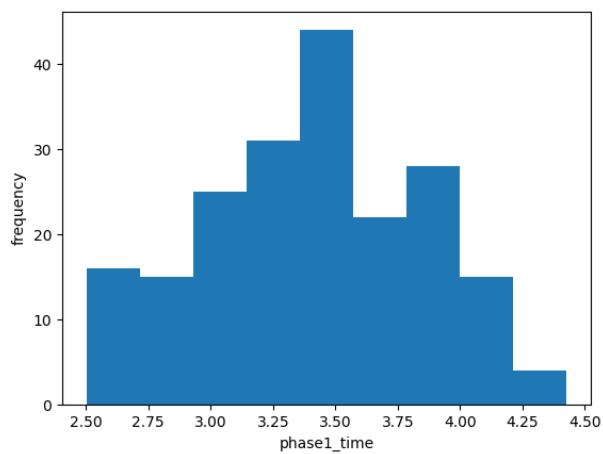


図 206 pht/experiment-win/1203-2/phase1/1203-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50191.eps

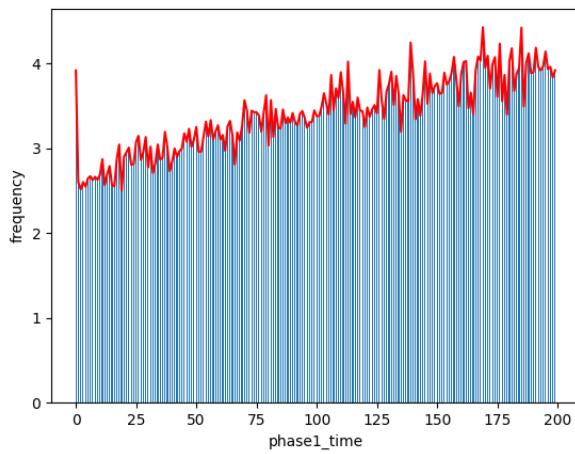


図 207 pht/experiment-win/1203-2/phase1/1203-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50191-in-chronological-order.eps

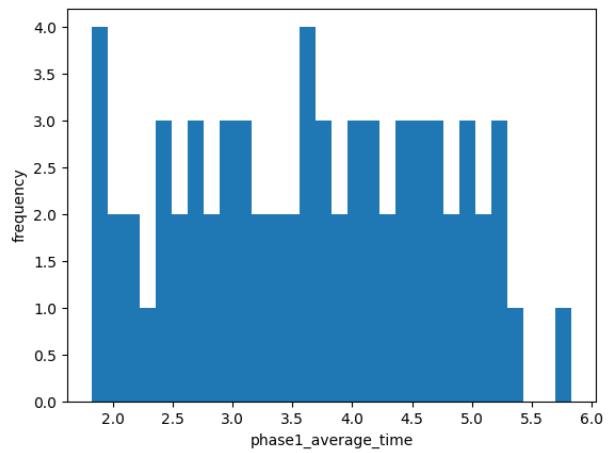


図 208 pht/experiment-win/1203-2/phase1/1203-2-70D-PC7ALL-DIF1-INT1-30analyze-phase1-average-time.eps

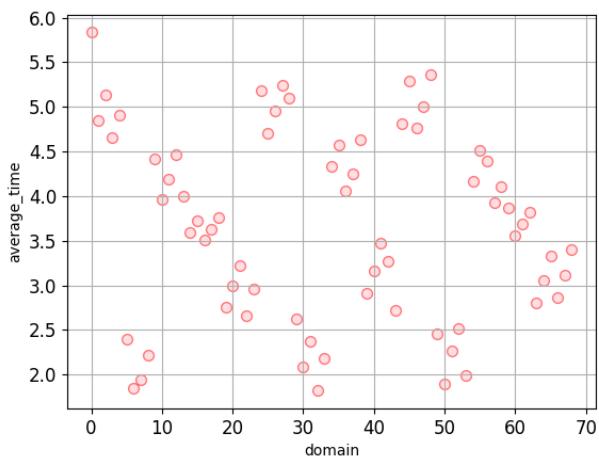


図 209 pht/experiment-win/1203-2/phase1/1203-2-70D-PC7ALL-DIF1-INT1-30analyze-phase1-average-time-scatter.eps

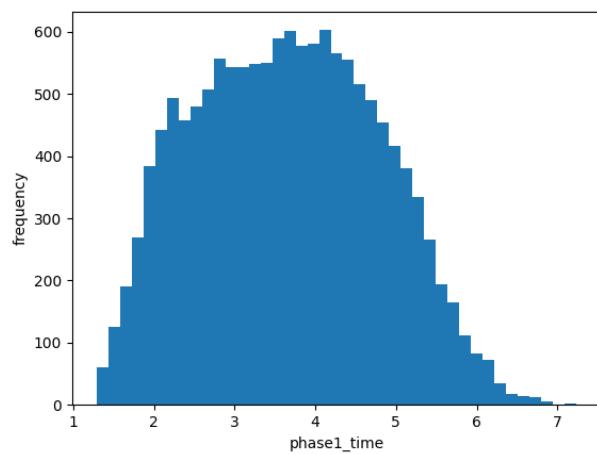


図 210 pht/experiment-win/1203-2/phase1/1203-2-70D-PC7ALL-DIF1-INT1-30analyze-phase1-time-auto.eps

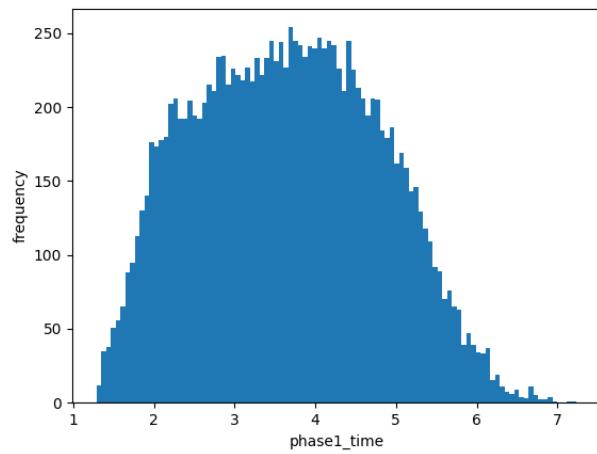


図 211 pht/experiment-win/1203-2/phase1/1203-2-70D-PC7ALL-DIF1-INT1-30analyze-phase1-time-bins100.eps

8 1204-1-70D-PC7ALL-DIF1-INT1-30

8 章にて報告する条件を表 7 に示す。

表 7 設定値の一覧

設定項目	設定値
起動ノード数 (ドメイン数 D)	70 [ノード]
履歴交差間隔	90 [秒]
履歴交差回数	200 [回]
ブロック不生成間隔	0.5[秒]
ブロック生成難易度	1 [0x]
PC 内同時起動台数	7 [台]
使用台数	10 [台]
ブロック不生成間隔 : 30 秒	70 [ノード]
ブロック不生成間隔 : 1 秒	0 [ノード]

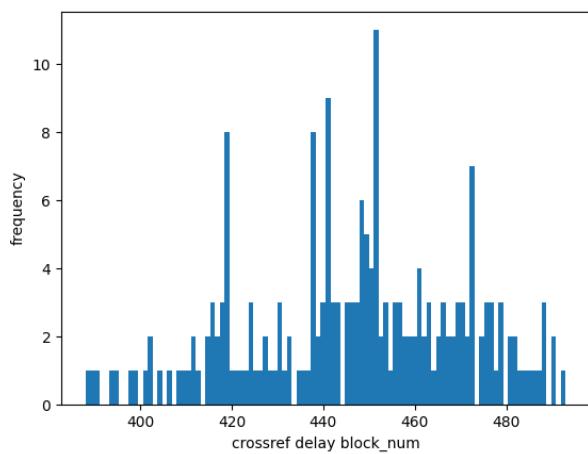


図 212 pht/experiment-win/1204-1/BC/1/1204-1-70D-PC7ALL-DIF1-INT1-30histogram-delay-blocknum.eps

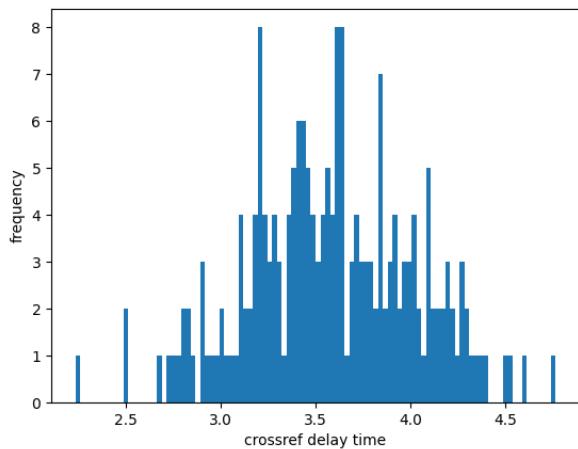


図 213 pht/experiment-win/1204-1/BC/1/1204-1-70D-PC7ALL-DIF1-INT1-30histogram-delay-time.eps

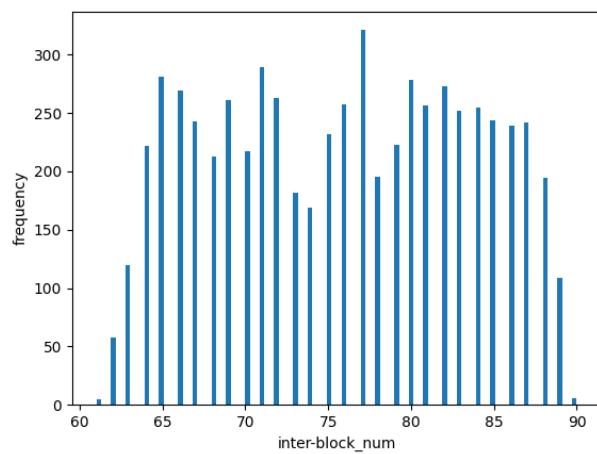


図 214 pht/experiment-win/1204-1/BC/1/1204-1-70D-PC7ALL-DIF1-INT1-30histogram-inter-blocknum.eps

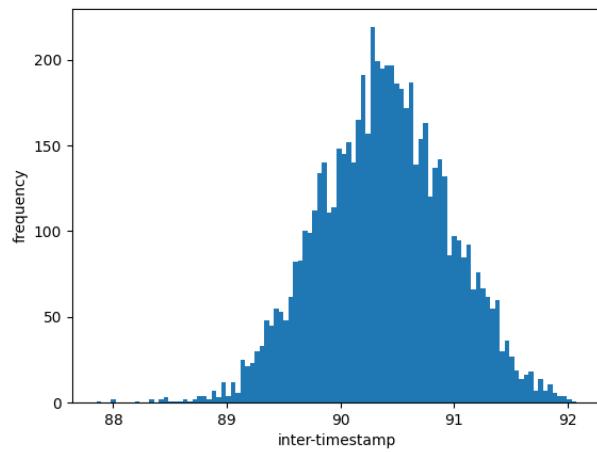


図 215 pht/experiment-win/1204-1/BC/1/1204-1-70D-PC7ALL-DIF1-INT1-30histogram-inter-timestamp.eps

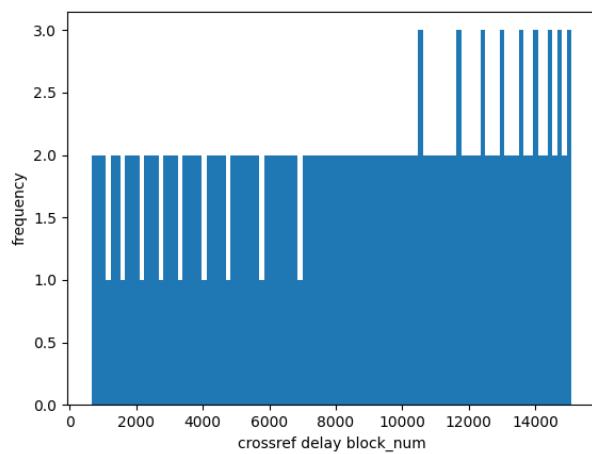


図 216 pht/experiment-win/1204-1/BC/1204-1-70D-PC7ALL-DIF1-INT1-30histogram-delay-blocknum.eps

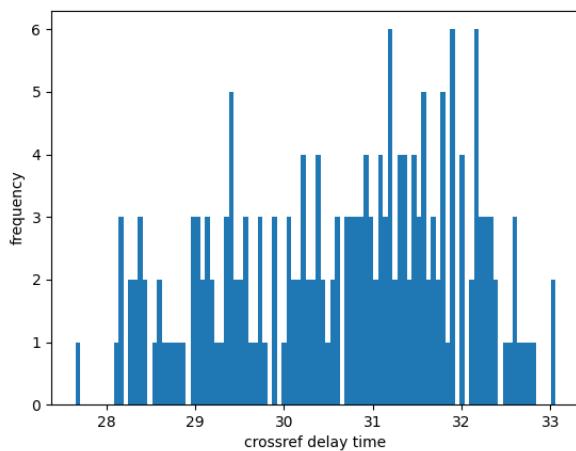


図 217 pht/experiment-win/1204-1/BC/1204-1-70D-PC7ALL-DIF1-INT1-30histogram-delay-time.eps

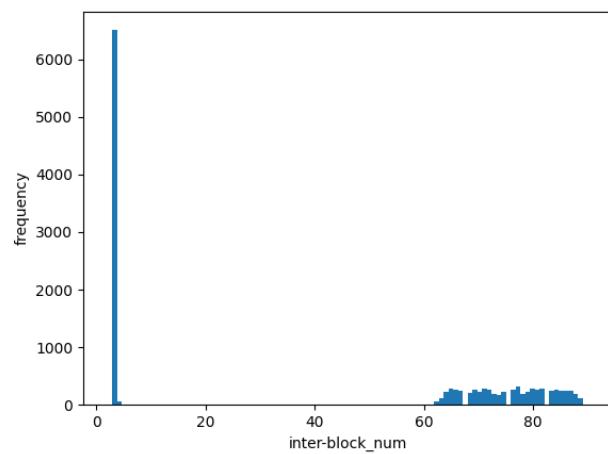


図 218 pht/experiment-win/1204-1/BC/1204-1-70D-PC7ALL-DIF1-INT1-30histogram-inter-blocknum.eps

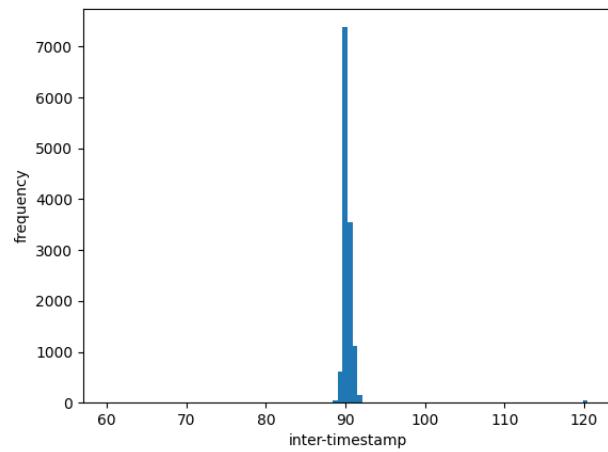


図 219 pht/experiment-win/1204-1/BC/1204-1-70D-PC7ALL-DIF1-INT1-30histogram-inter-timestamp.eps

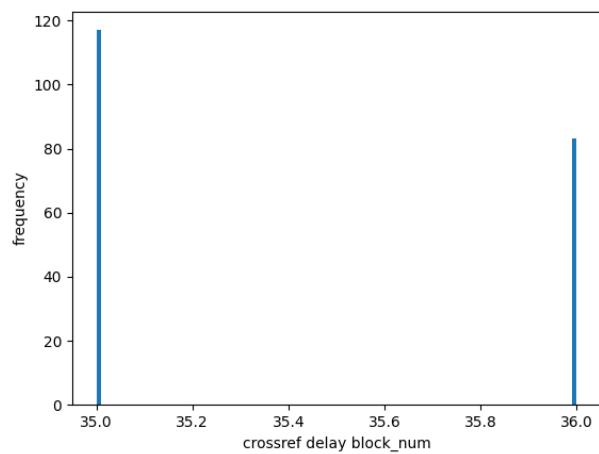


図 220 pht/experiment-win/1204-1/BC/30/1204-1-70D-PC7ALL-DIF1-INT1-30histogram-delay-blocknum.eps

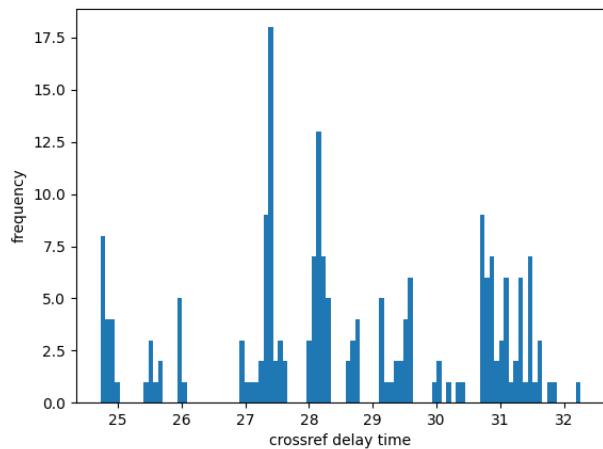


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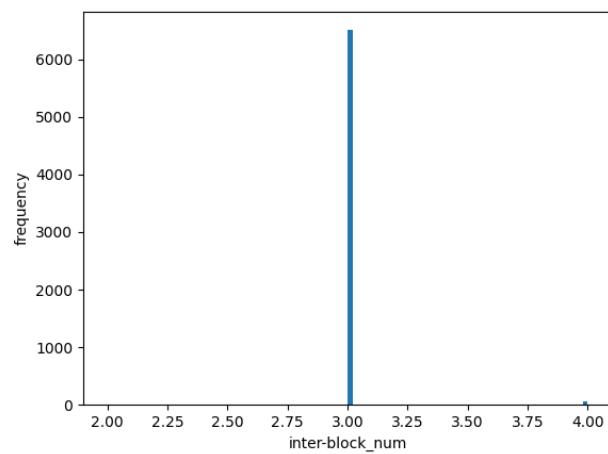


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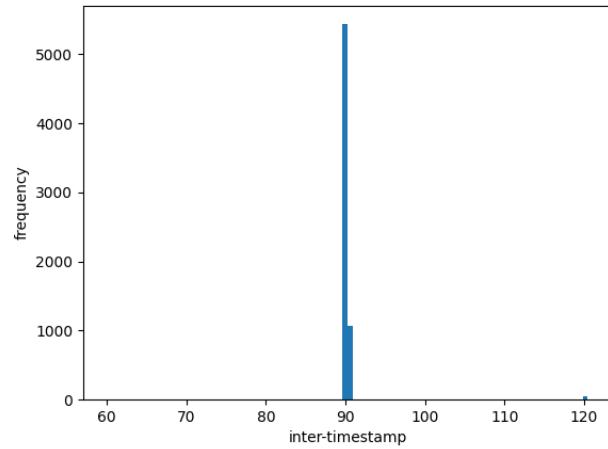


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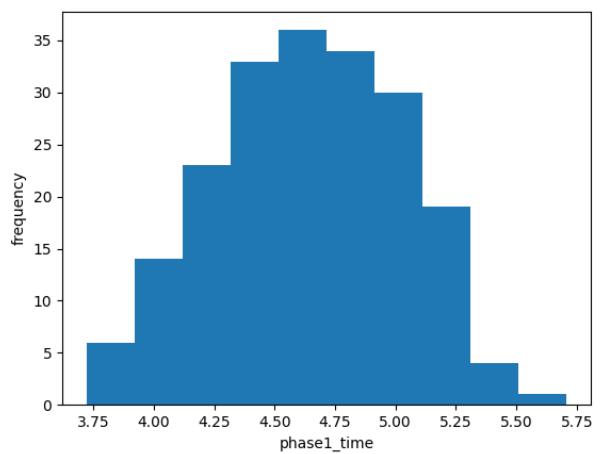


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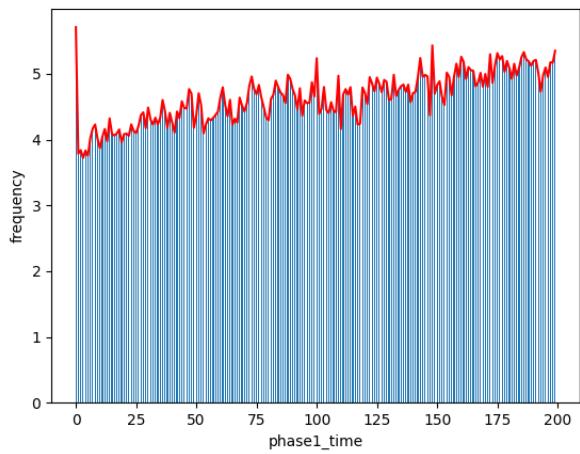


図 225 pht/experiment-win/1204-1/phase1/1204-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50051-in-chronological-order.eps

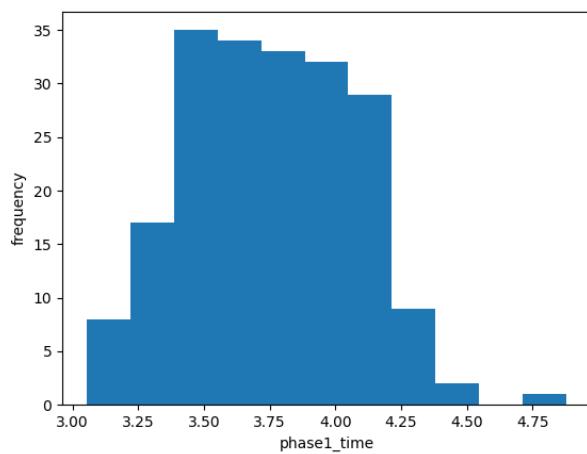


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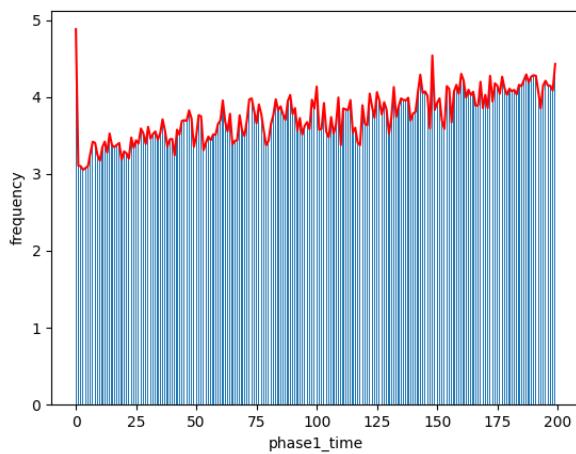


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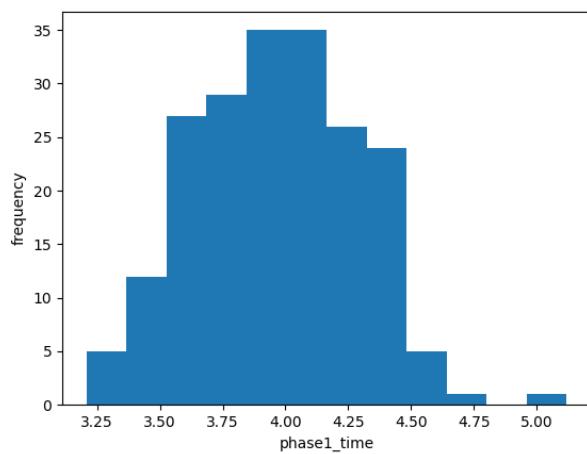


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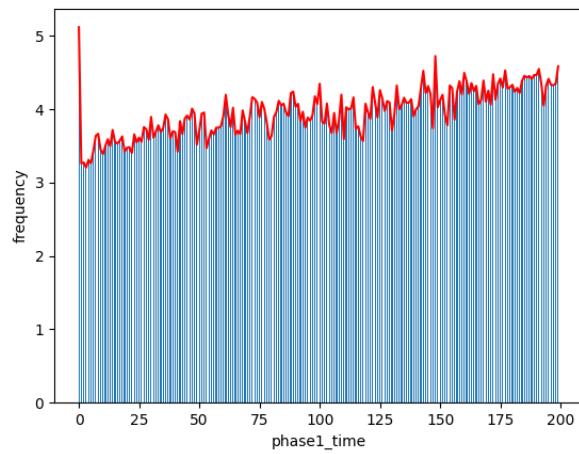


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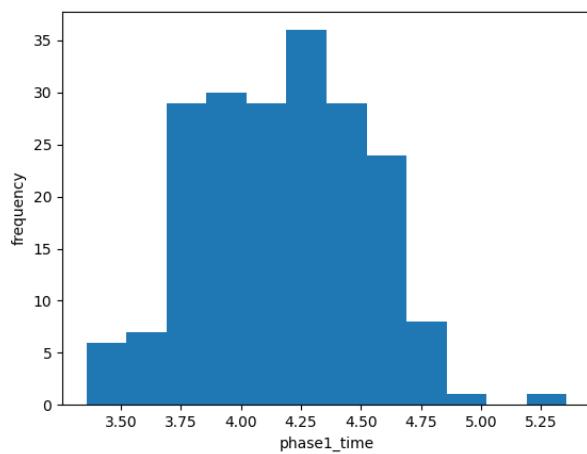


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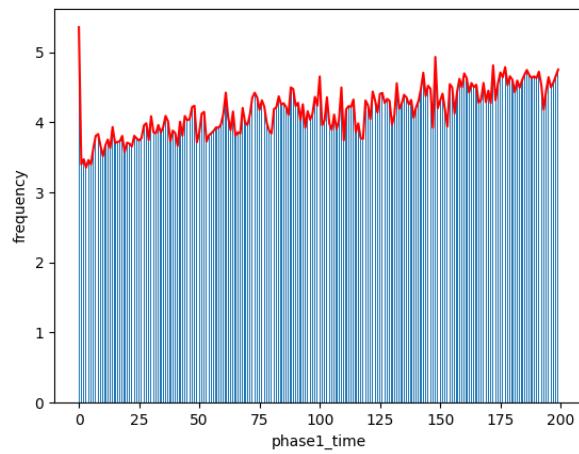


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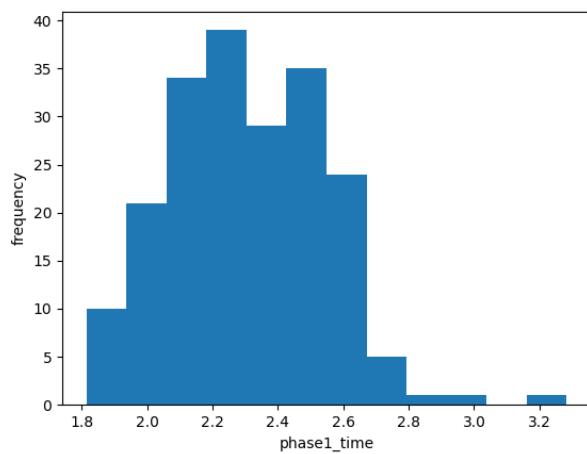


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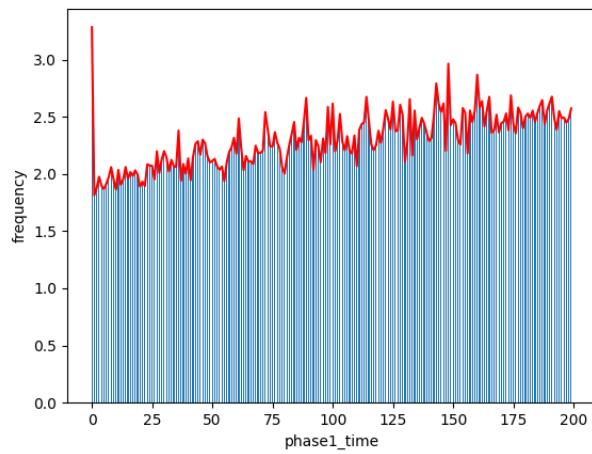


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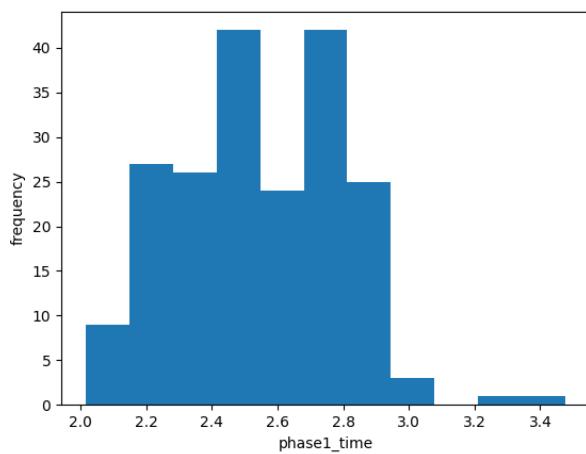


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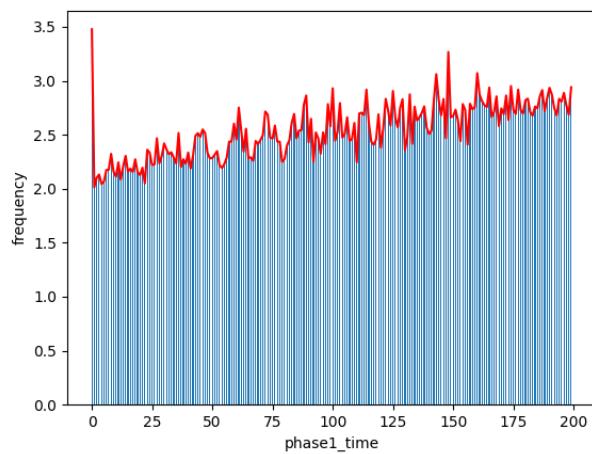


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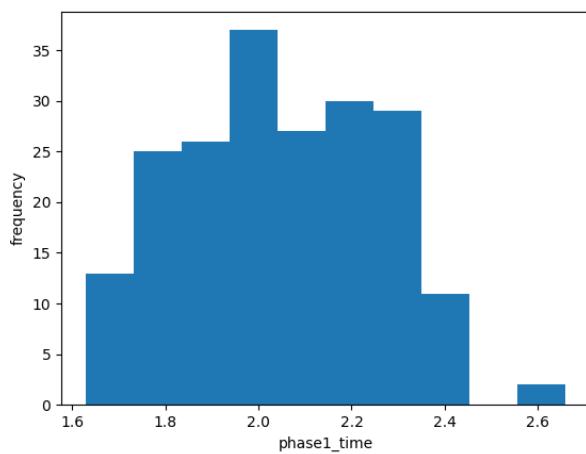


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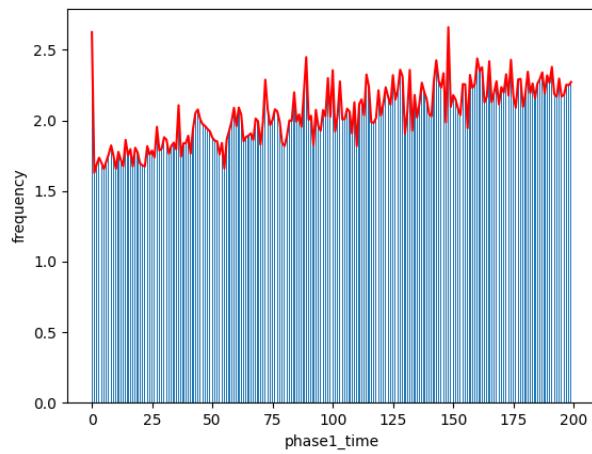


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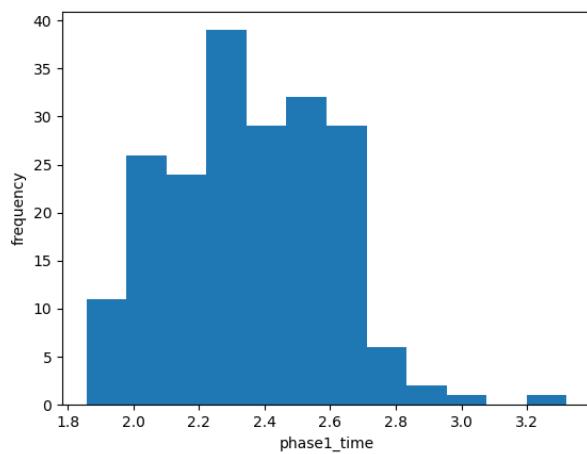


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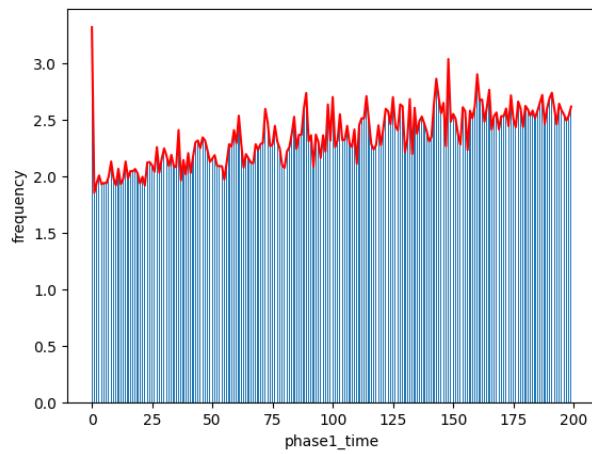


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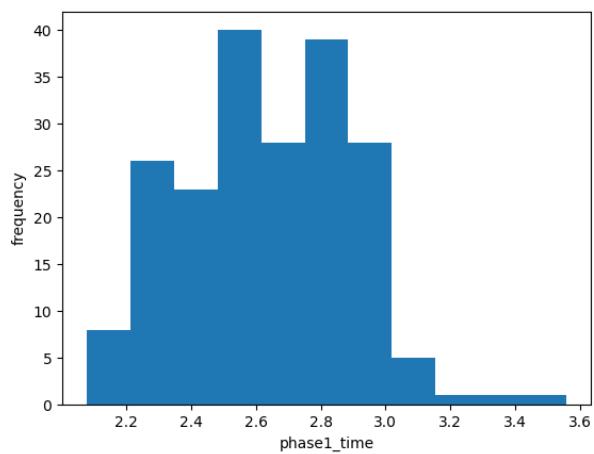


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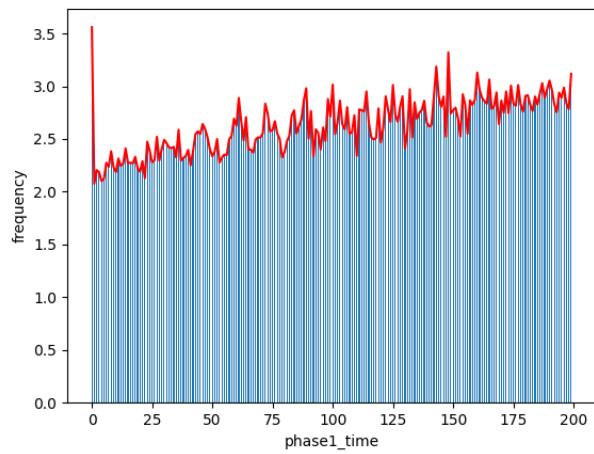


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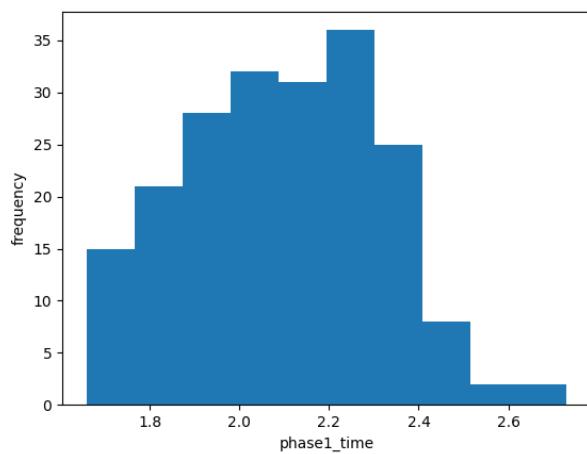


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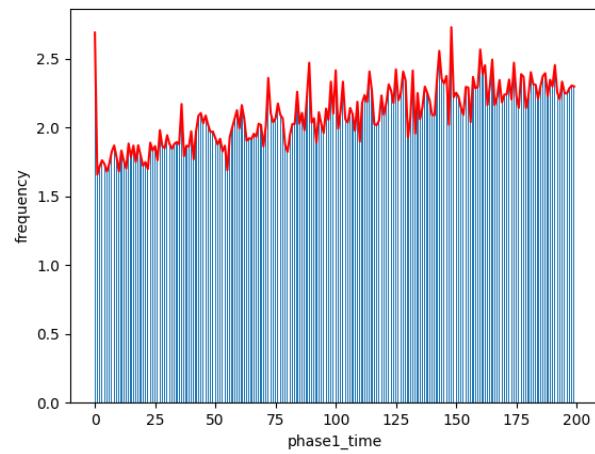


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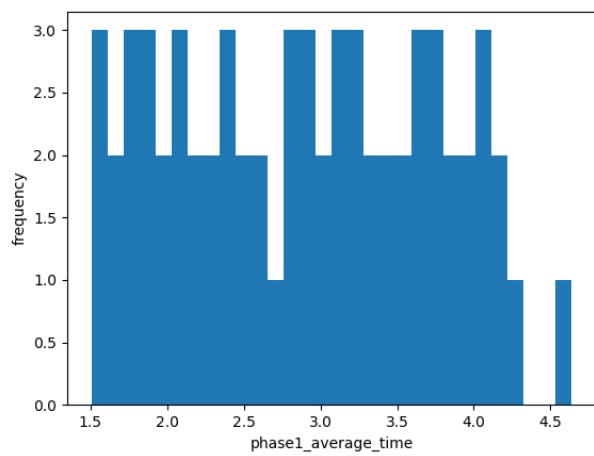


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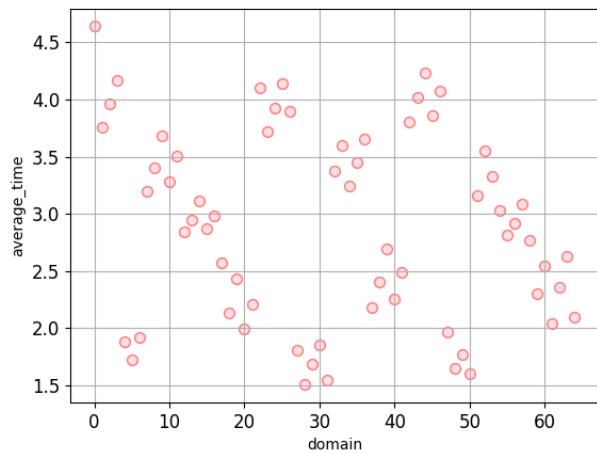


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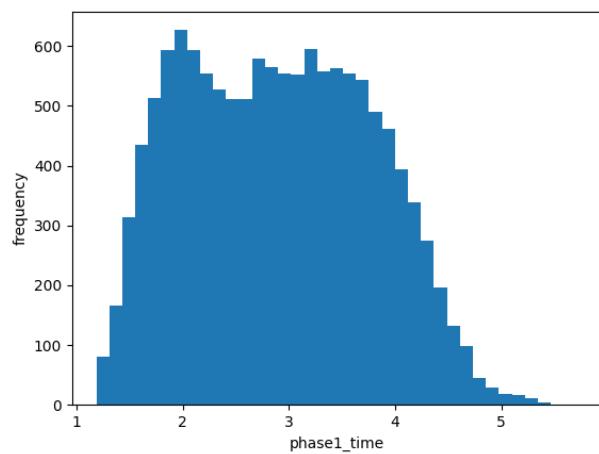


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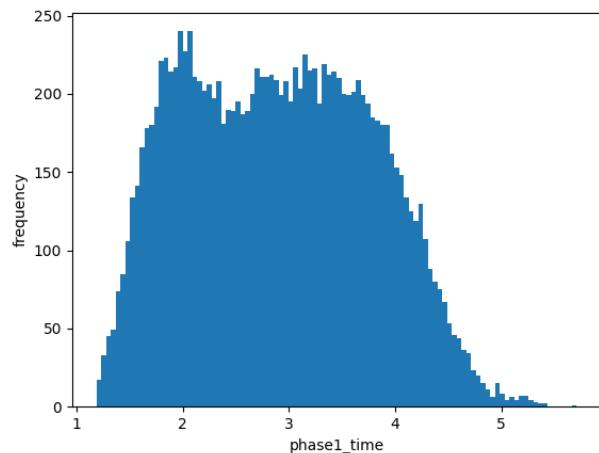


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9 1205-1-70D-PC7ALL-DIF1-INT1-30

9 章にて報告する条件を表 8 に示す。

表 8 設定値の一覧

設定項目	設定値
起動ノード数 (ドメイン数 D)	70 [ノード]
履歴交差間隔	90 [秒]
履歴交差回数	200 [回]
ブロック不生成間隔	0.5[秒]
ブロック生成難易度	1 [0x]
PC 内同時起動台数	7 [台]
使用台数	10 [台]
ブロック不生成間隔 : 30 秒	70 [ノード]
ブロック不生成間隔 : 1 秒	0 [ノード]

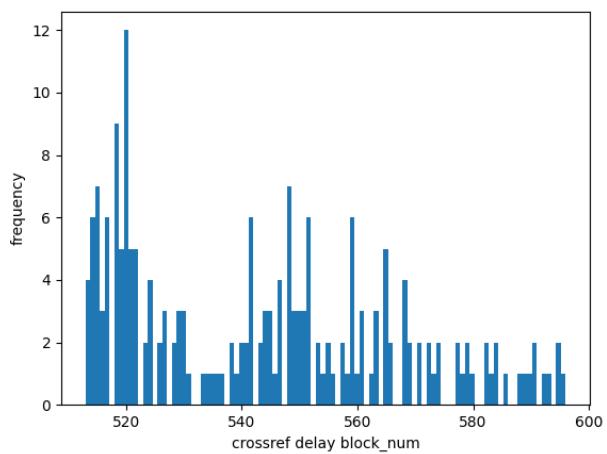


図 248 pht/experiment-win/1205-1/BC/1/1205-1-70D-PC7ALL-DIF1-INT1-30histogram-delay-blocknum.eps

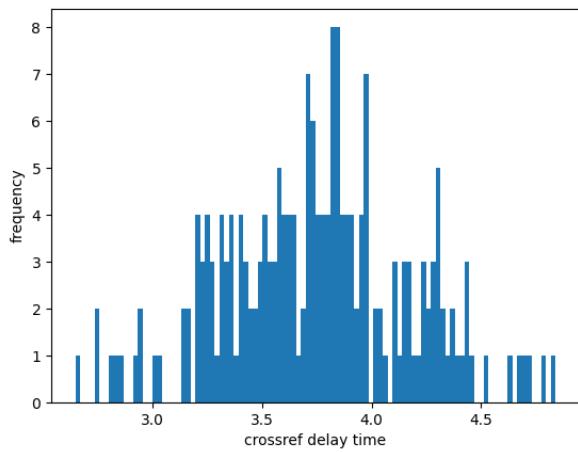


図 249 pht/experiment-win/1205-1/BC/1/1205-1-70D-PC7ALL-DIF1-INT1-30histogram-delay-time.eps

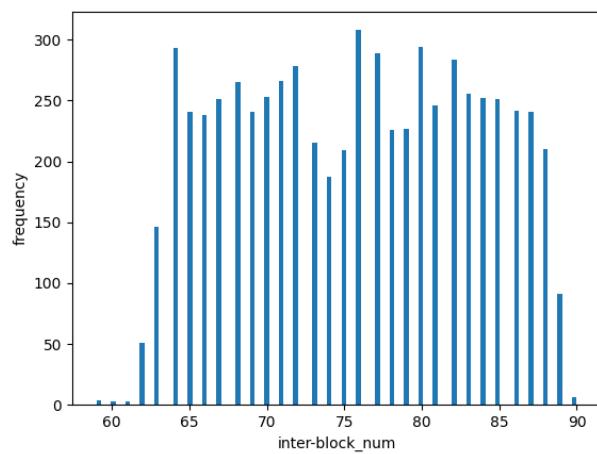


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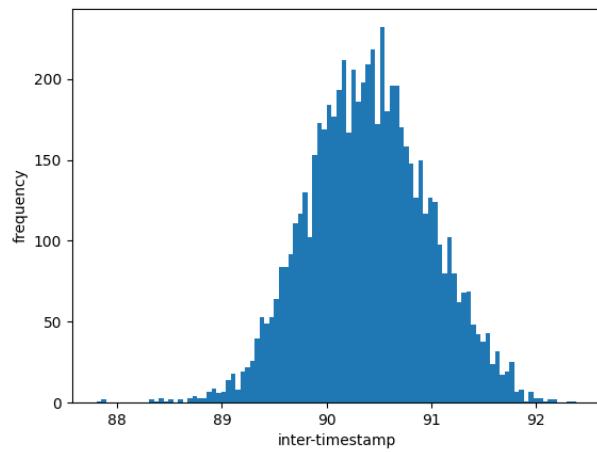


図 251 pht/experiment-win/1205-1/BC/1/1205-1-70D-PC7ALL-DIF1-INT1-30histogram-inter-timestamp.eps

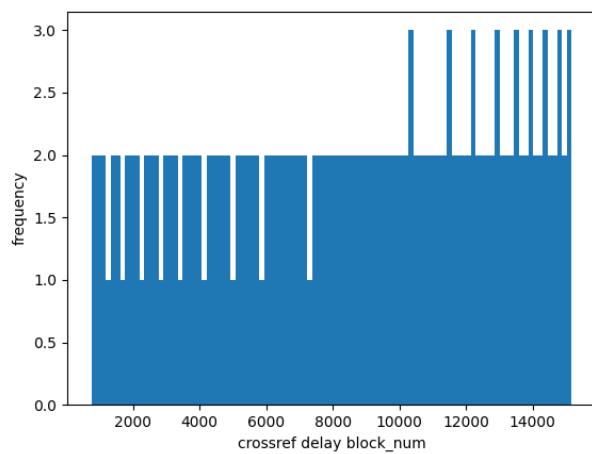


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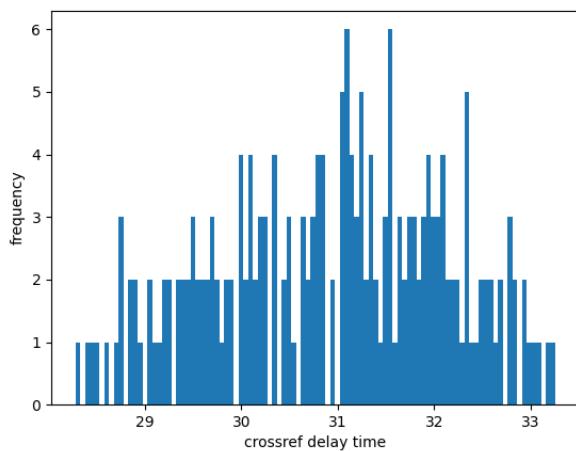


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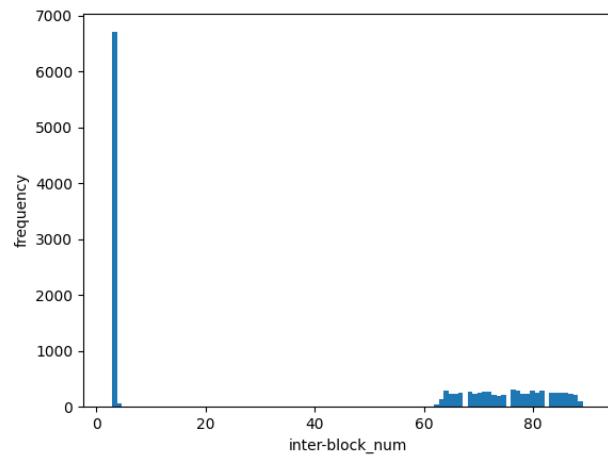


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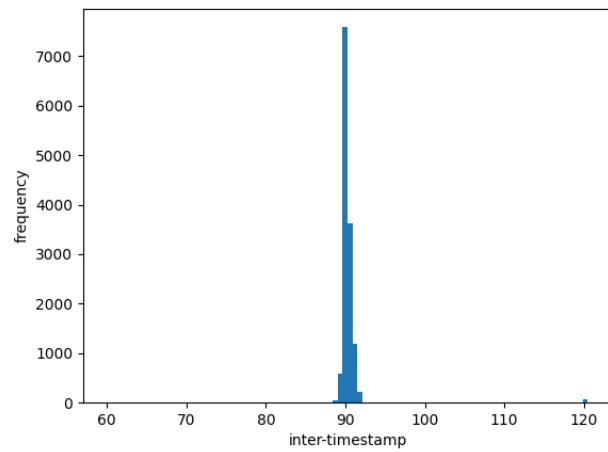


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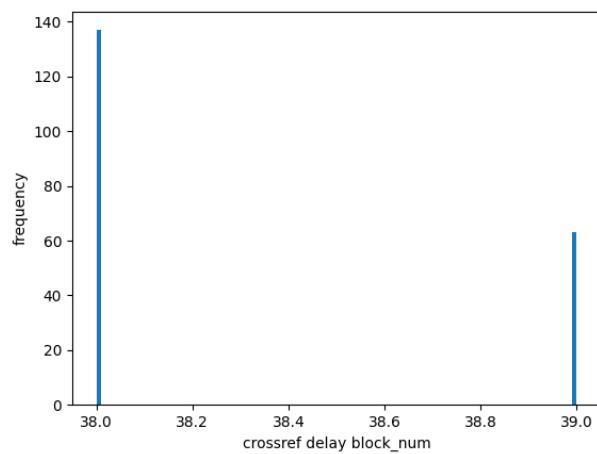


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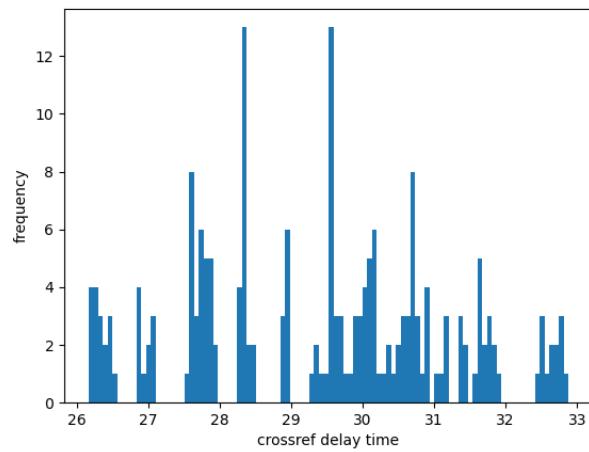


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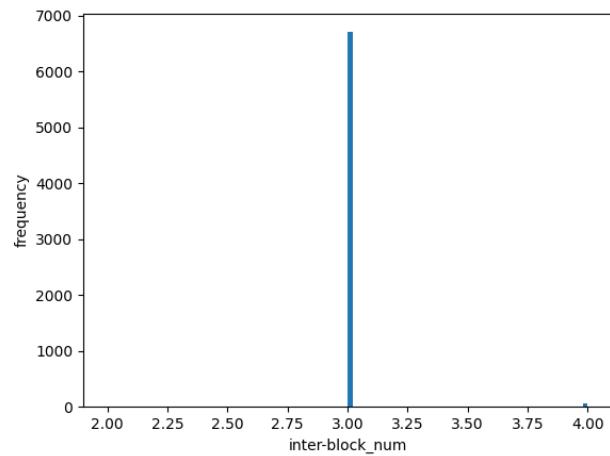


図 258 pht/experiment-win/1205-1/BC/30/1205-1-70D-PC7ALL-DIF1-INT1-30histogram-inter-blocknum.eps

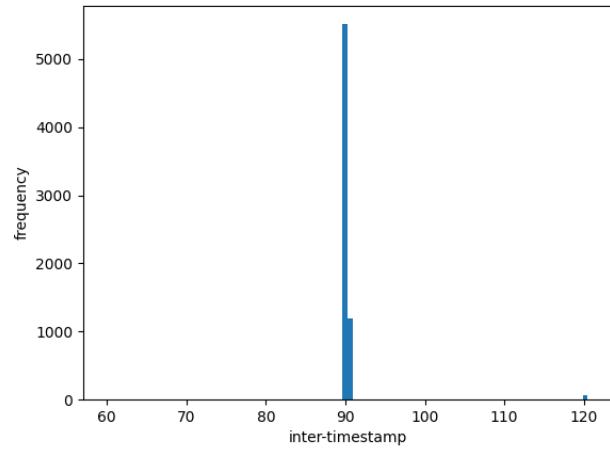


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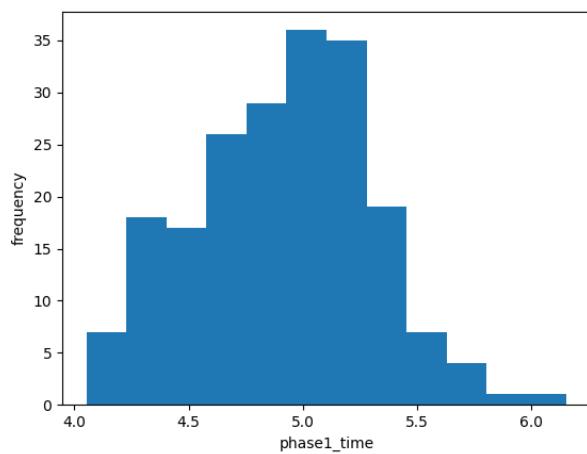


図 260 pht/experiment-win/1205-1/phase1/1205-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50051.eps

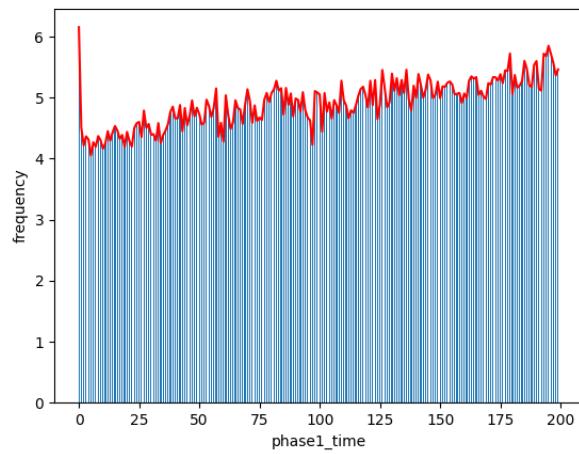


図 261 pht/experiment-win/1205-1/phase1/1205-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50051-in-chronological-order.eps

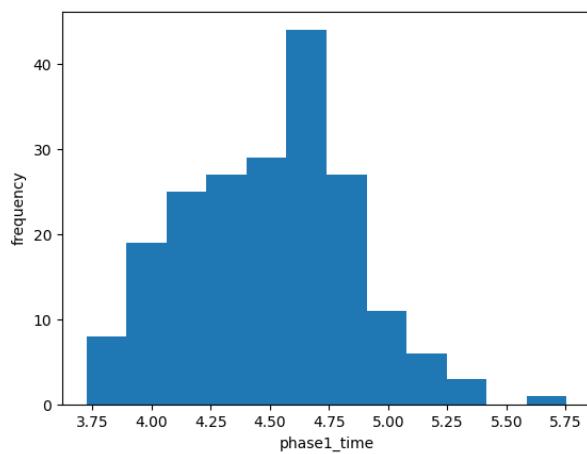


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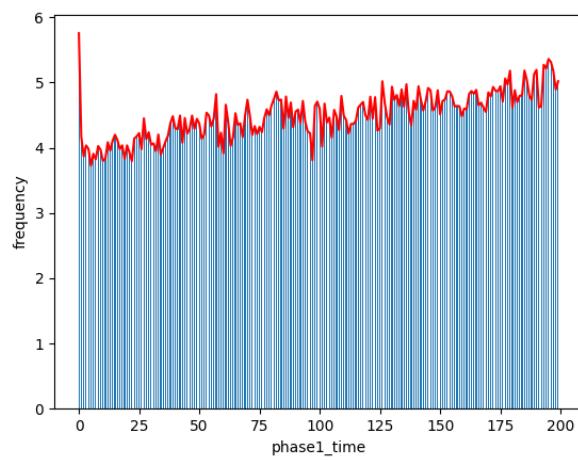


図 263 pht/experiment-win/1205-1/phase1/1205-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50053-in-chronological-order.eps

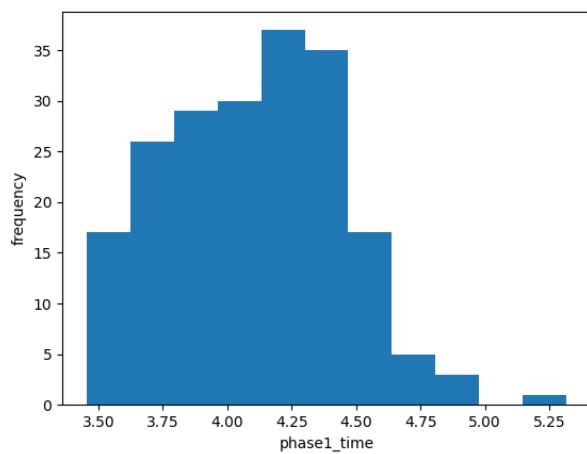


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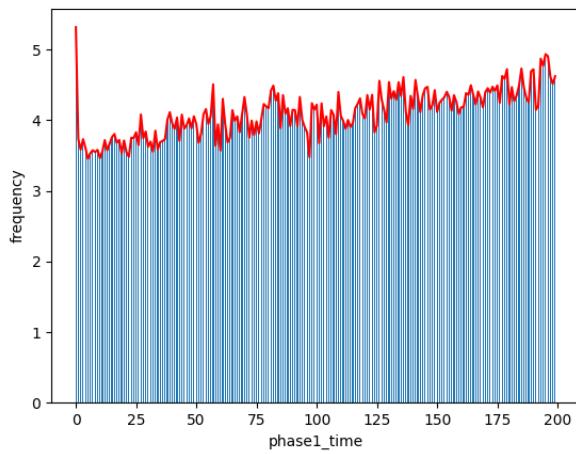


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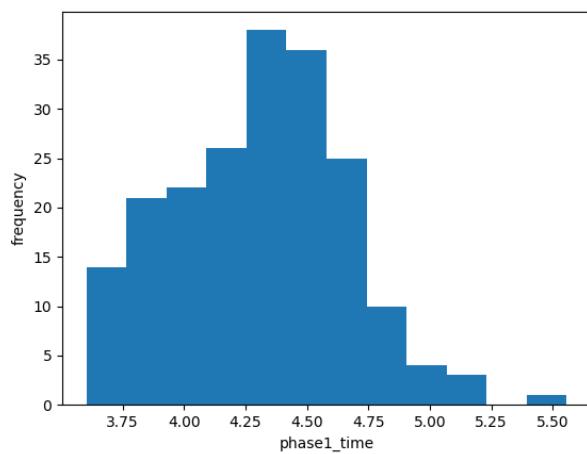


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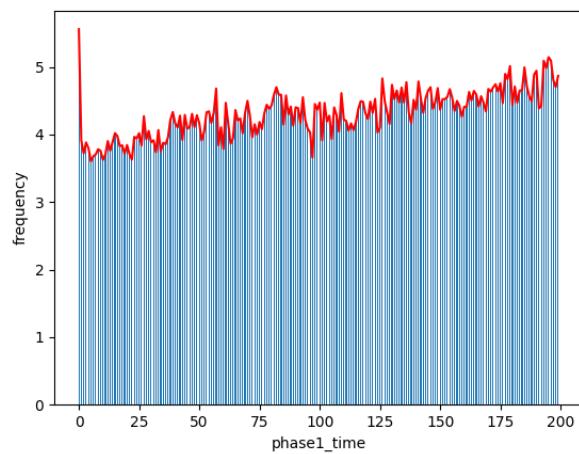


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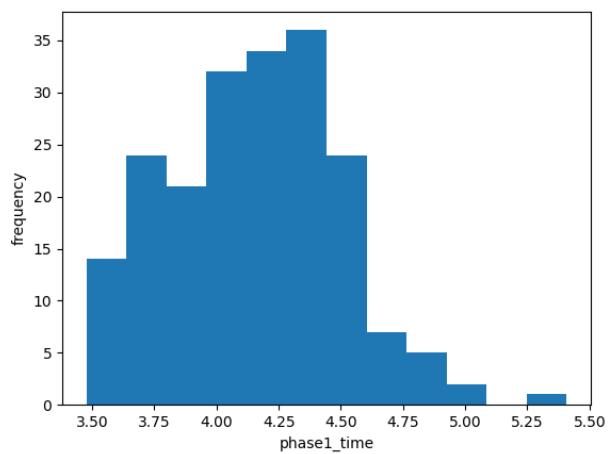


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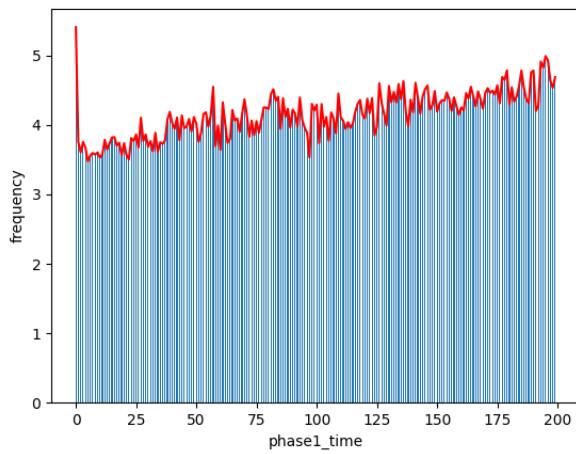


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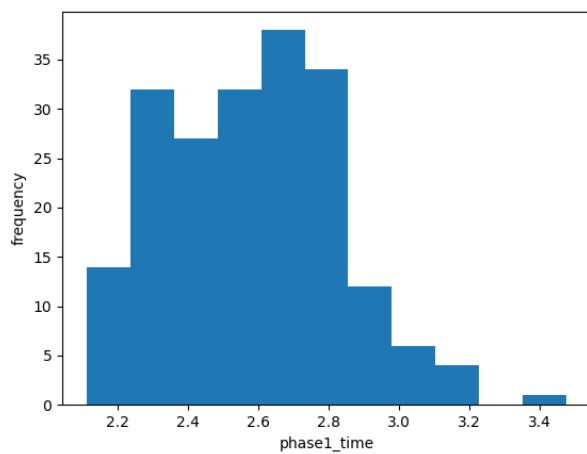


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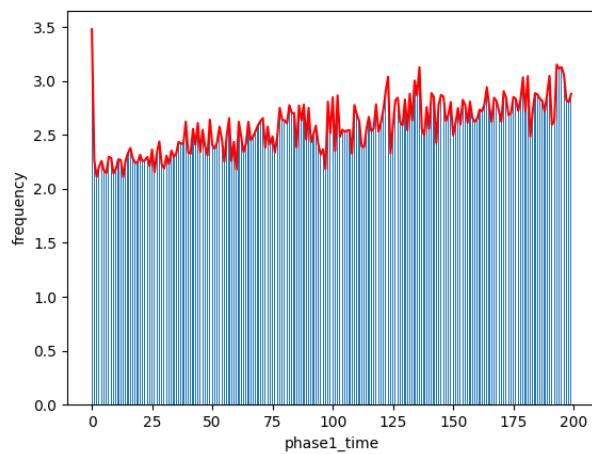


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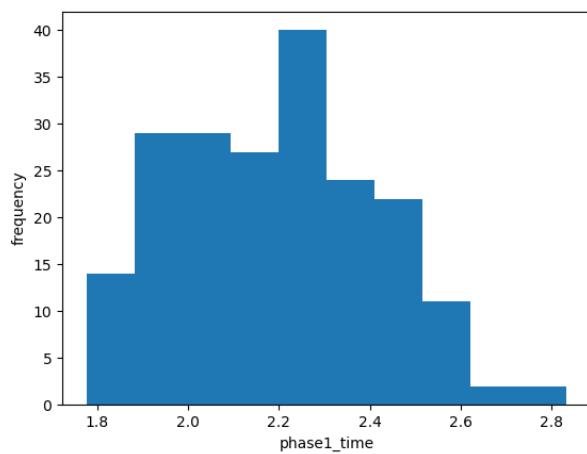


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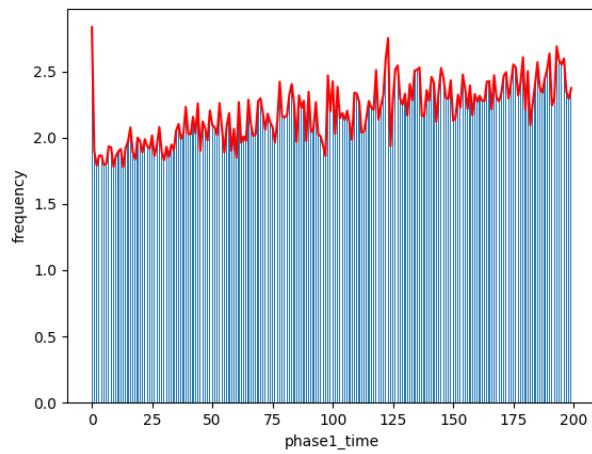


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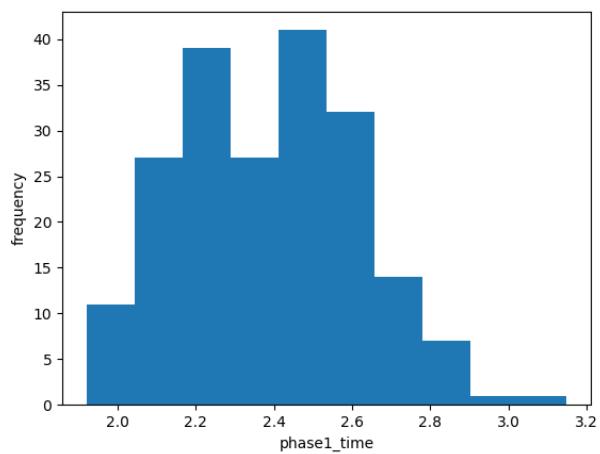


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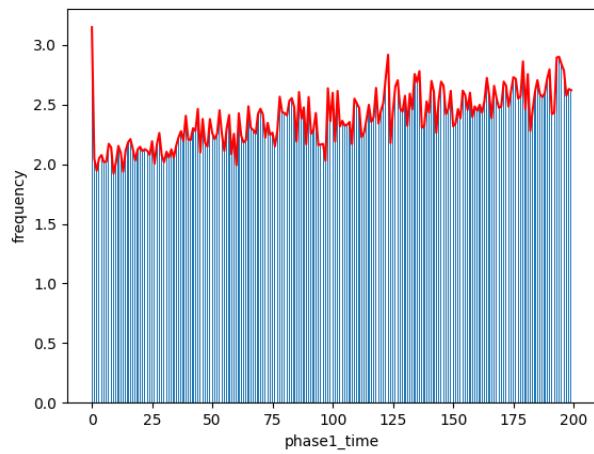


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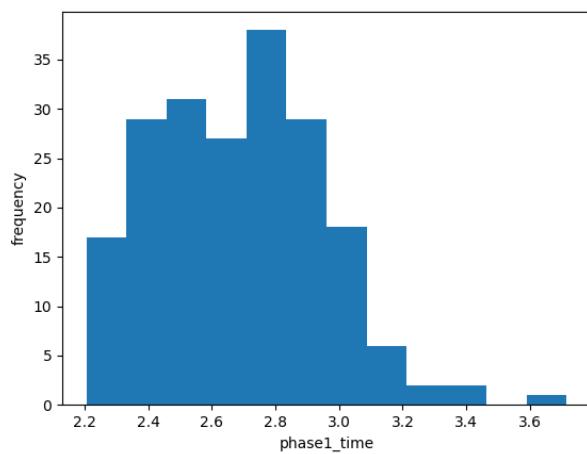


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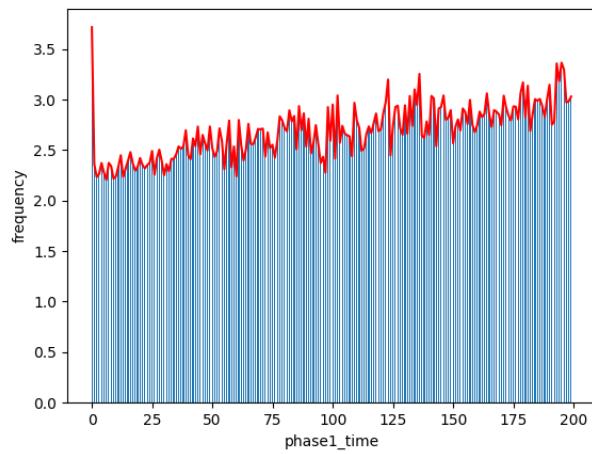


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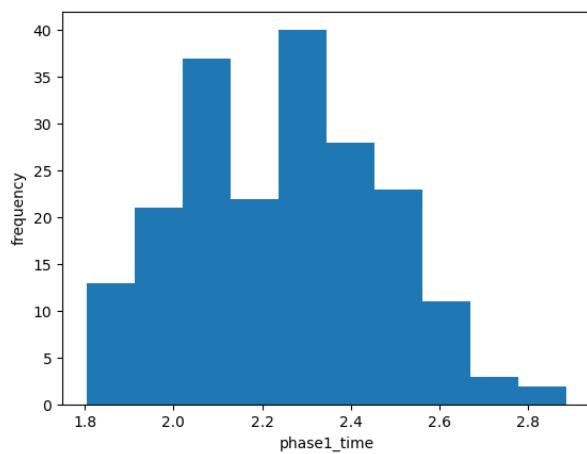


図 278 pht/experiment-win/1205-1/phase1/1205-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50189.eps

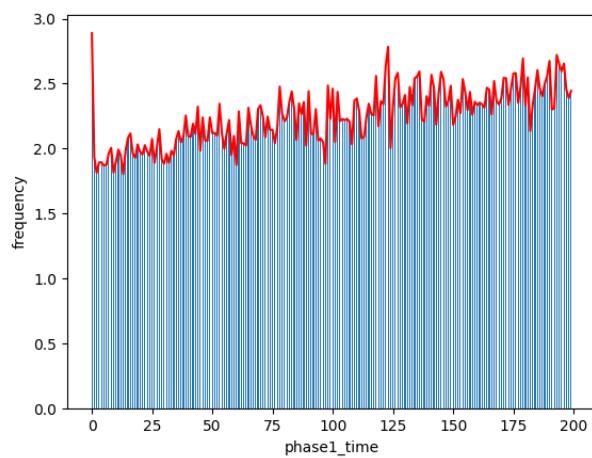


図 279 pht/experiment-win/1205-1/phase1/1205-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50189-in-chronological-order.eps

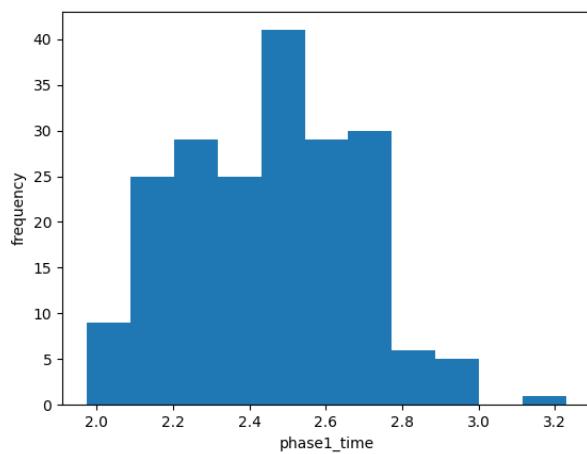


図 280 pht/experiment-win/1205-1/phase1/1205-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50191.eps

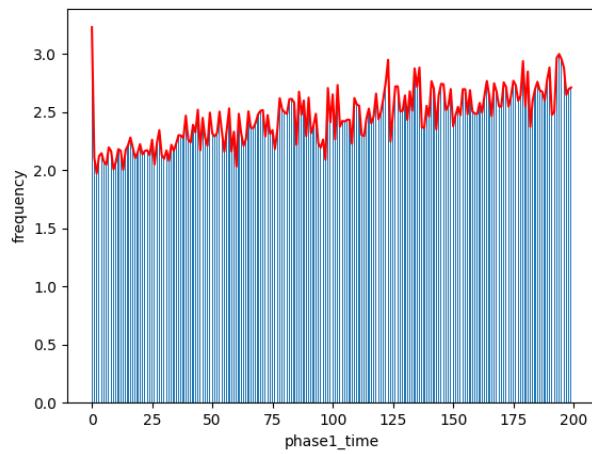


図 281 pht/experiment-win/1205-1/phase1/1205-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50191-in-chronological-order.eps

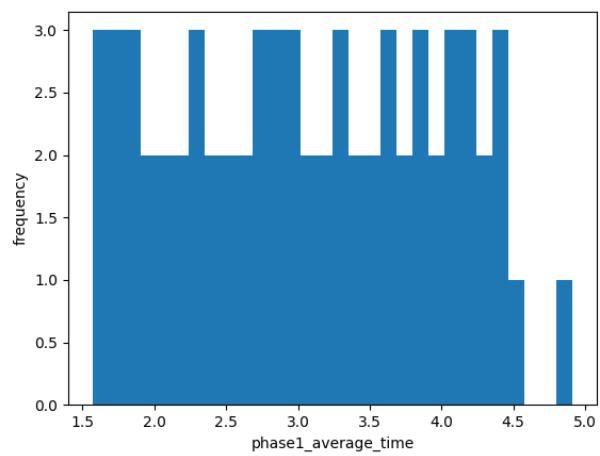


図 282 pht/experiment-win/1205-1/phase1/1205-1-70D-PC7ALL-DIF1-INT1-30analyze-phase1-average-time.eps

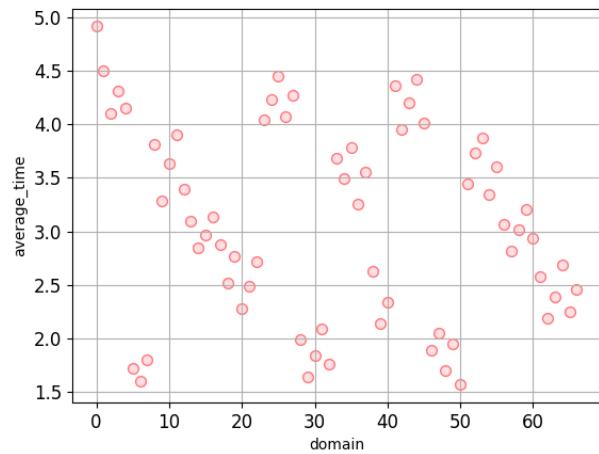


図 283 pht/experiment-win/1205-1/phase1/1205-1-70D-PC7ALL-DIF1-INT1-30analyze-phase1-average-time-scatter.eps

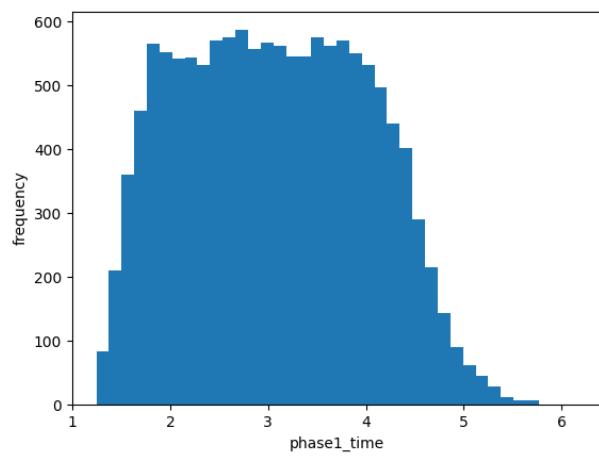


図 284 pht/experiment-win/1205-1/phase1/1205-1-70D-PC7ALL-DIF1-INT1-30analyze-phase1-time-auto.eps

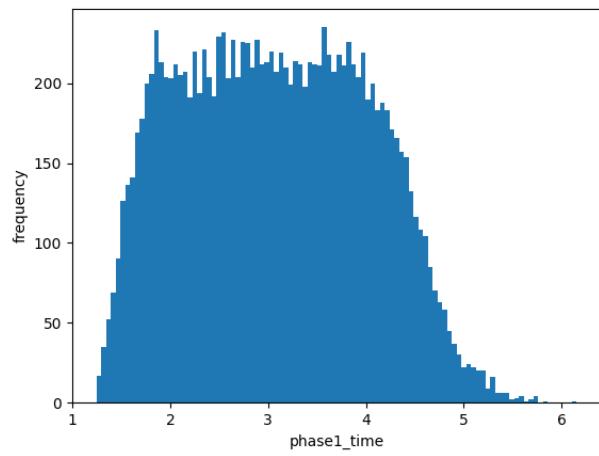


図 285 pht/experiment-win/1205-1/phase1/1205-1-70D-PC7ALL-DIF1-INT1-30analyze-phase1-time-bins100.eps

10 1205-2-70D-PC7ALL-DIF1-INT1-30

10 章にて報告する条件を表 9 に示す。

表 9 設定値の一覧

設定項目	設定値
起動ノード数 (ドメイン数 D)	70 [ノード]
履歴交差間隔	90 [秒]
履歴交差回数	200 [回]
ブロック不生成間隔	0.5[秒]
ブロック生成難易度	1 [0x]
PC 内同時起動台数	7 [台]
使用台数	10 [台]
ブロック不生成間隔 : 30 秒	70 [ノード]
ブロック不生成間隔 : 1 秒	0 [ノード]

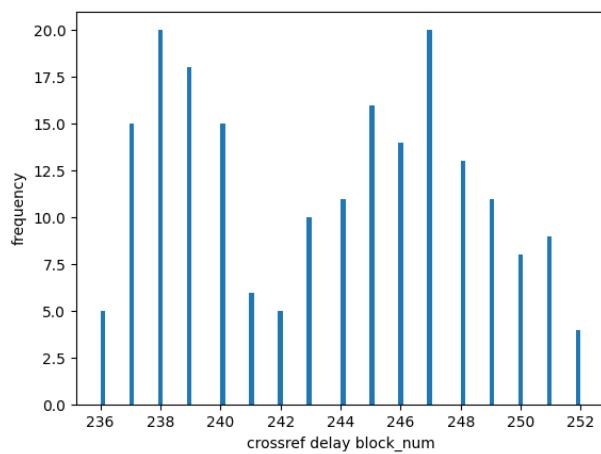


図 286 pht/experiment-win/1205-2/BC/1/1205-2-70D-PC7ALL-DIF1-INT1-30histogram-delay-blocknum.eps

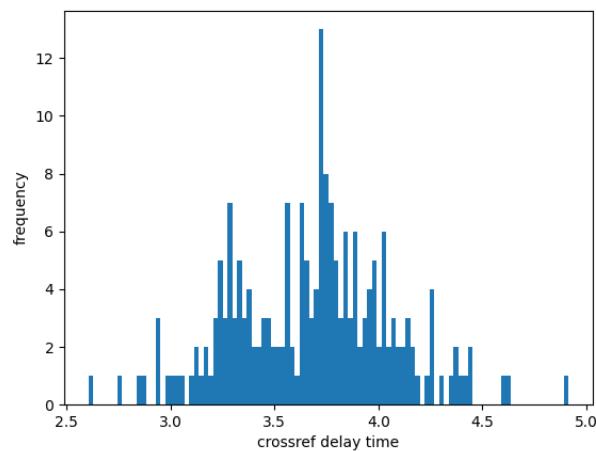


図 287 pht/experiment-win/1205-2/BC/1/1205-2-70D-PC7ALL-DIF1-INT1-30histogram-delay-time.eps

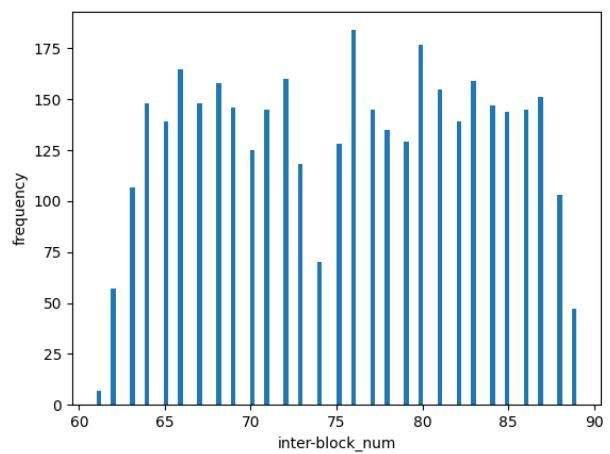


図 288 pht/experiment-win/1205-2/BC/1/1205-2-70D-PC7ALL-DIF1-INT1-30histogram-inter-blocknum.eps

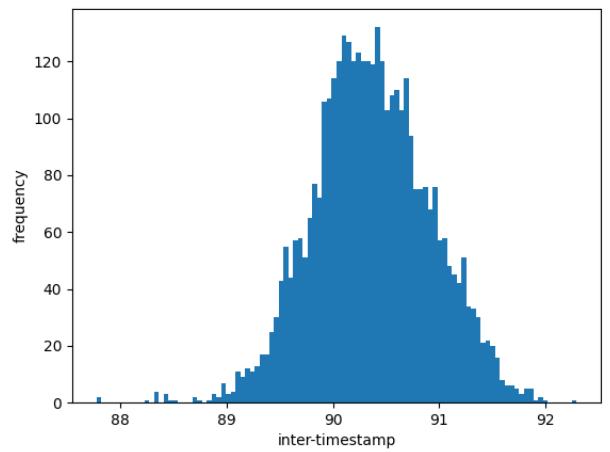


図 289 pht/experiment-win/1205-2/BC/1/1205-2-70D-PC7ALL-DIF1-INT1-30histogram-inter-timestamp.eps

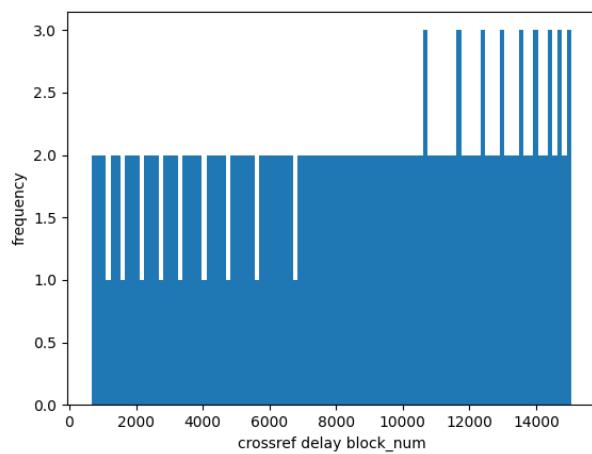


図 290 pht/experiment-win/1205-2/BC/1205-2-70D-PC7ALL-DIF1-INT1-30histogram-delay-blocknum.eps

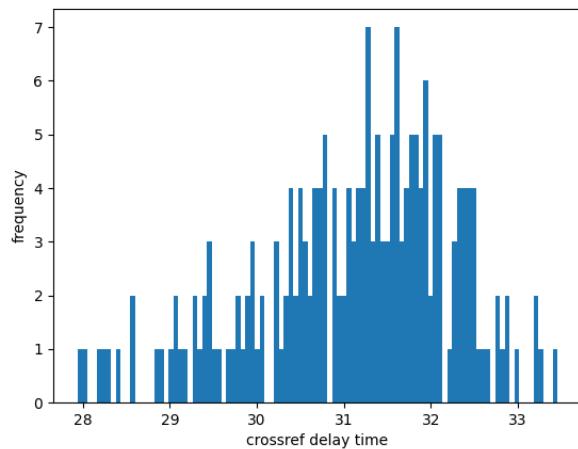


図 291 pht/experiment-win/1205-2/BC/1205-2-70D-PC7ALL-DIF1-INT1-30histogram-delay-time.eps

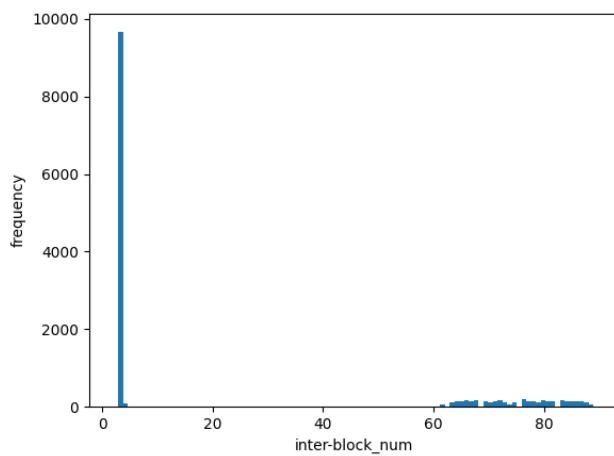


図 292 pht/experiment-win/1205-2/BC/1205-2-70D-PC7ALL-DIF1-INT1-30histogram-inter-blocknum.eps

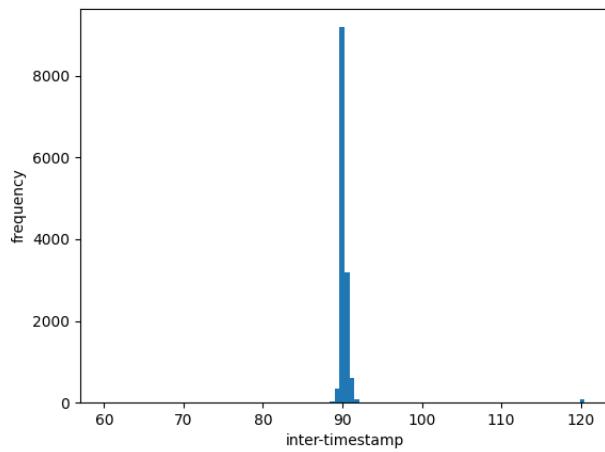


図 293 pht/experiment-win/1205-2/BC/1205-2-70D-PC7ALL-DIF1-INT1-30histogram-inter-timestamp.eps

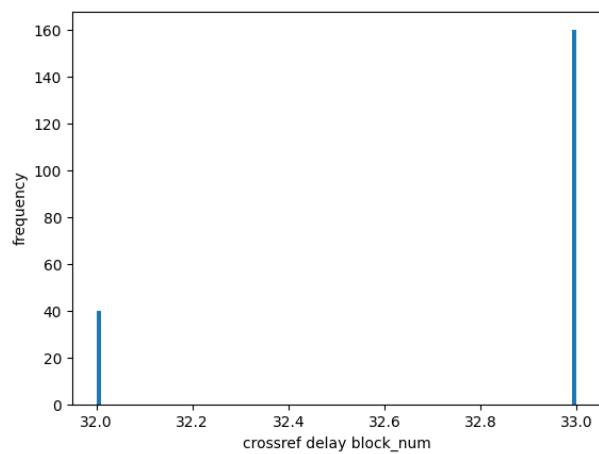


図 294 pht/experiment-win/1205-2/BC/30/1205-2-70D-PC7ALL-DIF1-INT1-30histogram-delay-blocknum.eps

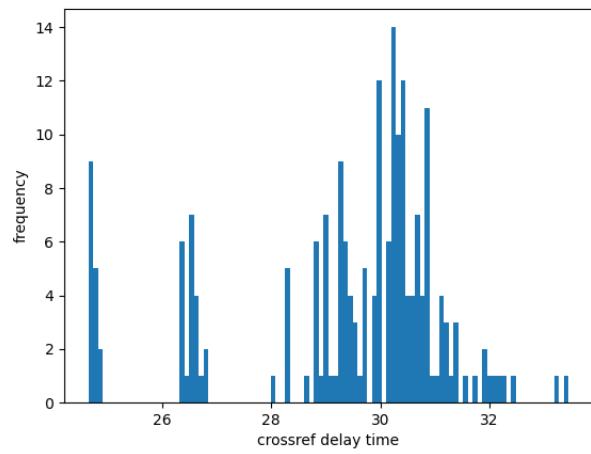


図 295 pht/experiment-win/1205-2/BC/30/1205-2-70D-PC7ALL-DIF1-INT1-30histogram-delay-time.eps

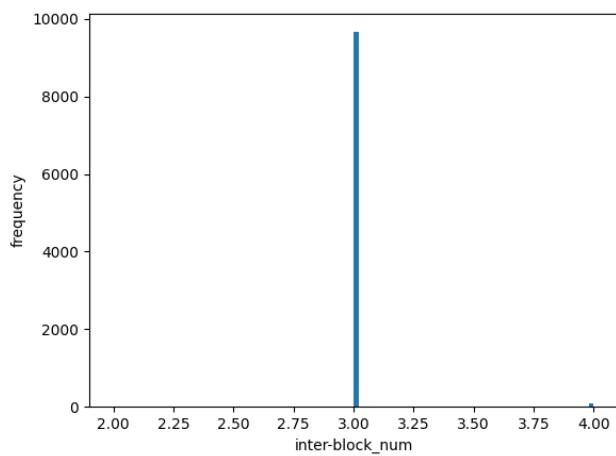


図 296 pht/experiment-win/1205-2/BC/30/1205-2-70D-PC7ALL-DIF1-INT1-30histogram-inter-blocknum.eps

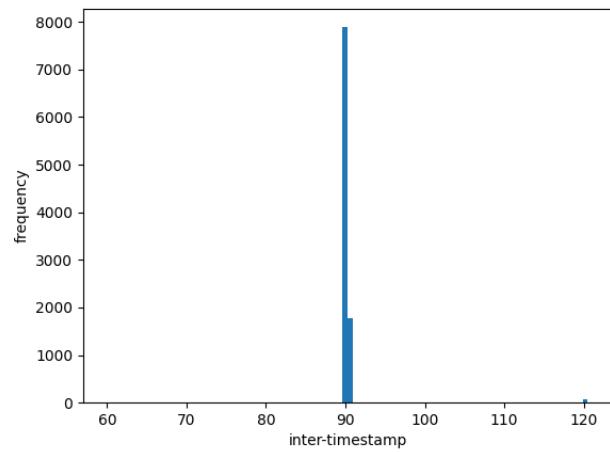


図 297 pht/experiment-win/1205-2/BC/30/1205-2-70D-PC7ALL-DIF1-INT1-30histogram-inter-timestamp.eps

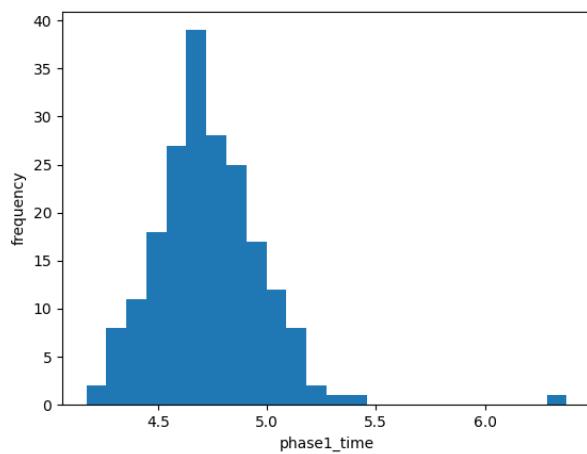


図 298 pht/experiment-win/1205-2/phase1/1205-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50051.eps

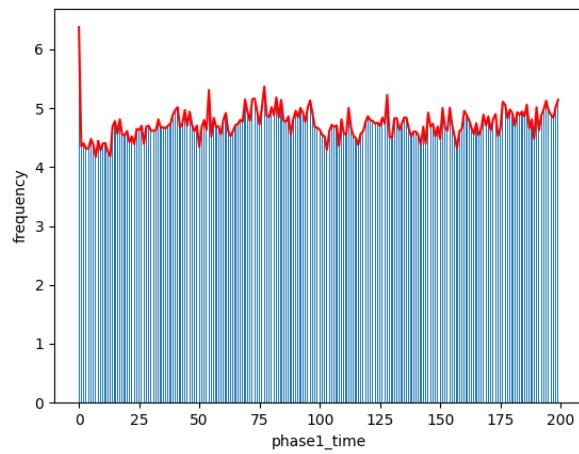


図 299 pht/experiment-win/1205-2/phase1/1205-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50051-in-chronological-order.eps

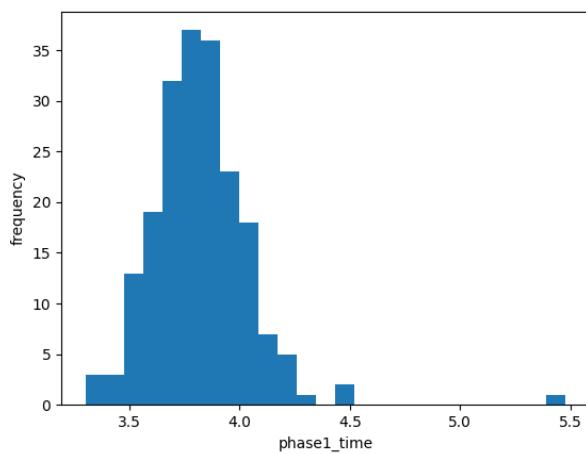


図 300 pht/experiment-win/1205-2/phase1/1205-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50053.eps

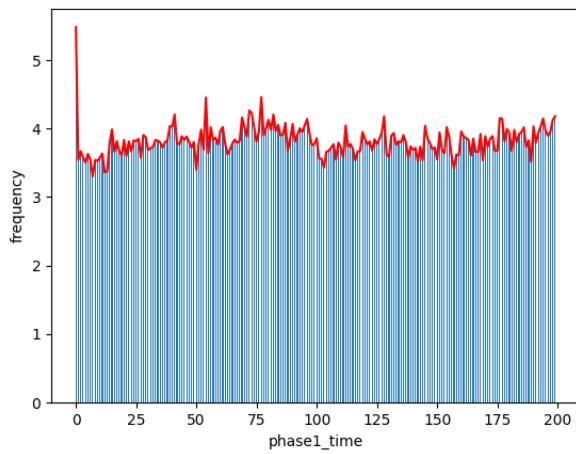


図 301 pht/experiment-win/1205-2/phase1/1205-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50053-in-chronological-order.eps

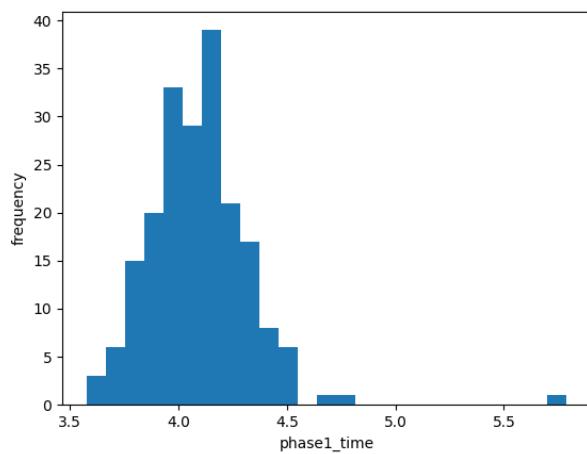


図 302 pht/experiment-win/1205-2/phase1/1205-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50055.eps

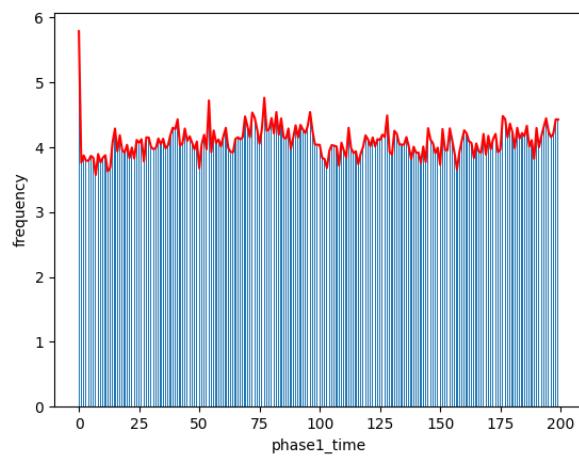


図 303 pht/experiment-win/1205-2/phase1/1205-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50055-in-chronological-order.eps

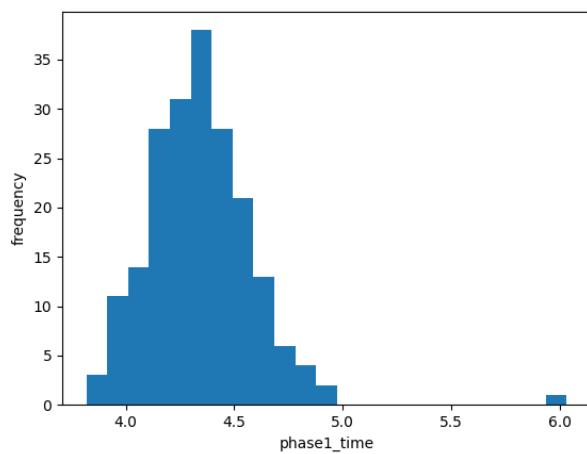


図 304 pht/experiment-win/1205-2/phase1/1205-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50057.eps

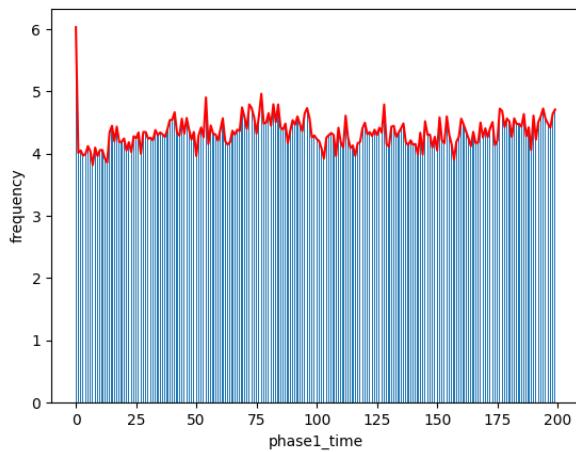


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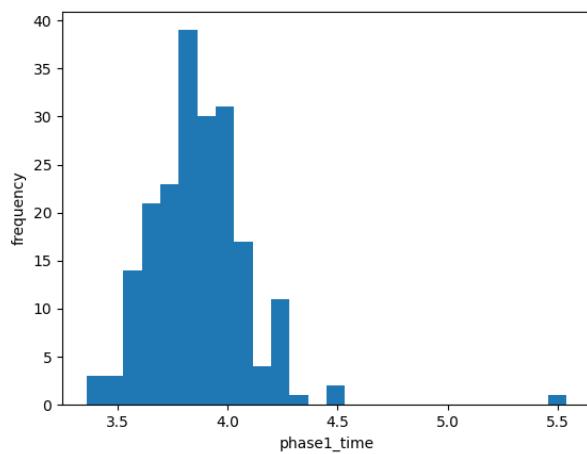


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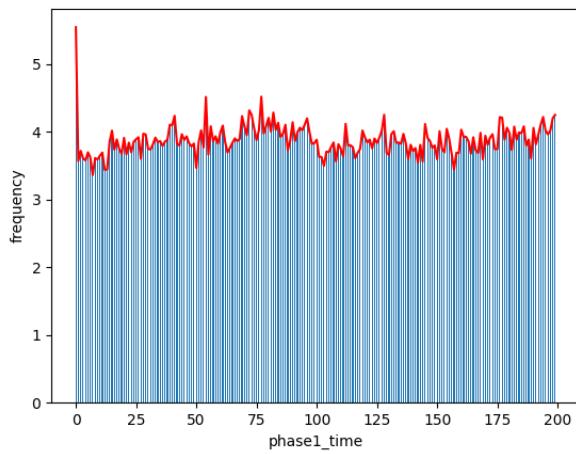


図 307 pht/experiment-win/1205-2/phase1/1205-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50059-in-chronological-order.eps

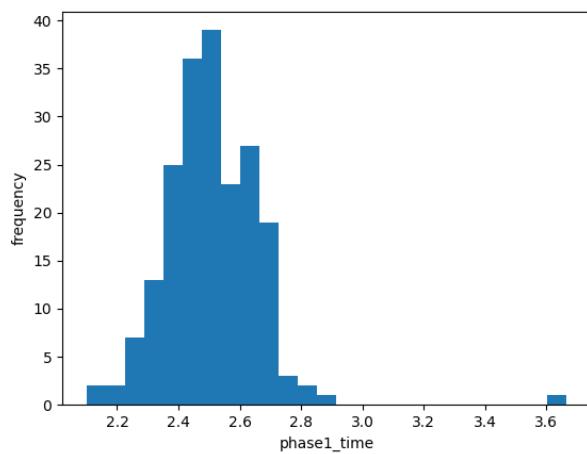


図 308 pht/experiment-win/1205-2/phase1/1205-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50181.eps

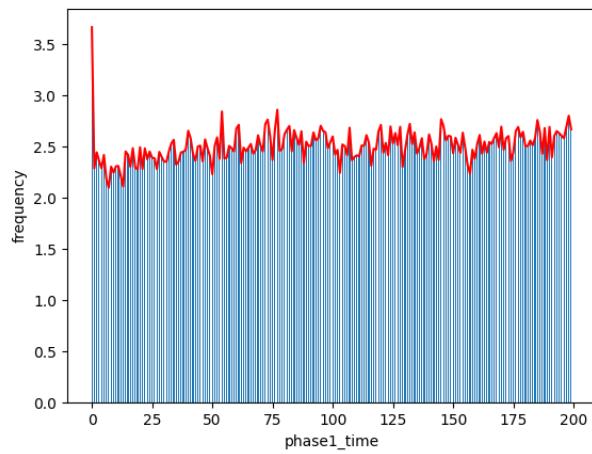


図 309 pht/experiment-win/1205-2/phase1/1205-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50181-in-chronological-order.eps

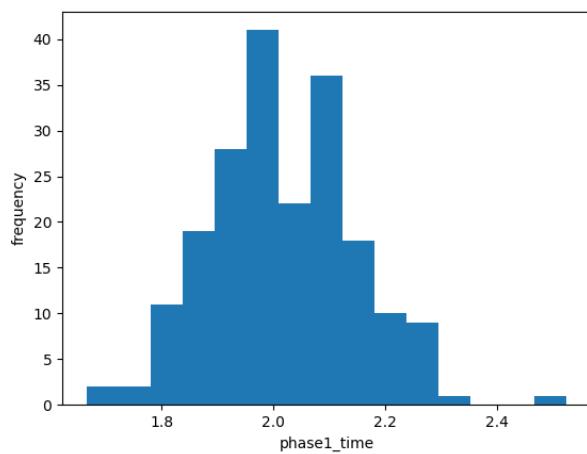


図 310 pht/experiment-win/1205-2/phase1/1205-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50183.eps

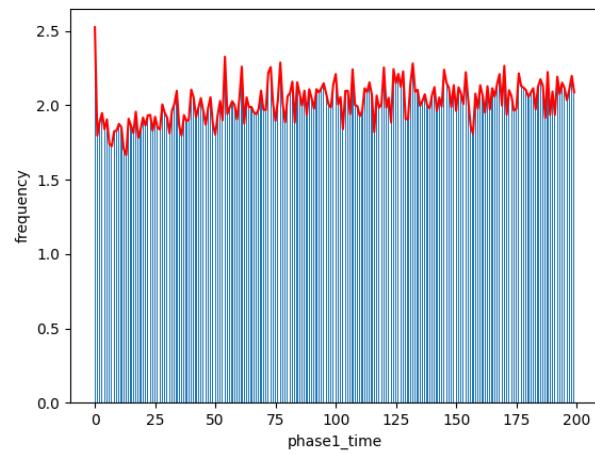


図 311 pht/experiment-win/1205-2/phase1/1205-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50183-in-chronological-order.eps

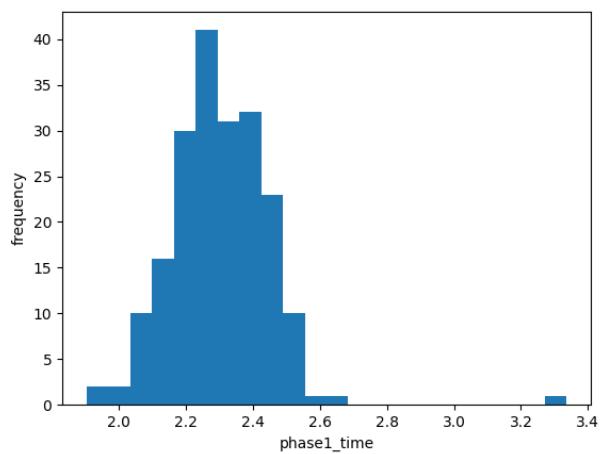


図 312 pht/experiment-win/1205-2/phase1/1205-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50185.eps

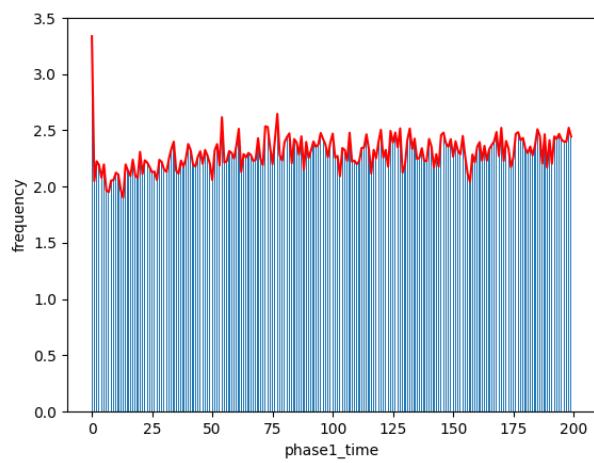


図 313 pht/experiment-win/1205-2/phase1/1205-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50185-in-chronological-order.eps

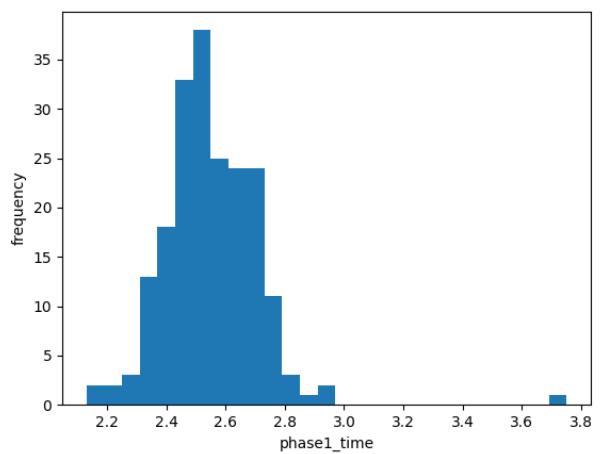


図 314 pht/experiment-win/1205-2/phase1/1205-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50187.eps

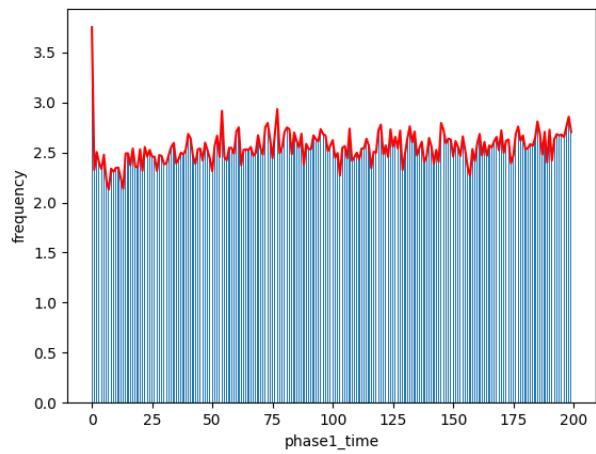


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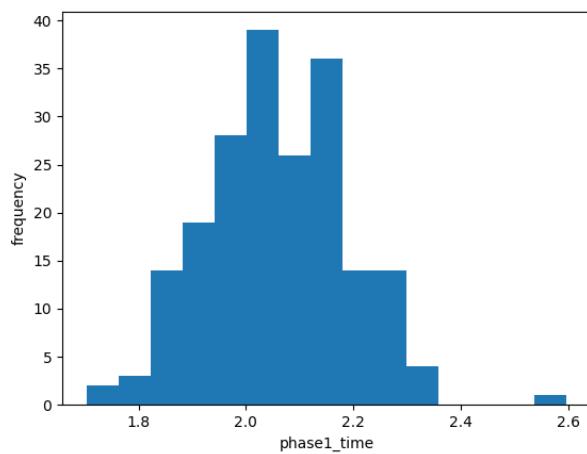


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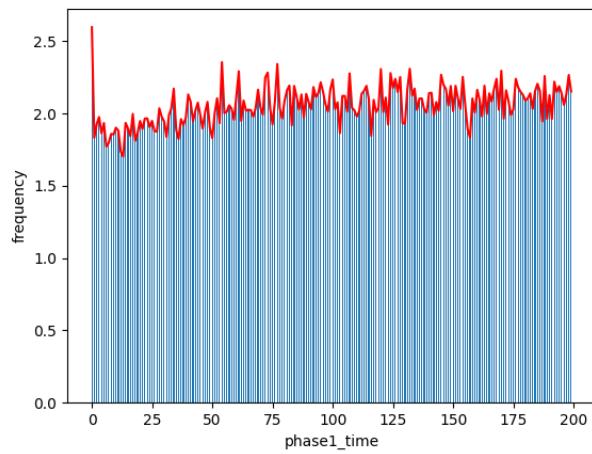


図 317 pht/experiment-win/1205-2/phase1/1205-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50189-in-chronological-order.eps

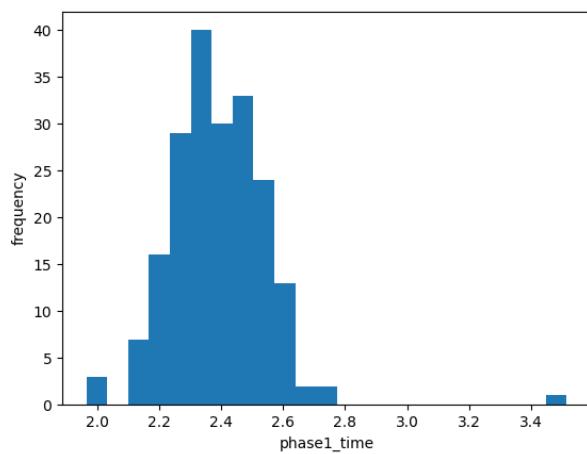


図 318 pht/experiment-win/1205-2/phase1/1205-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50191.eps

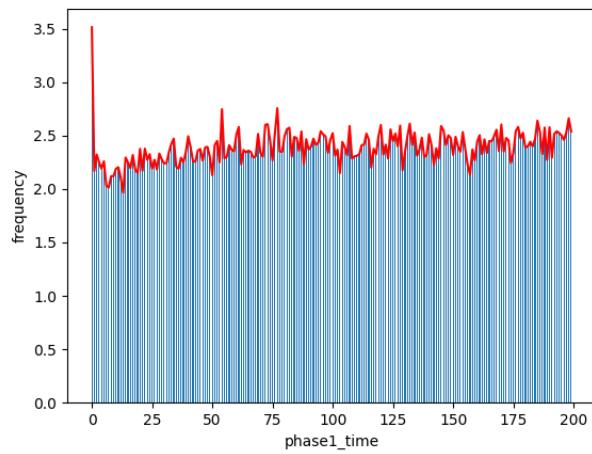


図 319 pht/experiment-win/1205-2/phase1/1205-2-70D-PC7ALL-DIF1-INT1-30Phase1-time50191-in-chronological-order.eps

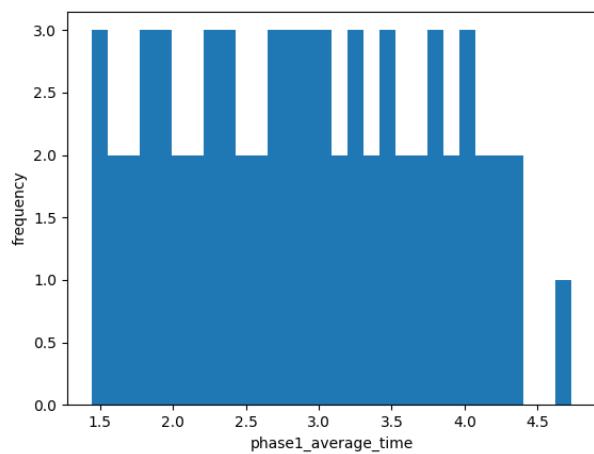


図 320 pht/experiment-win/1205-2/phase1/1205-2-70D-PC7ALL-DIF1-INT1-30analyze-phase1-average-time.eps

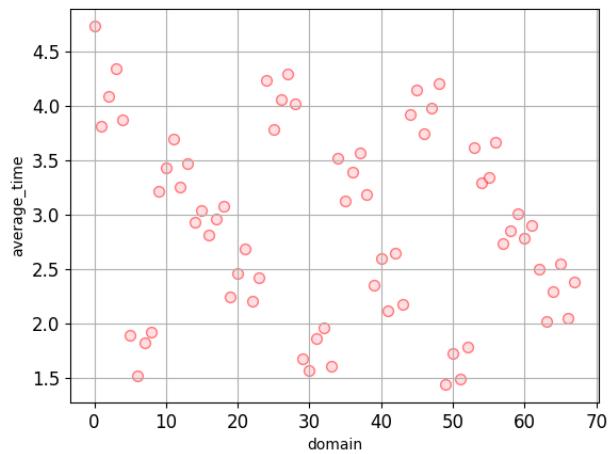


図 321 pht/experiment-win/1205-2/phase1/1205-2-70D-PC7ALL-DIF1-INT1-30analyze-phase1-average-time-scatter.eps

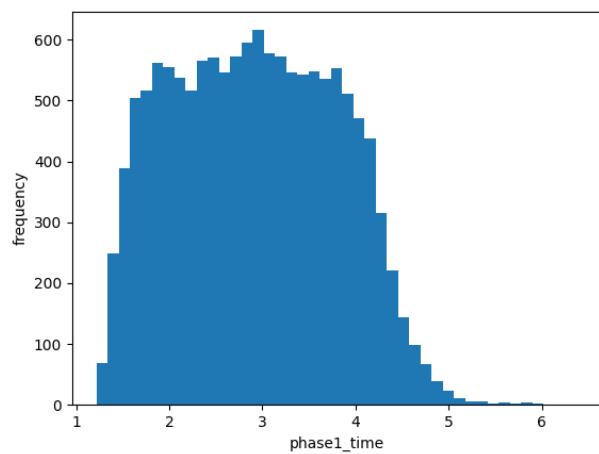


図 322 pht/experiment-win/1205-2/phase1/1205-2-70D-PC7ALL-DIF1-INT1-30analyze-phase1-time-auto.eps

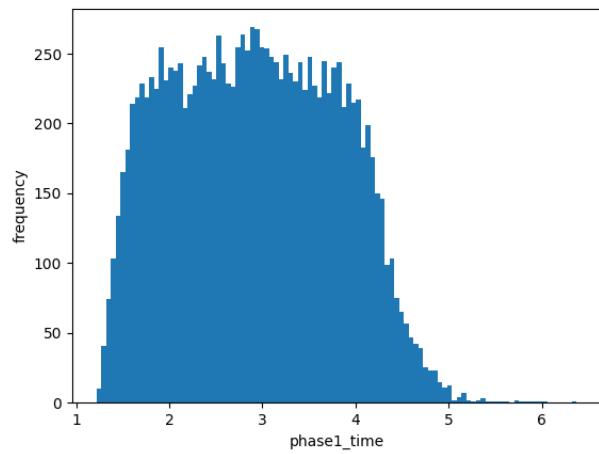


図 323 pht/experiment-win/1205-2/phase1/1205-2-70D-PC7ALL-DIF1-INT1-30analyze-phase1-time-bins100.eps

11 1206-1-70D-PC7ALL-DIF1-INT1-30

11 章にて報告する条件を表 10 に示す.

表 10 設定値の一覧

設定項目	設定値
起動ノード数 (ドメイン数 D)	70 [ノード]
履歴交差間隔	90 [秒]
履歴交差回数	200 [回]
ブロック不生成間隔	0.5[秒]
ブロック生成難易度	1 [0x]
PC 内同時起動台数	7 [台]
使用台数	10 [台]
ブロック不生成間隔 : 30 秒	70 [ノード]
ブロック不生成間隔 : 1 秒	0 [ノード]

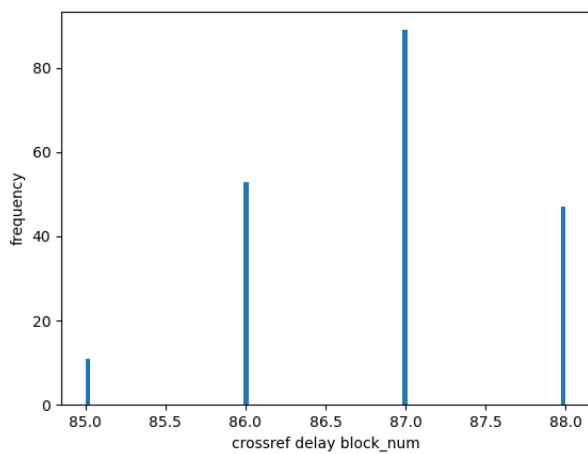


図 324 pht/experiment-win/1206-1/BC/1/1206-1-70D-PC7ALL-DIF1-INT1-30histogram-delay-blocknum.eps

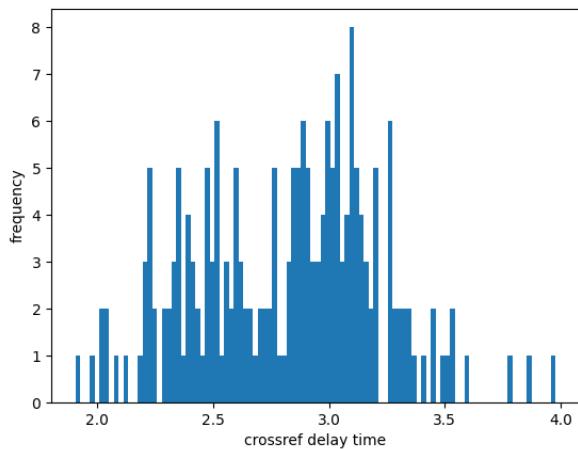


図 325 pht/experiment-win/1206-1/BC/1/1206-1-70D-PC7ALL-DIF1-INT1-30histogram-delay-time.eps

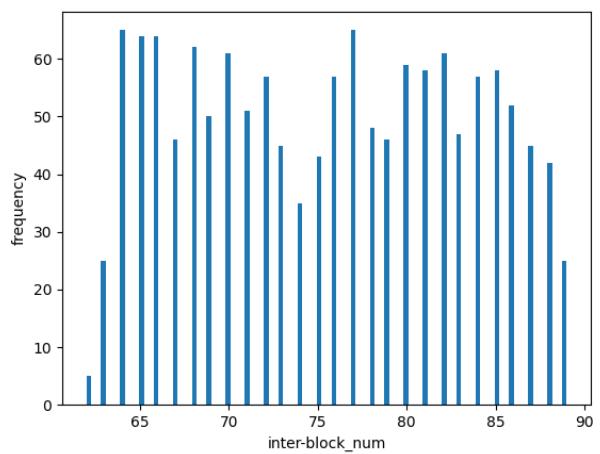


図 326 pht/experiment-win/1206-1/BC/1/1206-1-70D-PC7ALL-DIF1-INT1-30histogram-inter-blocknum.eps

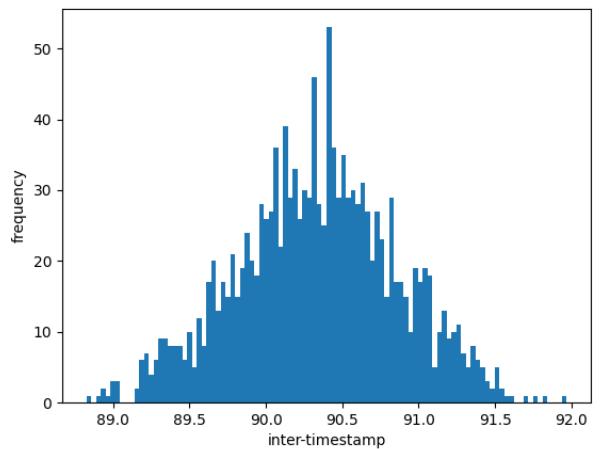


図 327 pht/experiment-win/1206-1/BC/1/1206-1-70D-PC7ALL-DIF1-INT1-30histogram-inter-timestamp.eps

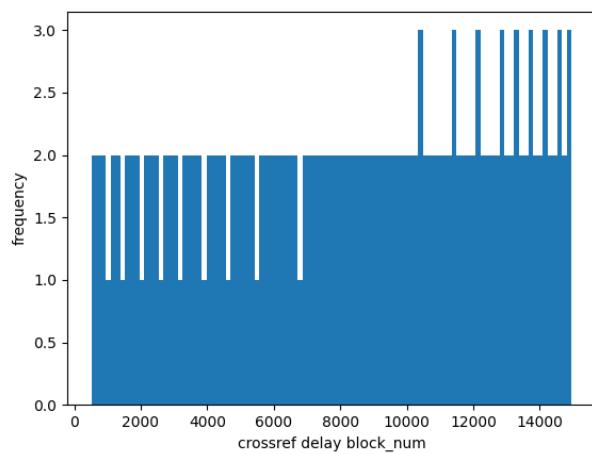


図 328 pht/experiment-win/1206-1/BC/1206-1-70D-PC7ALL-DIF1-INT1-30histogram-delay-blocknum.eps

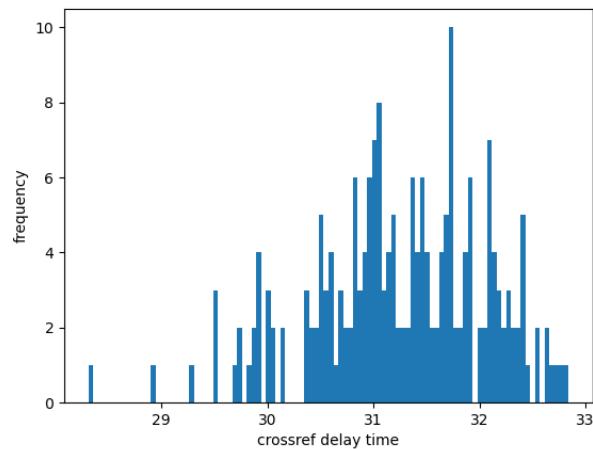


図 329 pht/experiment-win/1206-1/BC/1206-1-70D-PC7ALL-DIF1-INT1-30histogram-delay-time.eps

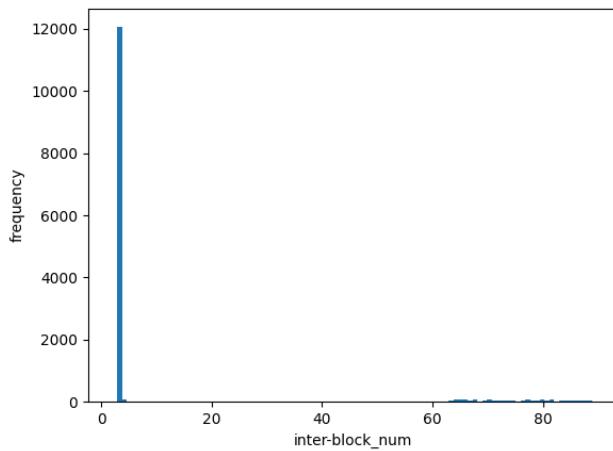


図 330 pht/experiment-win/1206-1/BC/1206-1-70D-PC7ALL-DIF1-INT1-30histogram-inter-blocknum.eps

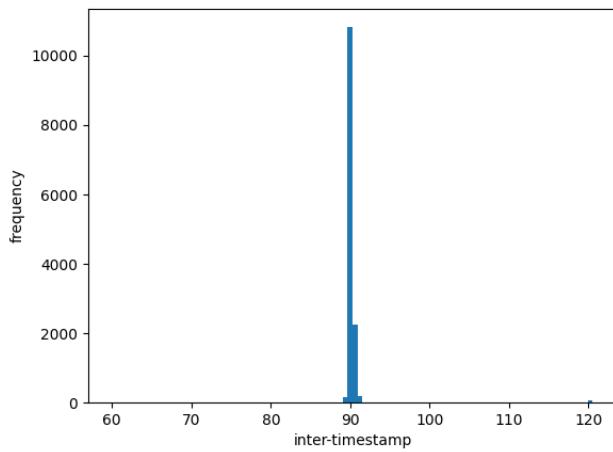


図 331 pht/experiment-win/1206-1/BC/1206-1-70D-PC7ALL-DIF1-INT1-30histogram-inter-timestamp.eps

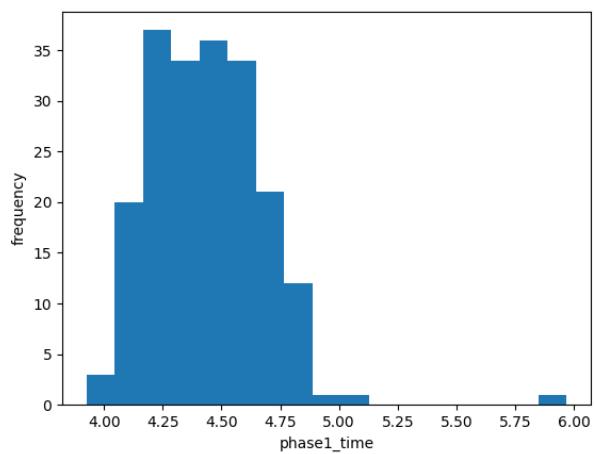


図 332 pht/experiment-win/1206-1/phase1/1206-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50051.eps

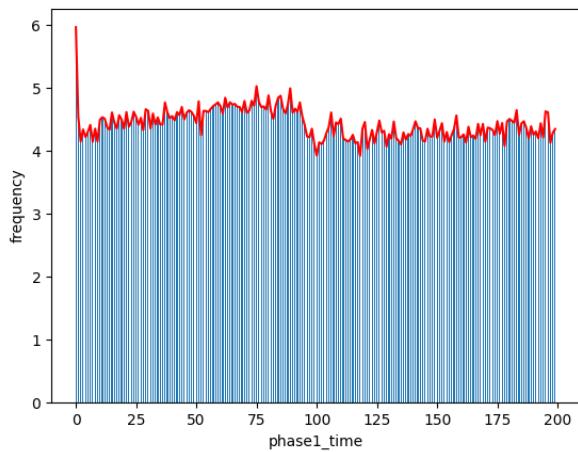


図 333 pht/experiment-win/1206-1/phase1/1206-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50051-in-chronological-order.eps

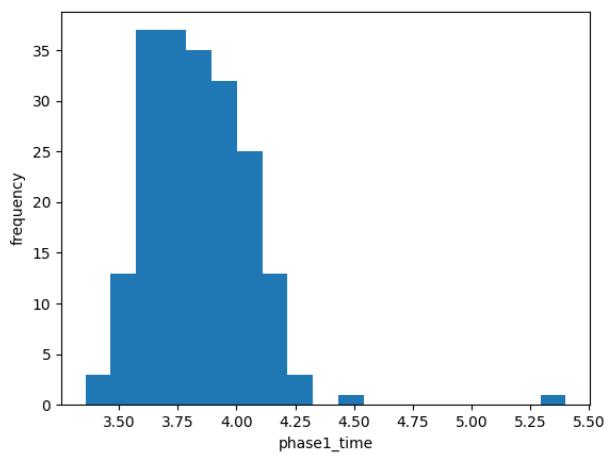


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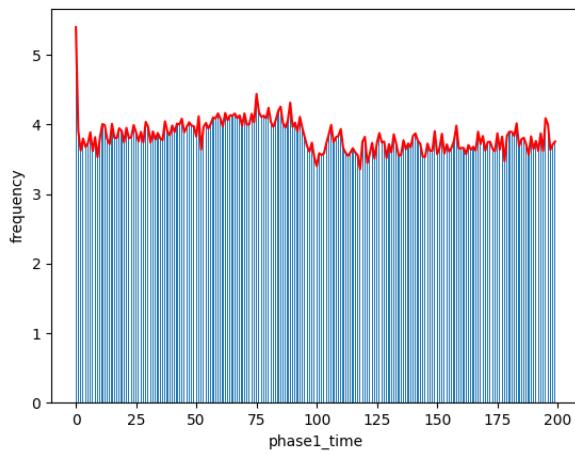


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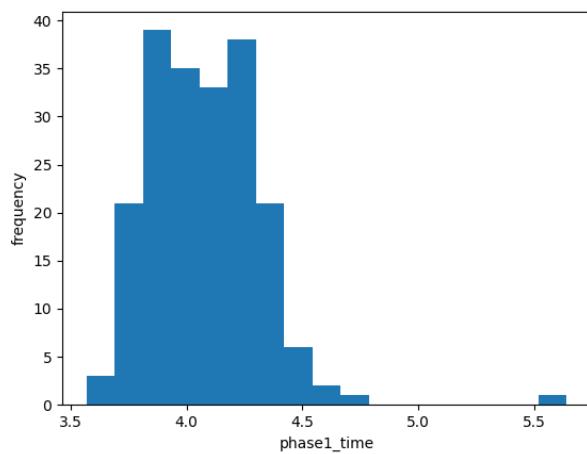


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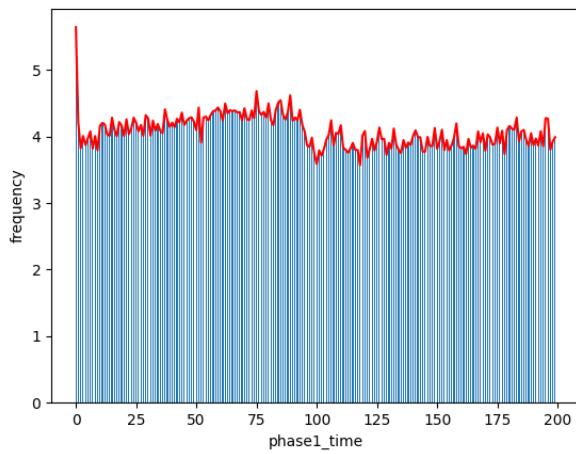


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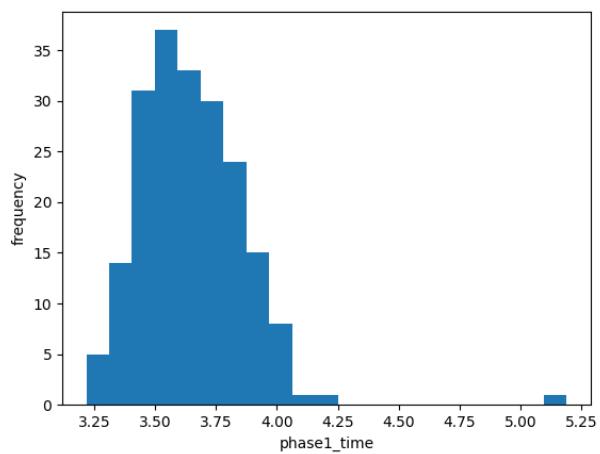


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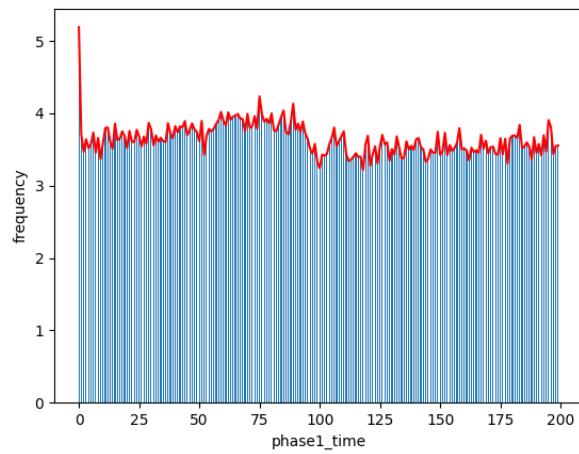


図 339 pht/experiment-win/1206-1/phase1/1206-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50057-in-chronological-order.eps

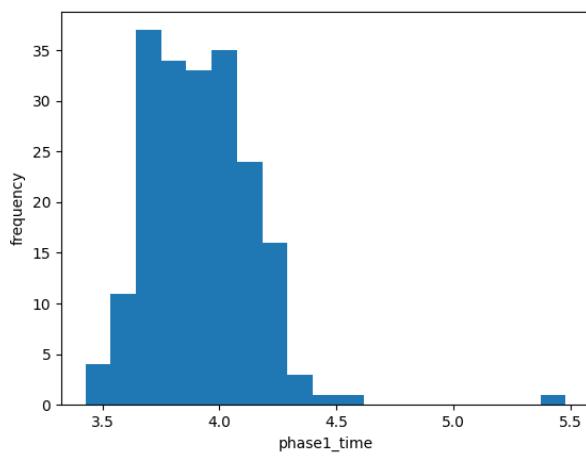


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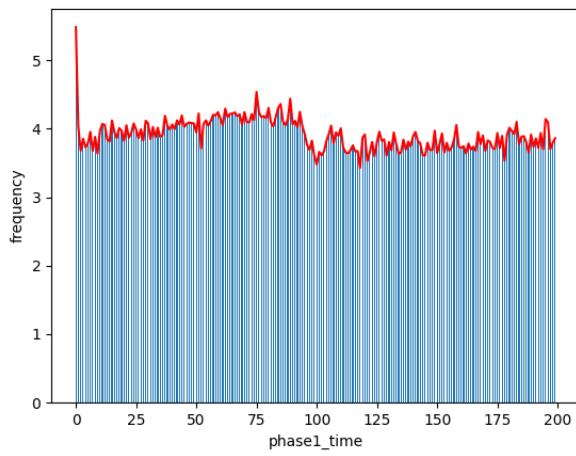


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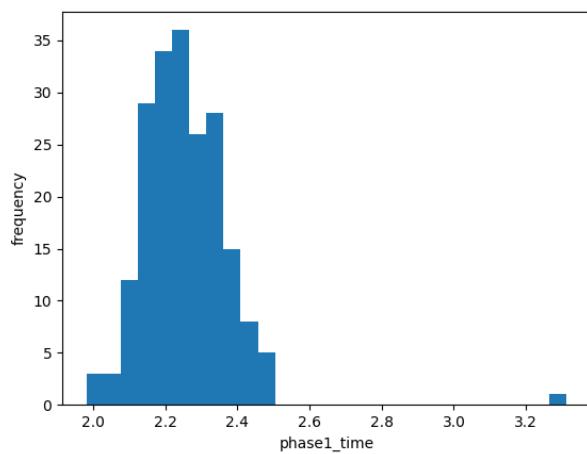


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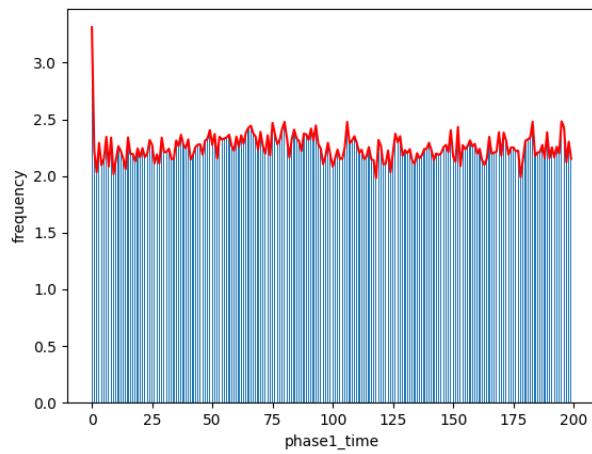


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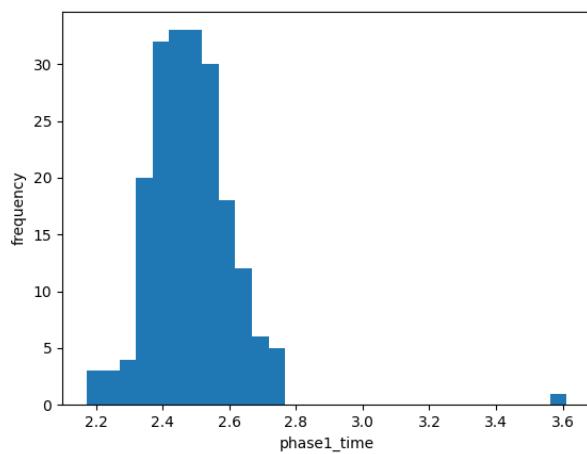


図 344 pht/experiment-win/1206-1/phase1/1206-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50183.eps

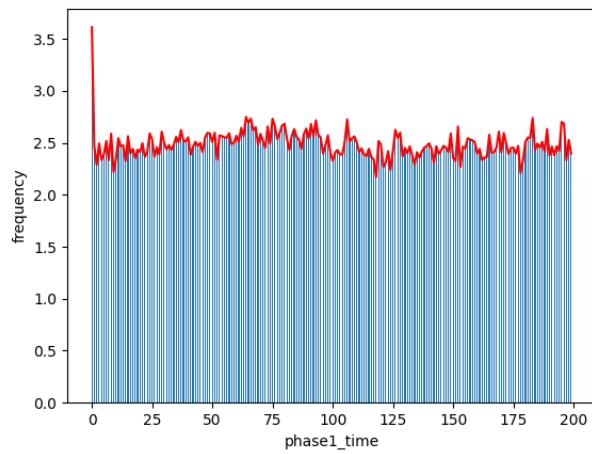


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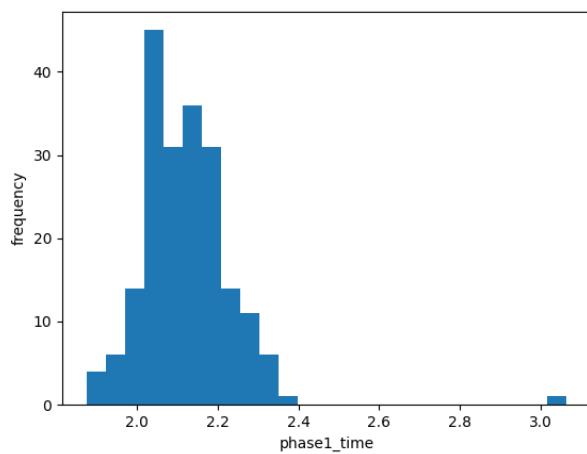


図 346 pht/experiment-win/1206-1/phase1/1206-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50185.eps

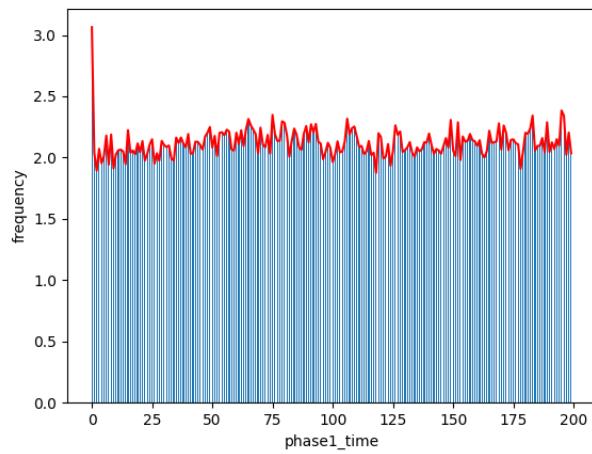


図 347 pht/experiment-win/1206-1/phase1/1206-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50185-in-chronological-order.eps

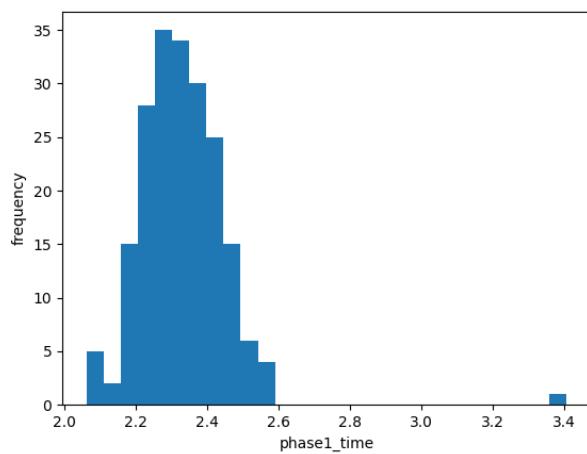


図 348 pht/experiment-win/1206-1/phase1/1206-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50187.eps

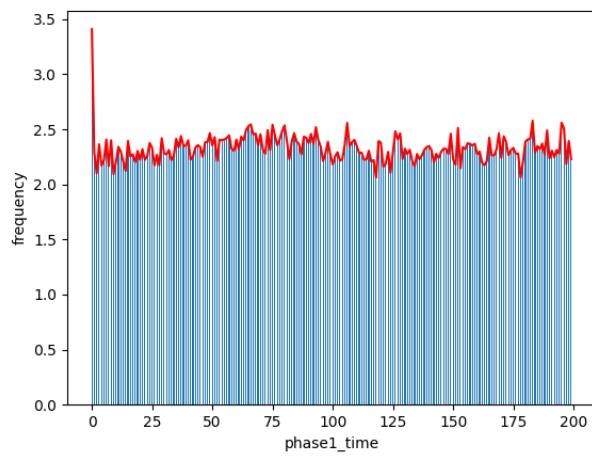


図 349 pht/experiment-win/1206-1/phase1/1206-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50187-in-chronological-order.eps

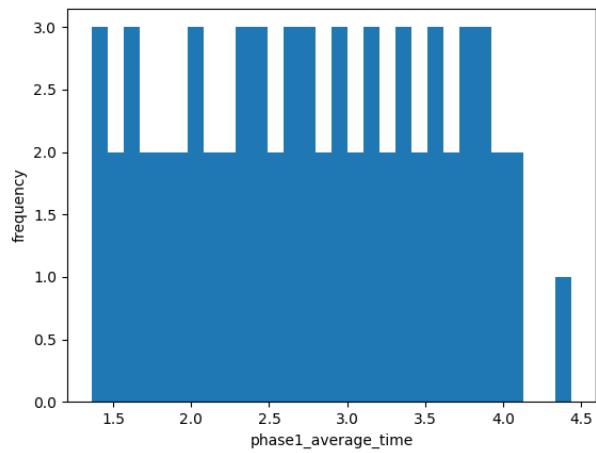


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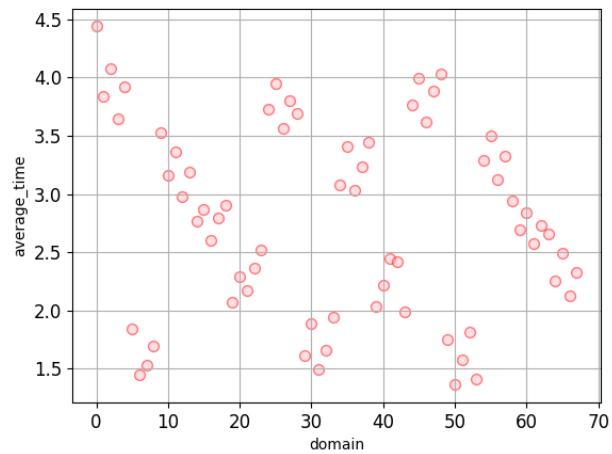


図 351 pht/experiment-win/1206-1/phase1/1206-1-70D-PC7ALL-DIF1-INT1-30analyze-phase1-average-time-scatter.eps

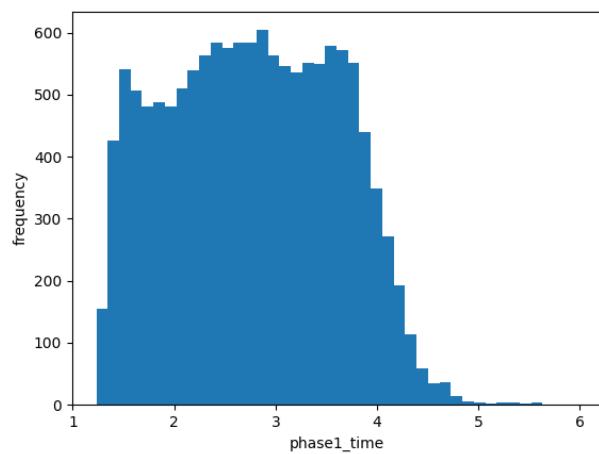


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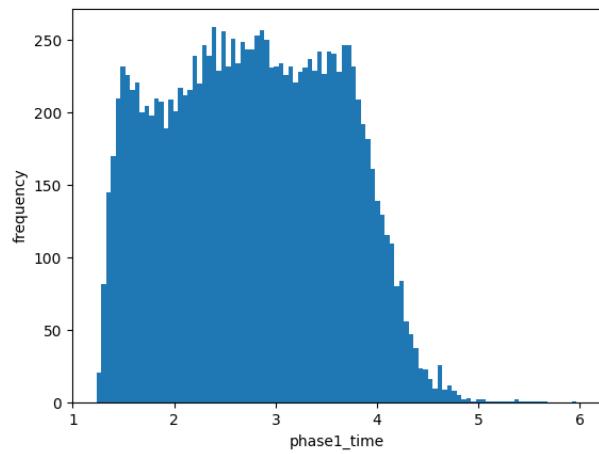


図 353 pht/experiment-win/1206-1/phase1/1206-1-70D-PC7ALL-DIF1-INT1-30analyze-phase1-time-bins100.eps

12 1206-2-70D-PC7ALL-DIF1-INT1-30

12 章にて報告する条件を表 11 に示す。

表 11 設定値の一覧

設定項目	設定値
起動ノード数 (ドメイン数 D)	70 [ノード]
履歴交差間隔	90 [秒]
履歴交差回数	200 [回]
ブロック不生成間隔	0.5[秒]
ブロック生成難易度	1 [0x]
PC 内同時起動台数	7 [台]
使用台数	10 [台]
ブロック不生成間隔 : 30 秒	70 [ノード]
ブロック不生成間隔 : 1 秒	0 [ノード]

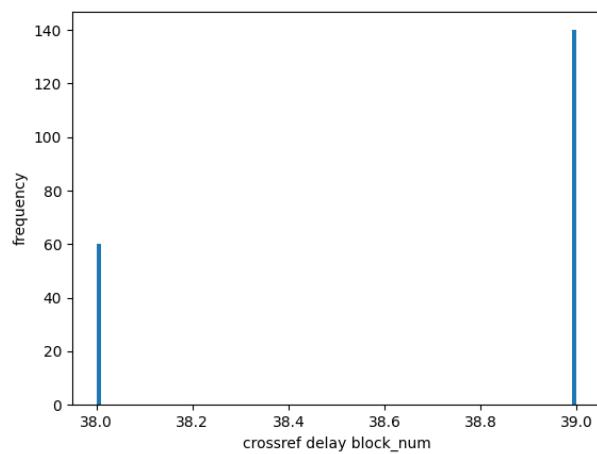


図 354 pht/experiment-win/1206-2/BC/1206-2-70D-PC7ALL-DIF1-INT1-30r1histogram-delay-blocknum.eps

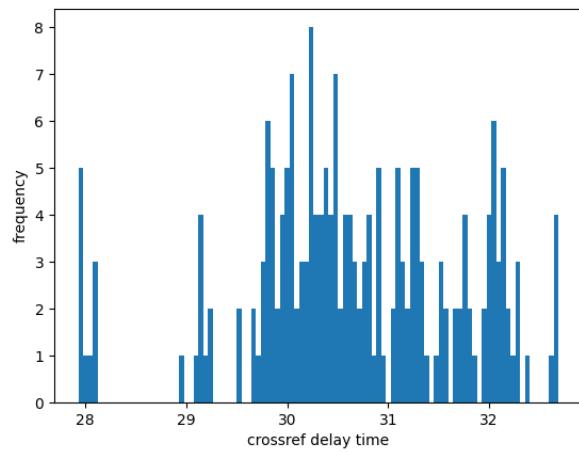


図 355 pht/experiment-win/1206-2/BC/1206-2-70D-PC7ALL-DIF1-INT1-30r1histogram-delay-time.eps

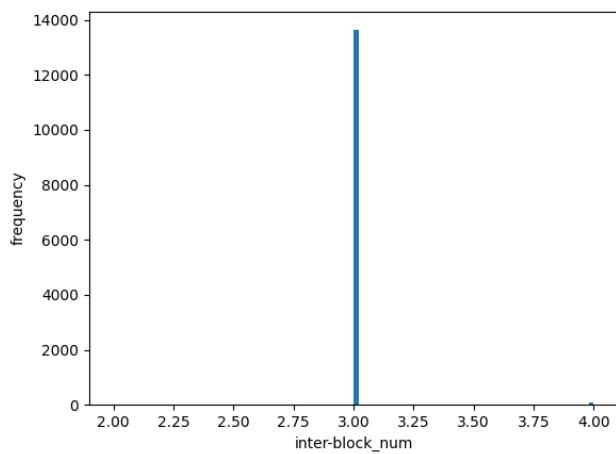


図 356 pht/experiment-win/1206-2/BC/1206-2-70D-PC7ALL-DIF1-INT1-30r1histogram-inter-blocknum.eps

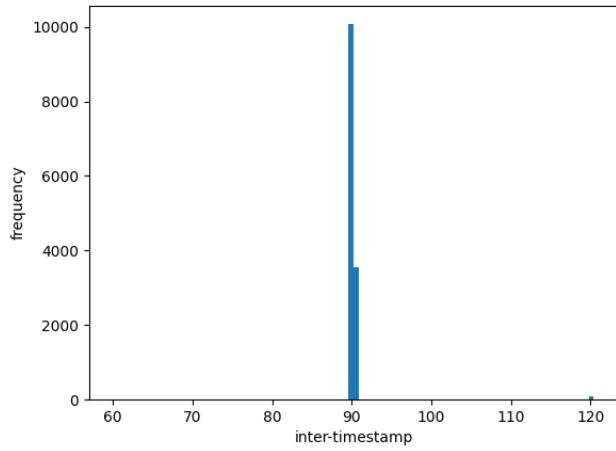


図 357 pht/experiment-win/1206-2/BC/1206-2-70D-PC7ALL-DIF1-INT1-30r1histogram-inter-timestamp.eps

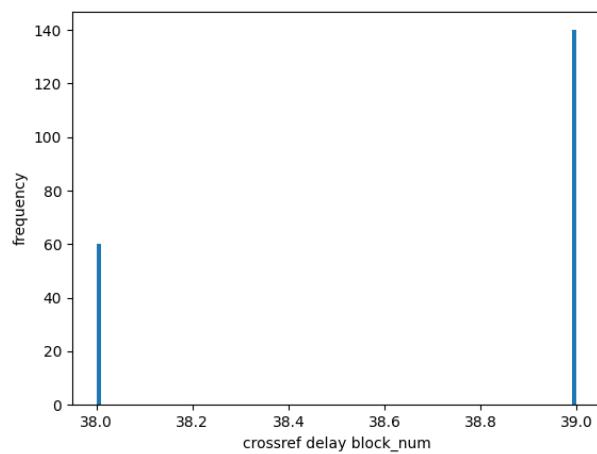


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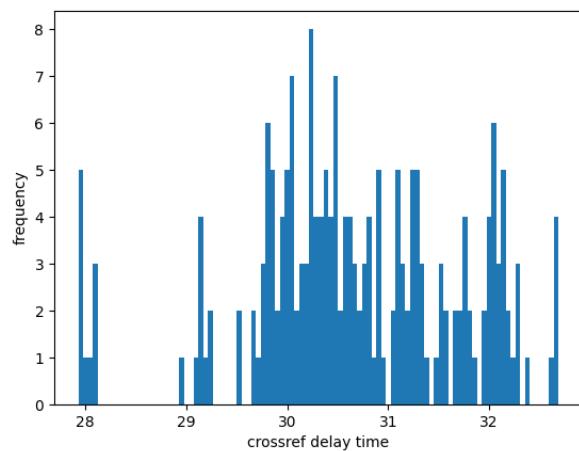


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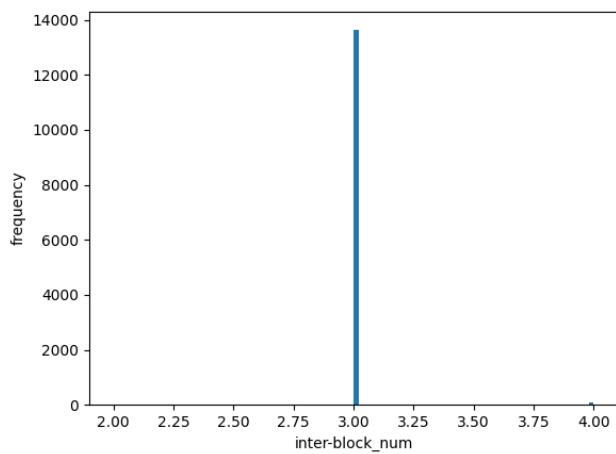


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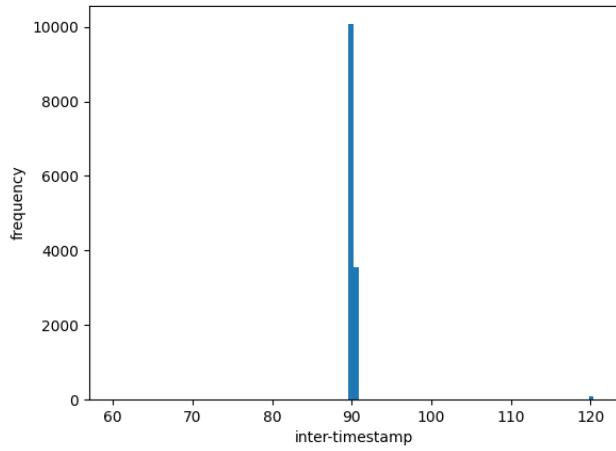


図 361 pht/experiment-win/1206-2/BC/30/1206-2-70D-PC7ALL-DIF1-INT1-30r1histogram-inter-timestamp.eps

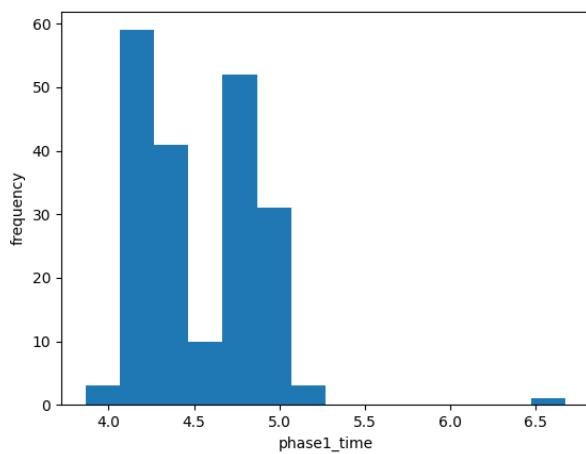


図 362 pht/experiment-win/1206-2/phase1/1206-2-70D-PC7ALL-DIF1-INT1-30r1Phase1-time50051.eps

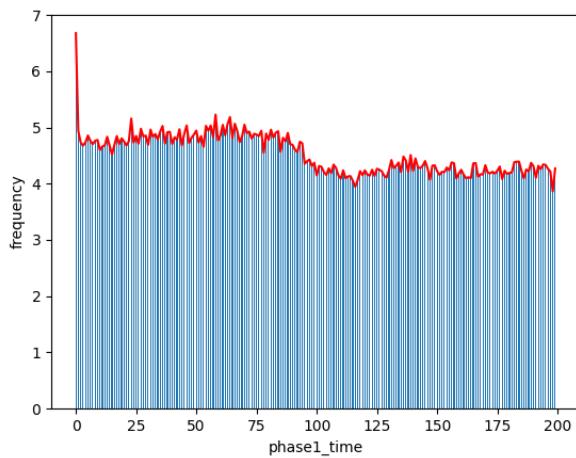


図 363 pht/experiment-win/1206-2/phase1/1206-2-70D-PC7ALL-DIF1-INT1-30r1Phase1-time50051-in-chronological-order.eps

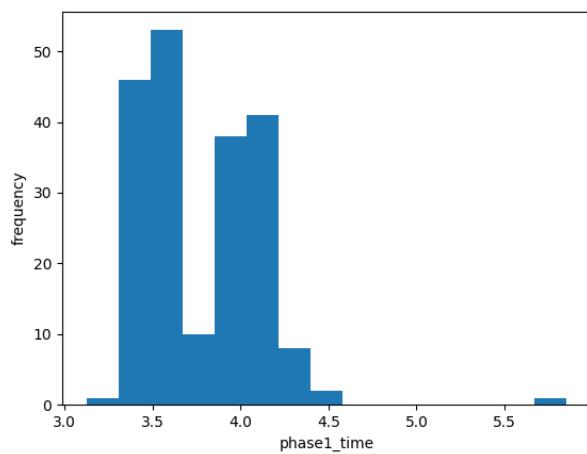


図 364 pht/experiment-win/1206-2/phase1/1206-2-70D-PC7ALL-DIF1-INT1-30r1Phase1-time50053.eps

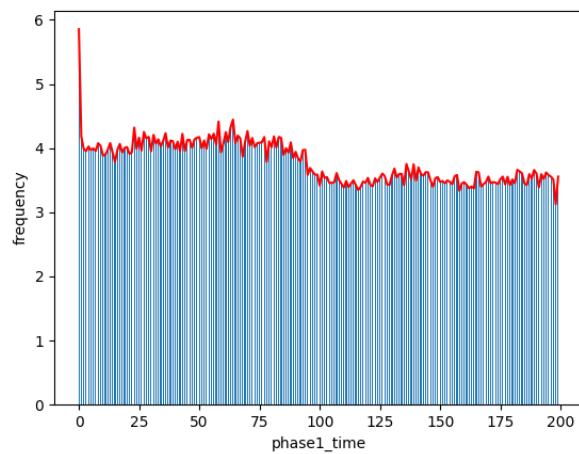


図 365 pht/experiment-win/1206-2/phase1/1206-2-70D-PC7ALL-DIF1-INT1-30r1Phase1-time50053-in-chronological-order.eps

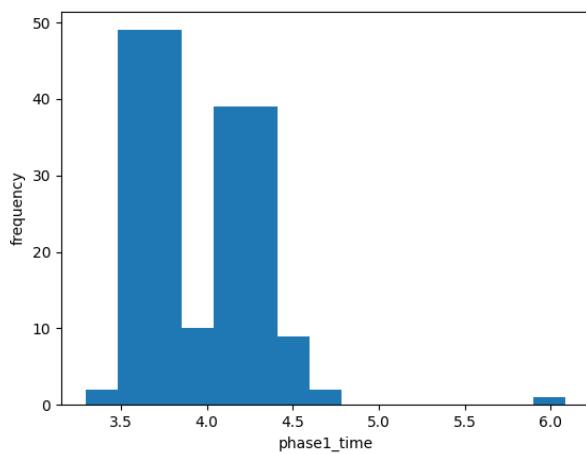


図 366 pht/experiment-win/1206-2/phase1/1206-2-70D-PC7ALL-DIF1-INT1-30r1Phase1-time50055.eps

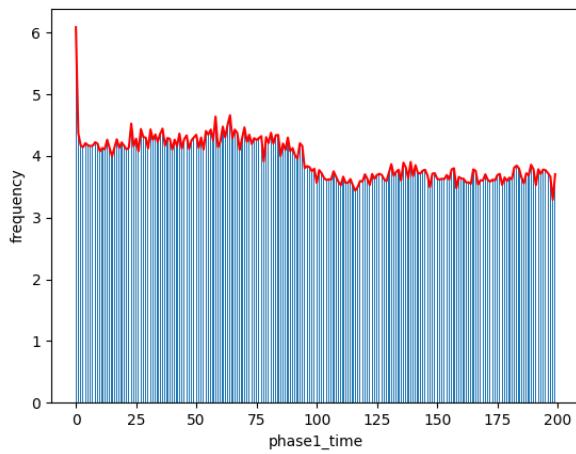


図 367 pht/experiment-win/1206-2/phase1/1206-2-70D-PC7ALL-DIF1-INT1-30r1Phase1-time50055-in-chronological-order.eps

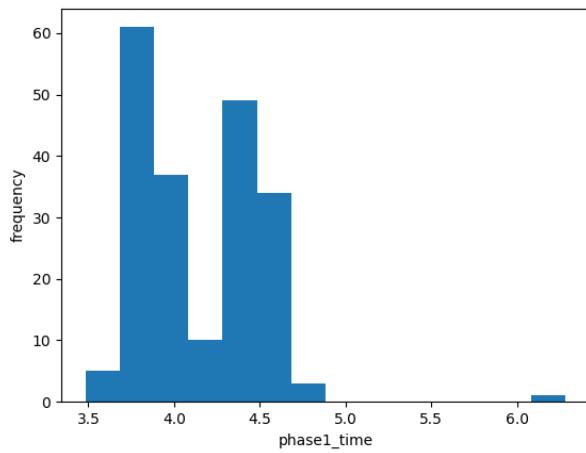


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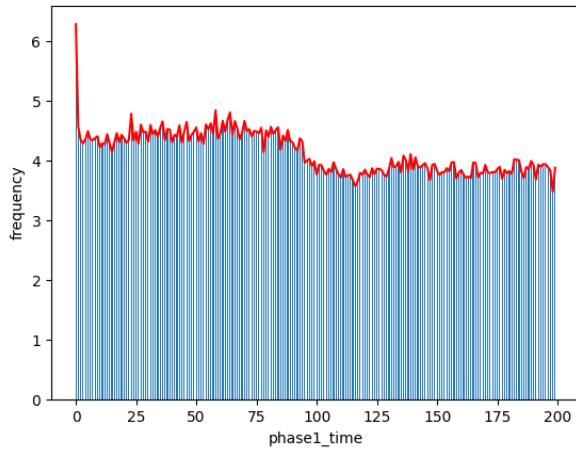


図 369 pht/experiment-win/1206-2/phase1/1206-2-70D-PC7ALL-DIF1-INT1-30r1Phase1-time50057-in-chronological-order.eps

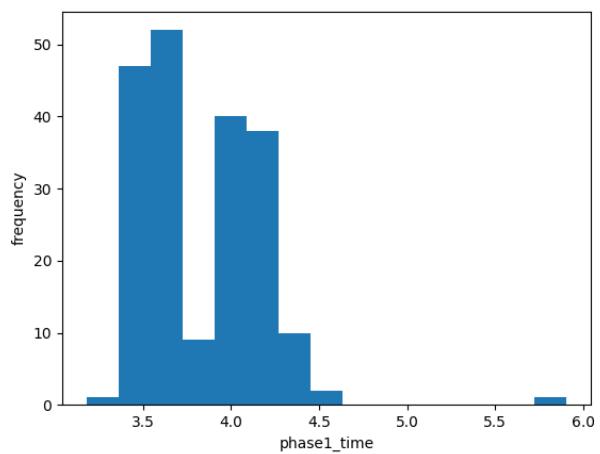


図 370 pht/experiment-win/1206-2/phase1/1206-2-70D-PC7ALL-DIF1-INT1-30r1Phase1-time50059.eps

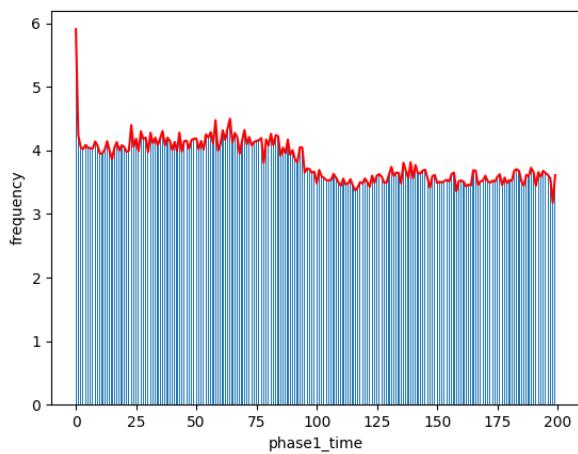


図 371 pht/experiment-win/1206-2/phase1/1206-2-70D-PC7ALL-DIF1-INT1-30r1Phase1-time50059-in-chronological-order.eps

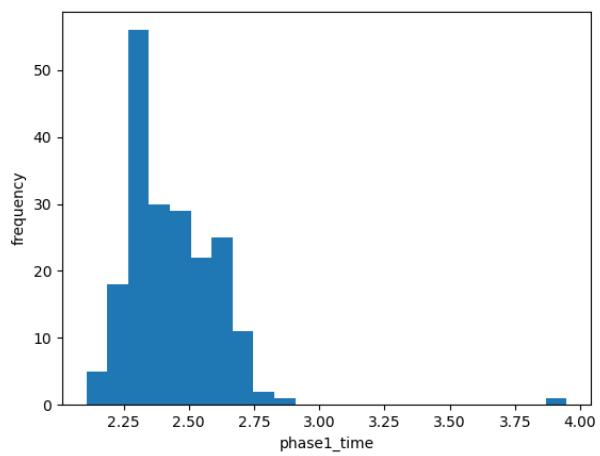


図 372 pht/experiment-win/1206-2/phase1/1206-2-70D-PC7ALL-DIF1-INT1-30r1Phase1-time50181.eps

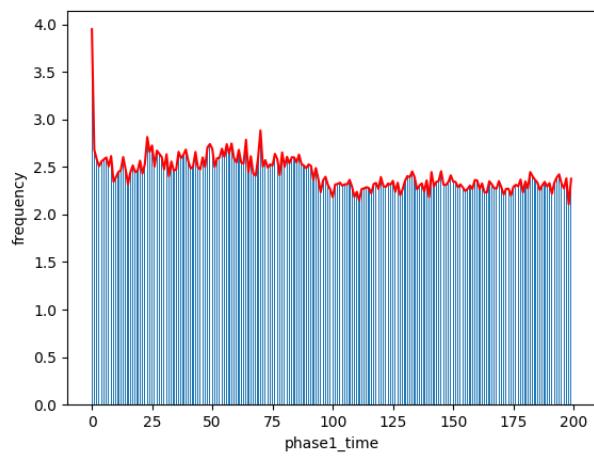


図 373 pht/experiment-win/1206-2/phase1/1206-2-70D-PC7ALL-DIF1-INT1-30r1Phase1-time50181-in-chronological-order.eps

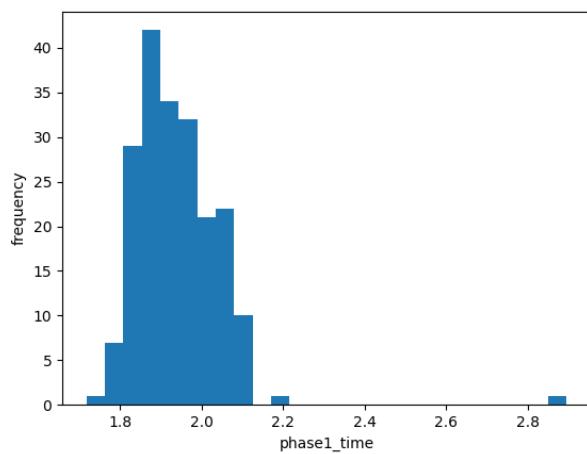


図 374 pht/experiment-win/1206-2/phase1/1206-2-70D-PC7ALL-DIF1-INT1-30r1Phase1-time50183.eps

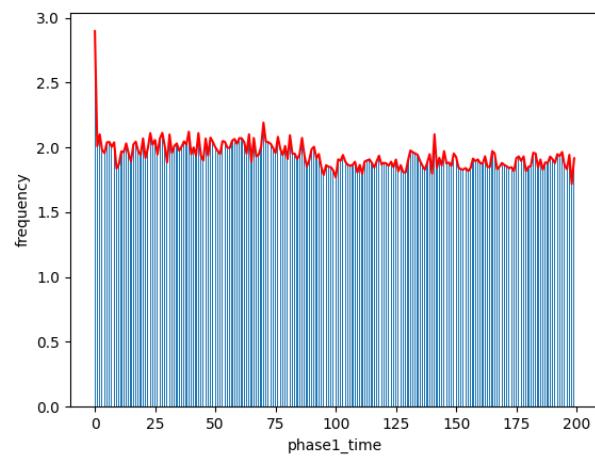


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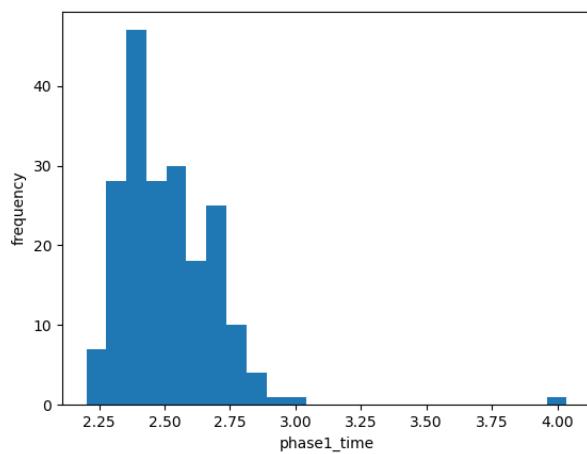


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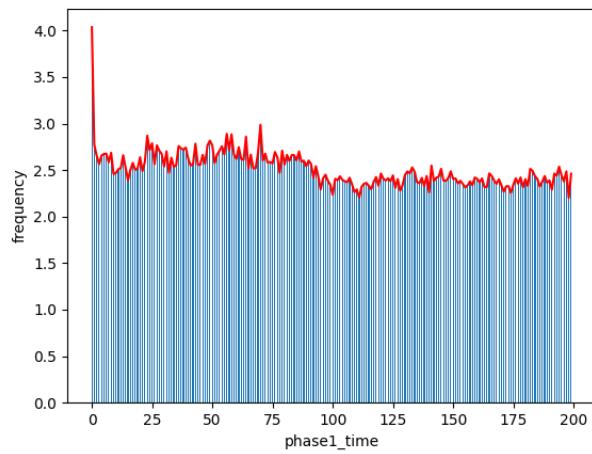


図 377 pht/experiment-win/1206-2/phase1/1206-2-70D-PC7ALL-DIF1-INT1-30r1Phase1-time50187-in-chronological-order.eps

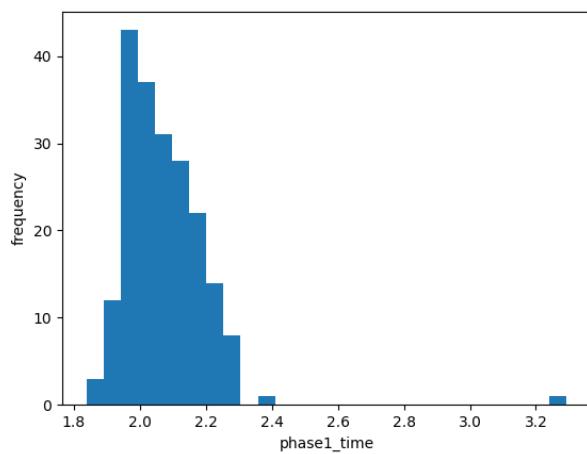


図 378 pht/experiment-win/1206-2/phase1/1206-2-70D-PC7ALL-DIF1-INT1-30r1Phase1-time50189.eps

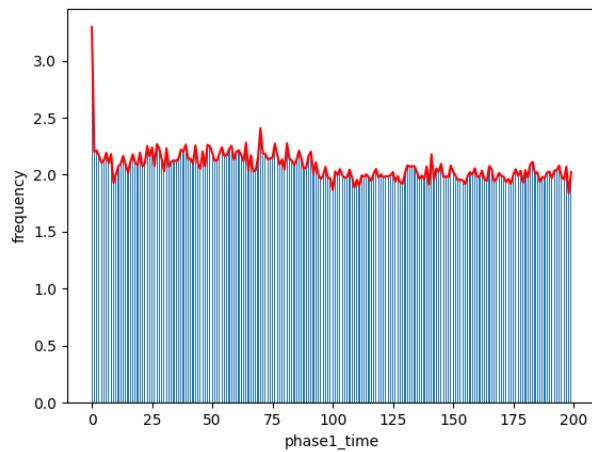


図 379 pht/experiment-win/1206-2/phase1/1206-2-70D-PC7ALL-DIF1-INT1-30r1Phase1-time50189-in-chronological-order.eps

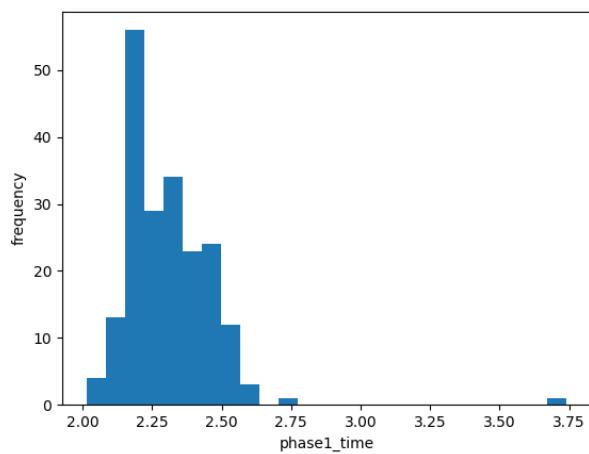


図 380 pht/experiment-win/1206-2/phase1/1206-2-70D-PC7ALL-DIF1-INT1-30r1Phase1-time50191.eps

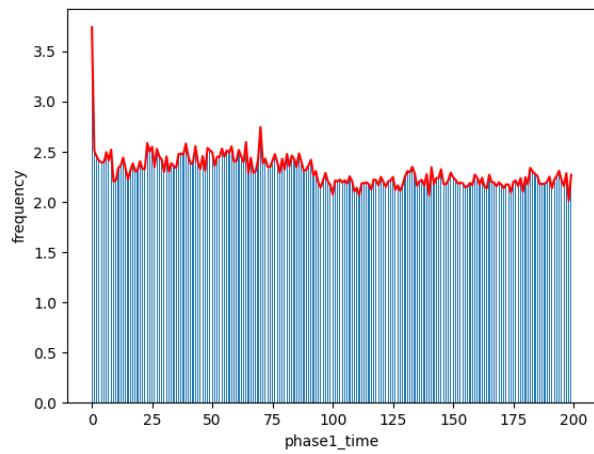


図 381 pht/experiment-win/1206-2/phase1/1206-2-70D-PC7ALL-DIF1-INT1-30r1Phase1-time50191-in-chronological-order.eps

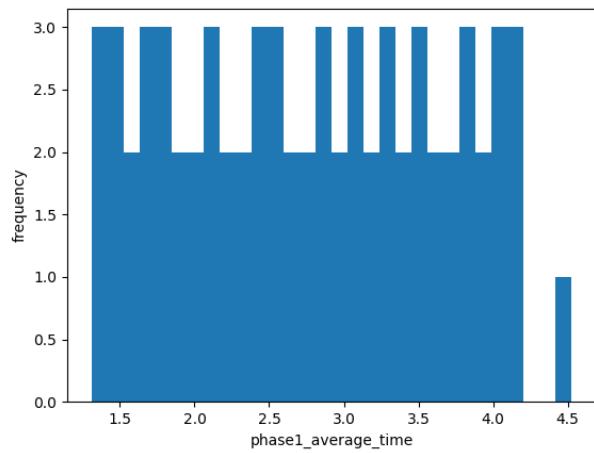


図 382 pht/experiment-win/1206-2/phase1/1206-2-70D-PC7ALL-DIF1-INT1-30r1analyze-phase1-average-time.eps

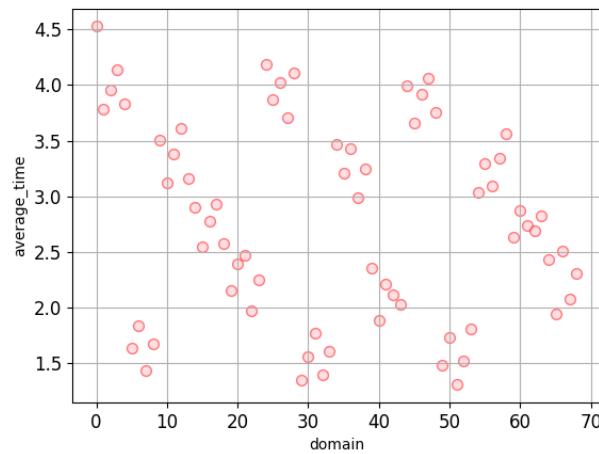


図 383 pht/experiment-win/1206-2/phase1/1206-2-70D-PC7ALL-DIF1-INT1-30r1analyze-phase1-average-time-scatter.eps

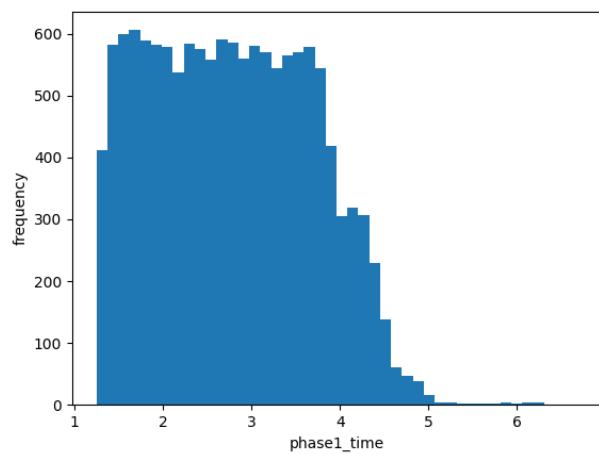


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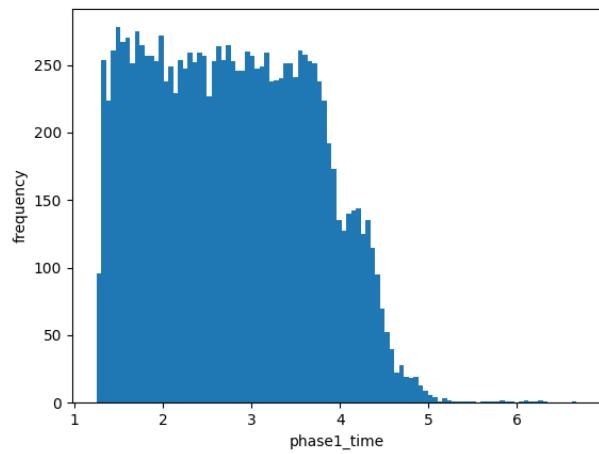


図 385 pht/experiment-win/1206-2/phase1/1206-2-70D-PC7ALL-DIF1-INT1-30r1analyze-phase1-time-bins100.eps

13 1207-70D-PC7ALL-DIF1-INT1-30

13 章にて報告する条件を表 12 に示す.

表 12 設定値の一覧

設定項目	設定値
起動ノード数 (ドメイン数 D)	70 [ノード]
履歴交差間隔	90 [秒]
履歴交差回数	200 [回]
ブロック不生成間隔	0.5[秒]
ブロック生成難易度	1 [0x]
PC 内同時起動台数	7 [台]
使用台数	10 [台]
ブロック不生成間隔 : 30 秒	70 [ノード]
ブロック不生成間隔 : 1 秒	0 [ノード]

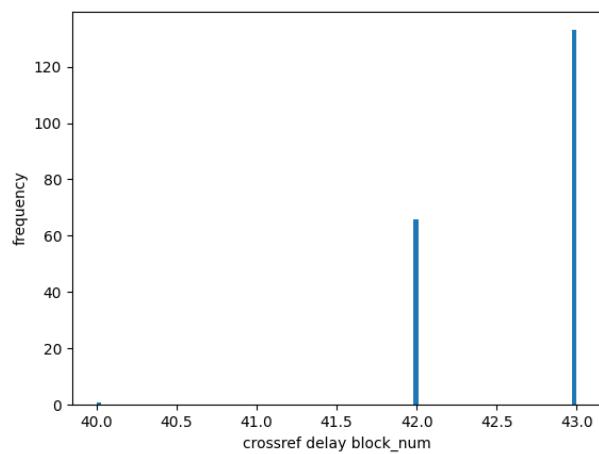


図 386 pht/experiment-win/1207/BC/1207-70D-PC7ALL-DIF1-INT1-30histogram-delay-blocknum.eps

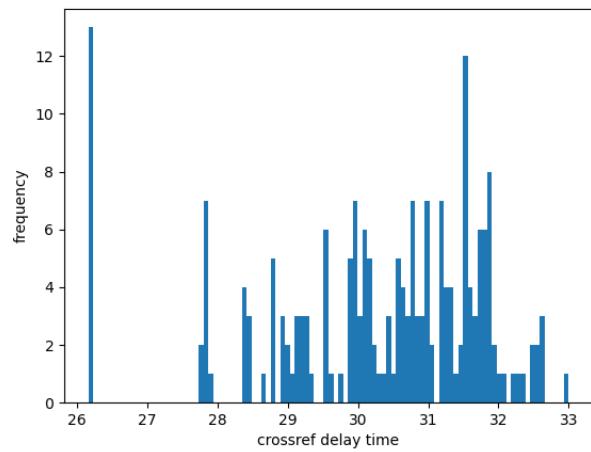


図 387 pht/experiment-win/1207/BC/1207-70D-PC7ALL-DIF1-INT1-30histogram-delay-time.eps

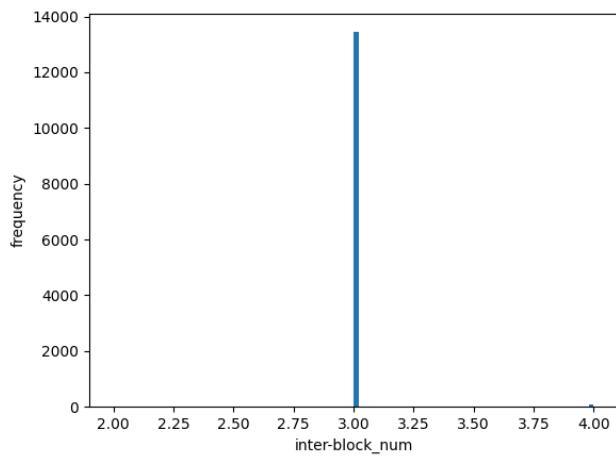


図 388 pht/experiment-win/1207/BC/1207-70D-PC7ALL-DIF1-INT1-30histogram-inter-blocknum.eps

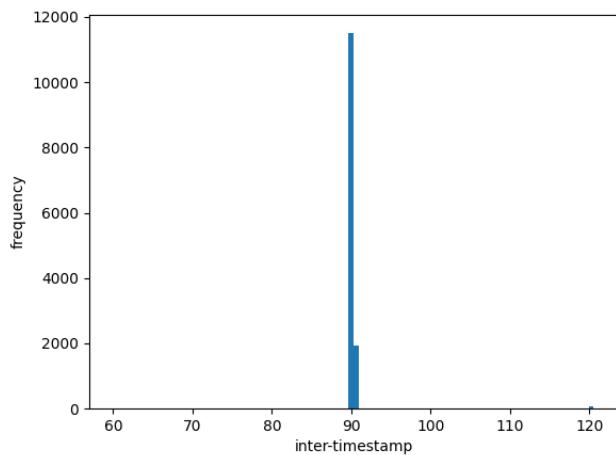


図 389 pht/experiment-win/1207/BC/1207-70D-PC7ALL-DIF1-INT1-30histogram-inter-timestamp.eps

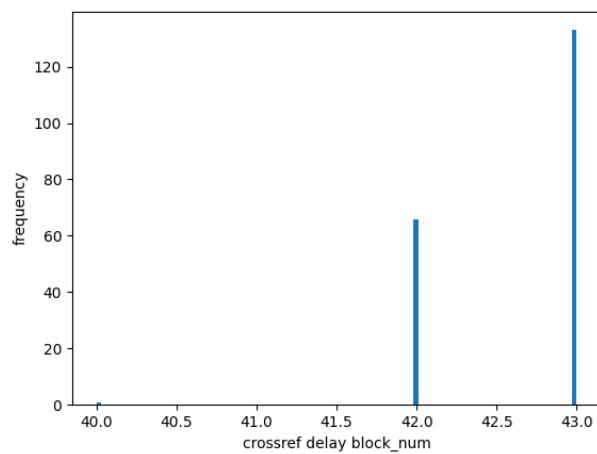


図 390 pht/experiment-win/1207/BC/30/1207-70D-PC7ALL-DIF1-INT1-30histogram-delay-blocknum.eps

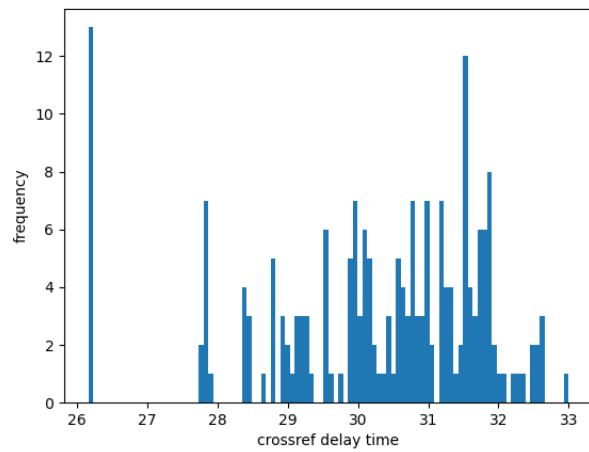


図 391 pht/experiment-win/1207/BC/30/1207-70D-PC7ALL-DIF1-INT1-30histogram-delay-time.eps

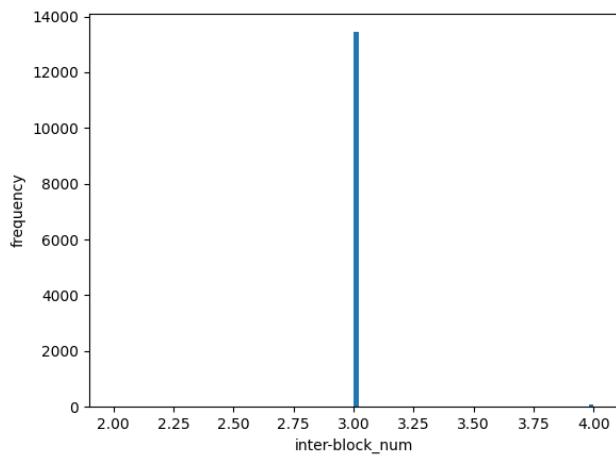


図 392 pht/experiment-win/1207/BC/30/1207-70D-PC7ALL-DIF1-INT1-30histogram-inter-blocknum.eps

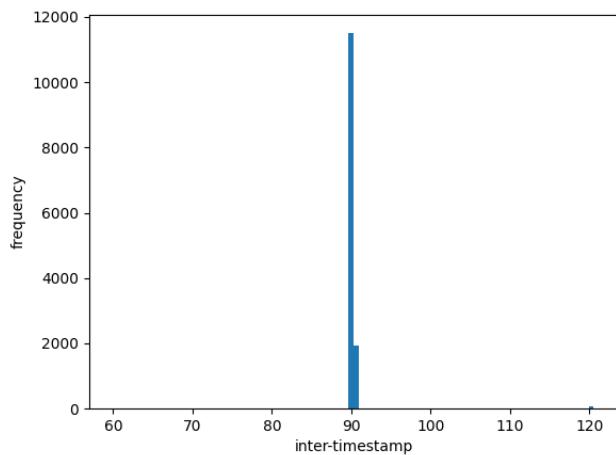


図 393 pht/experiment-win/1207/BC/30/1207-70D-PC7ALL-DIF1-INT1-30histogram-inter-timestamp.eps

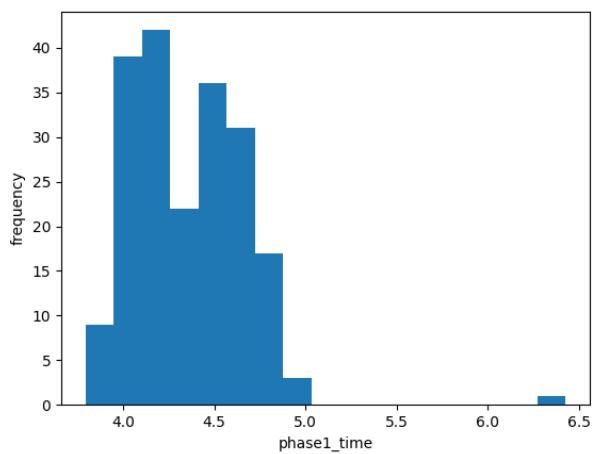


図 394 pht/experiment-win/1207/phase1/1207-70D-PC7ALL-DIF1-INT1-30Phase1-time50051.eps

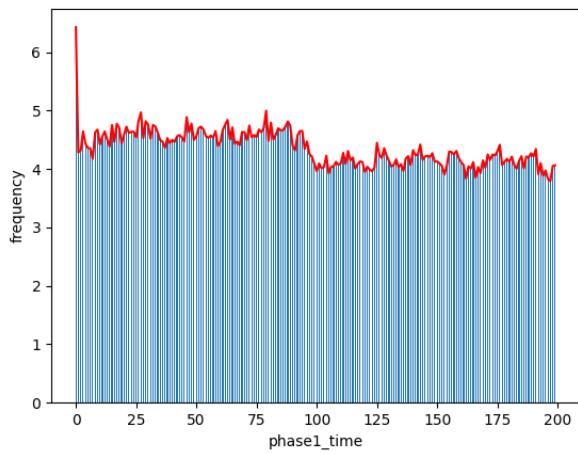


図 395 pht/experiment-win/1207/phase1/1207-70D-PC7ALL-DIF1-INT1-30Phase1-time50051-in-chronological-order.eps

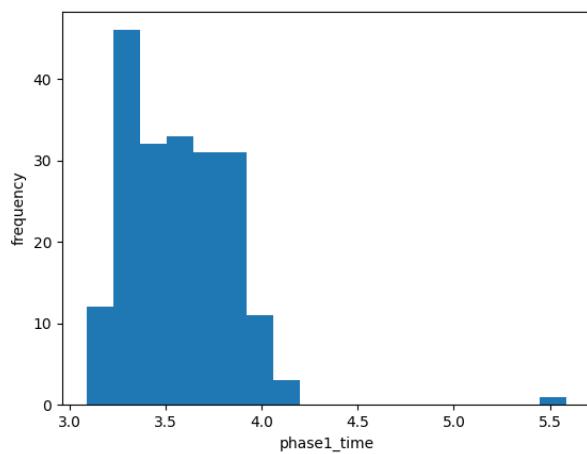


図 396 pht/experiment-win/1207/phase1/1207-70D-PC7ALL-DIF1-INT1-30Phase1-time50053.eps

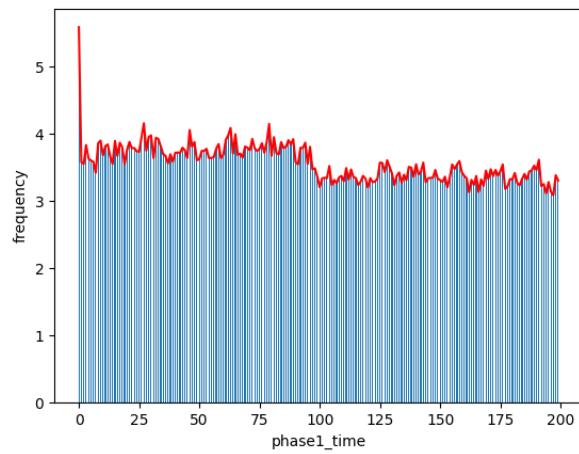


図 397 pht/experiment-win/1207/phase1/1207-70D-PC7ALL-DIF1-INT1-30Phase1-time50053-in-chronological-order.eps

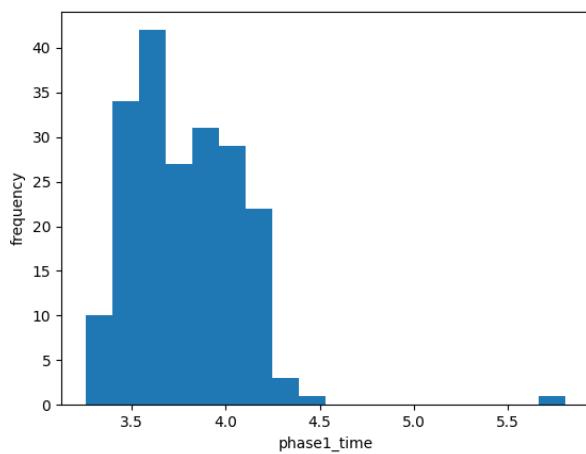


図 398 pht/experiment-win/1207/phase1/1207-70D-PC7ALL-DIF1-INT1-30Phase1-time50055.eps

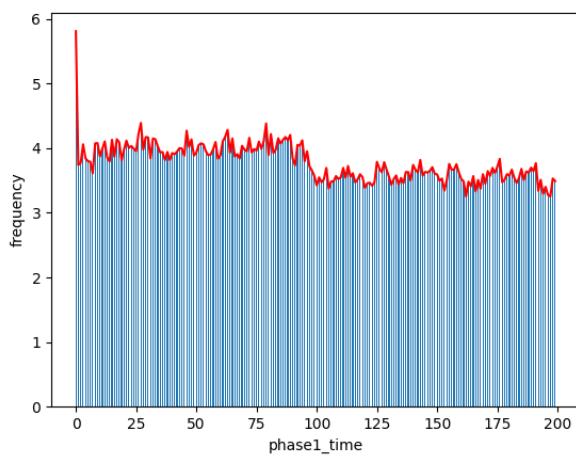


図 399 pht/experiment-win/1207/phase1/1207-70D-PC7ALL-DIF1-INT1-30Phase1-time50055-in-chronological-order.eps

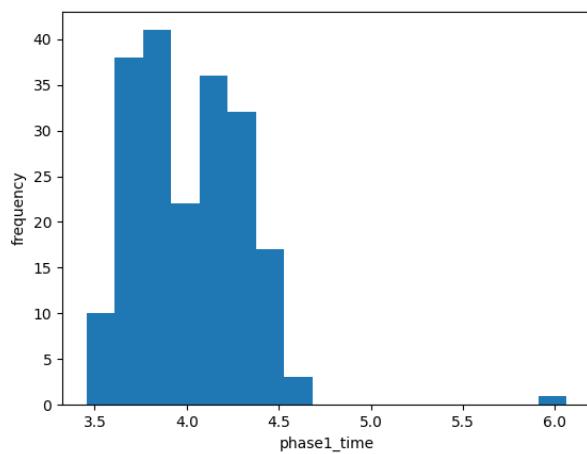


図 400 pht/experiment-win/1207/phase1/1207-70D-PC7ALL-DIF1-INT1-30Phase1-time50057.eps

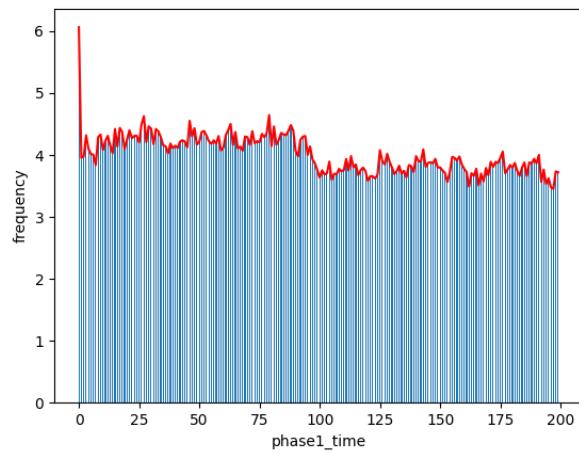


図 401 pht/experiment-win/1207/phase1/1207-70D-PC7ALL-DIF1-INT1-30Phase1-time50057-in-chronological-order.eps

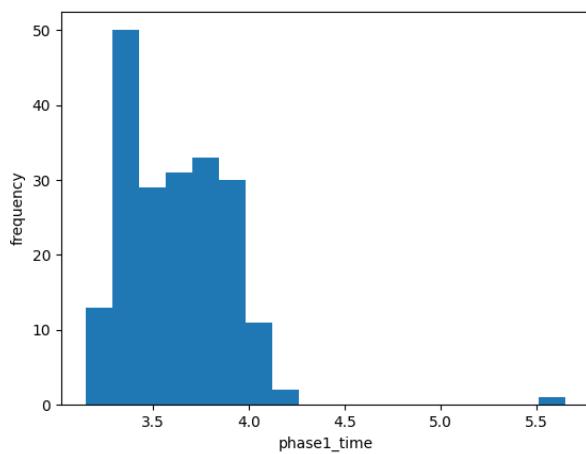


図 402 pht/experiment-win/1207/phase1/1207-70D-PC7ALL-DIF1-INT1-30Phase1-time50059.eps

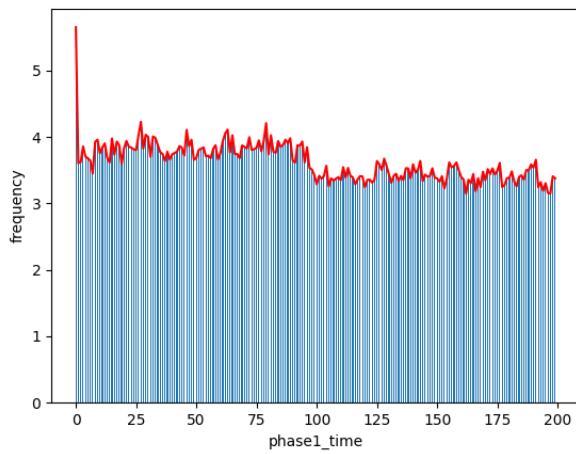


図 403 pht/experiment-win/1207/phase1/1207-70D-PC7ALL-DIF1-INT1-30Phase1-time50059-in-chronological-order.eps

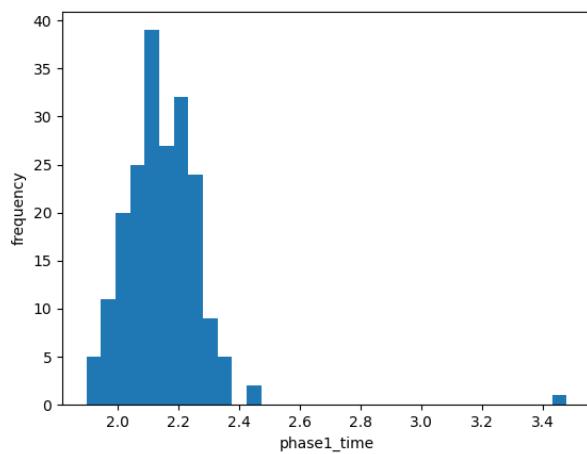


図 404 pht/experiment-win/1207/phase1/1207-70D-PC7ALL-DIF1-INT1-30Phase1-time50181.eps

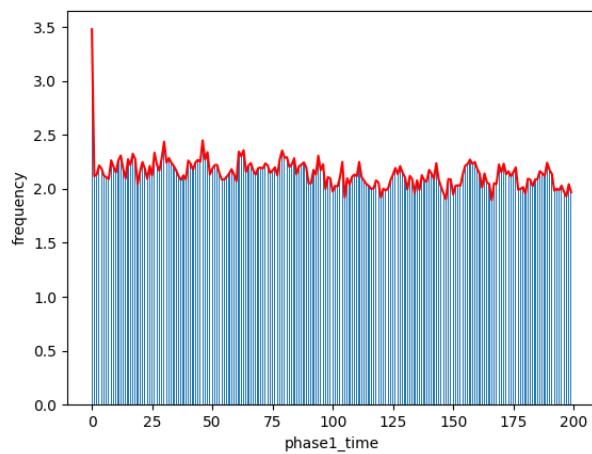


図 405 pht/experiment-win/1207/phase1/1207-70D-PC7ALL-DIF1-INT1-30Phase1-time50181-in-chronological-order.eps

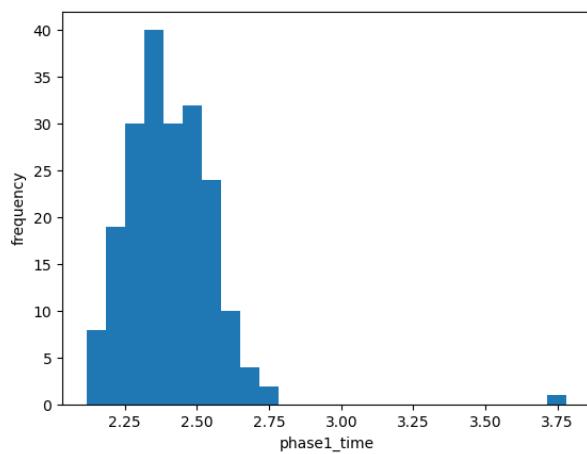


図 406 pht/experiment-win/1207/phase1/1207-70D-PC7ALL-DIF1-INT1-30Phase1-time50183.eps

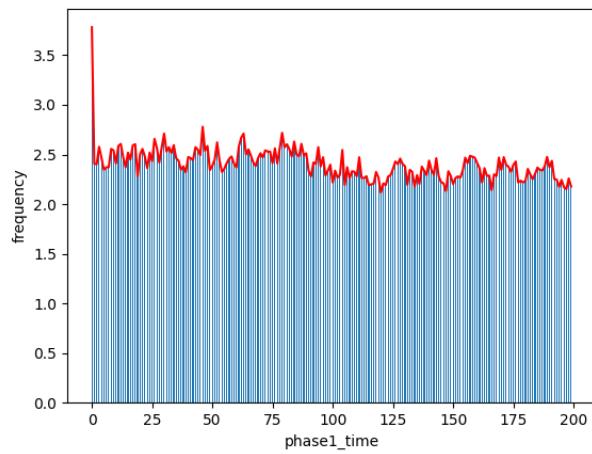


図 407 pht/experiment-win/1207/phase1/1207-70D-PC7ALL-DIF1-INT1-30Phase1-time50183-in-chronological-order.eps

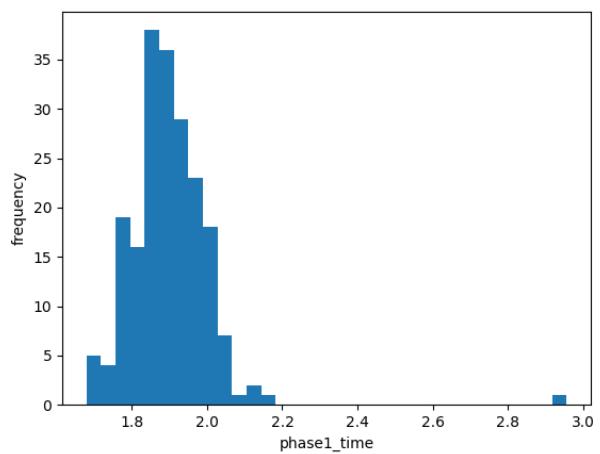


図 408 pht/experiment-win/1207/phase1/1207-70D-PC7ALL-DIF1-INT1-30Phase1-time50185.eps

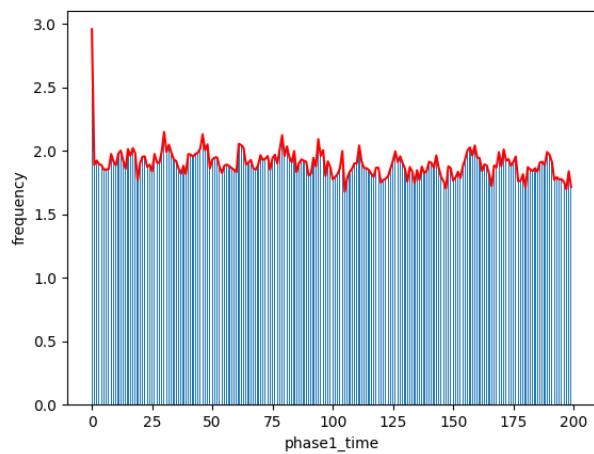


図 409 pht/experiment-win/1207/phase1/1207-70D-PC7ALL-DIF1-INT1-30Phase1-time50185-in-chronological-order.eps

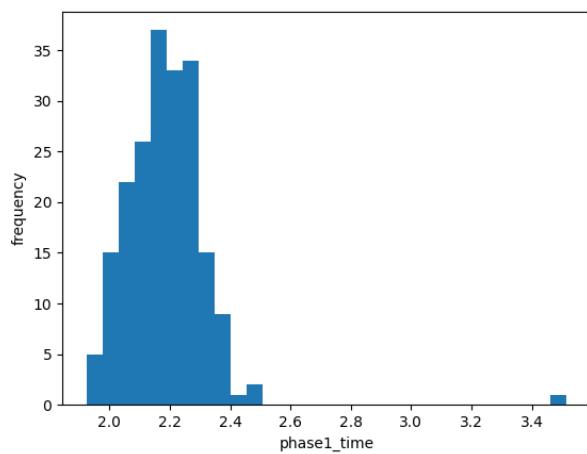


図 410 pht/experiment-win/1207/phase1/1207-70D-PC7ALL-DIF1-INT1-30Phase1-time50187.eps

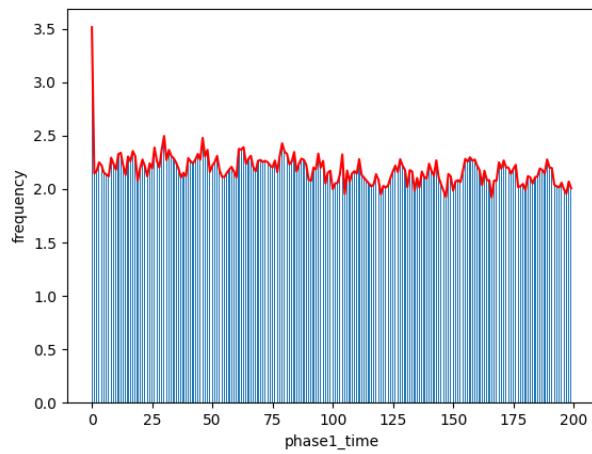


図 411 pht/experiment-win/1207/phase1/1207-70D-PC7ALL-DIF1-INT1-30Phase1-time50187-in-chronological-order.eps

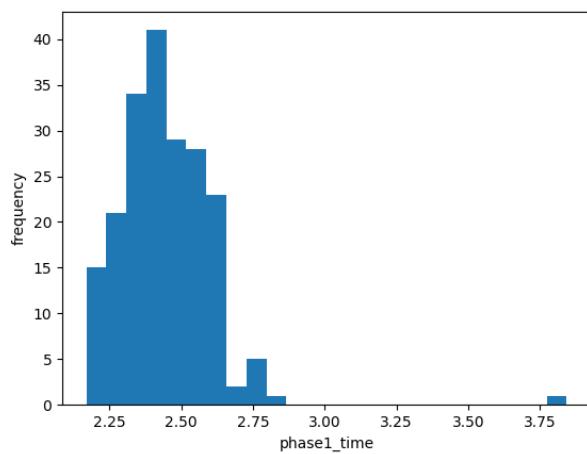


図 412 pht/experiment-win/1207/phase1/1207-70D-PC7ALL-DIF1-INT1-30Phase1-time50189.eps

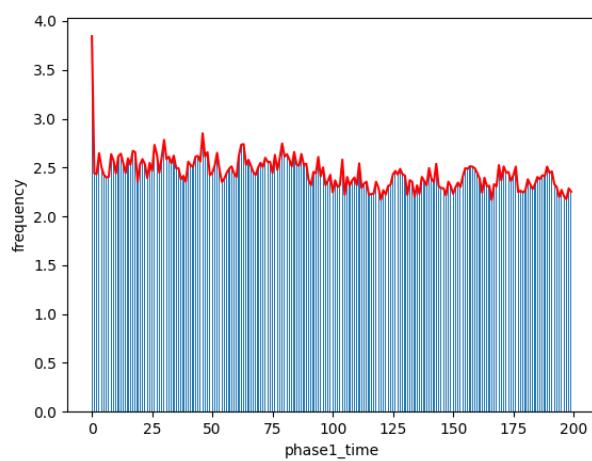


図 413 pht/experiment-win/1207/phase1/1207-70D-PC7ALL-DIF1-INT1-30Phase1-time50189-in-chronological-order.eps

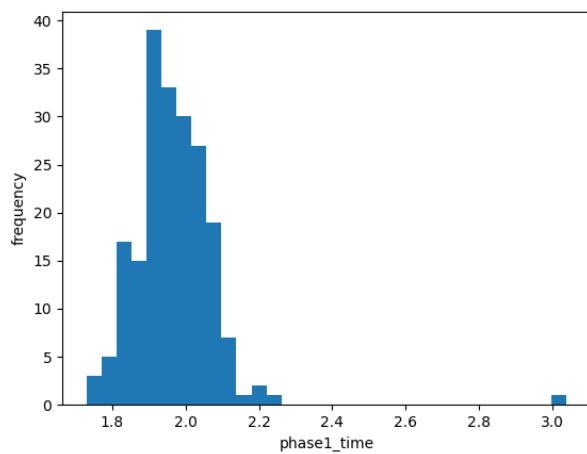


図 414 pht/experiment-win/1207/phase1/1207-70D-PC7ALL-DIF1-INT1-30Phase1-time50191.eps

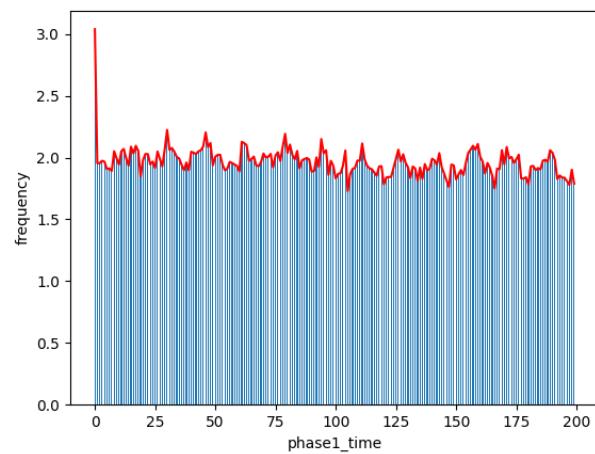


図 415 pht/experiment-win/1207/phase1/1207-70D-PC7ALL-DIF1-INT1-30Phase1-time50191-in-chronological-order.eps

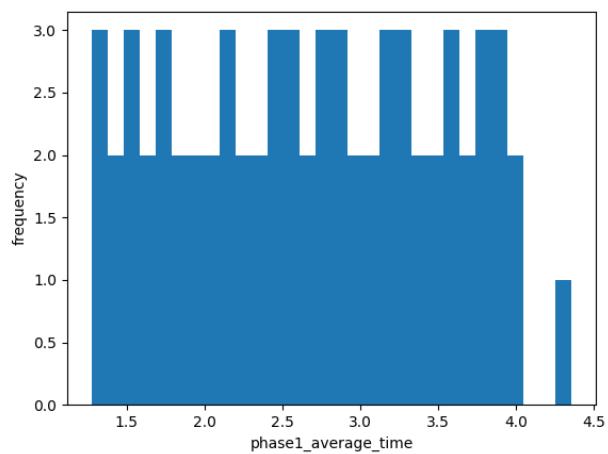


図 416 pht/experiment-win/1207/phase1/1207-70D-PC7ALL-DIF1-INT1-30analyze-phase1-average-time.eps

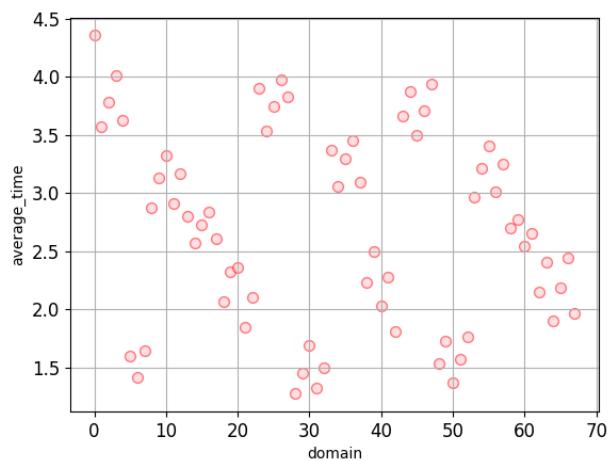


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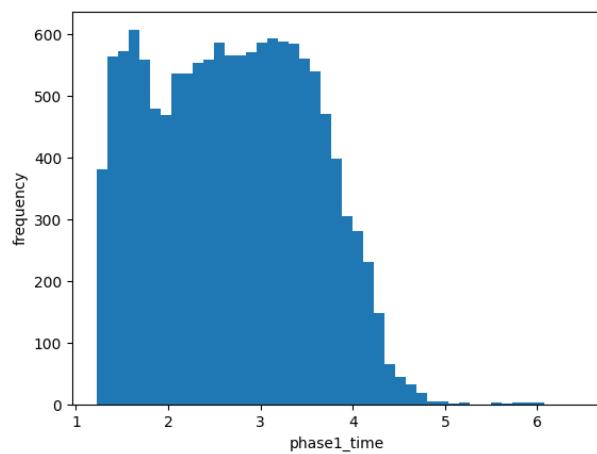


図 418 pht/experiment-win/1207/phase1/1207-70D-PC7ALL-DIF1-INT1-30analyze-phase1-time-auto.eps

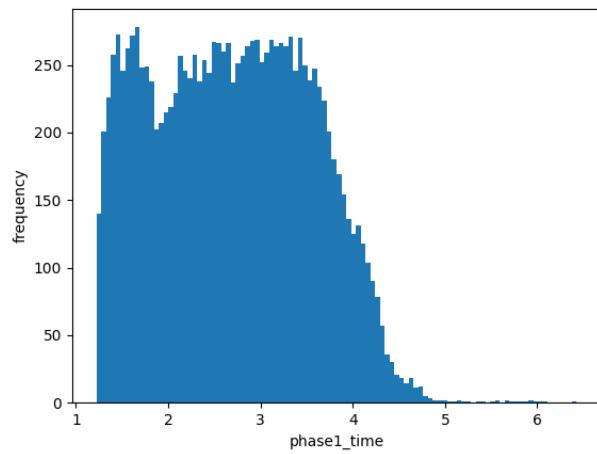


図 419 pht/experiment-win/1207/phase1/1207-70D-PC7ALL-DIF1-INT1-30analyze-phase1-time-bins100.eps

14 1208-75D-PC3ALL-DIF1-INT1-30

14 章にて報告する条件を表 13 に示す。

表 13 設定値の一覧

設定項目	設定値
起動ノード数 (ドメイン数 D)	70 [ノード]
履歴交差間隔	90 [秒]
履歴交差回数	200 [回]
ブロック不生成間隔	0.5[秒]
ブロック生成難易度	1 [0x]
PC 内同時起動台数	7 [台]
使用台数	10 [台]
ブロック不生成間隔 : 30 秒	70 [ノード]
ブロック不生成間隔 : 1 秒	0 [ノード]

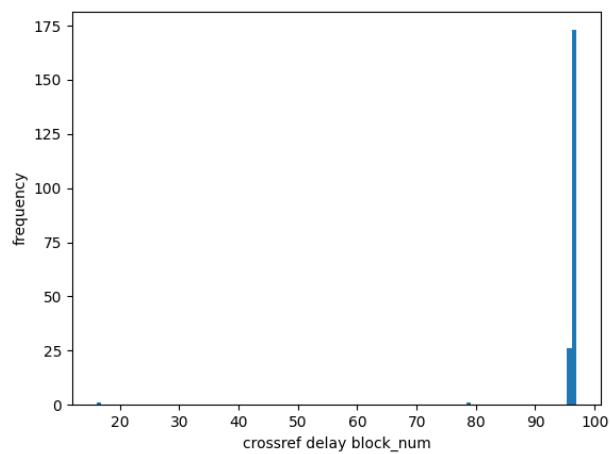


図 420 pht/experiment-win/1208/BC/1208-75D-PC3ALL-DIF1-INT1-30histogram-delay-blocknum.eps

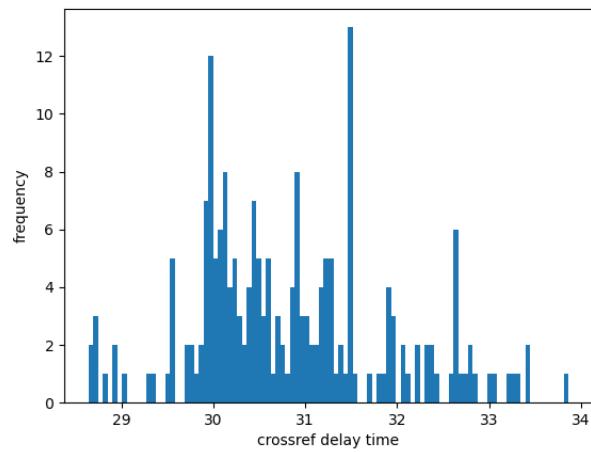


図 421 pht/experiment-win/1208/BC/1208-75D-PC3ALL-DIF1-INT1-30histogram-delay-time.eps

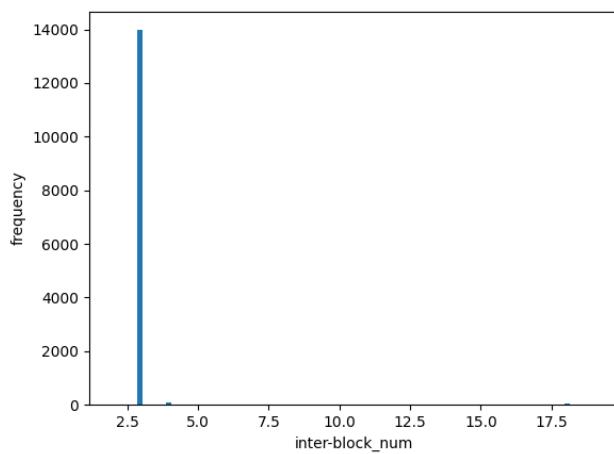


図 422 pht/experiment-win/1208/BC/1208-75D-PC3ALL-DIF1-INT1-30histogram-inter-blocknum.eps

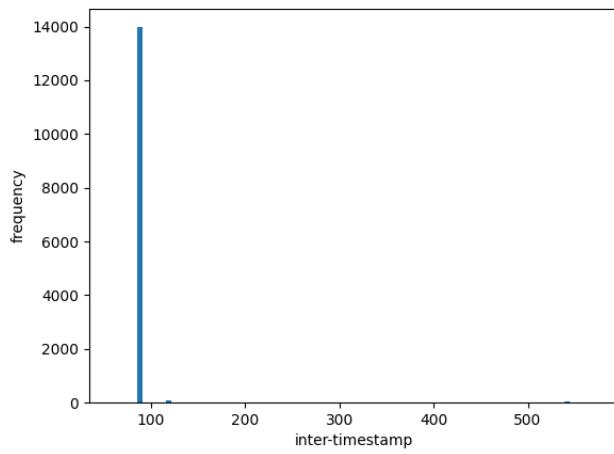


図 423 pht/experiment-win/1208/BC/1208-75D-PC3ALL-DIF1-INT1-30histogram-inter-timestamp.eps

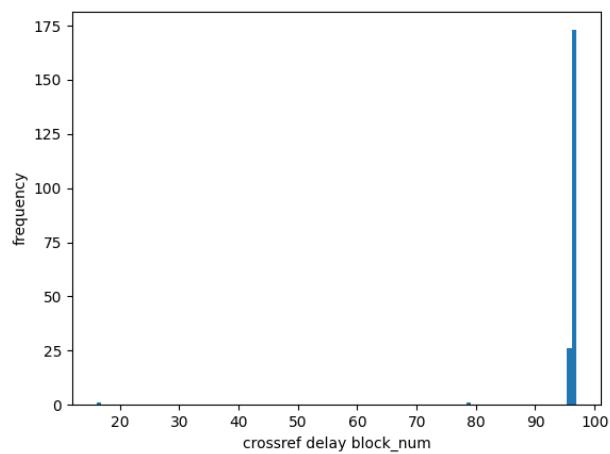


図 424 pht/experiment-win/1208/BC/30/1208-75D-PC3ALL-DIF1-INT1-30histogram-delay-blocknum.eps

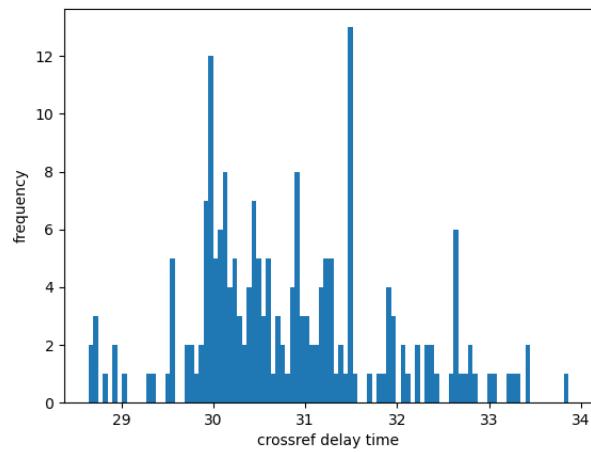


図 425 pht/experiment-win/1208/BC/30/1208-75D-PC3ALL-DIF1-INT1-30histogram-delay-time.eps

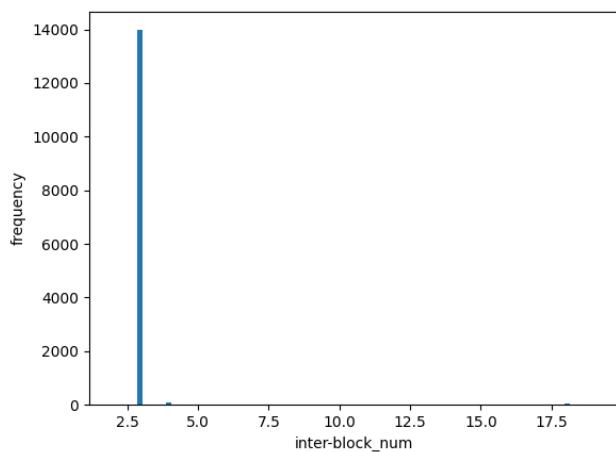


図 426 pht/experiment-win/1208/BC/30/1208-75D-PC3ALL-DIF1-INT1-30histogram-inter-blocknum.eps

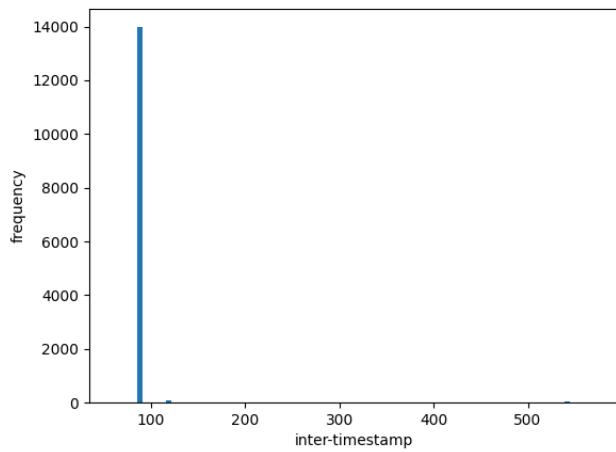


図 427 pht/experiment-win/1208/BC/30/1208-75D-PC3ALL-DIF1-INT1-30histogram-inter-timestamp.eps

15 1212-1-70D-PC7ALL-DIF1-INT1-30

15 章にて報告する条件を表 14 に示す.

表 14 設定値の一覧

設定項目	設定値
起動ノード数 (ドメイン数 D)	70 [ノード]
履歴交差間隔	90 [秒]
履歴交差回数	200 [回]
ブロック不生成間隔	0.5[秒]
ブロック生成難易度	1 [0x]
PC 内同時起動台数	7 [台]
使用台数	10 [台]
ブロック不生成間隔 : 30 秒	70 [ノード]
ブロック不生成間隔 : 1 秒	0 [ノード]

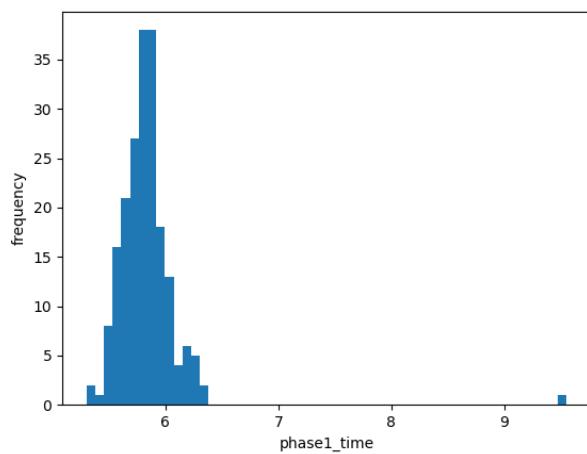


図 428 pht/experiment-win/1208/phase1/1212-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50051.eps

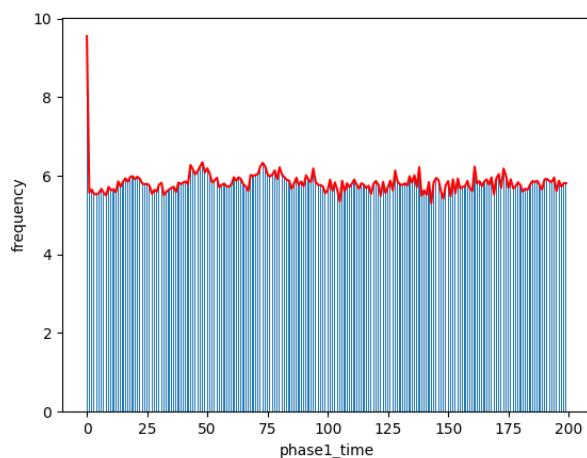


図 429 pht/experiment-win/1208/phase1/1212-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50051-in-chronological-order.eps

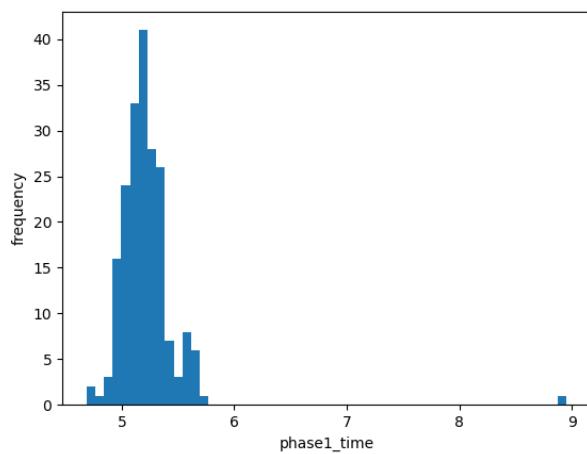


図 430 pht/experiment-win/1208/phase1/1212-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50053.eps

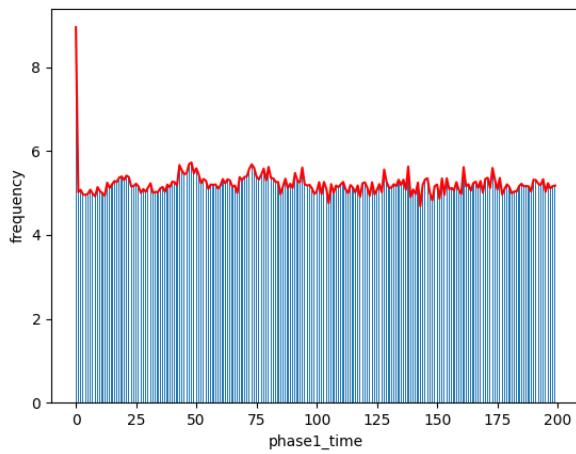


図 431 pht/experiment-win/1208/phase1/1212-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50053-in-chronological-order.eps

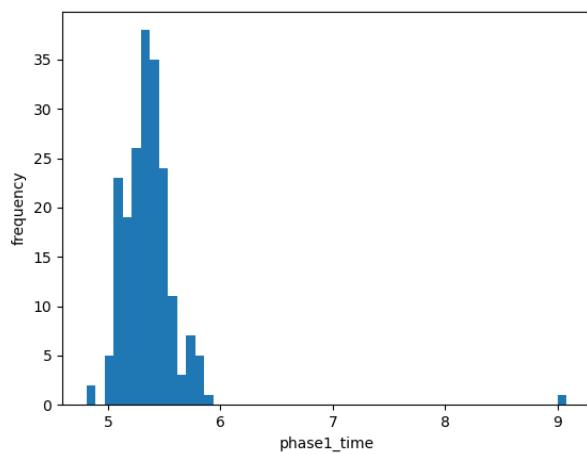


図 432 pht/experiment-win/1208/phase1/1212-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50055.eps

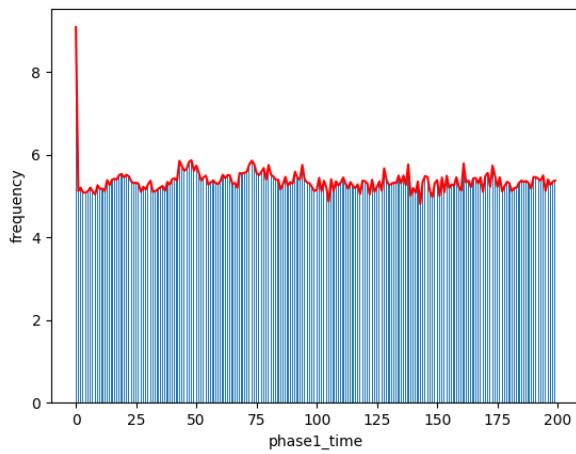


図 433 pht/experiment-win/1208/phase1/1212-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50055-in-chronological-order.eps

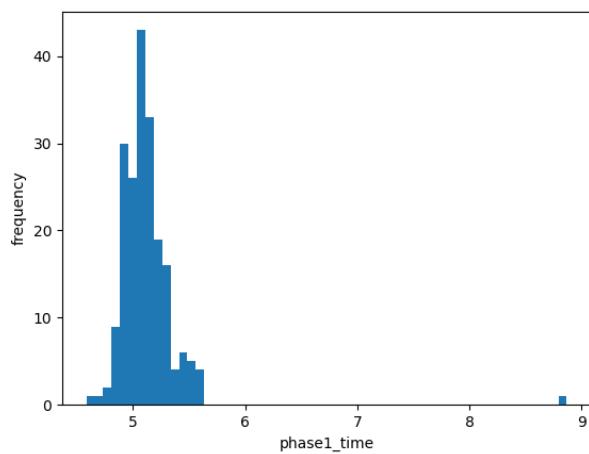


図 434 pht/experiment-win/1208/phase1/1212-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50059.eps

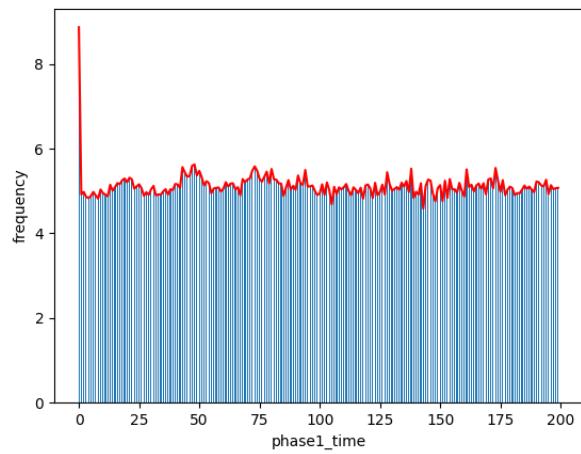


図 435 pht/experiment-win/1208/phase1/1212-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50059-in-chronological-order.eps

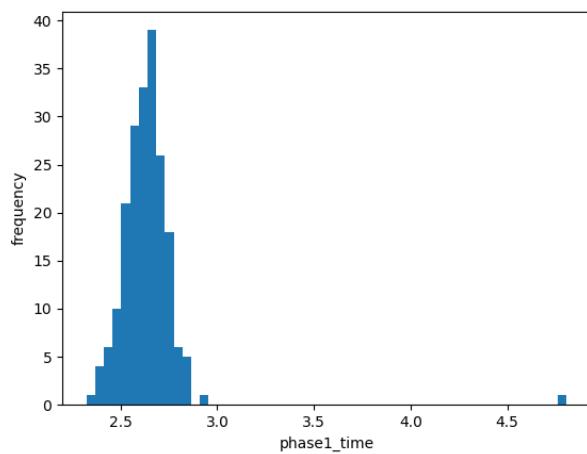


図 436 pht/experiment-win/1208/phase1/1212-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50191.eps

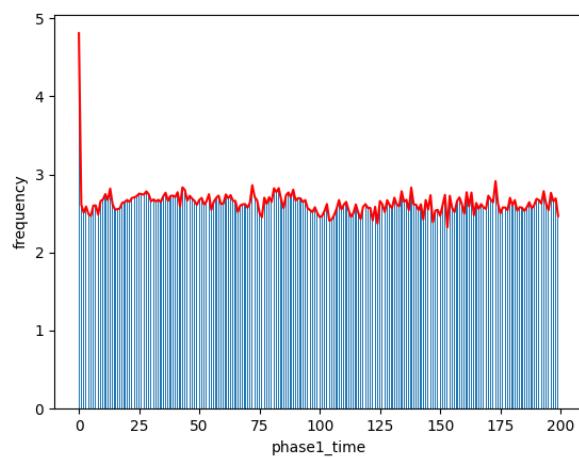


図 437 pht/experiment-win/1208/phase1/1212-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50191-in-chronological-order.eps

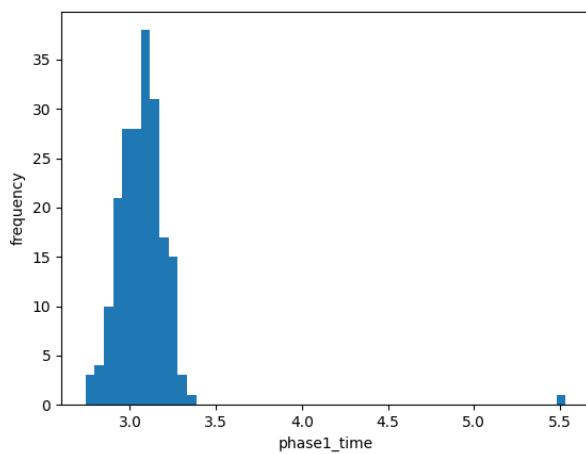


図 438 pht/experiment-win/1208/phase1/1212-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50193.eps

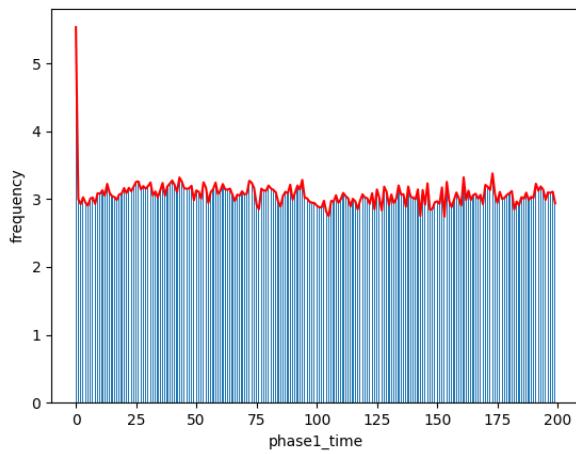


図 439 pht/experiment-win/1208/phase1/1212-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50193-in-chronological-order.eps

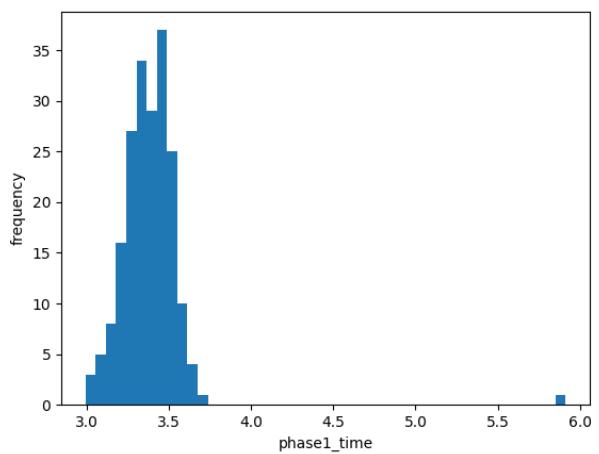


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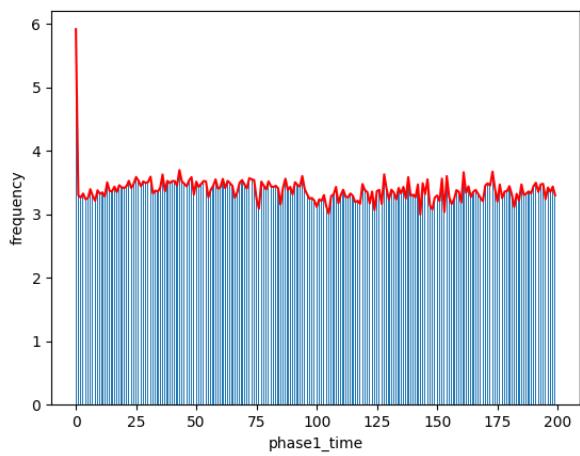


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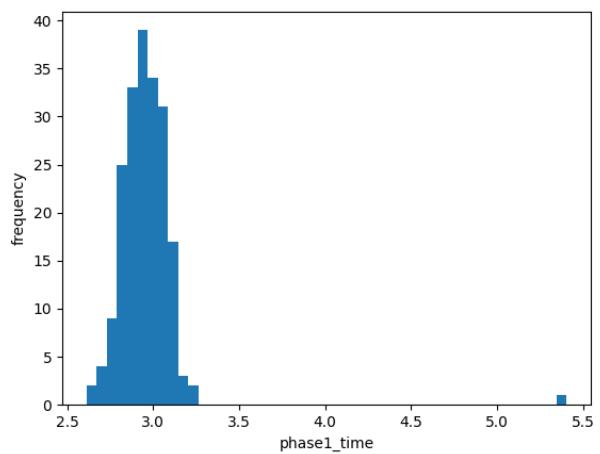


図 442 pht/experiment-win/1208/phase1/1212-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50197.eps

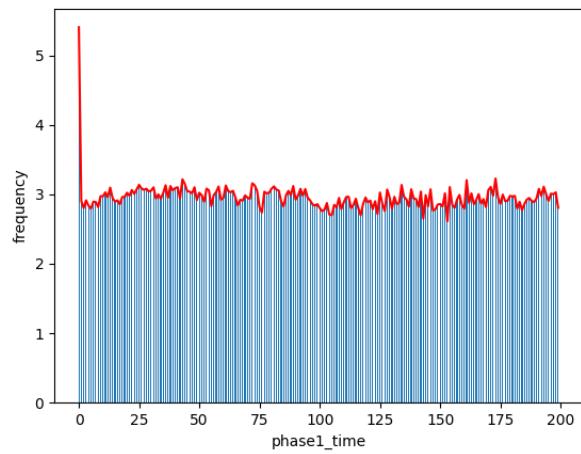


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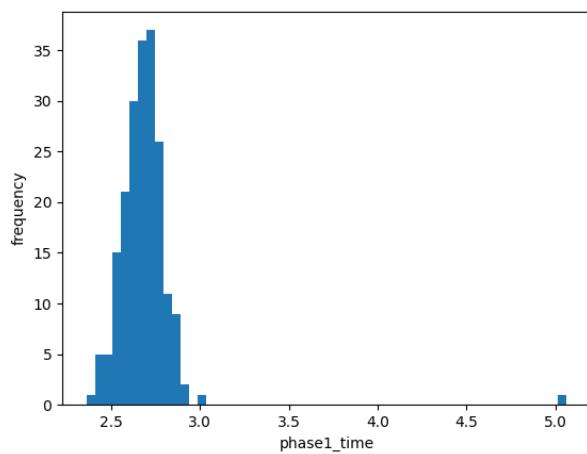


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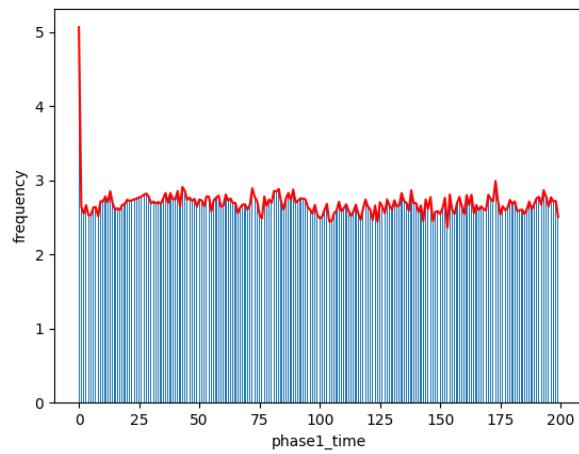


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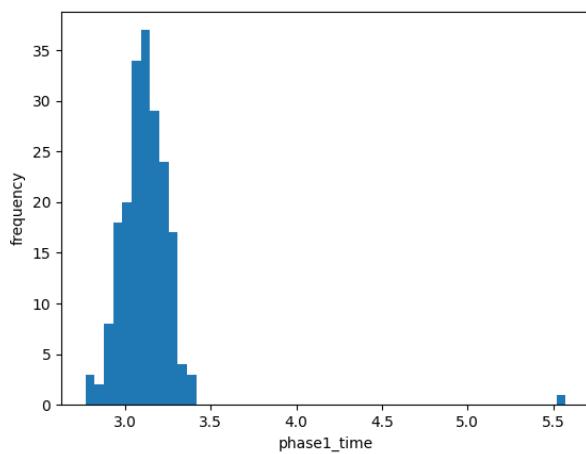


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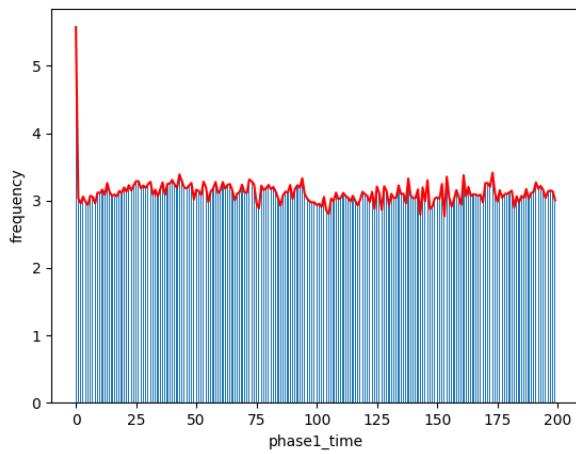


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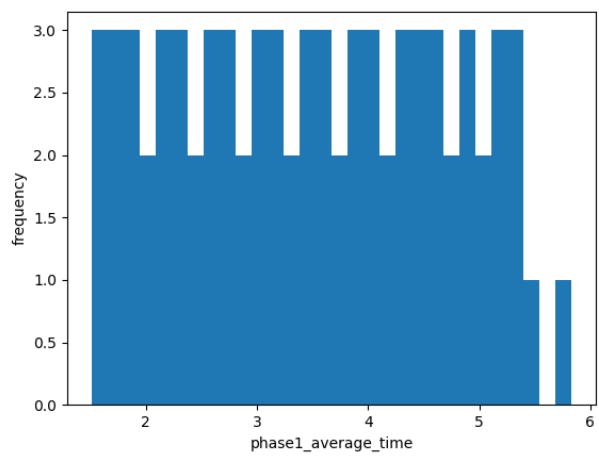


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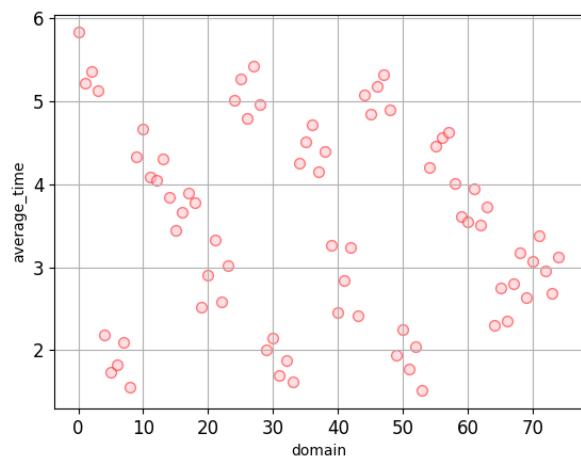


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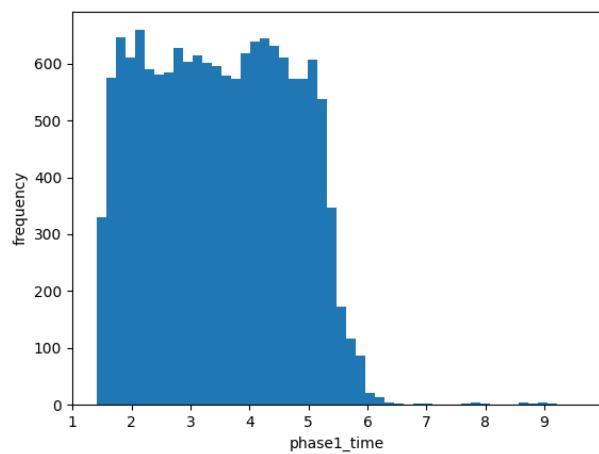


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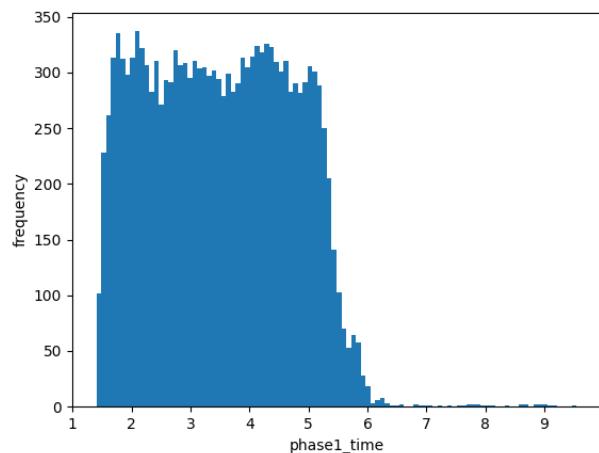


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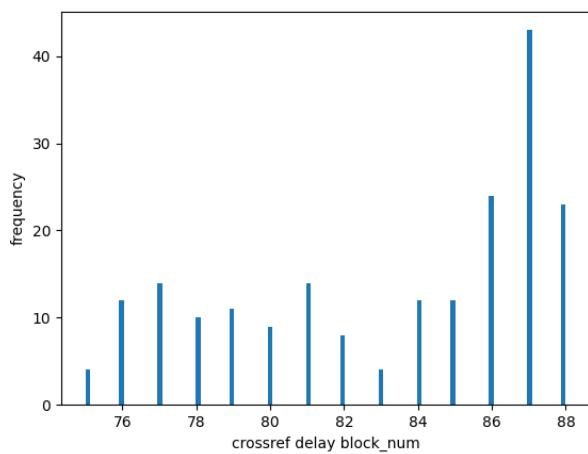


図 452 pht/experiment-win/1212-1/BC/1/1212-1-70D-PC7ALL-DIF1-INT1-30histogram-delay-blocknum.eps

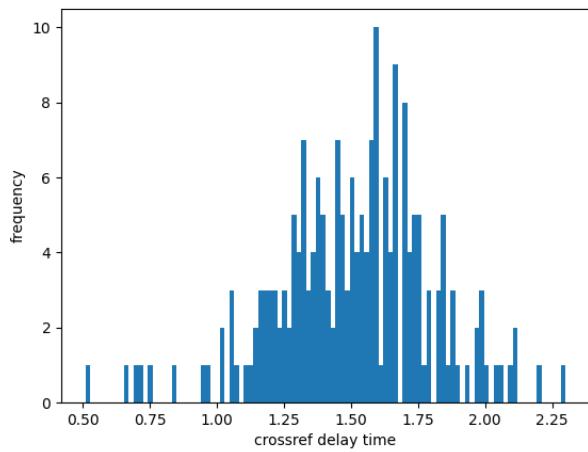


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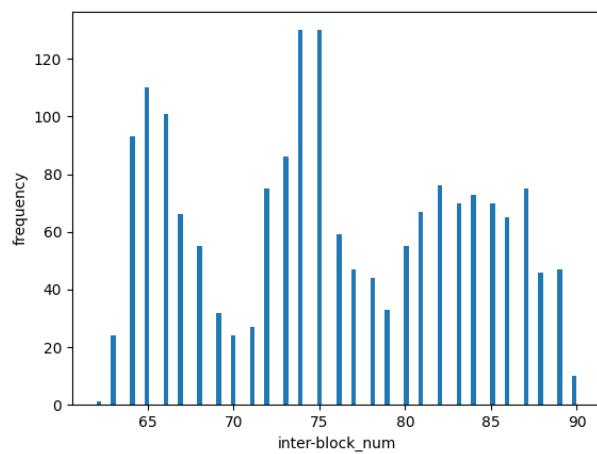


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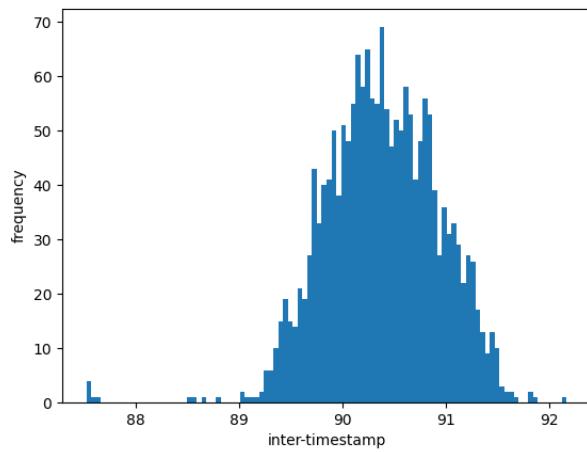


図 455 pht/experiment-win/1212-1/BC/1/1212-1-70D-PC7ALL-DIF1-INT1-30histogram-inter-timestamp.eps

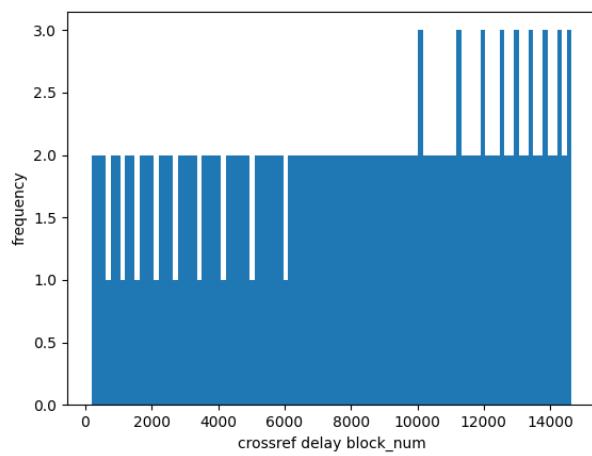


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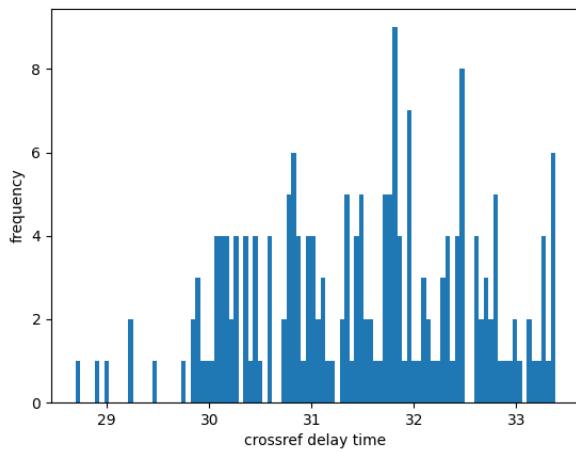


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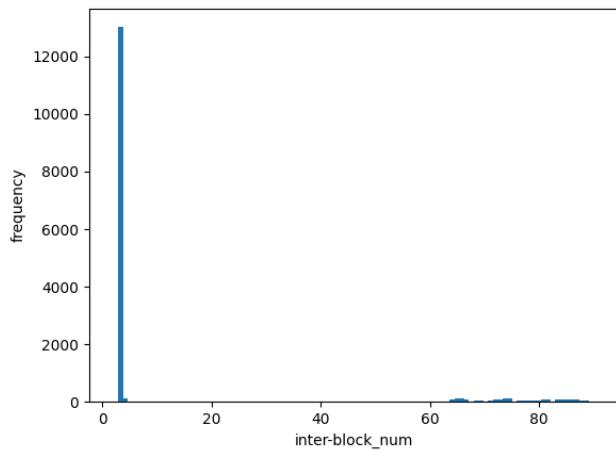


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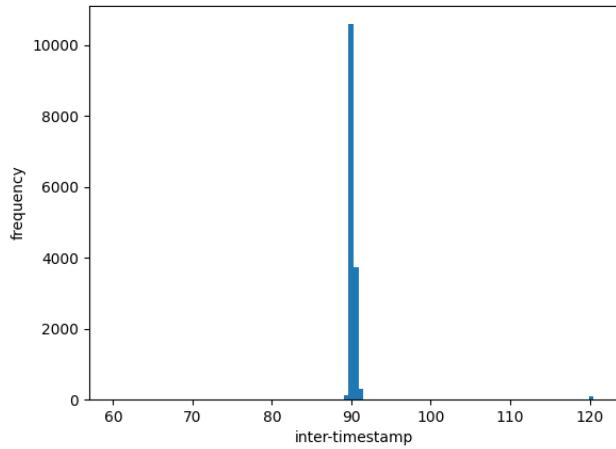


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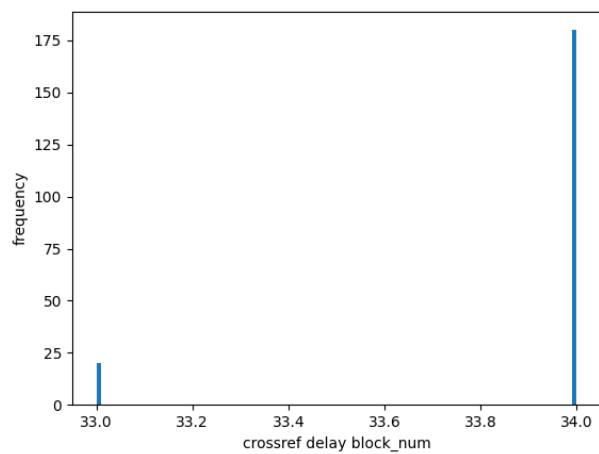


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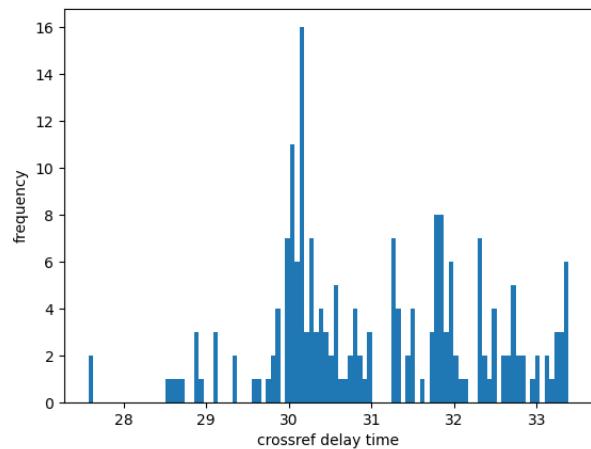


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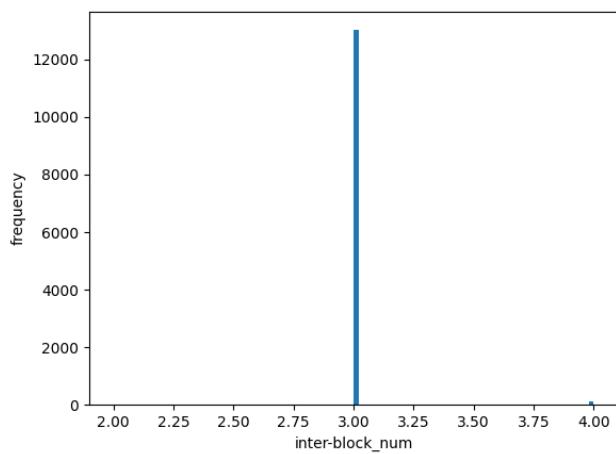


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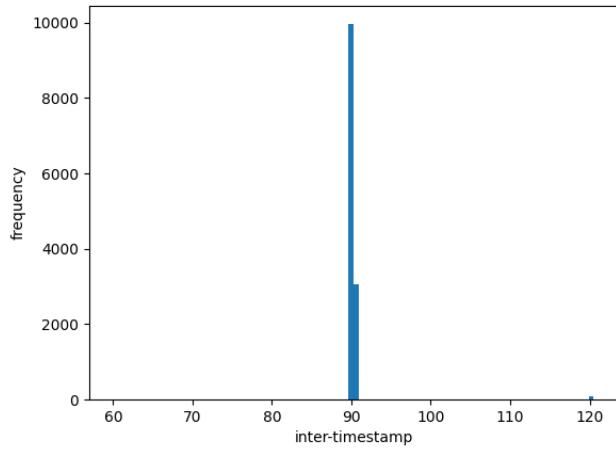


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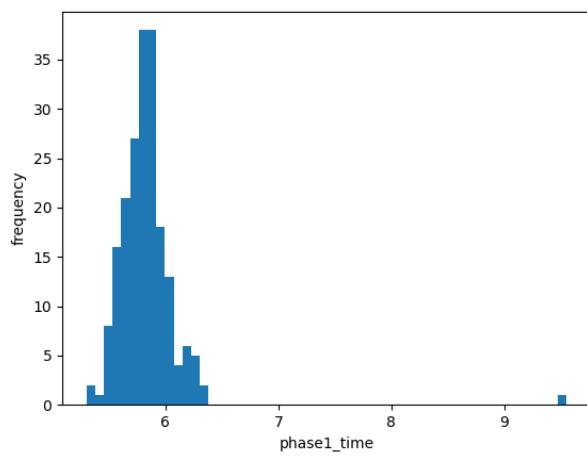


図 464 pht/experiment-win/1212-1/phase1/1212-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50051.eps

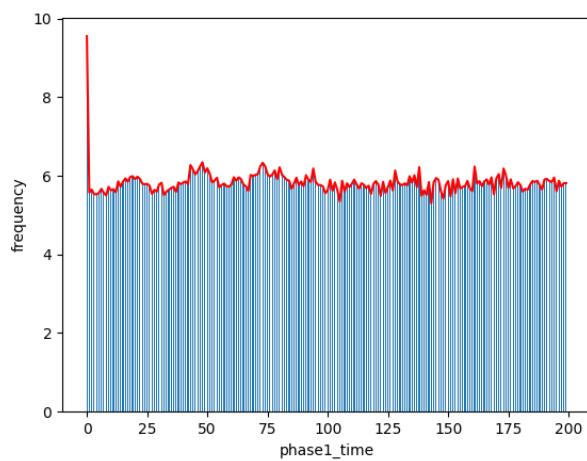


図 465 pht/experiment-win/1212-1/phase1/1212-1-70D-PC7ALL-DIF1-INT1-30Phase1-time50051-in-chronological-order.eps

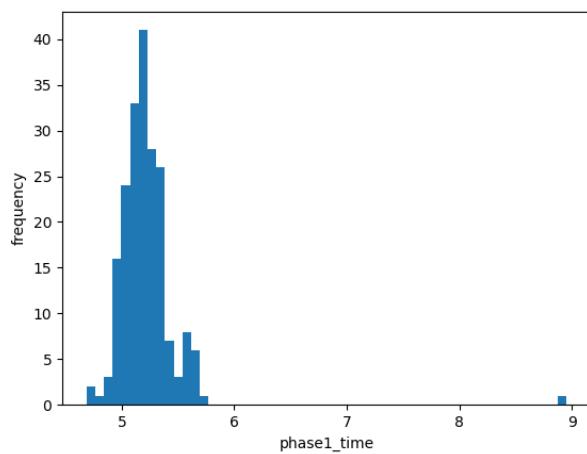


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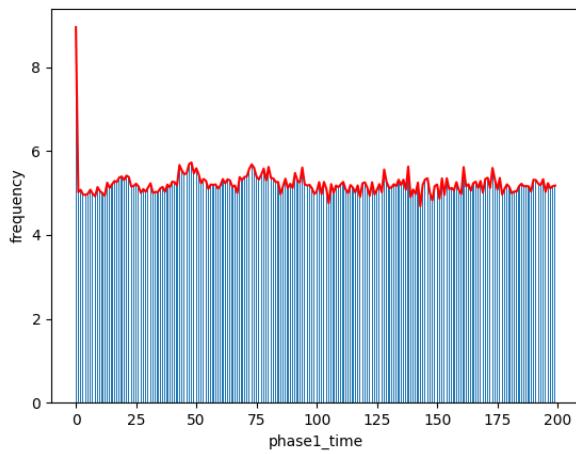


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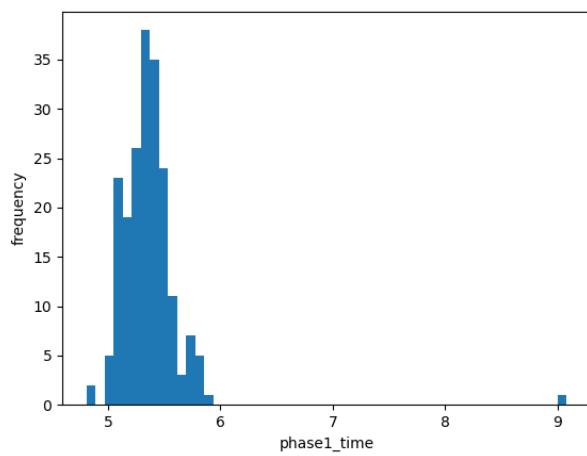


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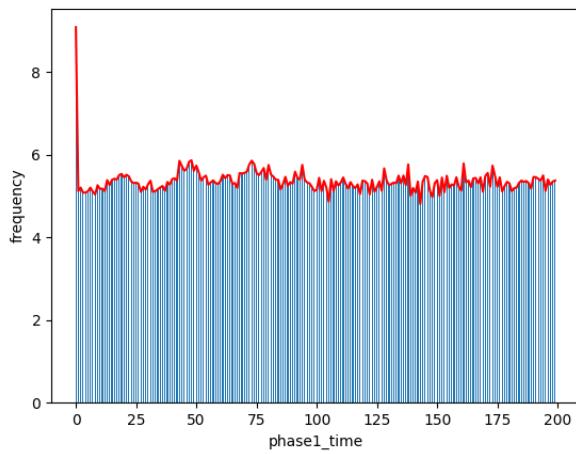


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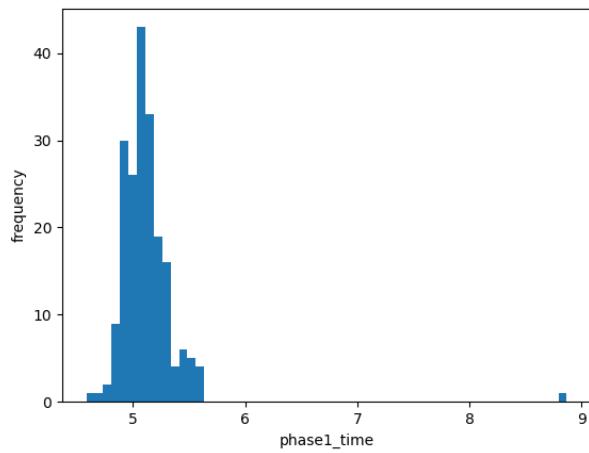


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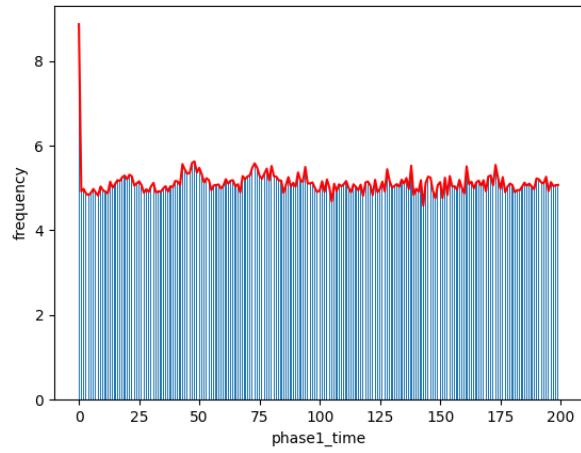


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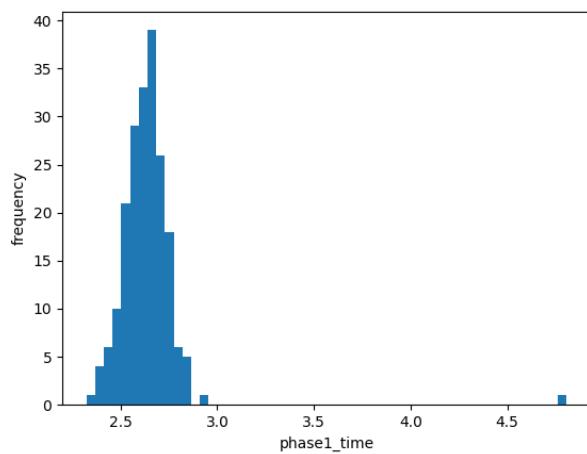


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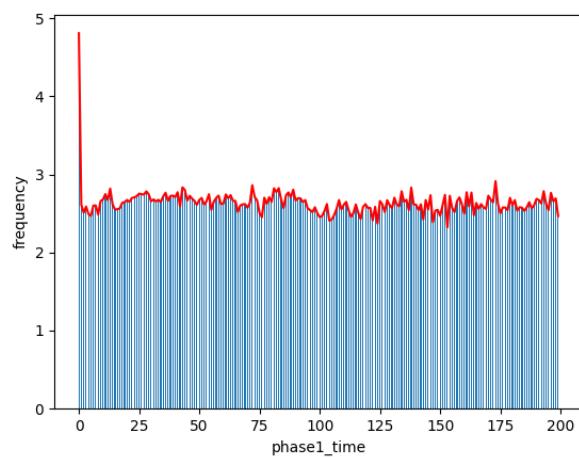


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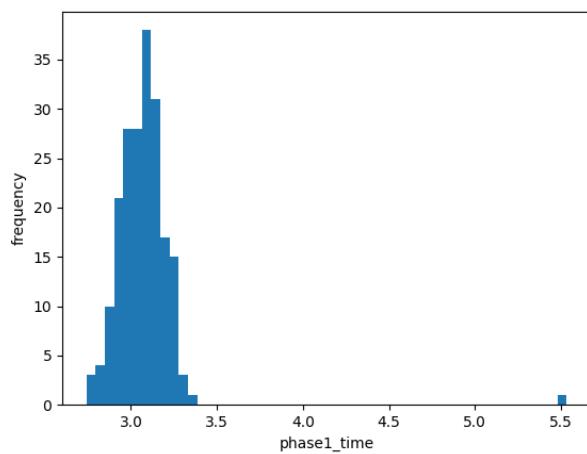


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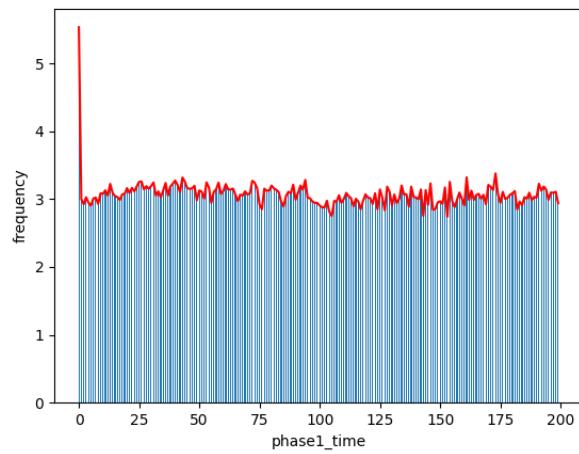


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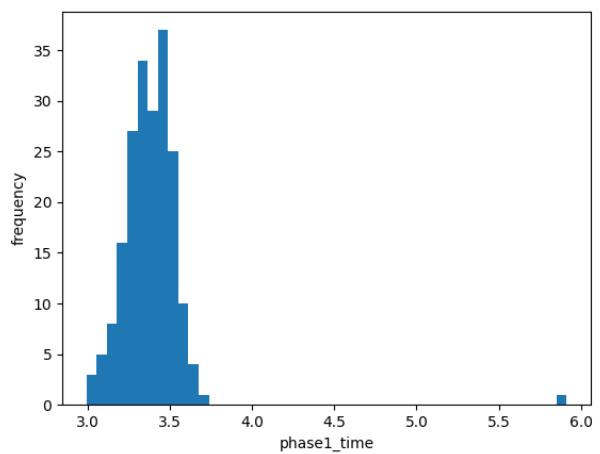


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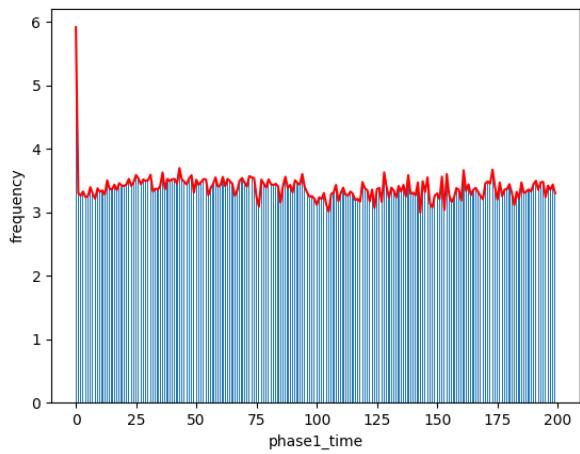


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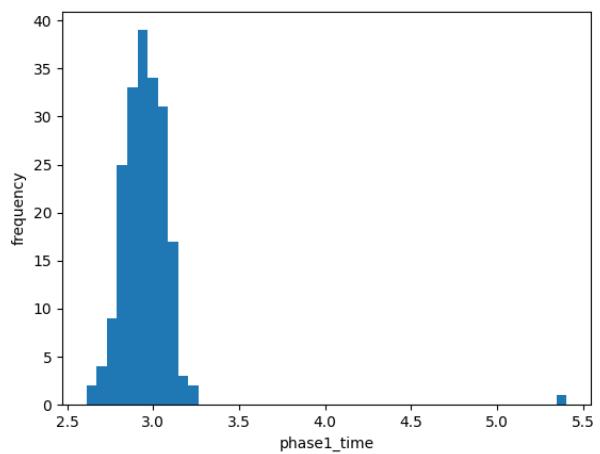


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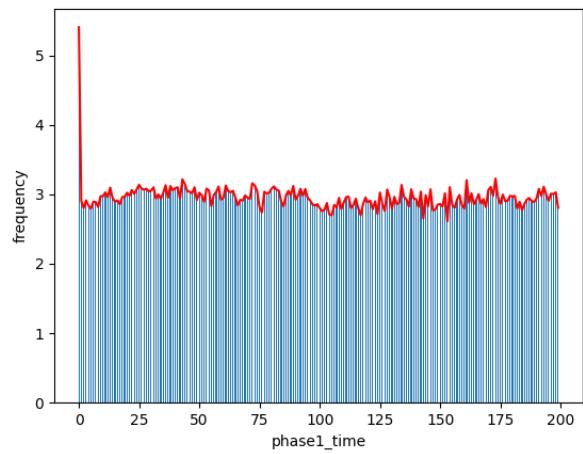


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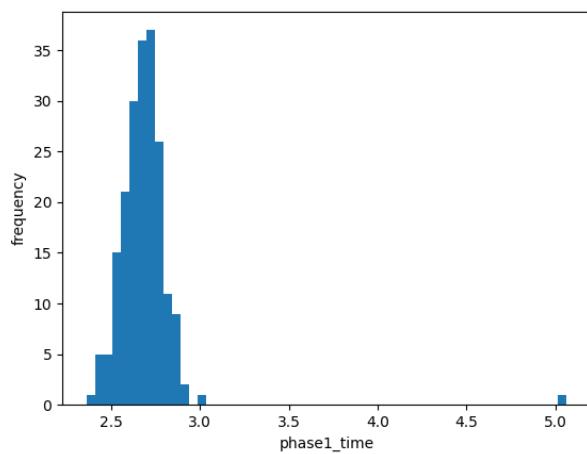


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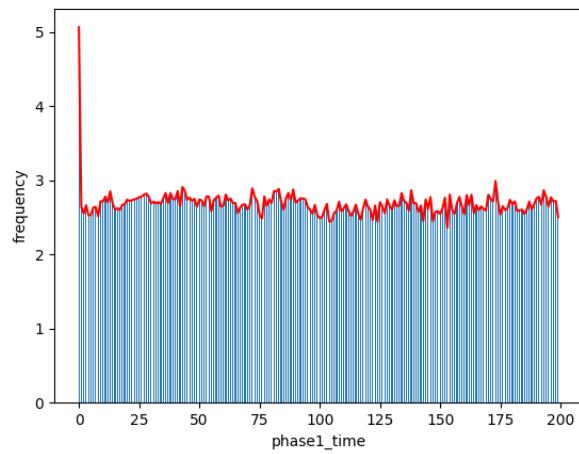


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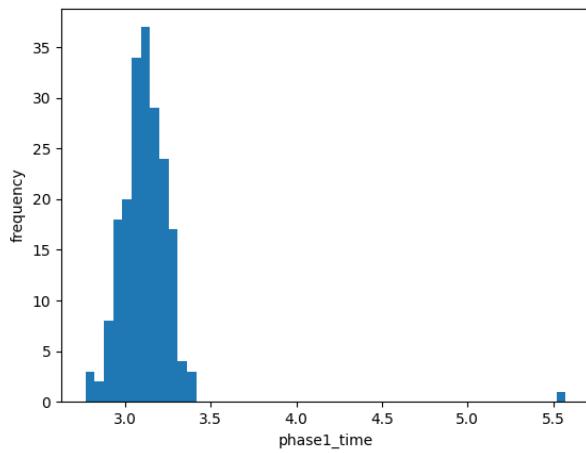


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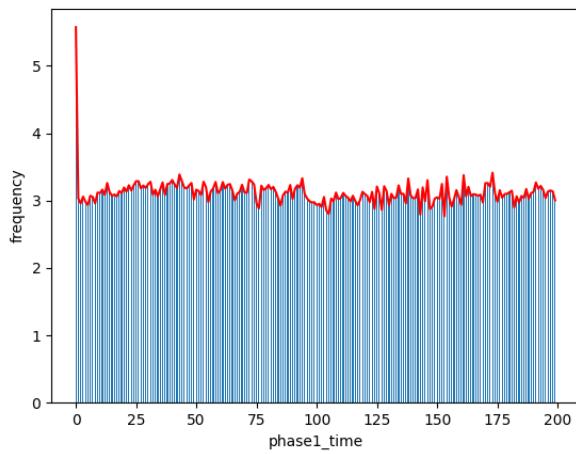


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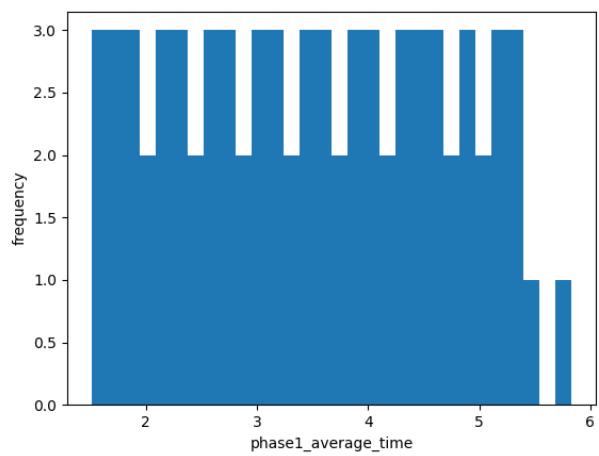


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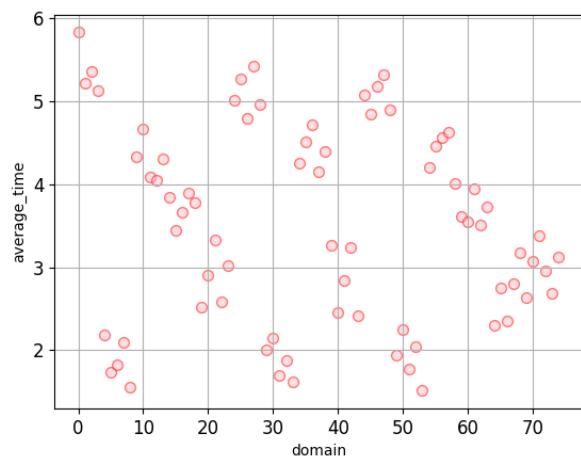


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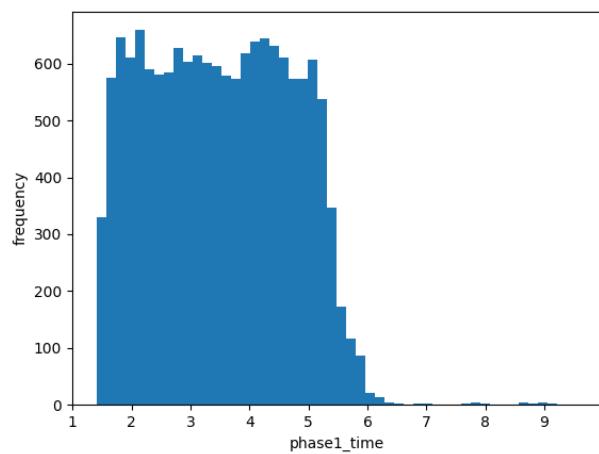


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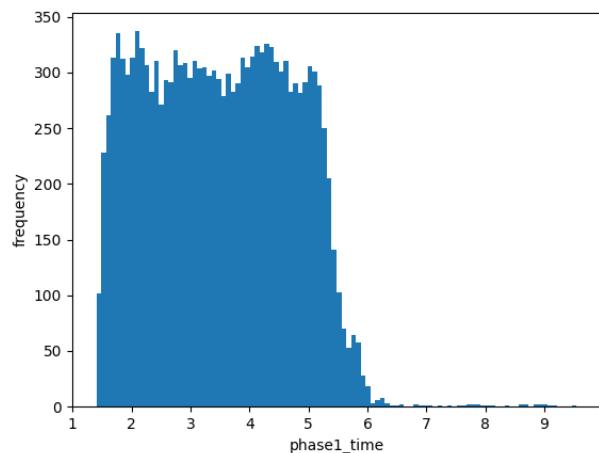


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16 1213-1-70D-PC7ALL-DIF1-INT1-30

16 章にて報告する条件を表 15 に示す.

表 15 設定値の一覧

設定項目	設定値
起動ノード数 (ドメイン数 D)	70 [ノード]
履歴交差間隔	90 [秒]
履歴交差回数	200 [回]
ブロック不生成間隔	0.5[秒]
ブロック生成難易度	1 [0x]
PC 内同時起動台数	7 [台]
使用台数	10 [台]
ブロック不生成間隔 : 30 秒	70 [ノード]
ブロック不生成間隔 : 1 秒	0 [ノード]

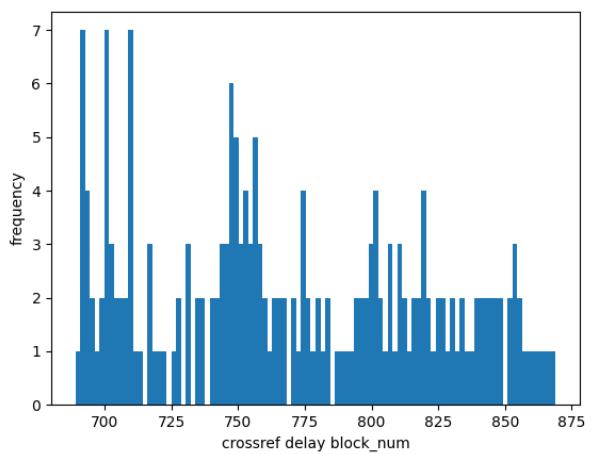


図 488 pht/experiment-win/1213-1/BC/1/1213-1-70D-PC7ALL-DIF1-INT1-30histogram-delay-blocknum.eps

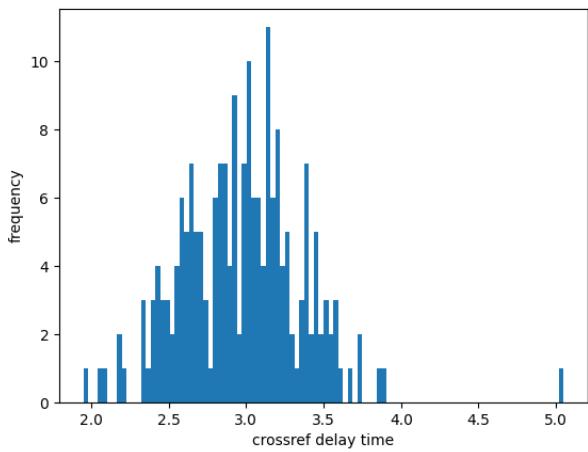


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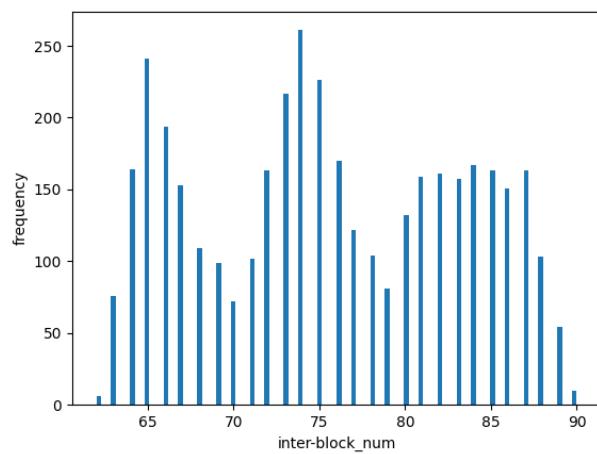


図 490 pht/experiment-win/1213-1/BC/1/1213-1-70D-PC7ALL-DIF1-INT1-30histogram-inter-blocknum.eps

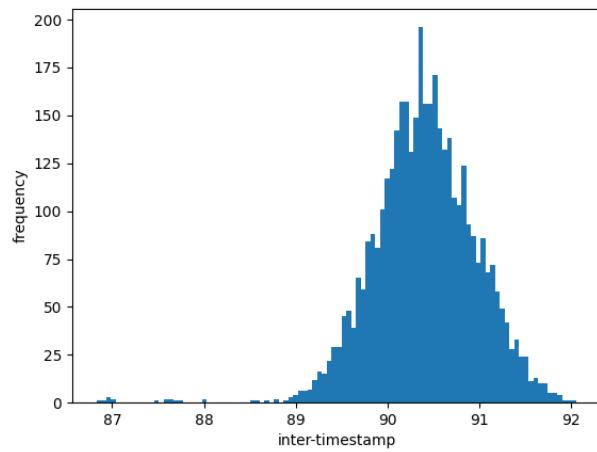


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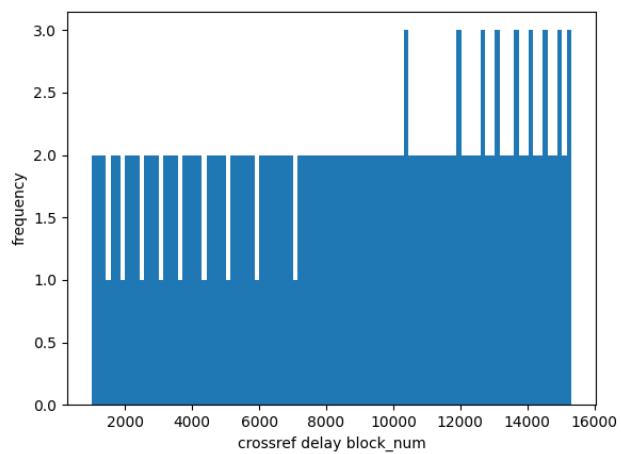


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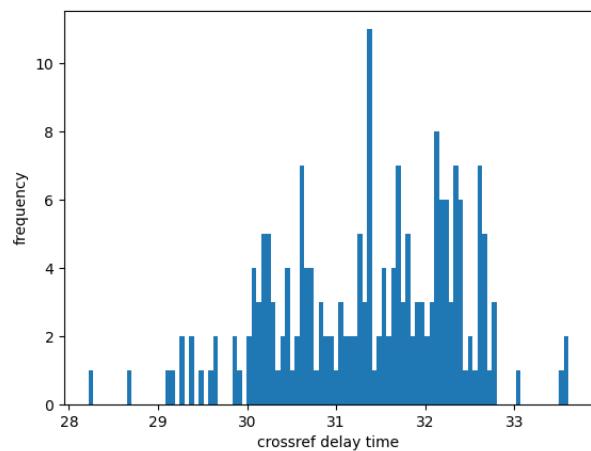


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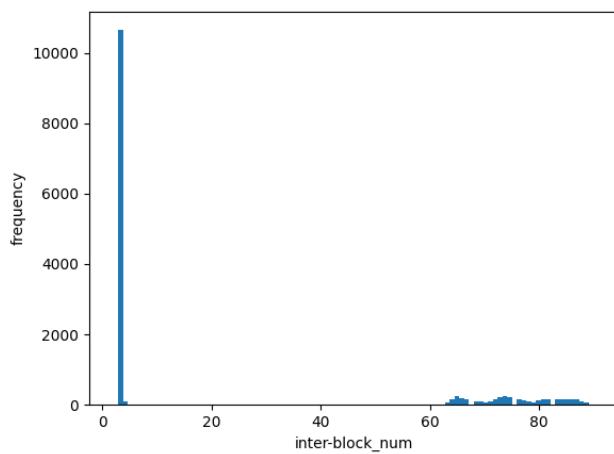


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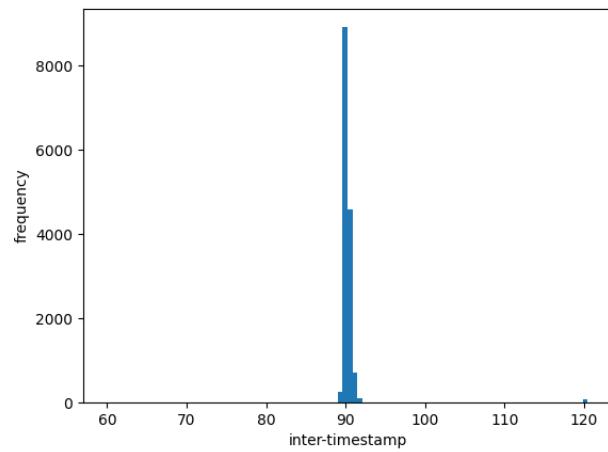


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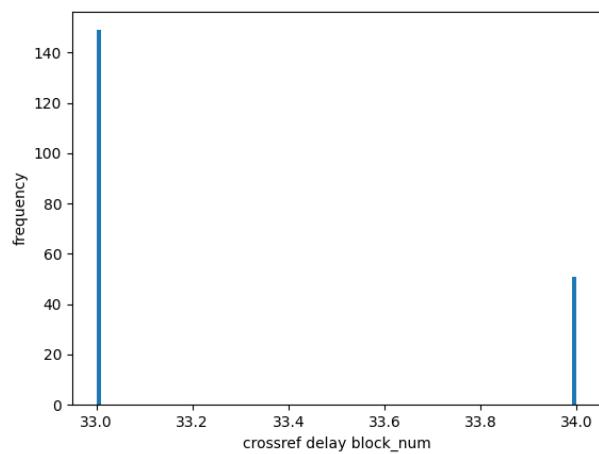


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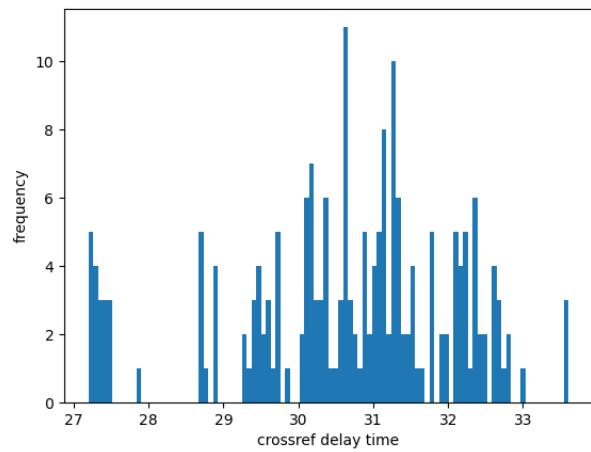


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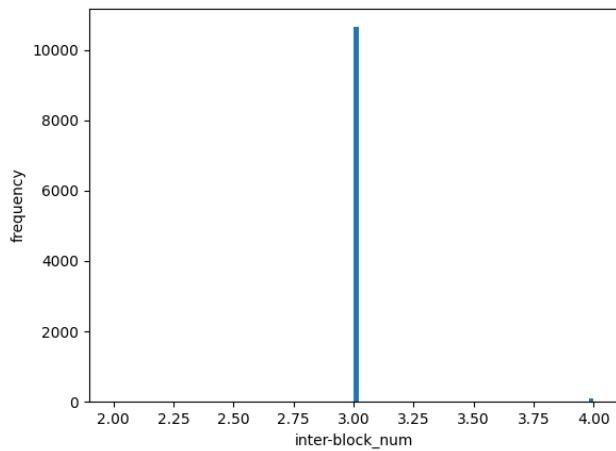


図 498 pht/experiment-win/1213-1/BC/30/1213-1-70D-PC7ALL-DIF1-INT1-30histogram-inter-blocknum.eps

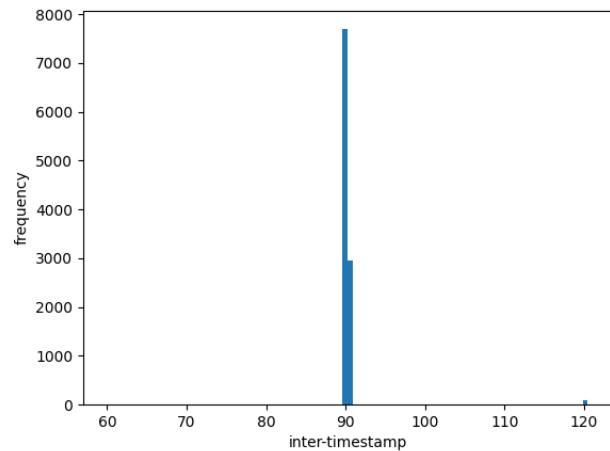


図 499 pht/experiment-win/1213-1/BC/30/1213-1-70D-PC7ALL-DIF1-INT1-30histogram-inter-timestamp.eps

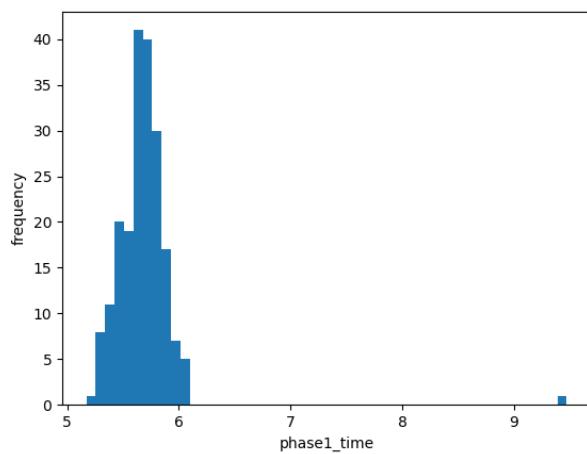


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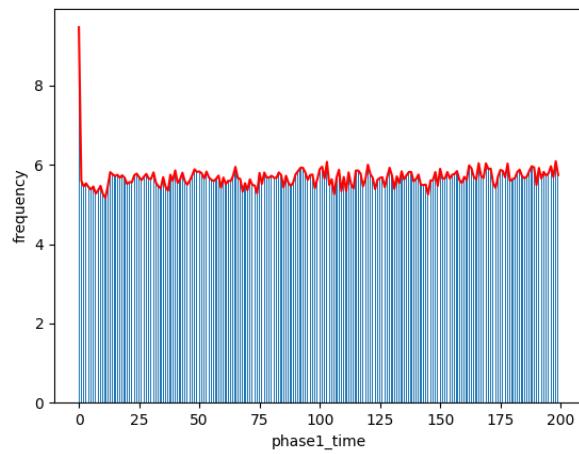


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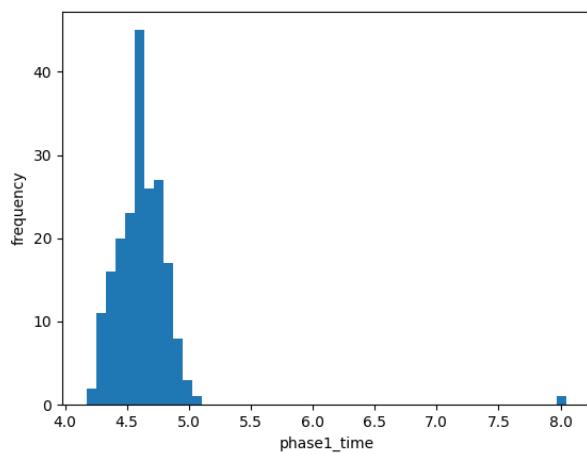


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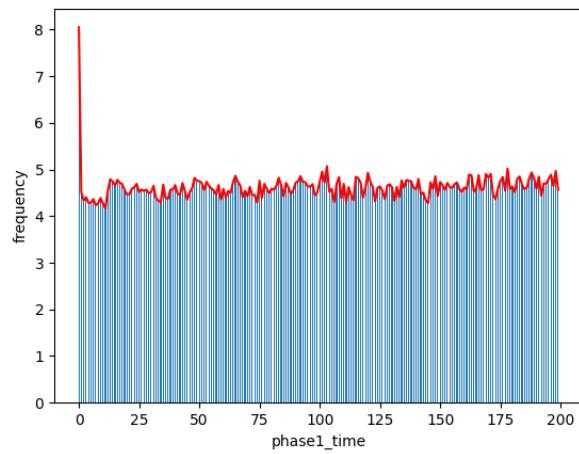


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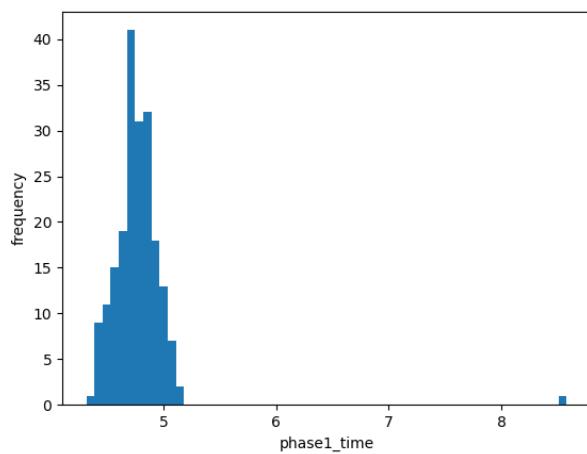


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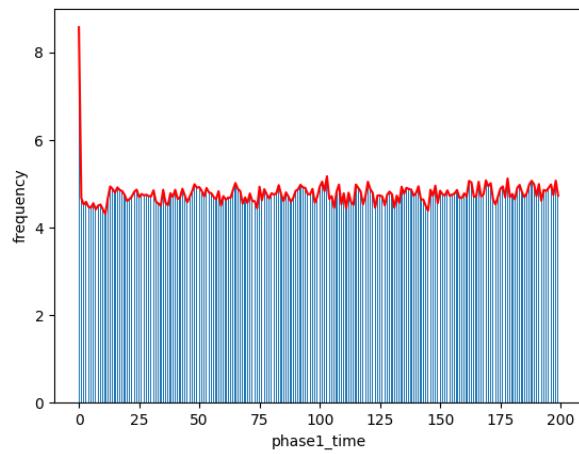


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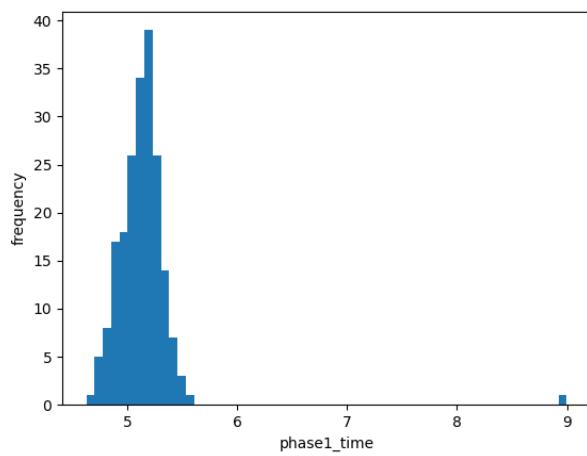


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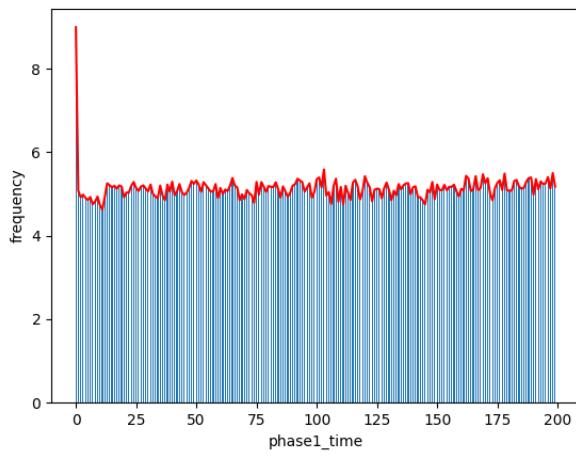


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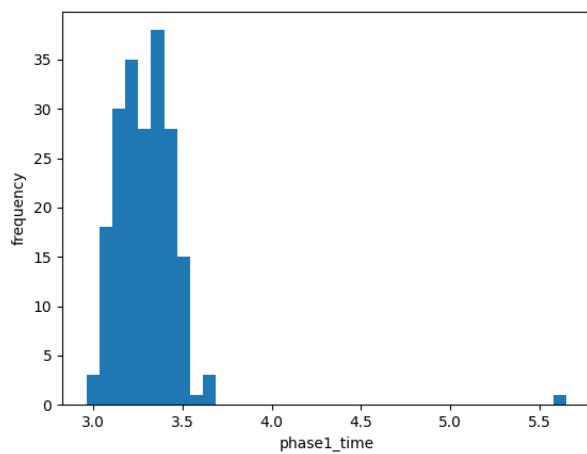


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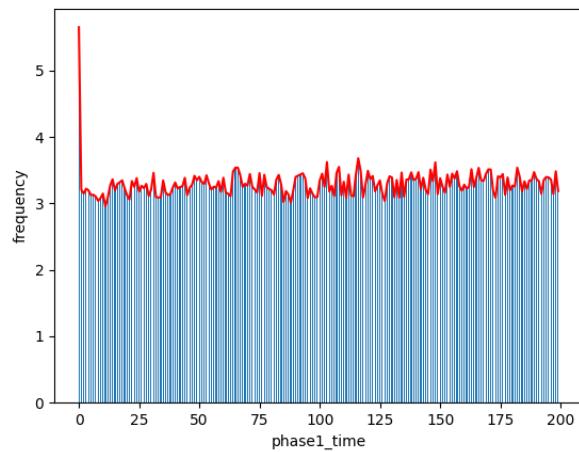


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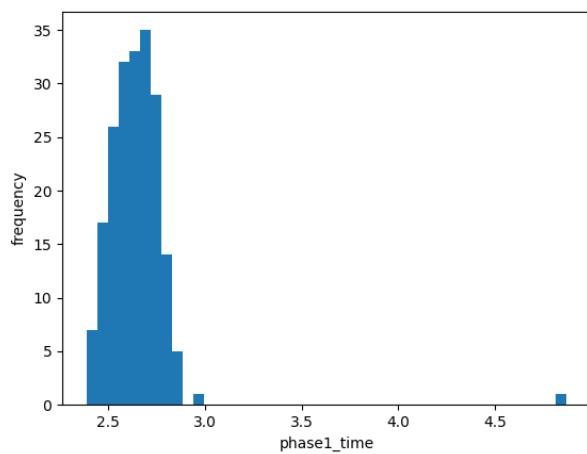


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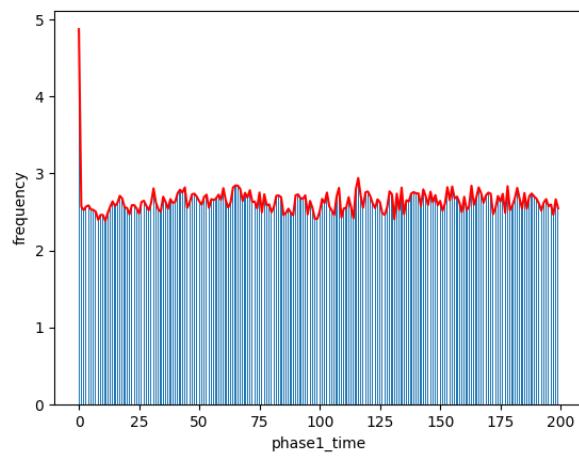


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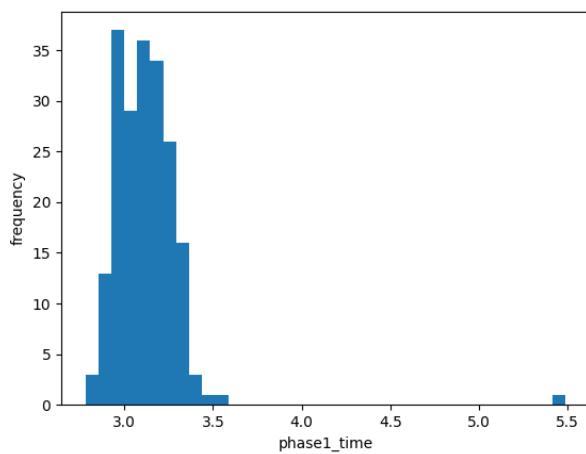


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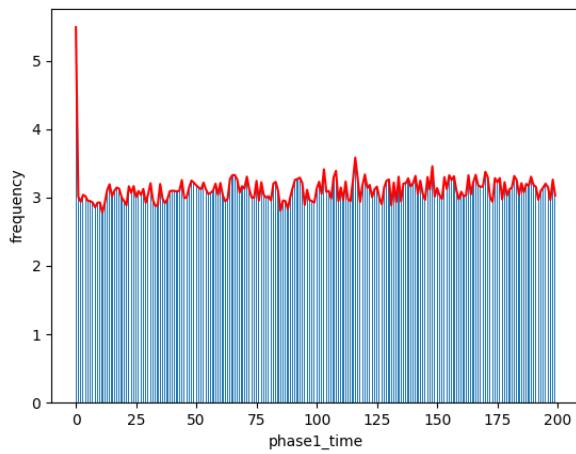


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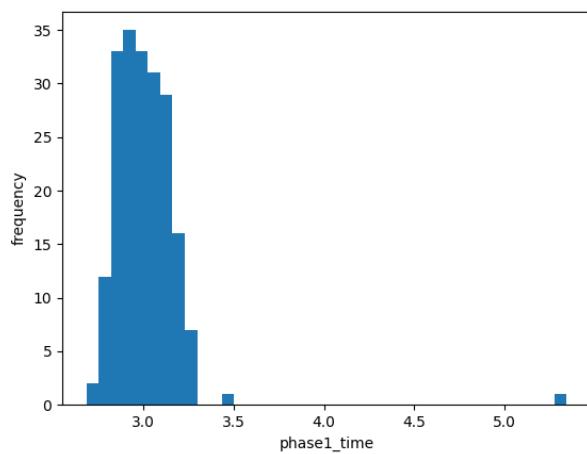


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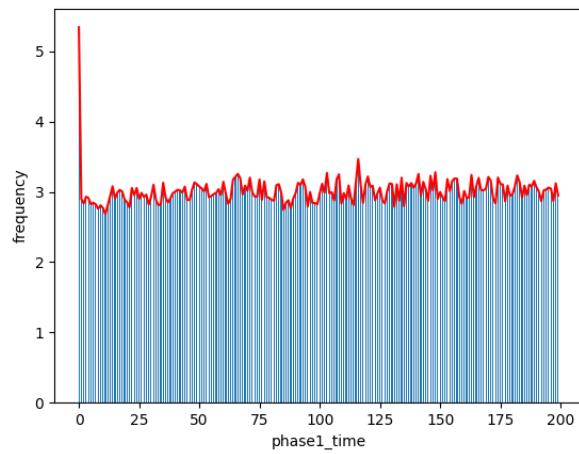


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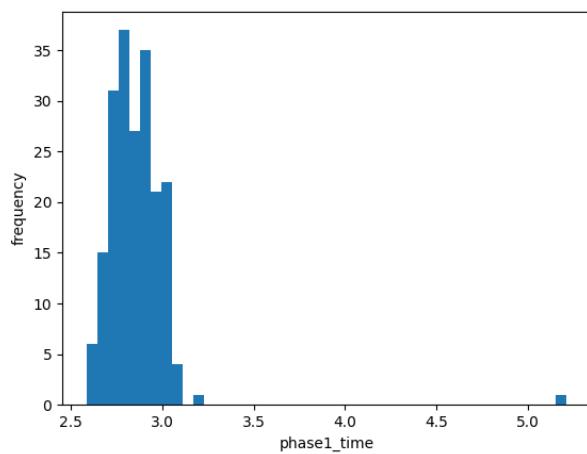


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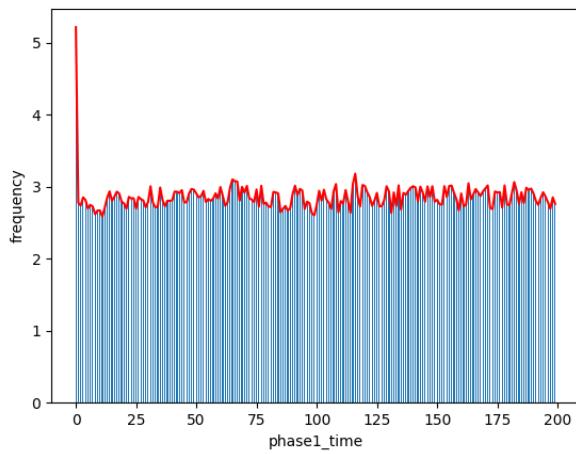


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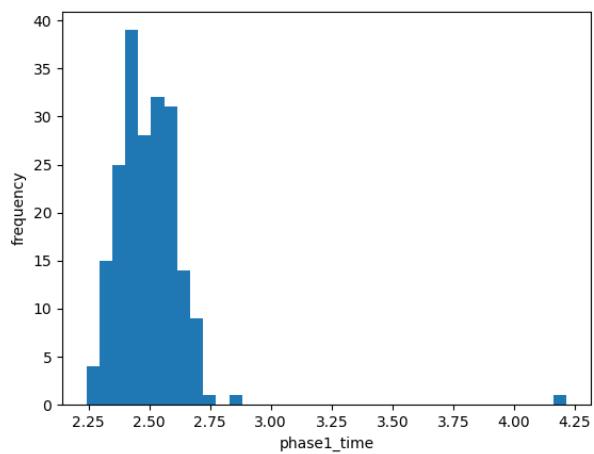


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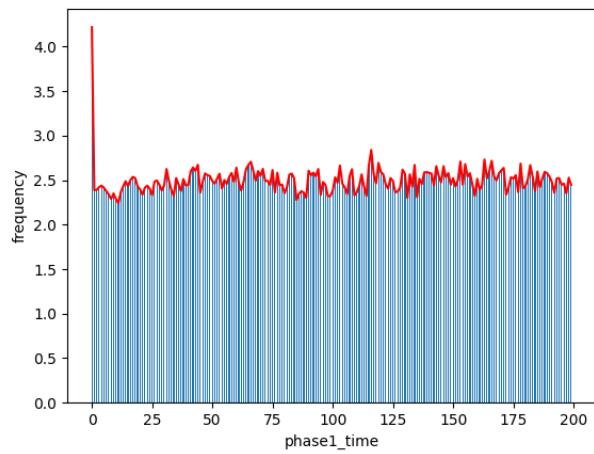


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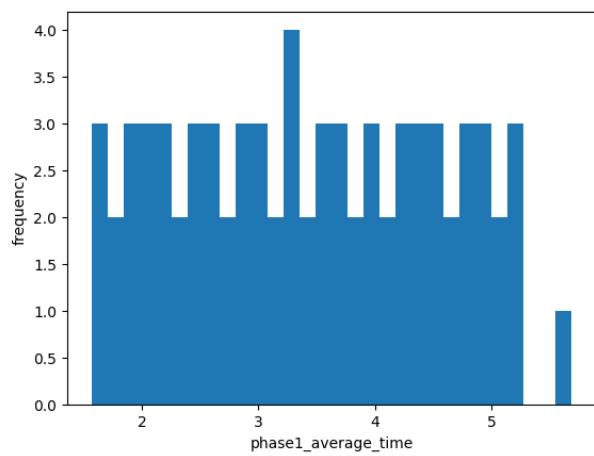


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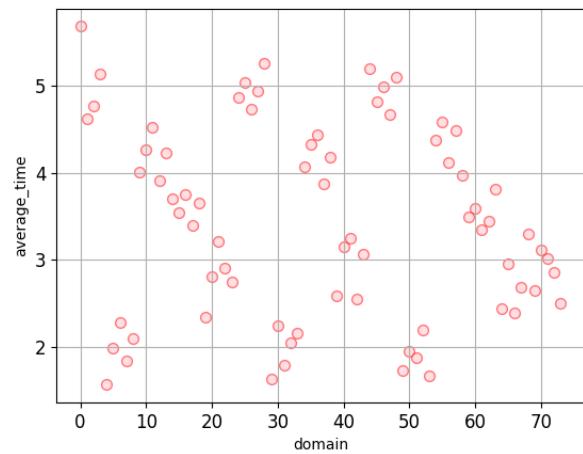


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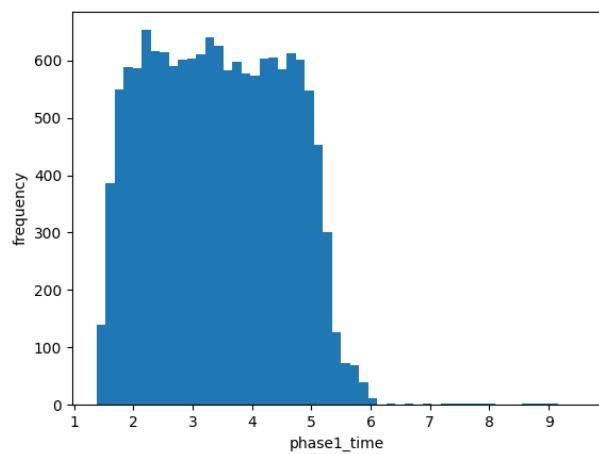


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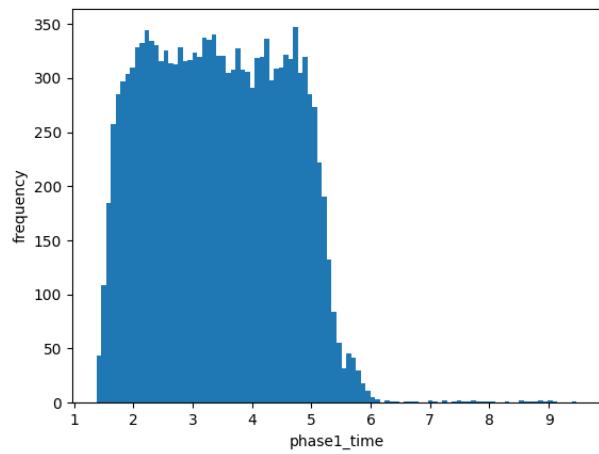


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17 1213-2-75D-PC3ALL-DIF1-INT1-30

17 章にて報告する条件を表 16 に示す。

表 16 設定値の一覧

設定項目	設定値
起動ノード数 (ドメイン数 D)	70 [ノード]
履歴交差間隔	90 [秒]
履歴交差回数	200 [回]
ブロック不生成間隔	0.5[秒]
ブロック生成難易度	1 [0x]
PC 内同時起動台数	7 [台]
使用台数	10 [台]
ブロック不生成間隔 : 30 秒	70 [ノード]
ブロック不生成間隔 : 1 秒	0 [ノード]

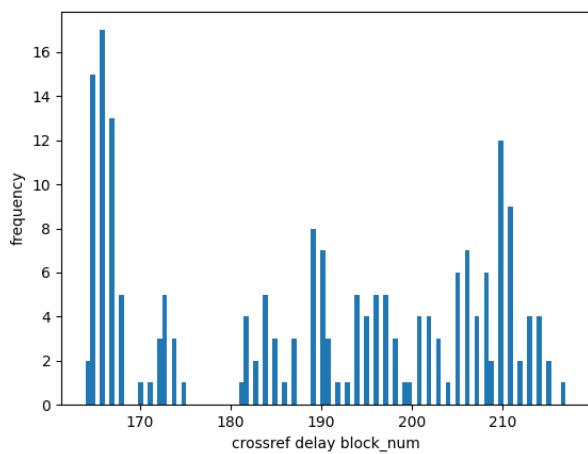


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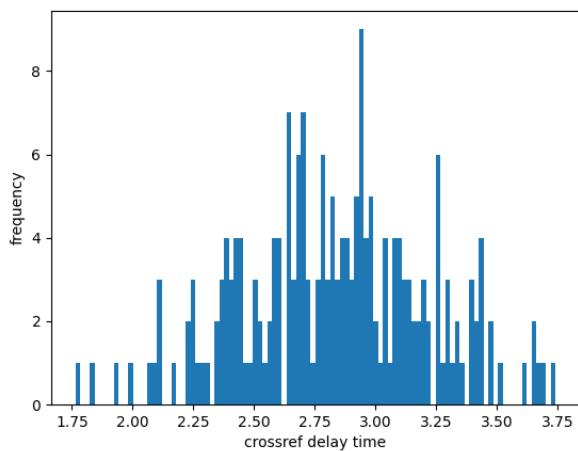


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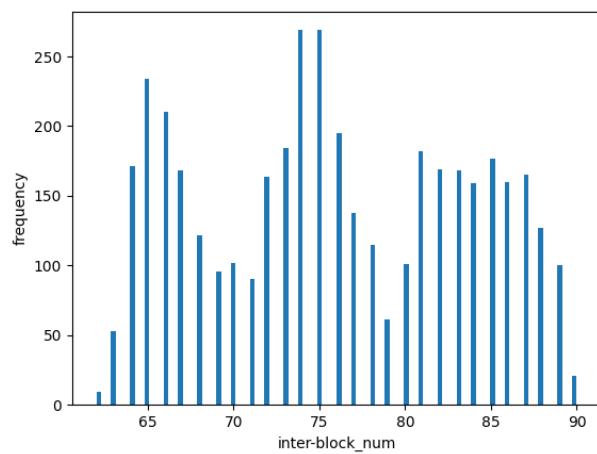


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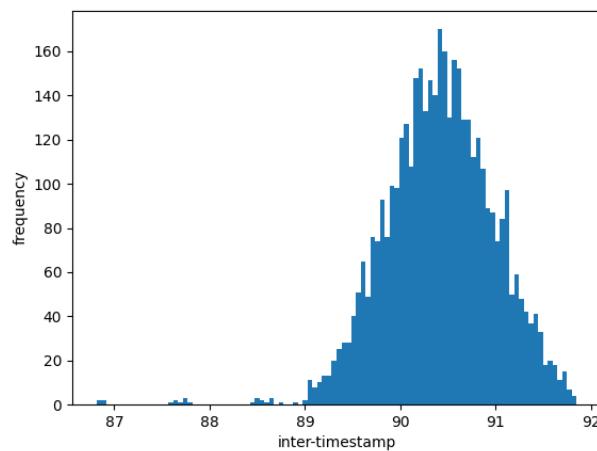


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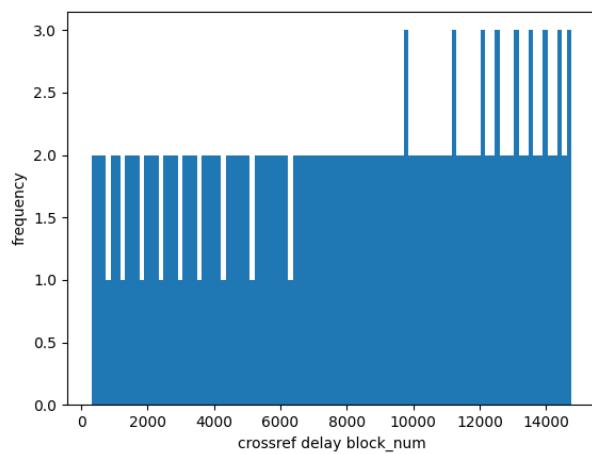


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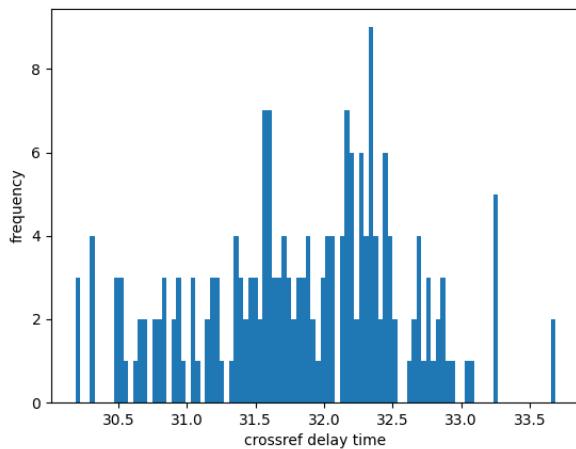


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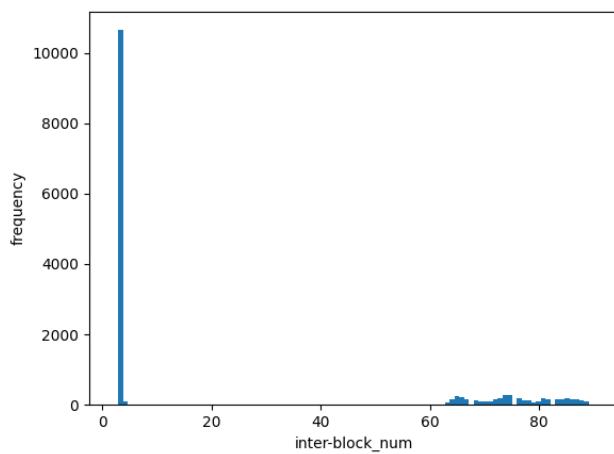


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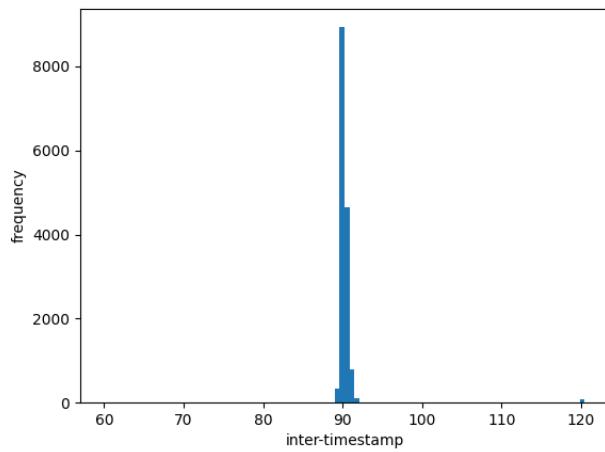


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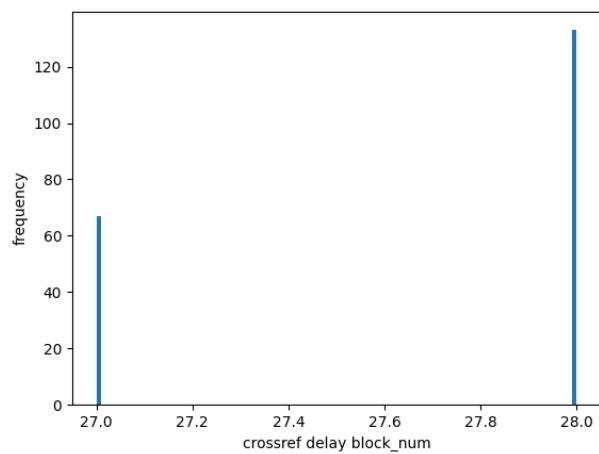


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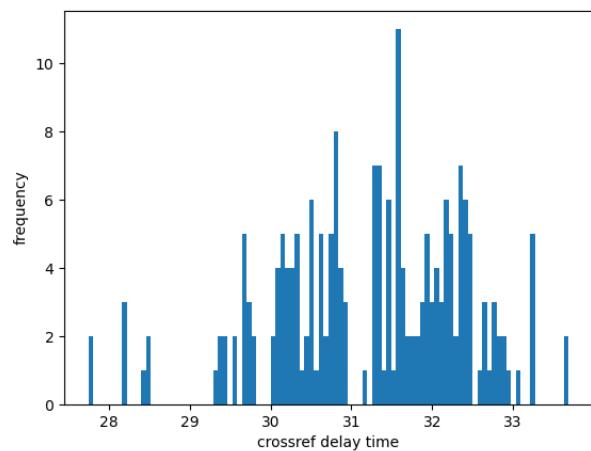


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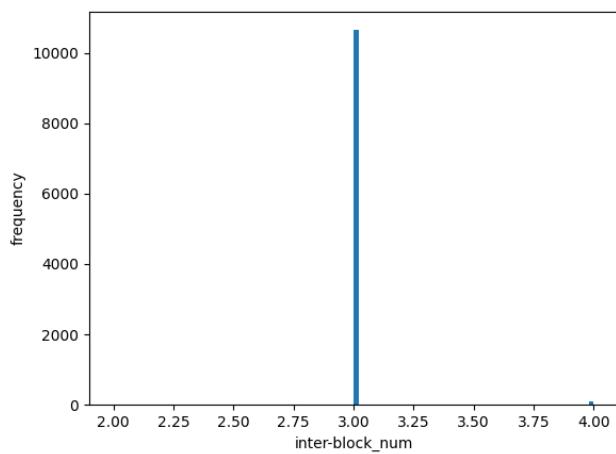


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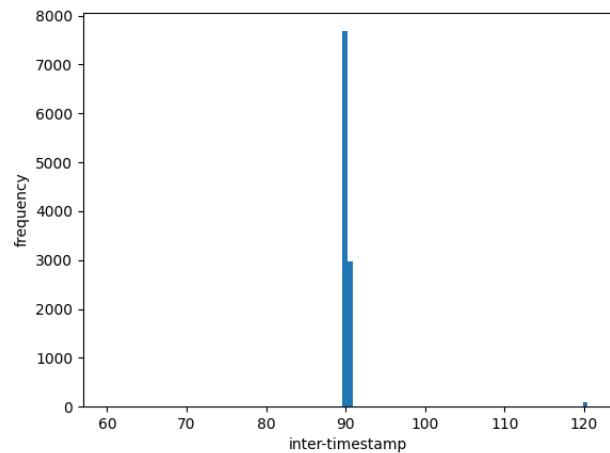


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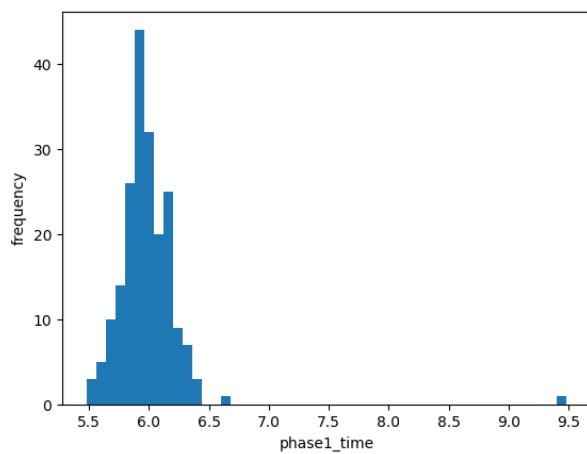


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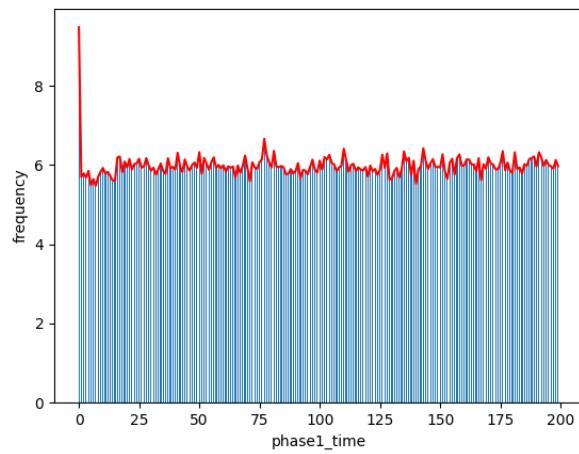


図 537 pht/experiment-win/1213-2/phase1/1213-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50051-in-chronological-order.eps

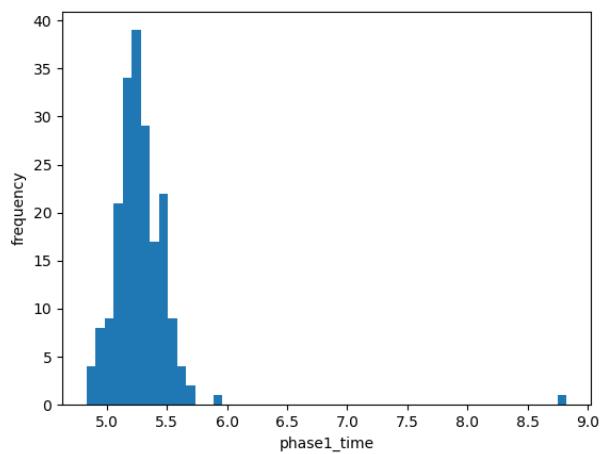


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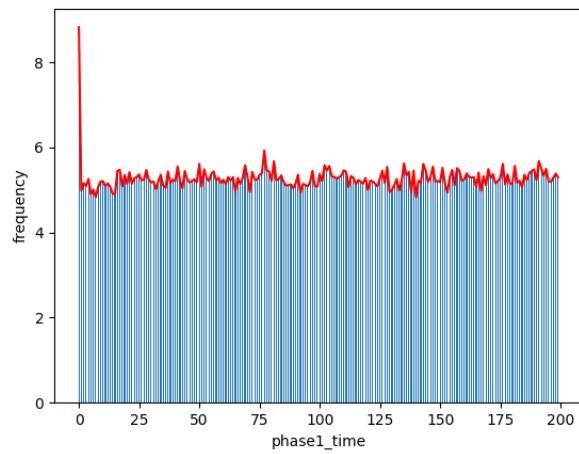


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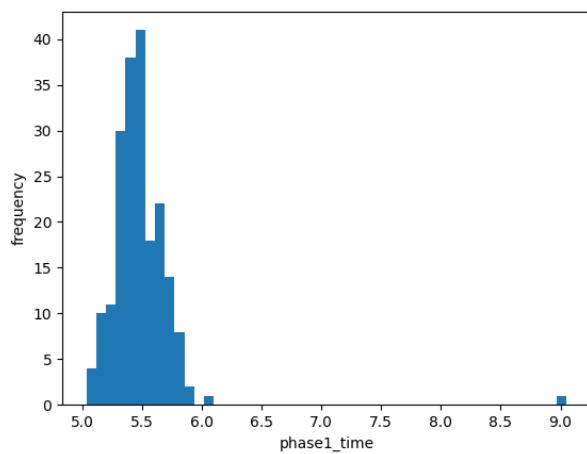


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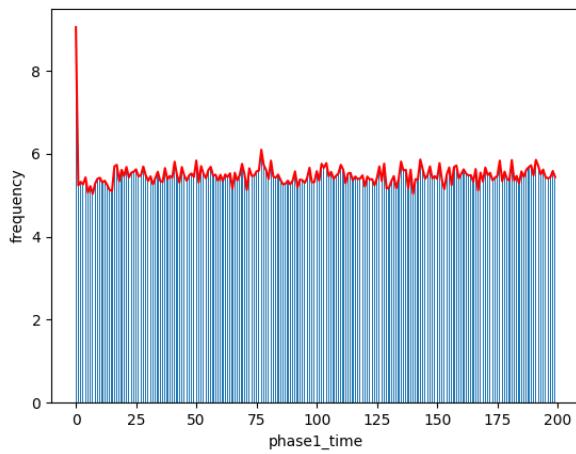


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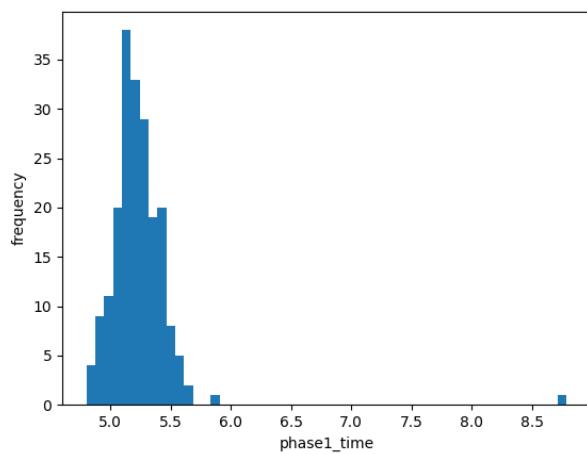


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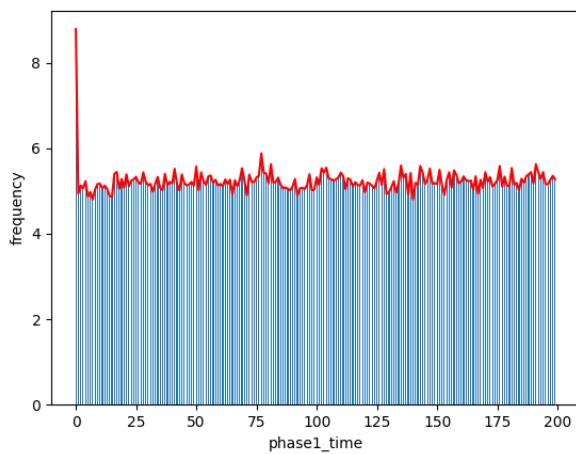


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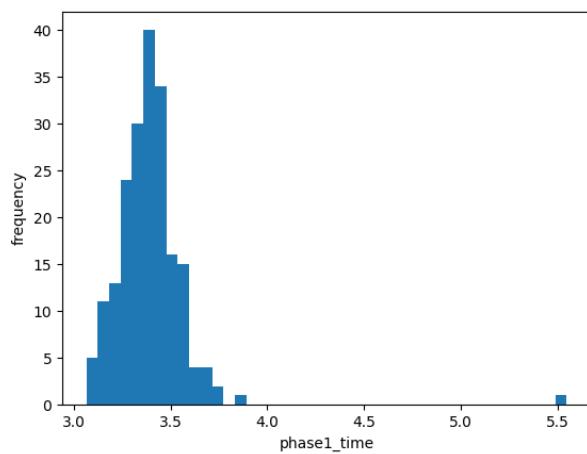


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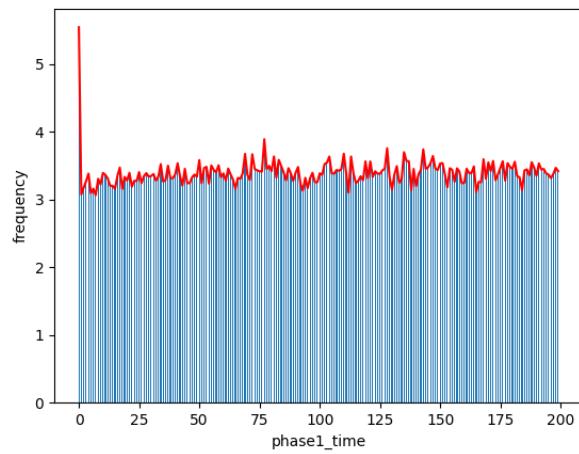


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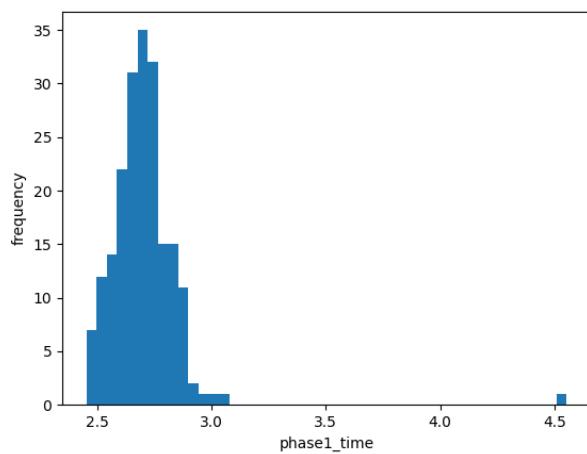


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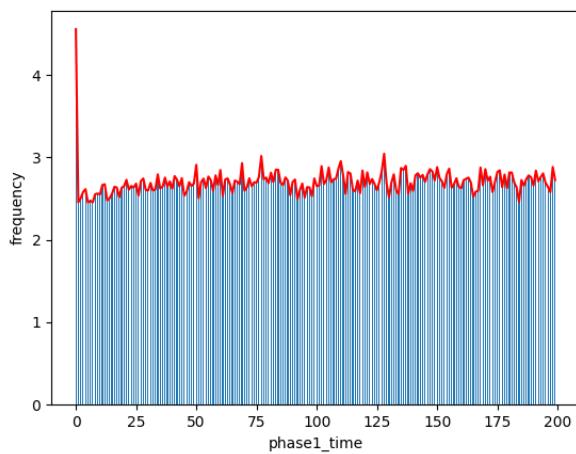


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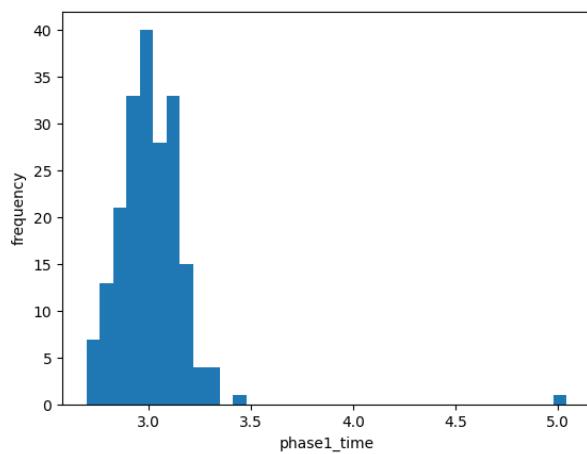


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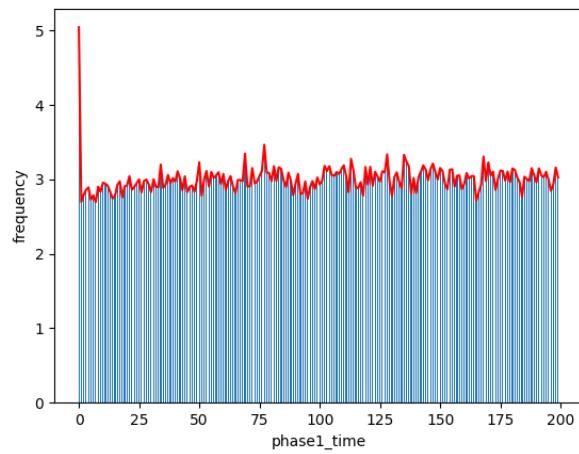


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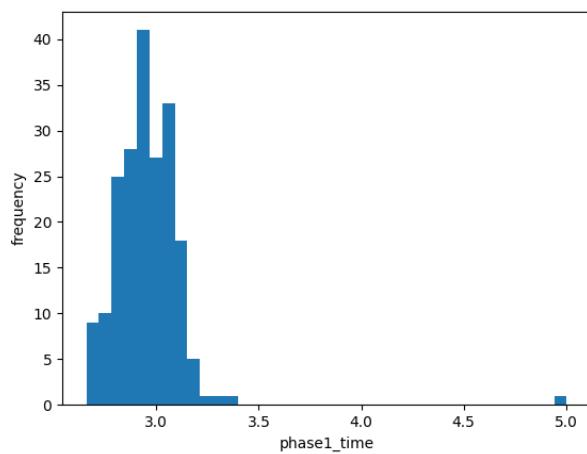


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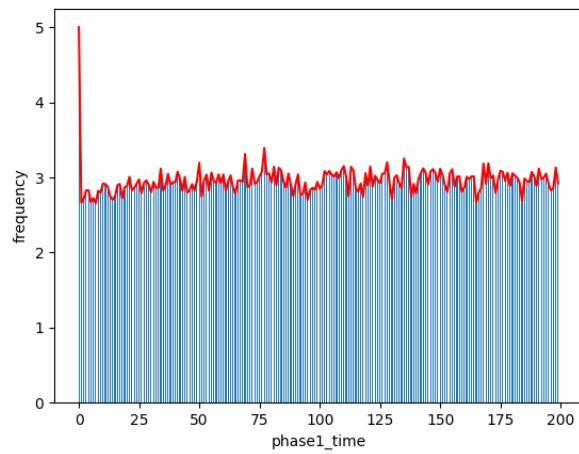


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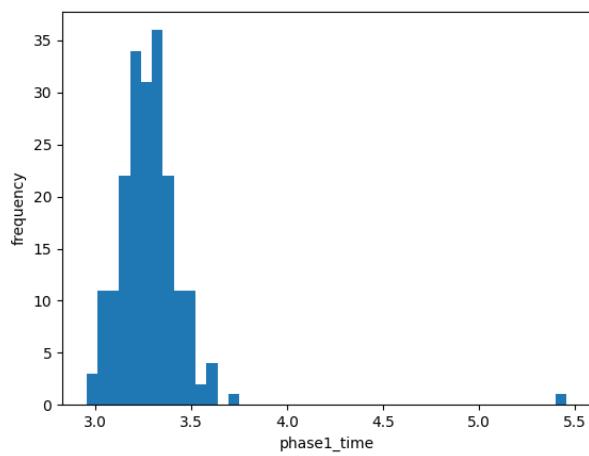


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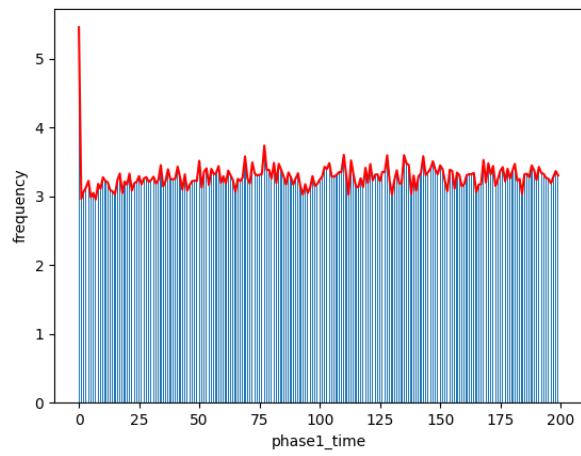


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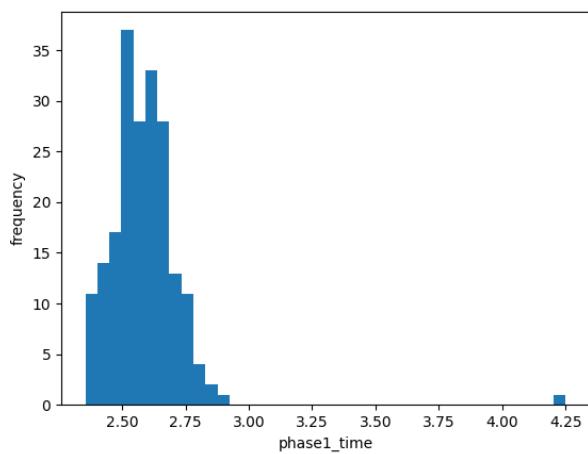


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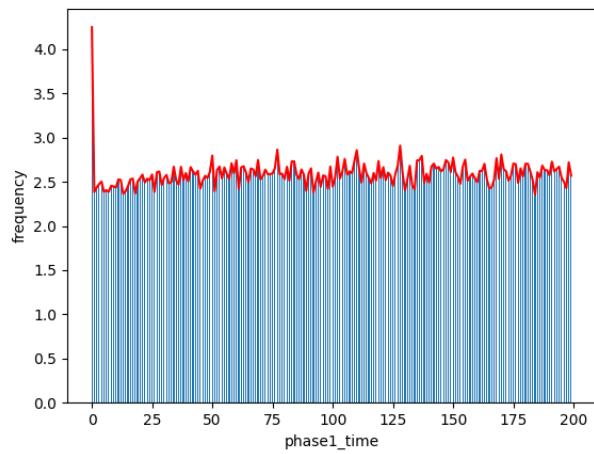


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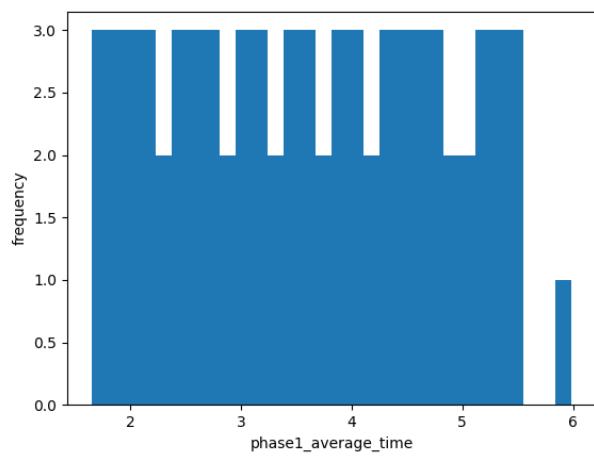


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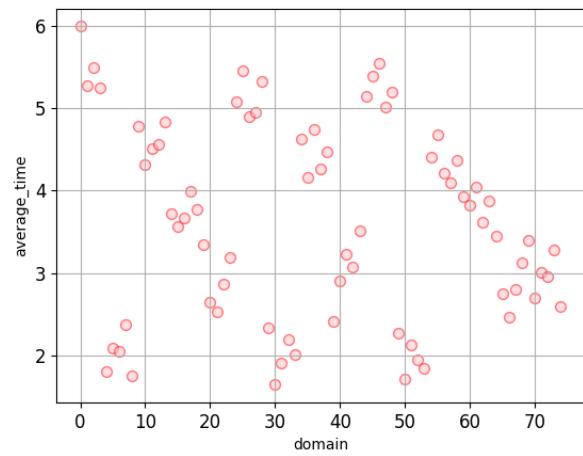


図 557 pht/experiment-win/1213-2/phase1/1213-2-75D-PC3ALL-DIF1-INT1-30analyze-phase1-average-time-scatter.eps

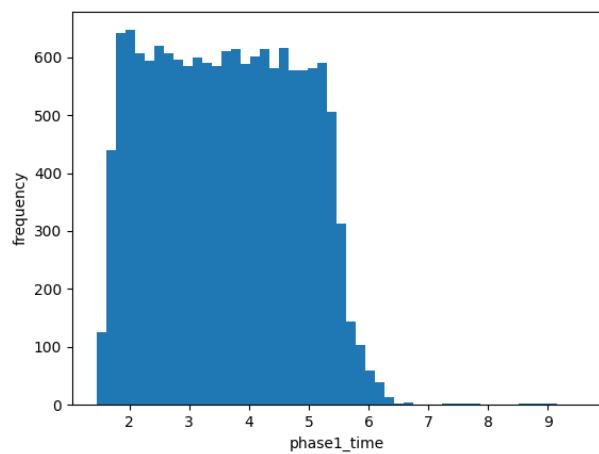


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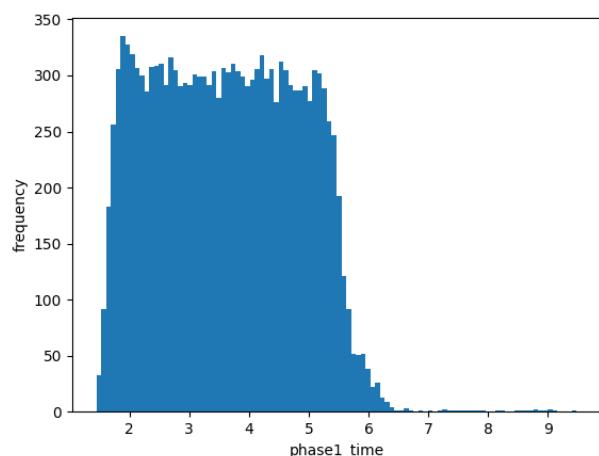


図 559 pht/experiment-win/1213-2/phase1/1213-2-75D-PC3ALL-DIF1-INT1-30analyze-phase1-time-bins100.eps

18 1214-1-75D-PC3ALL-DIF1-INT1-30

18 章にて報告する条件を表 17 に示す.

表 17 設定値の一覧

設定項目	設定値
起動ノード数 (ドメイン数 D)	70 [ノード]
履歴交差間隔	90 [秒]
履歴交差回数	200 [回]
ブロック不生成間隔	0.5[秒]
ブロック生成難易度	1 [0x]
PC 内同時起動台数	7 [台]
使用台数	10 [台]
ブロック不生成間隔 : 30 秒	70 [ノード]
ブロック不生成間隔 : 1 秒	0 [ノード]

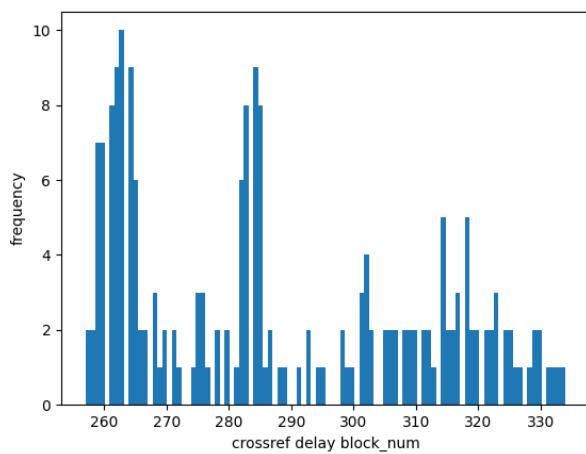


図 560 pht/experiment-win/1214-1/BC/1/1214-1-75D-PC3ALL-DIF1-INT1-30histogram-delay-blocknum.eps

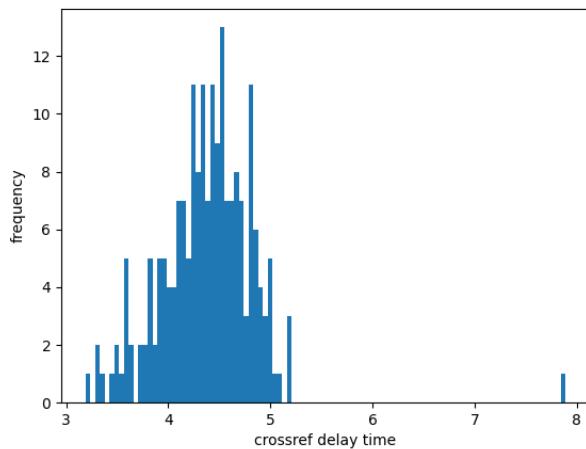


図 561 pht/experiment-win/1214-1/BC/1/1214-1-75D-PC3ALL-DIF1-INT1-30histogram-delay-time.eps

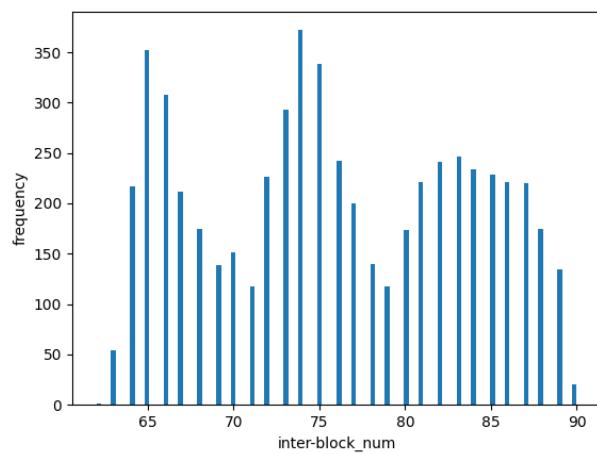


図 562 pht/experiment-win/1214-1/BC/1/1214-1-75D-PC3ALL-DIF1-INT1-30histogram-inter-blocknum.eps

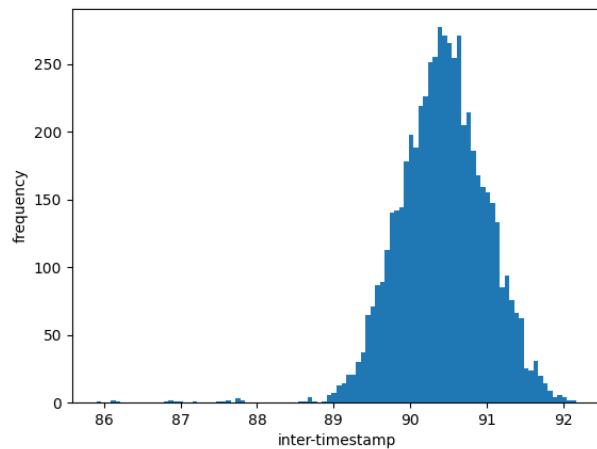


図 563 pht/experiment-win/1214-1/BC/1/1214-1-75D-PC3ALL-DIF1-INT1-30histogram-inter-timestamp.eps

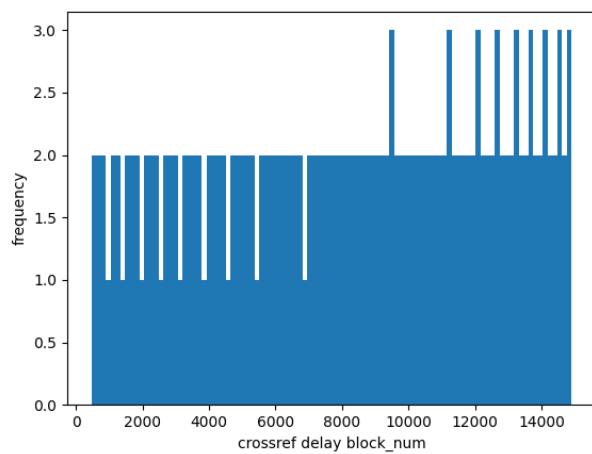


図 564 pht/experiment-win/1214-1/BC/1214-1-75D-PC3ALL-DIF1-INT1-30histogram-delay-blocknum.eps

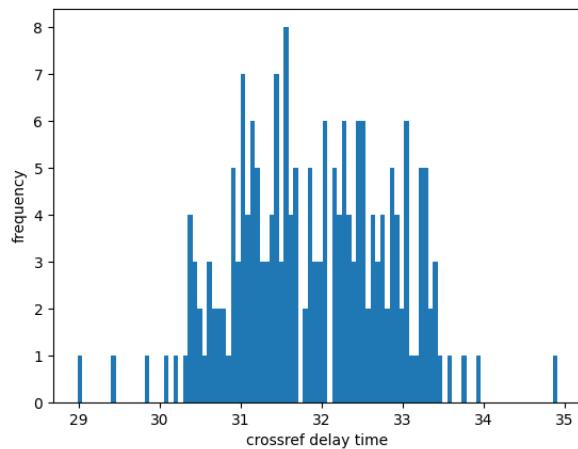


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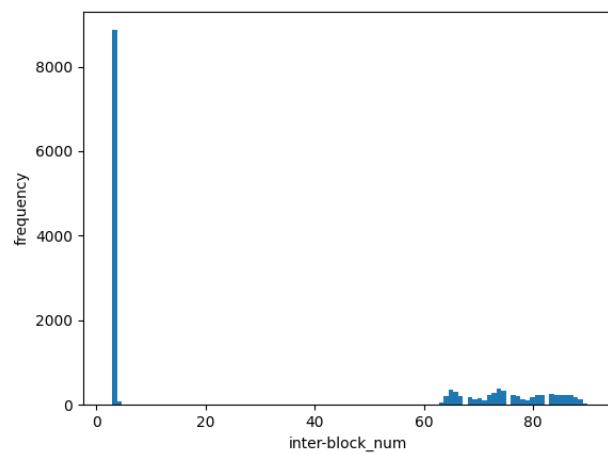


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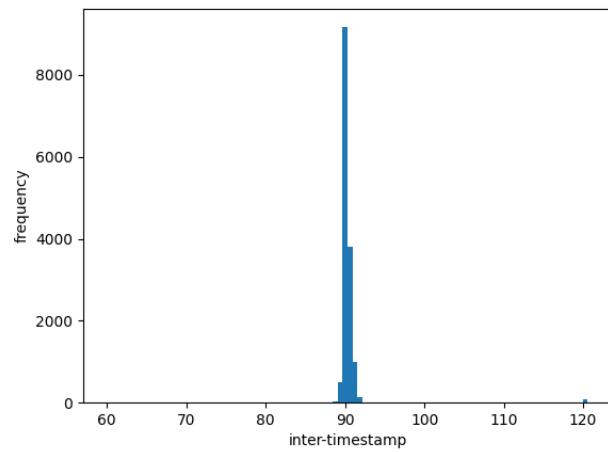


図 567 pht/experiment-win/1214-1/BC/1214-1-75D-PC3ALL-DIF1-INT1-30histogram-inter-timestamp.eps

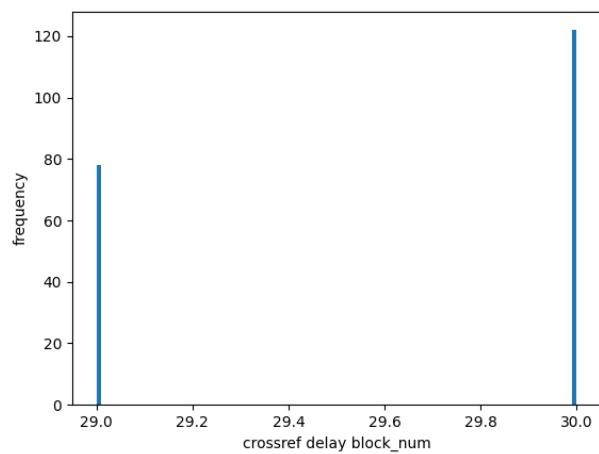


図 568 pht/experiment-win/1214-1/BC/30/1214-1-75D-PC3ALL-DIF1-INT1-30histogram-delay-blocknum.eps

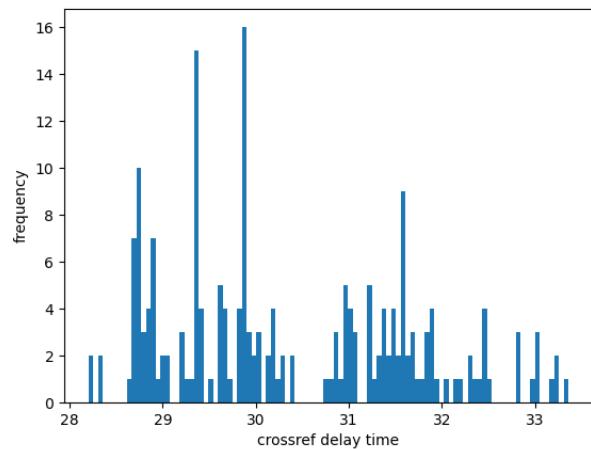


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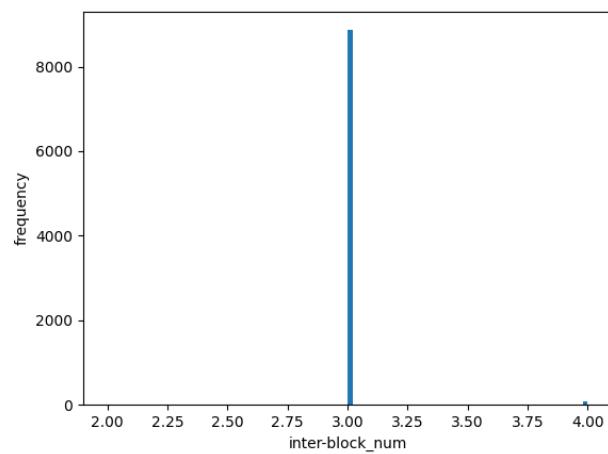


図 570 pht/experiment-win/1214-1/BC/30/1214-1-75D-PC3ALL-DIF1-INT1-30histogram-inter-blocknum.eps

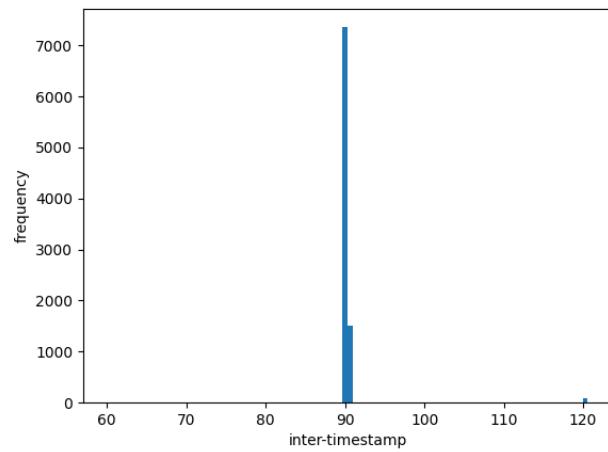


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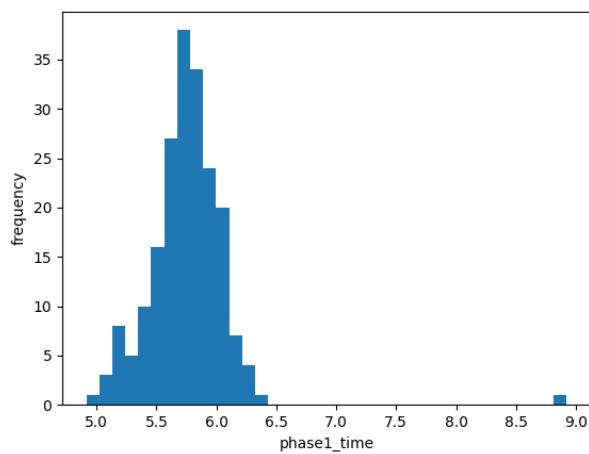


図 572 pht/experiment-win/1214-1/phase1/1214-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50051.eps

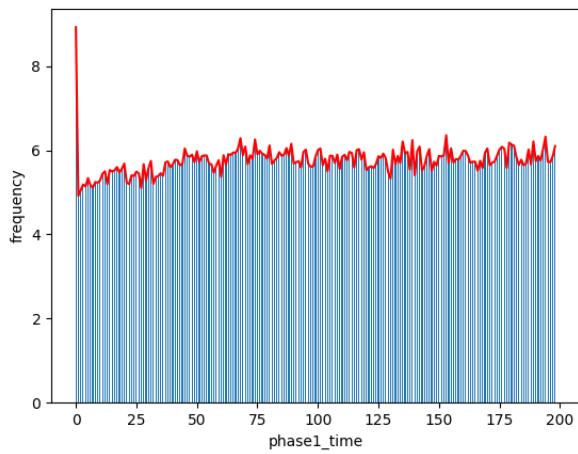


図 573 pht/experiment-win/1214-1/phase1/1214-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50051-in-chronological-order.eps

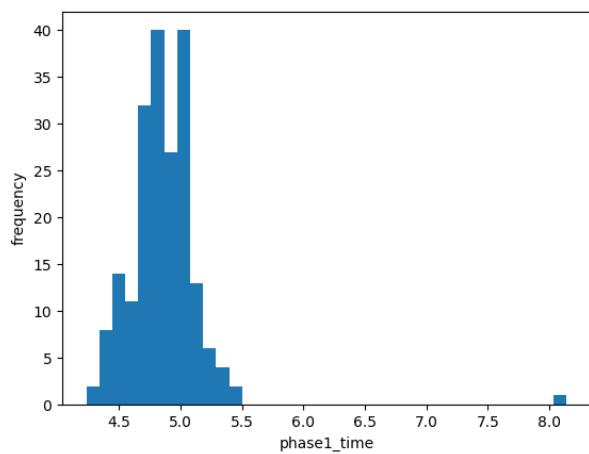


図 574 pht/experiment-win/1214-1/phase1/1214-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50053.eps

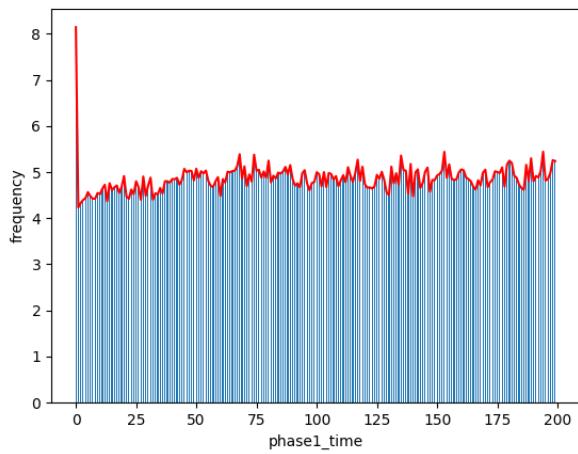


図 575 pht/experiment-win/1214-1/phase1/1214-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50053-in-chronological-order.eps

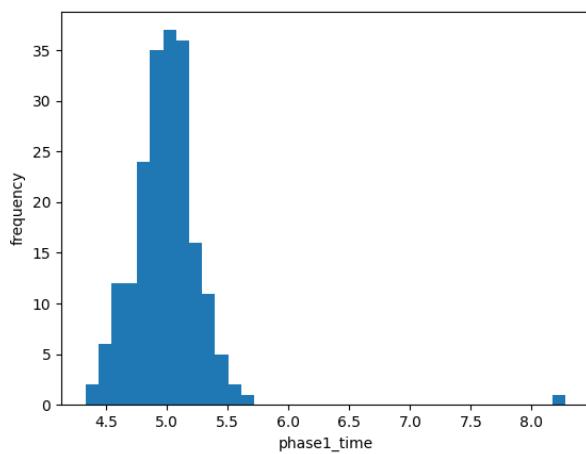


図 576 pht/experiment-win/1214-1/phase1/1214-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50055.eps

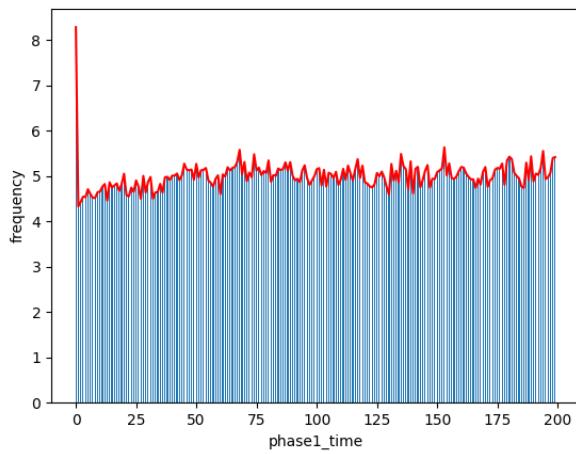


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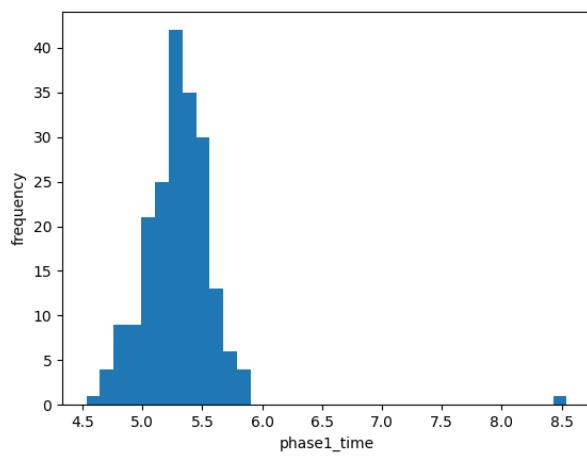


図 578 pht/experiment-win/1214-1/phase1/1214-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50059.eps

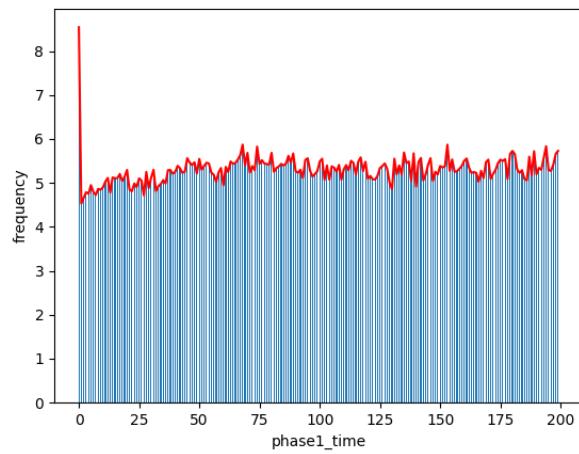


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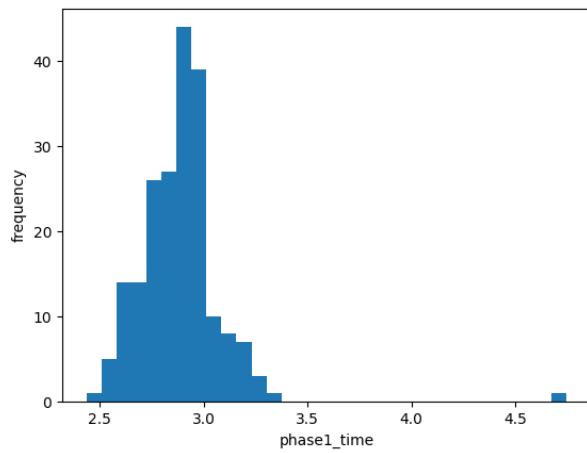


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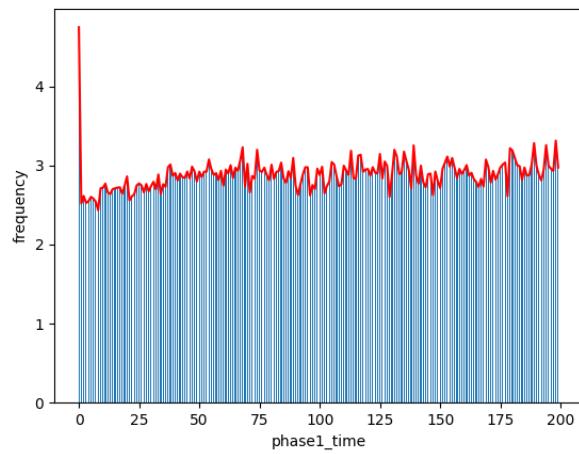


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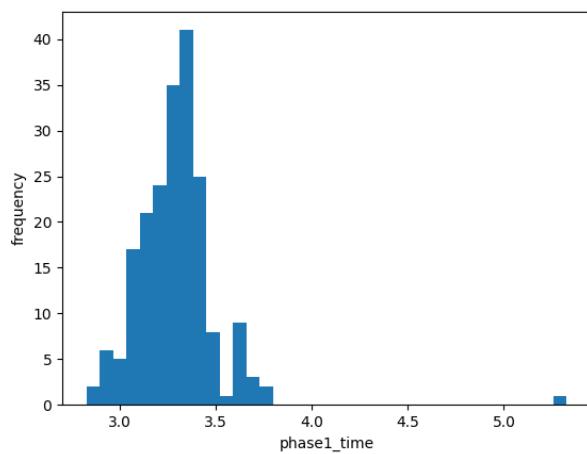


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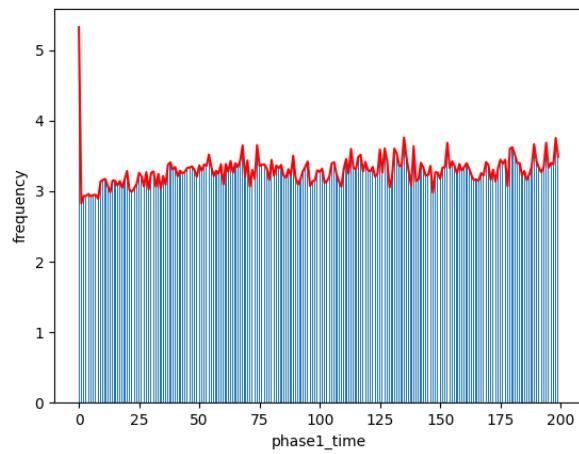


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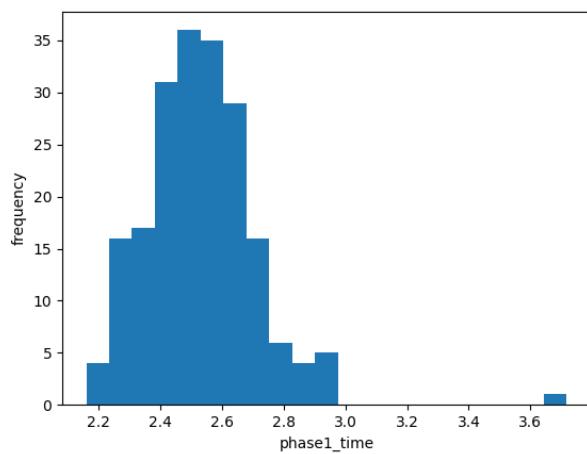


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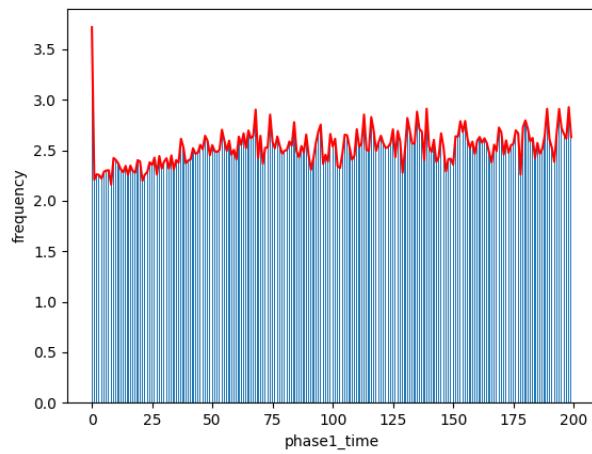


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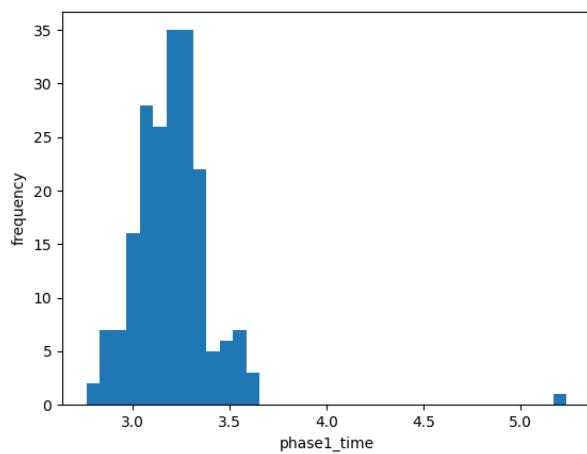


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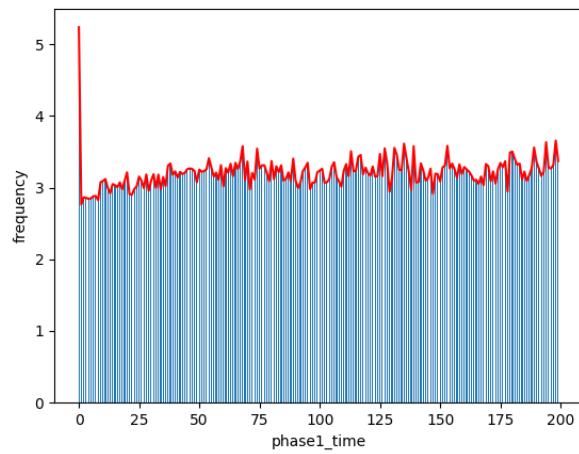


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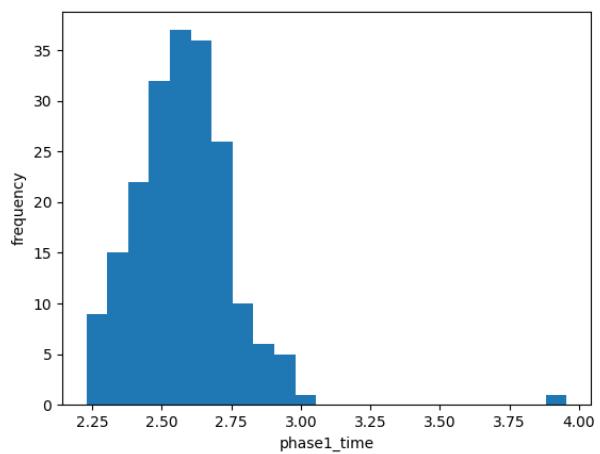


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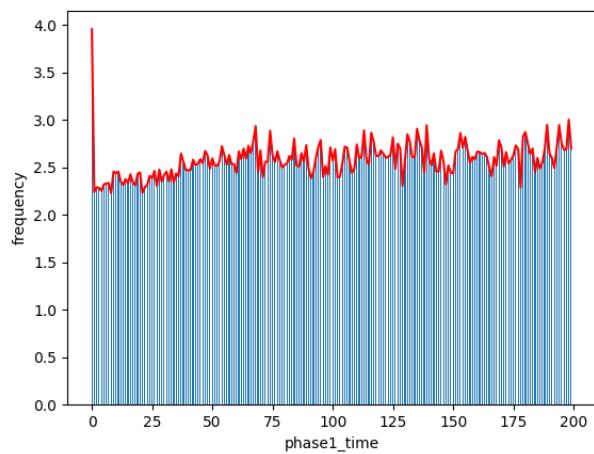


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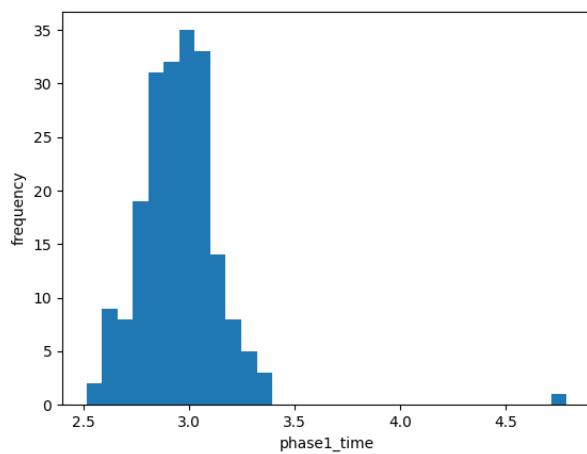


図 590 pht/experiment-win/1214-1/phase1/1214-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50201.eps

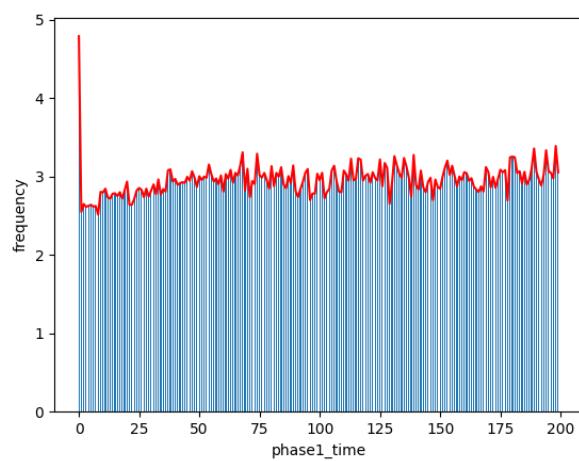


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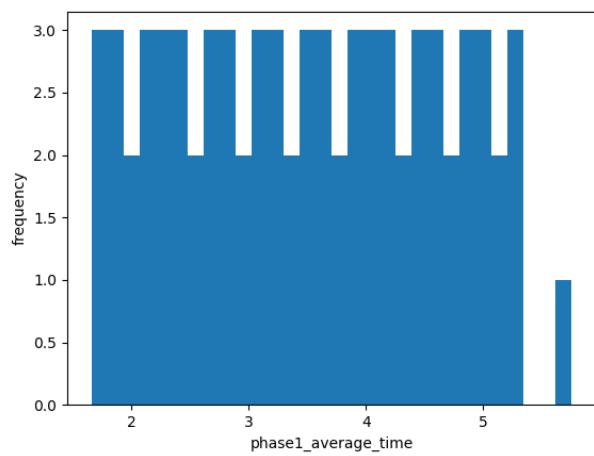


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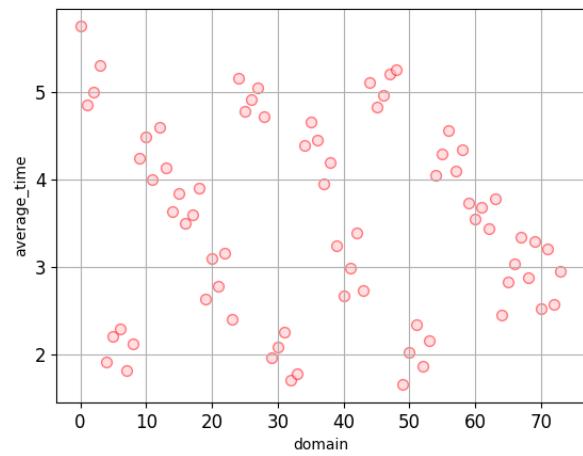


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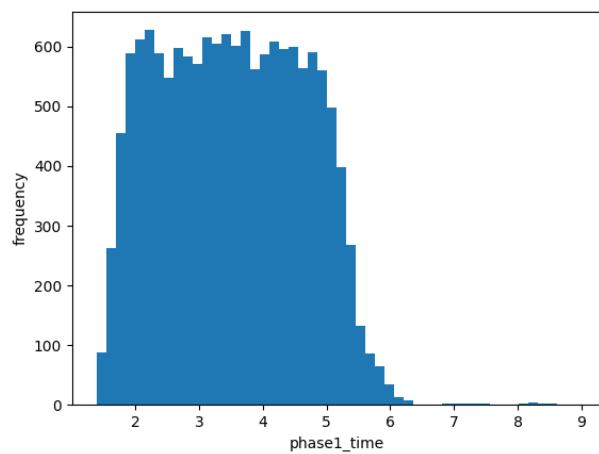


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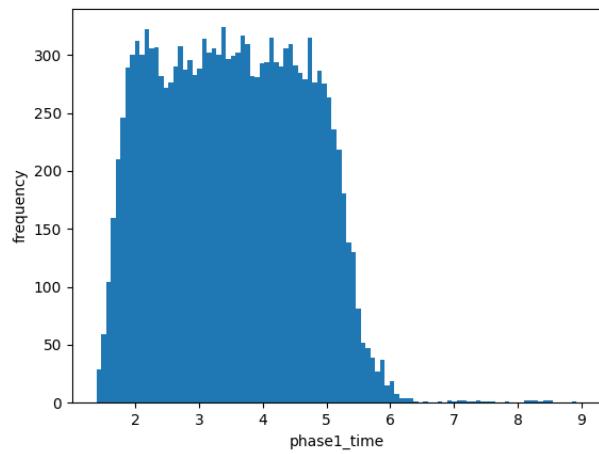


図 595 pht/experiment-win/1214-1/phase1/1214-1-75D-PC3ALL-DIF1-INT1-30analyze-phase1-time-bins100.eps

19 1215-1-75D-PC3ALL-DIF1-INT1-30

19 章にて報告する条件を表 18 に示す。

表 18 設定値の一覧

設定項目	設定値
起動ノード数 (ドメイン数 D)	70 [ノード]
履歴交差間隔	90 [秒]
履歴交差回数	200 [回]
ブロック不生成間隔	0.5[秒]
ブロック生成難易度	1 [0x]
PC 内同時起動台数	7 [台]
使用台数	10 [台]
ブロック不生成間隔 : 30 秒	70 [ノード]
ブロック不生成間隔 : 1 秒	0 [ノード]

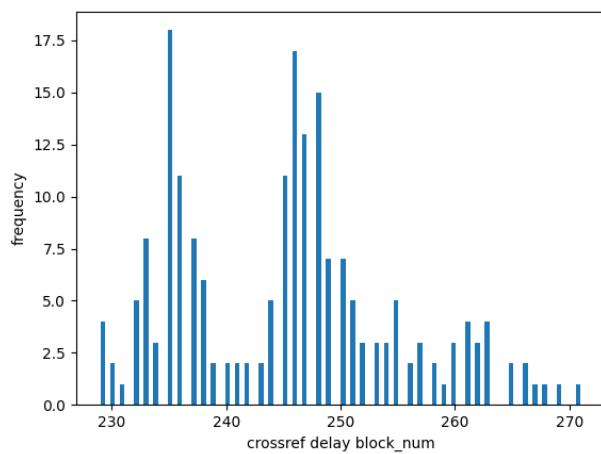


図 596 pht/experiment-win/1215-1/BC/1/1215-1-75D-PC3ALL-DIF1-INT1-30histogram-delay-blocknum.eps

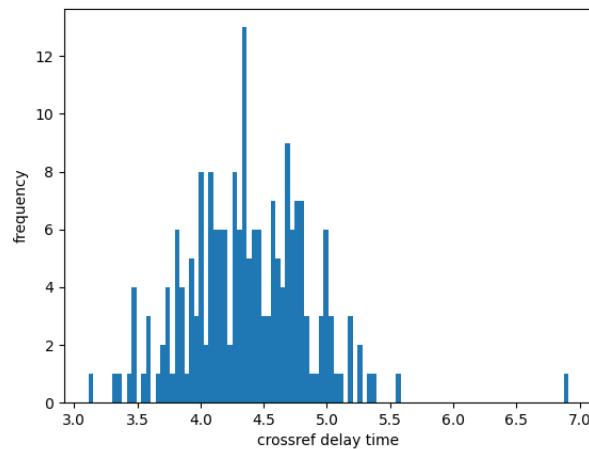


図 597 pht/experiment-win/1215-1/BC/1/1215-1-75D-PC3ALL-DIF1-INT1-30histogram-delay-time.eps

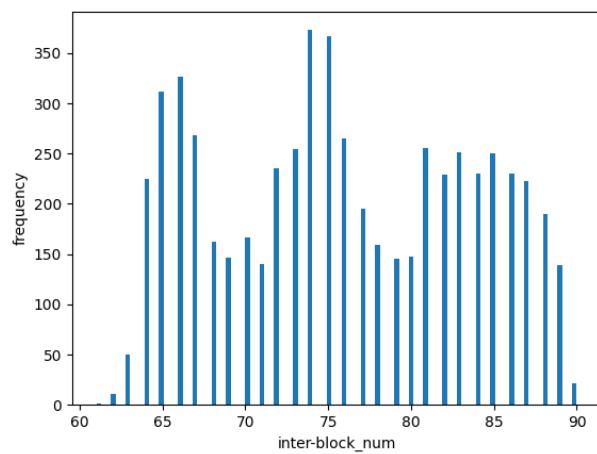


図 598 pht/experiment-win/1215-1/BC/1/1215-1-75D-PC3ALL-DIF1-INT1-30histogram-inter-blocknum.eps

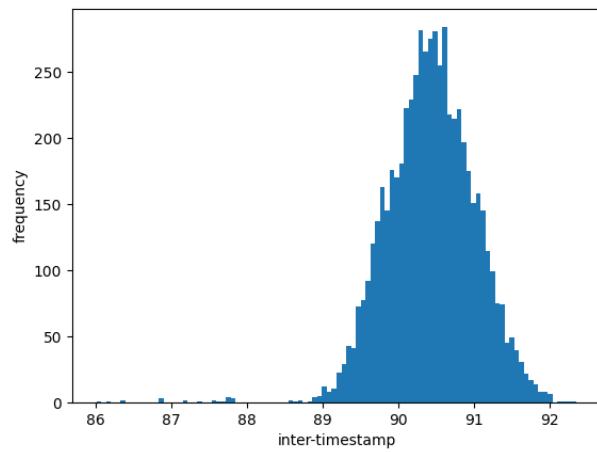


図 599 pht/experiment-win/1215-1/BC/1/1215-1-75D-PC3ALL-DIF1-INT1-30histogram-inter-timestamp.eps

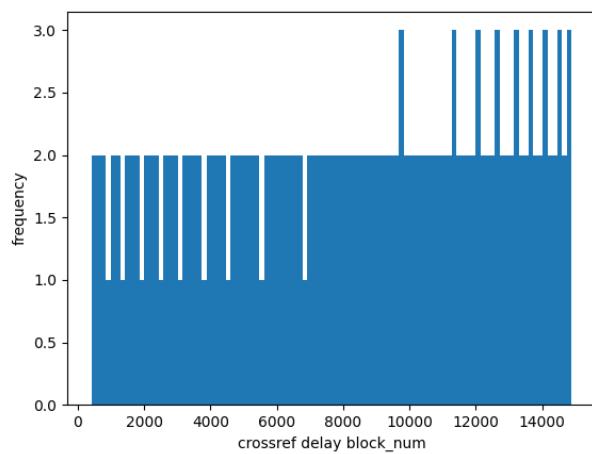


図 600 pht/experiment-win/1215-1/BC/1215-1-75D-PC3ALL-DIF1-INT1-30histogram-delay-blocknum.eps

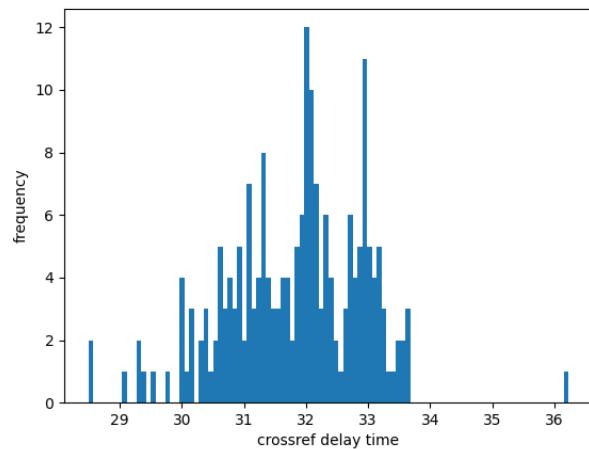


図 601 pht/experiment-win/1215-1/BC/1215-1-75D-PC3ALL-DIF1-INT1-30histogram-delay-time.eps

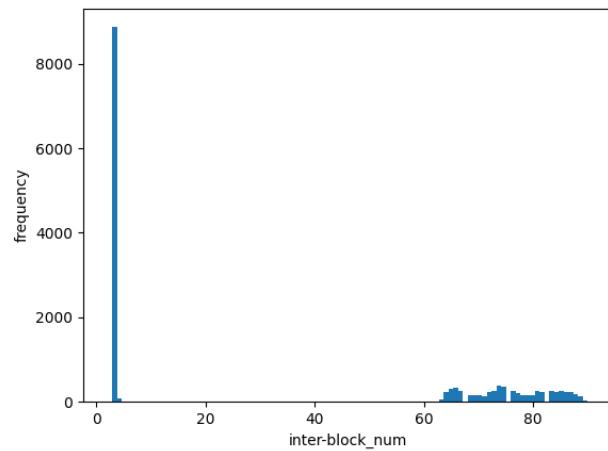


図 602 pht/experiment-win/1215-1/BC/1215-1-75D-PC3ALL-DIF1-INT1-30histogram-inter-blocknum.eps

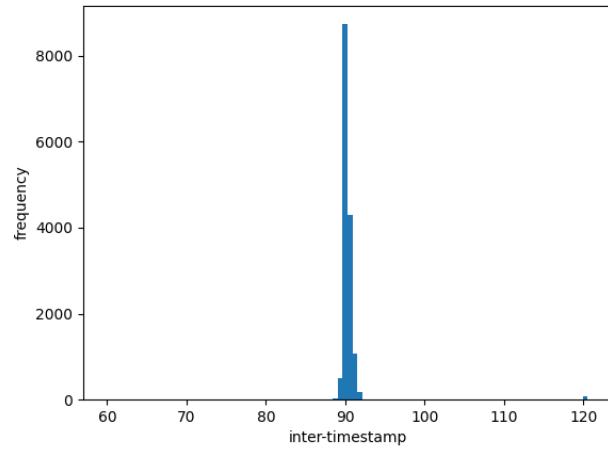


図 603 pht/experiment-win/1215-1/BC/1215-1-75D-PC3ALL-DIF1-INT1-30histogram-inter-timestamp.eps

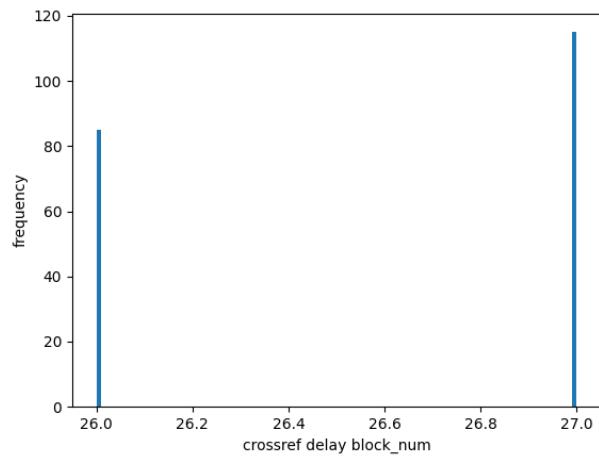


図 604 pht/experiment-win/1215-1/BC/30/1215-1-75D-PC3ALL-DIF1-INT1-30histogram-delay-blocknum.eps

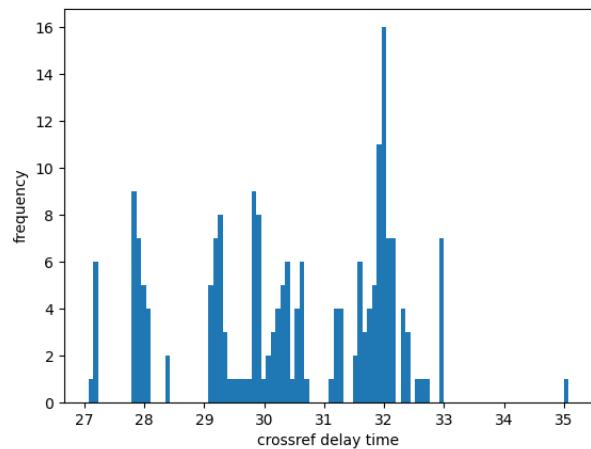


図 605 pht/experiment-win/1215-1/BC/30/1215-1-75D-PC3ALL-DIF1-INT1-30histogram-delay-time.eps

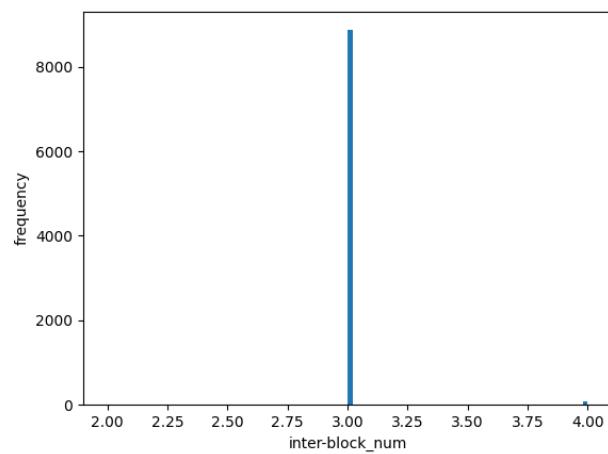


図 606 pht/experiment-win/1215-1/BC/30/1215-1-75D-PC3ALL-DIF1-INT1-30histogram-inter-blocknum.eps

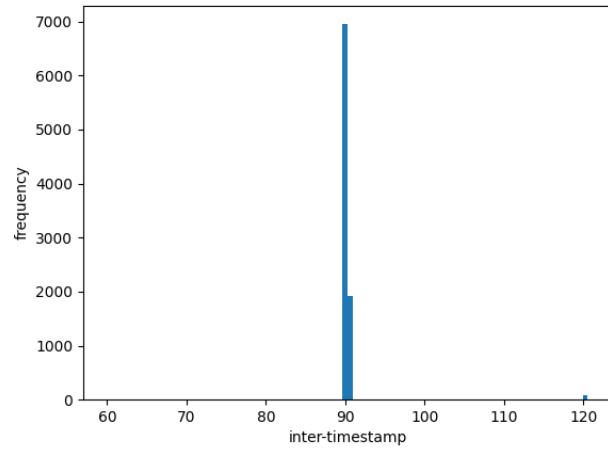


図 607 pht/experiment-win/1215-1/BC/30/1215-1-75D-PC3ALL-DIF1-INT1-30histogram-inter-timestamp.eps

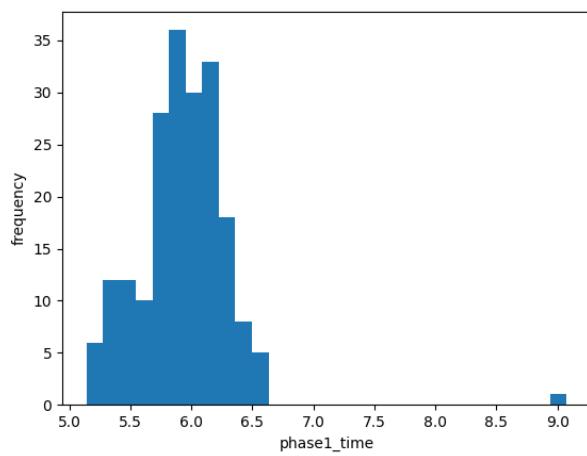


図 608 pht/experiment-win/1215-1/phase1/1215-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50051.eps

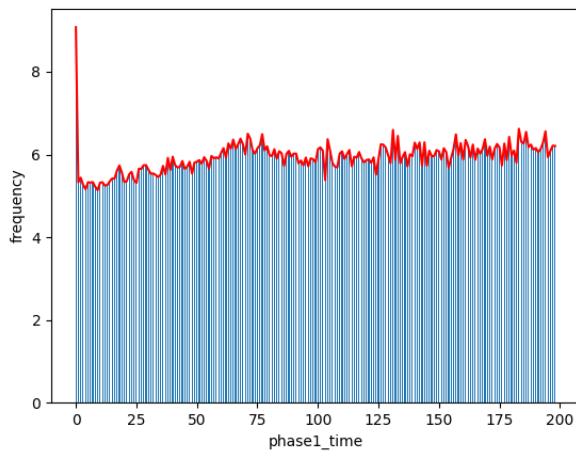


図 609 pht/experiment-win/1215-1/phase1/1215-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50051-in-chronological-order.eps

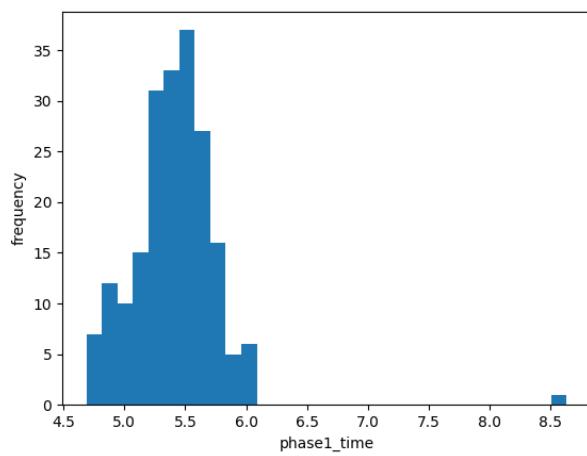


図 610 pht/experiment-win/1215-1/phase1/1215-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50053.eps

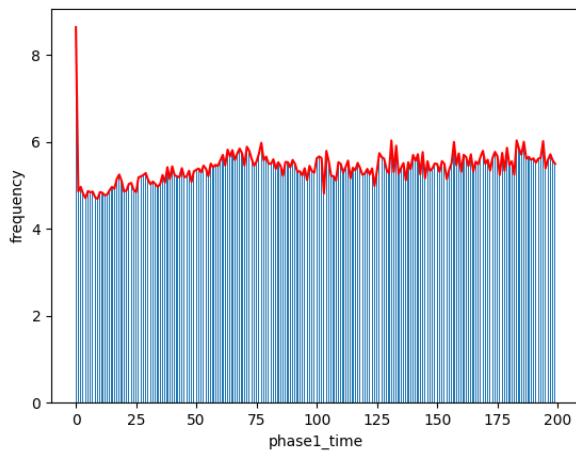


図 611 pht/experiment-win/1215-1/phase1/1215-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50053-in-chronological-order.eps

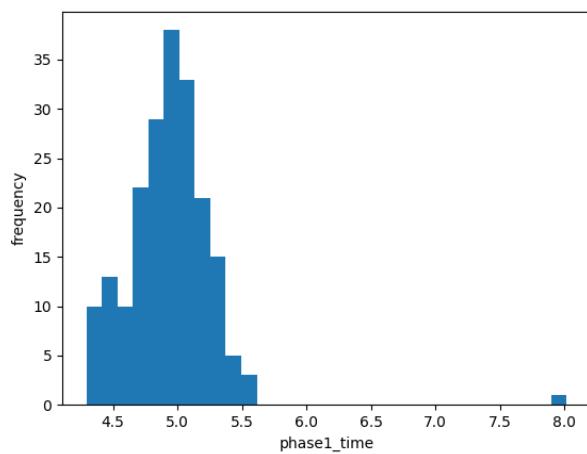


図 612 pht/experiment-win/1215-1/phase1/1215-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50055.eps

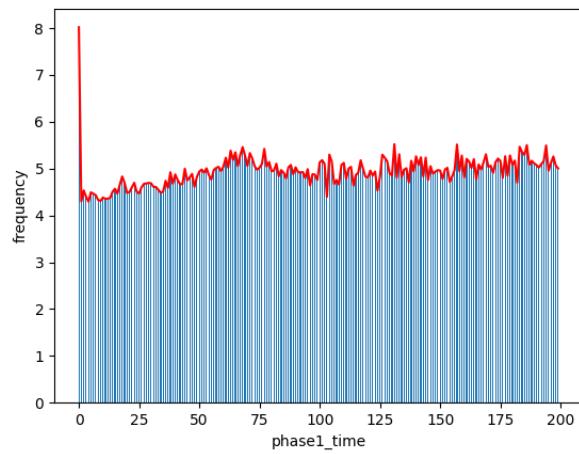


図 613 pht/experiment-win/1215-1/phase1/1215-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50055-in-chronological-order.eps

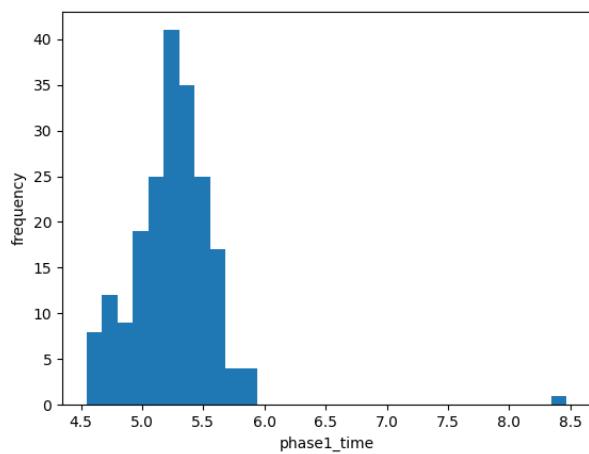


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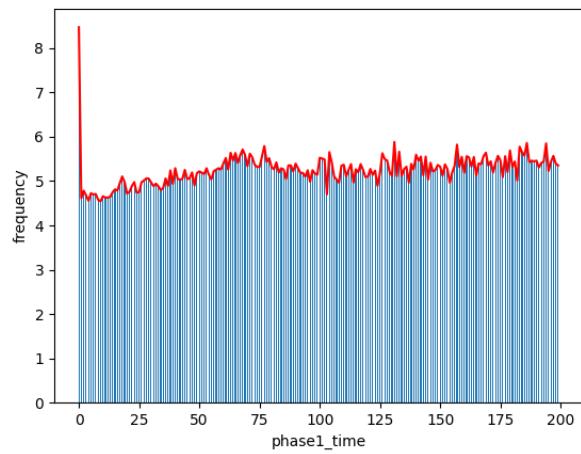


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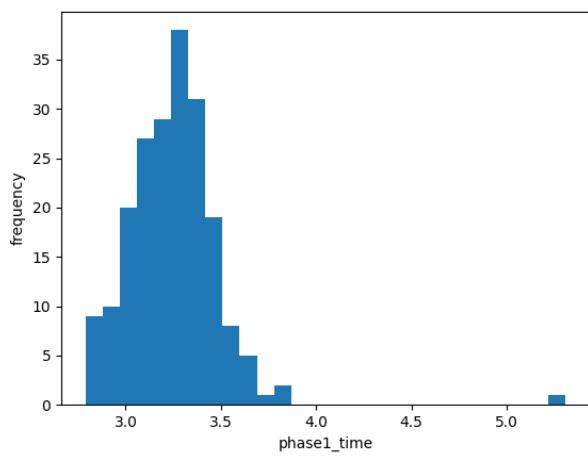


図 616 pht/experiment-win/1215-1/phase1/1215-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50191.eps

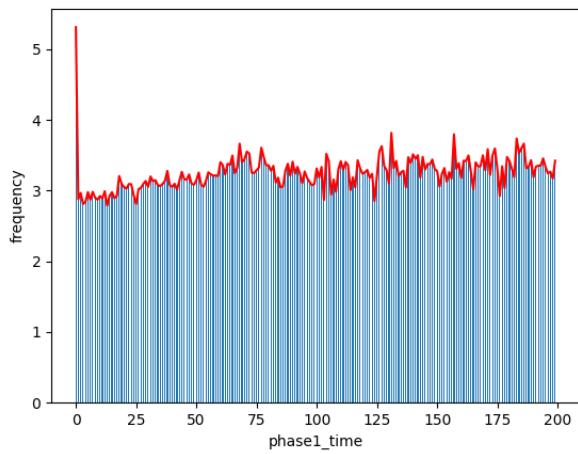


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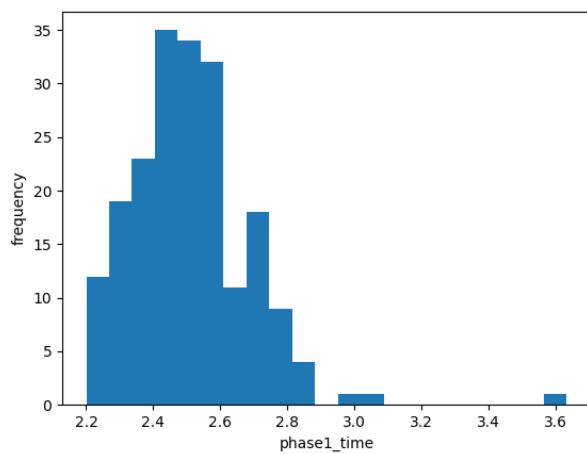


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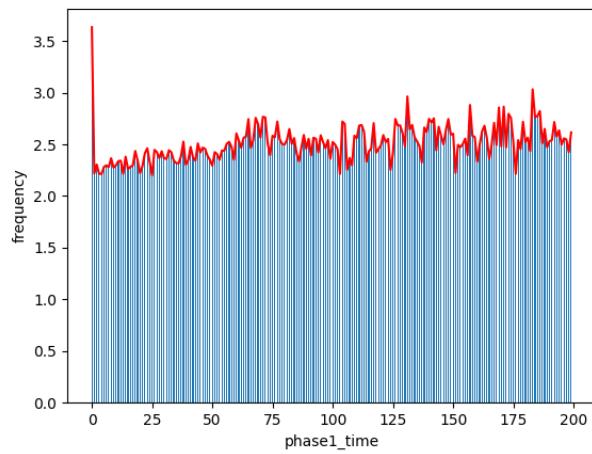


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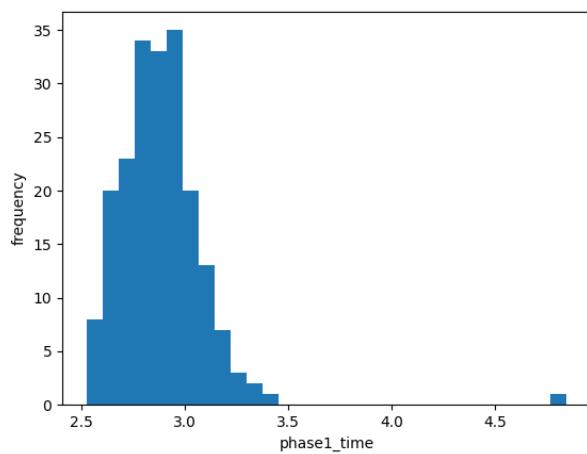


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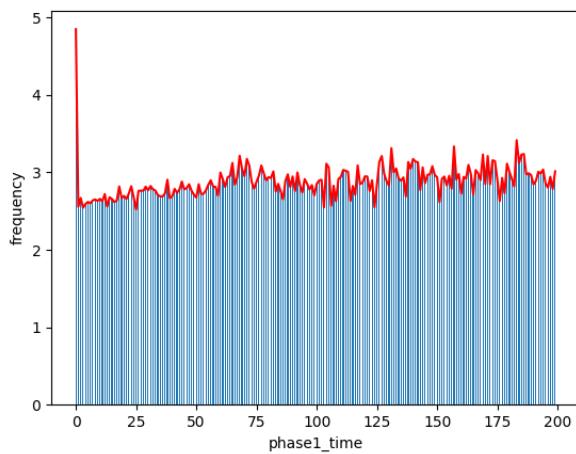


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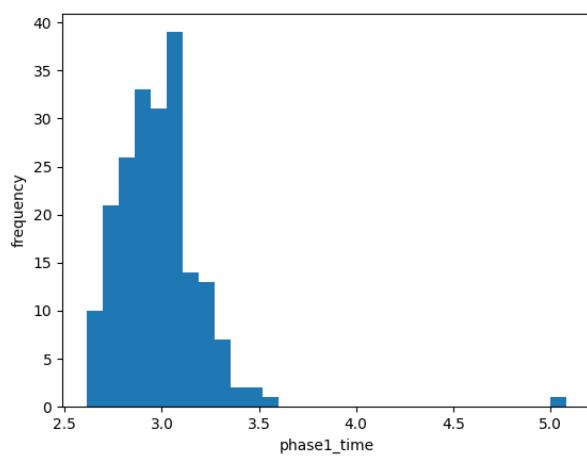


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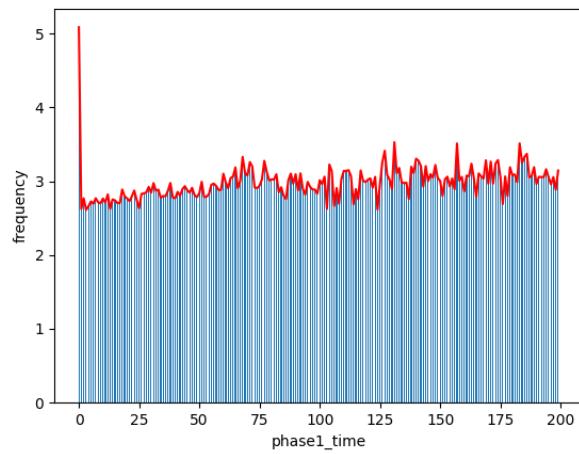


図 623 pht/experiment-win/1215-1/phase1/1215-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50197-in-chronological-order.eps

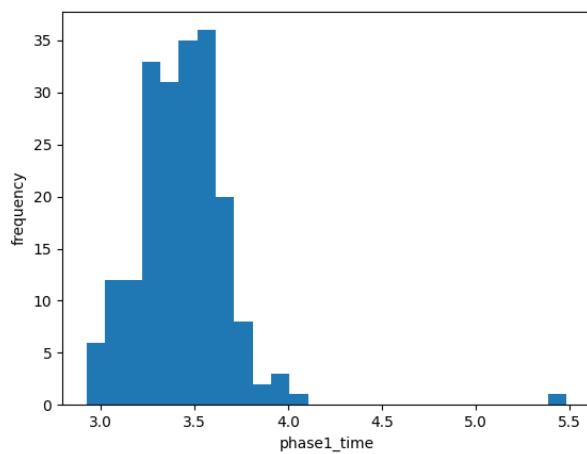


図 624 pht/experiment-win/1215-1/phase1/1215-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50199.eps

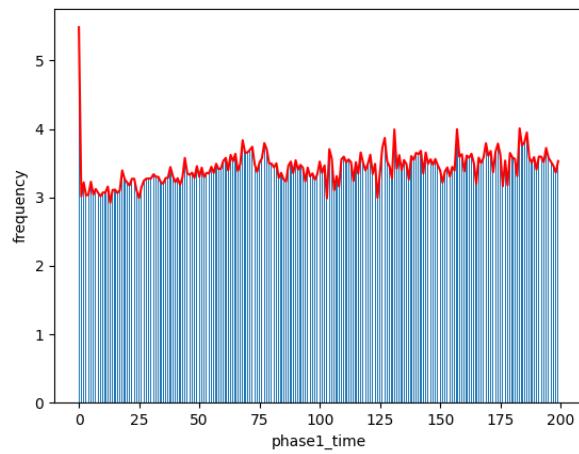


図 625 pht/experiment-win/1215-1/phase1/1215-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50199-in-chronological-order.eps

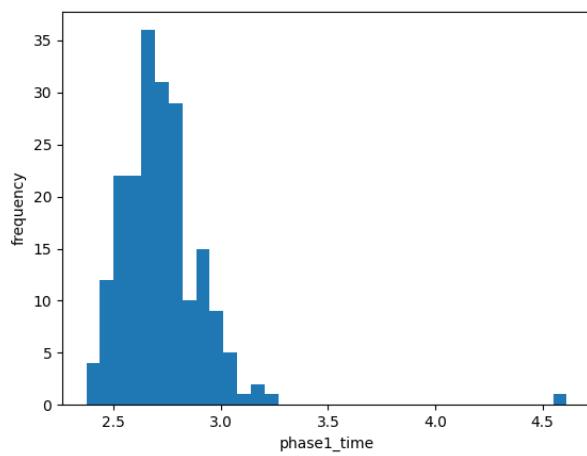


図 626 pht/experiment-win/1215-1/phase1/1215-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50201.eps

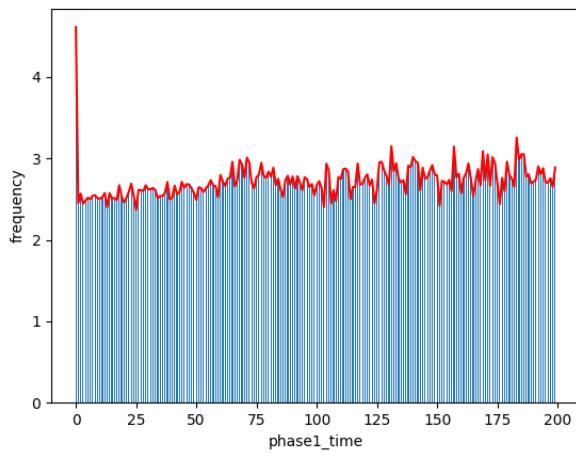


図 627 pht/experiment-win/1215-1/phase1/1215-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50201-in-chronological-order.eps

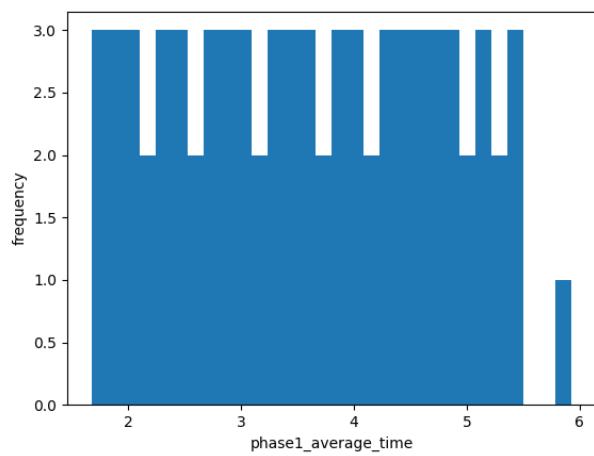


図 628 pht/experiment-win/1215-1/phase1/1215-1-75D-PC3ALL-DIF1-INT1-30analyze-phase1-average-time.eps

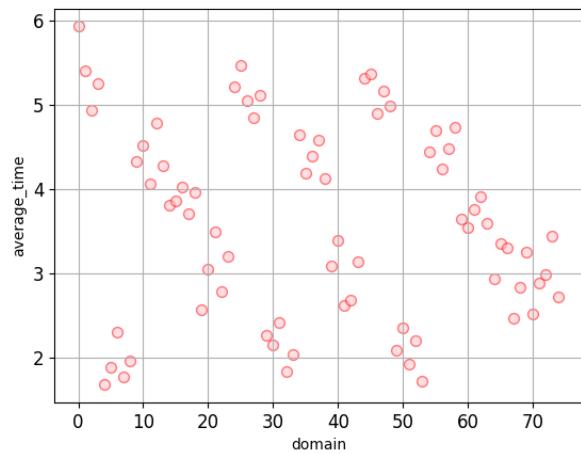


図 629 pht/experiment-win/1215-1/phase1/1215-1-75D-PC3ALL-DIF1-INT1-30analyze-phase1-average-time-scatter.eps

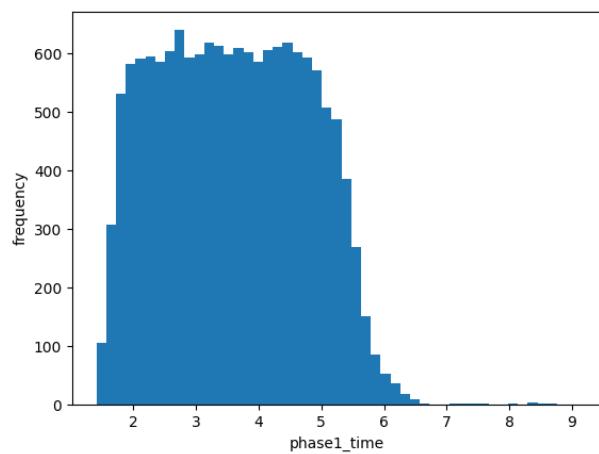


図 630 pht/experiment-win/1215-1/phase1/1215-1-75D-PC3ALL-DIF1-INT1-30analyze-phase1-time-auto.eps

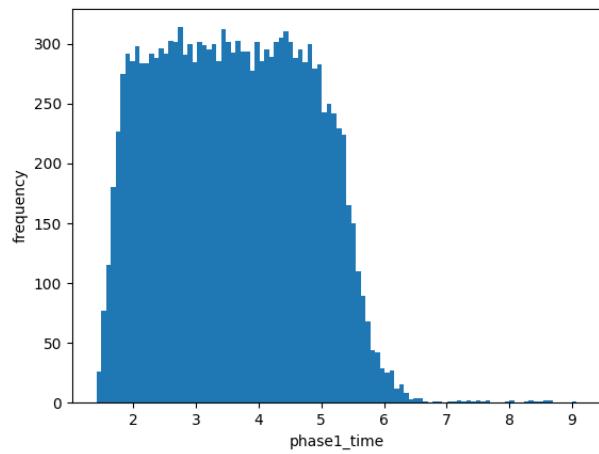


図 631 pht/experiment-win/1215-1/phase1/1215-1-75D-PC3ALL-DIF1-INT1-30analyze-phase1-time-bins100.eps

20 1215-2-75D-PC3ALL-DIF1-INT1-30

20 章にて報告する条件を表 19 に示す。

表 19 設定値の一覧

設定項目	設定値
起動ノード数 (ドメイン数 D)	70 [ノード]
履歴交差間隔	90 [秒]
履歴交差回数	200 [回]
ブロック不生成間隔	0.5[秒]
ブロック生成難易度	1 [0x]
PC 内同時起動台数	7 [台]
使用台数	10 [台]
ブロック不生成間隔 : 30 秒	70 [ノード]
ブロック不生成間隔 : 1 秒	0 [ノード]

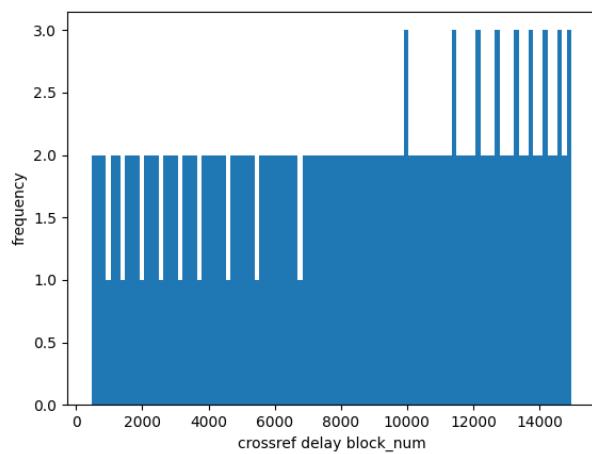


図 632 pht/experiment-win/1215-2/BC/1/1215-2-75D-PC3ALL-DIF1-INT1-30histogram-delay-blocknum.eps

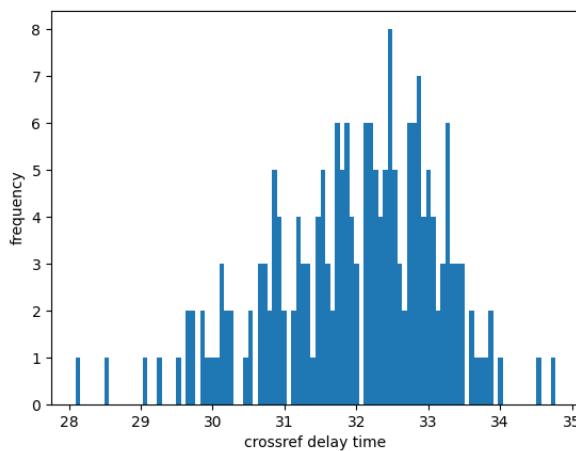


図 633 pht/experiment-win/1215-2/BC/1/1215-2-75D-PC3ALL-DIF1-INT1-30histogram-delay-time.eps

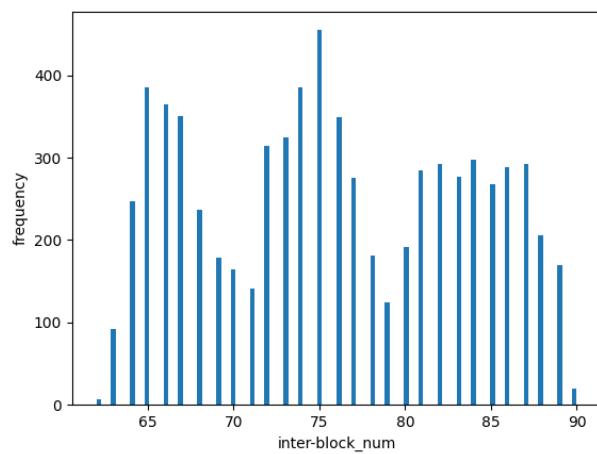


図 634 pht/experiment-win/1215-2/BC/1/1215-2-75D-PC3ALL-DIF1-INT1-30histogram-inter-blocknum.eps

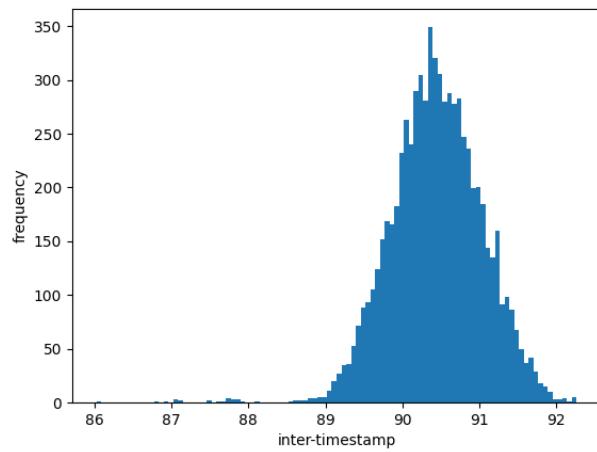


図 635 pht/experiment-win/1215-2/BC/1/1215-2-75D-PC3ALL-DIF1-INT1-30histogram-inter-timestamp.eps

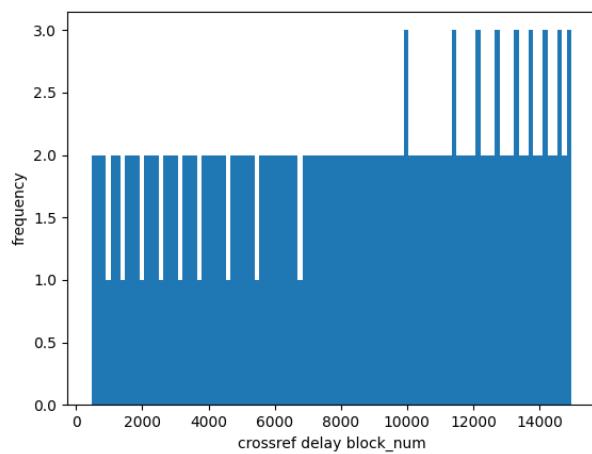


図 636 pht/experiment-win/1215-2/BC/1215-2-75D-PC3ALL-DIF1-INT1-30histogram-delay-blocknum.eps

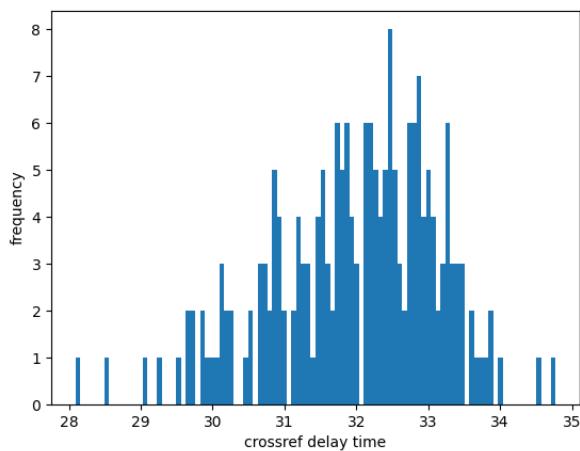


図 637 pht/experiment-win/1215-2/BC/1215-2-75D-PC3ALL-DIF1-INT1-30histogram-delay-time.eps

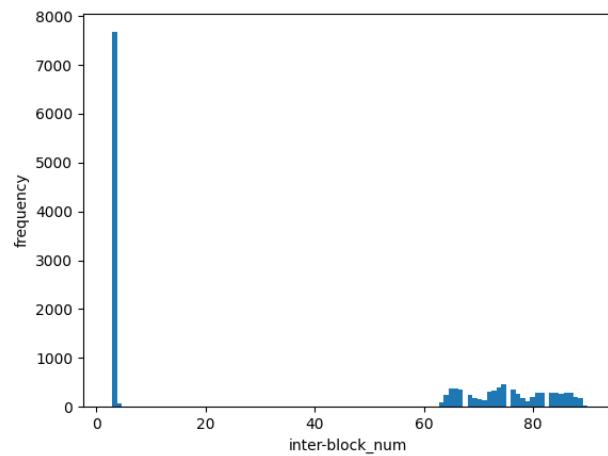


図 638 pht/experiment-win/1215-2/BC/1215-2-75D-PC3ALL-DIF1-INT1-30histogram-inter-blocknum.eps

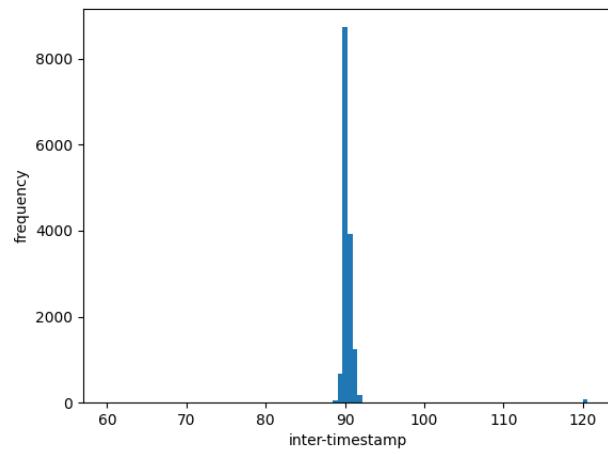


図 639 pht/experiment-win/1215-2/BC/1215-2-75D-PC3ALL-DIF1-INT1-30histogram-inter-timestamp.eps

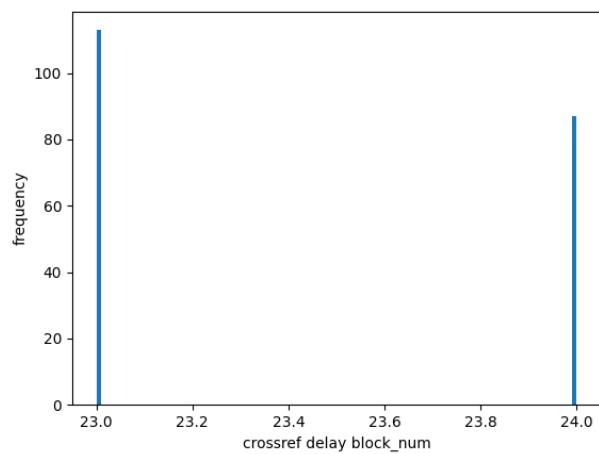


図 640 pht/experiment-win/1215-2/BC/30/1215-2-75D-PC3ALL-DIF1-INT1-30histogram-delay-blocknum.eps

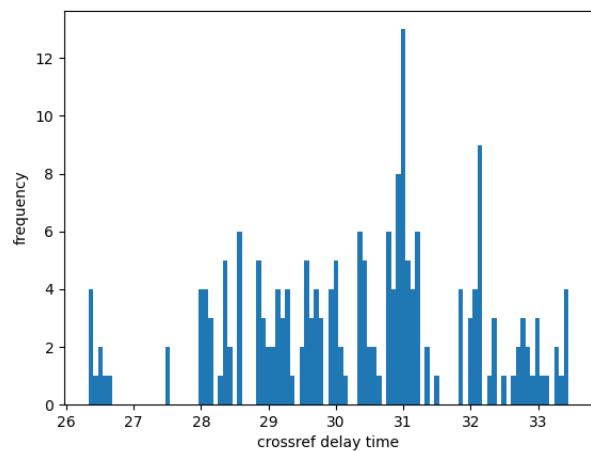


図 641 pht/experiment-win/1215-2/BC/30/1215-2-75D-PC3ALL-DIF1-INT1-30histogram-delay-time.eps

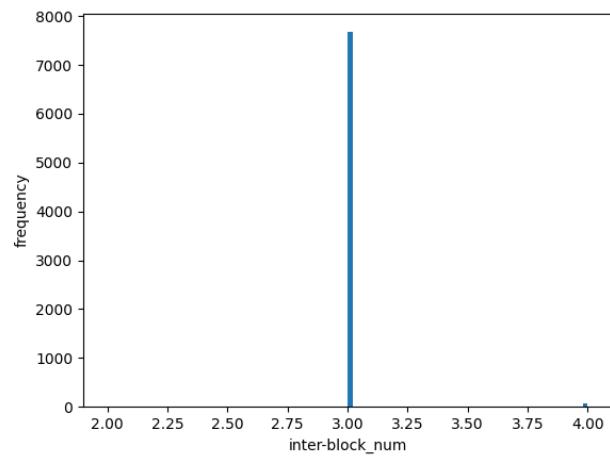


図 642 pht/experiment-win/1215-2/BC/30/1215-2-75D-PC3ALL-DIF1-INT1-30histogram-inter-blocknum.eps

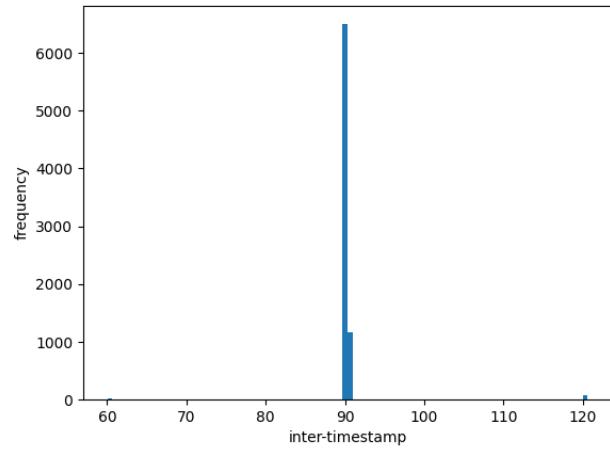


図 643 pht/experiment-win/1215-2/BC/30/1215-2-75D-PC3ALL-DIF1-INT1-30histogram-inter-timestamp.eps

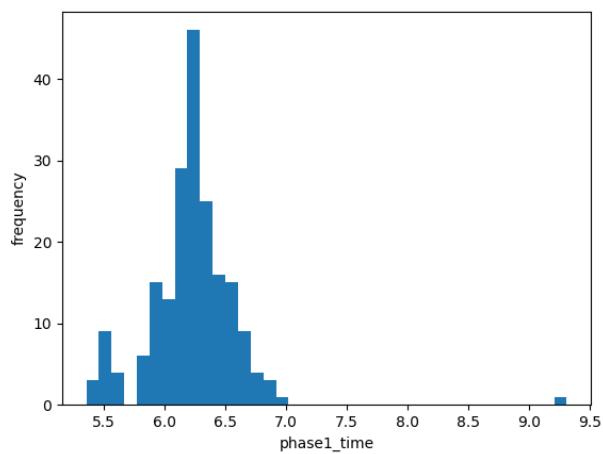


図 644 pht/experiment-win/1215-2/phase1/1215-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50051.eps

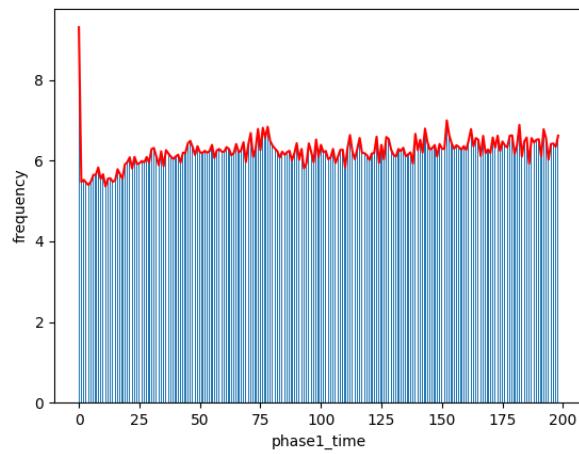


図 645 pht/experiment-win/1215-2/phase1/1215-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50051-in-chronological-order.eps

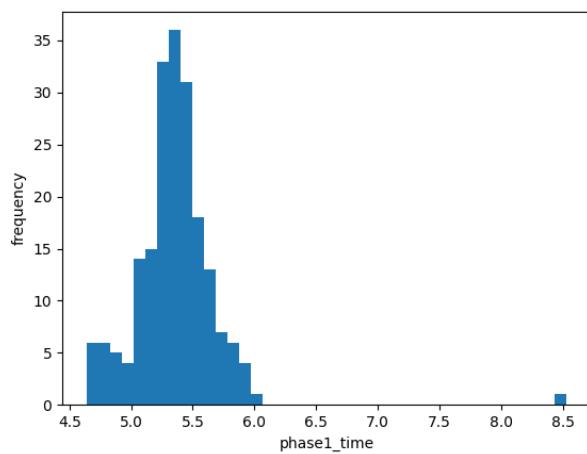


図 646 pht/experiment-win/1215-2/phase1/1215-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50053.eps

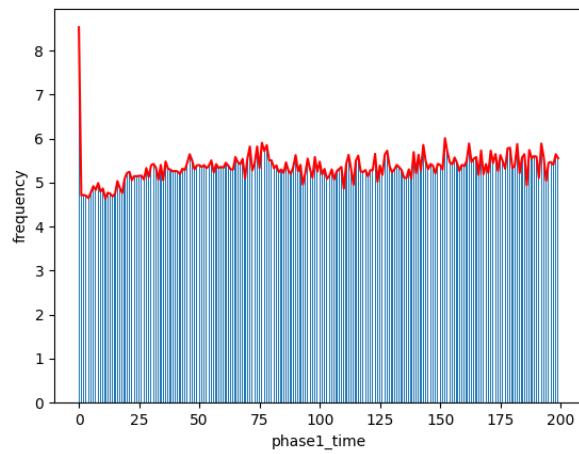


図 647 pht/experiment-win/1215-2/phase1/1215-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50053-in-chronological-order.eps

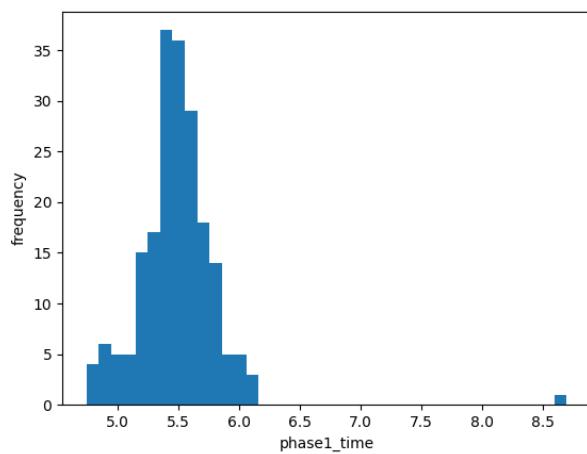


図 648 pht/experiment-win/1215-2/phase1/1215-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50055.eps

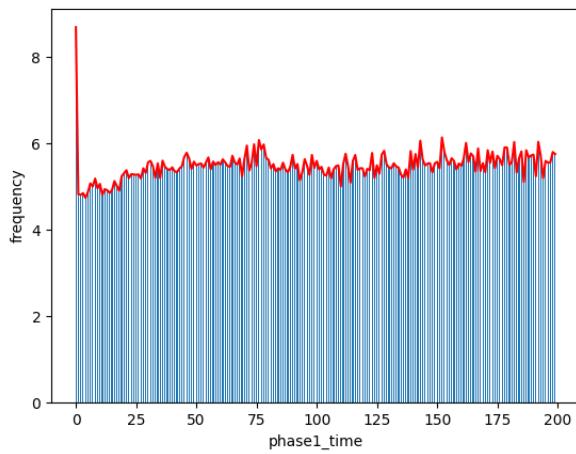


図 649 pht/experiment-win/1215-2/phase1/1215-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50055-in-chronological-order.eps

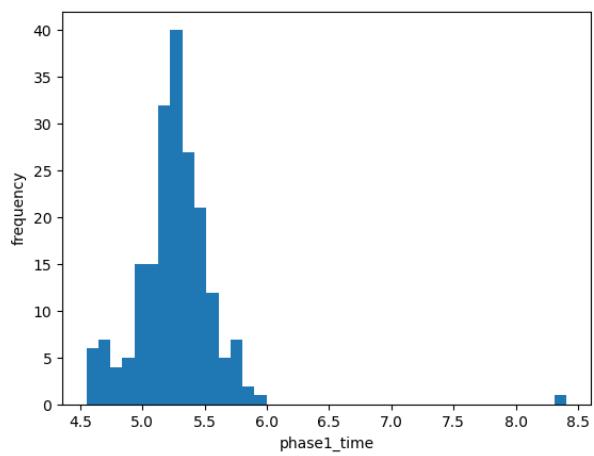


図 650 pht/experiment-win/1215-2/phase1/1215-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50059.eps

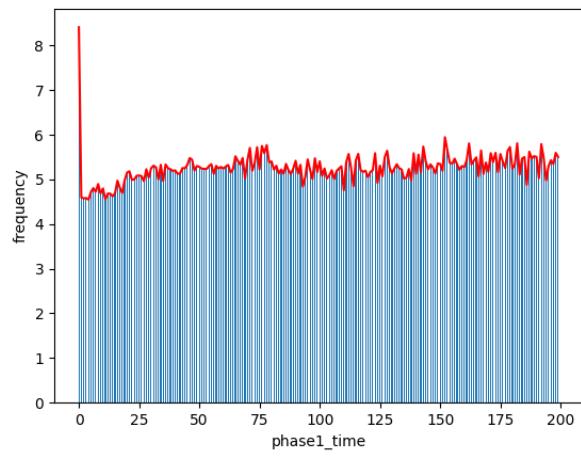


図 651 pht/experiment-win/1215-2/phase1/1215-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50059-in-chronological-order.eps

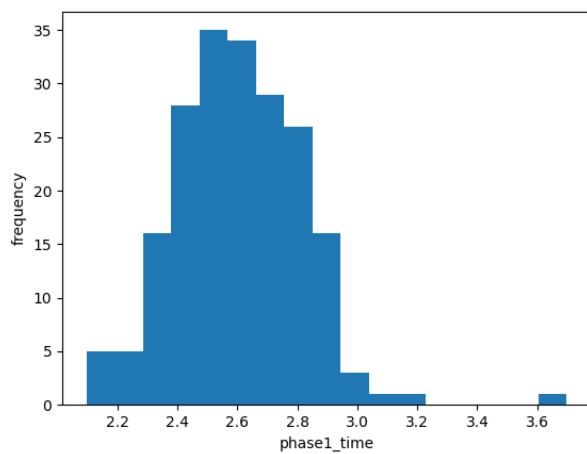


図 652 pht/experiment-win/1215-2/phase1/1215-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50191.eps

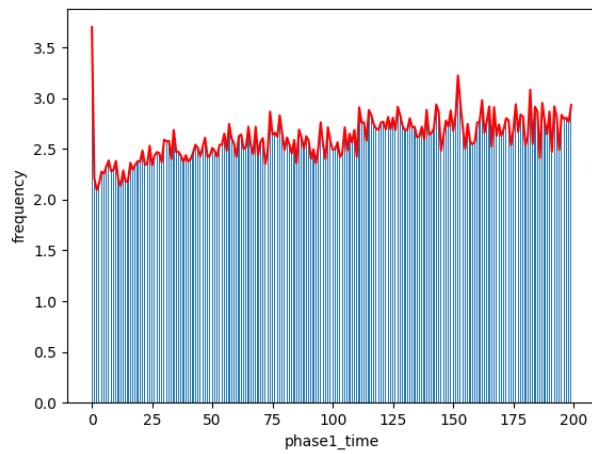


図 653 pht/experiment-win/1215-2/phase1/1215-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50191-in-chronological-order.eps

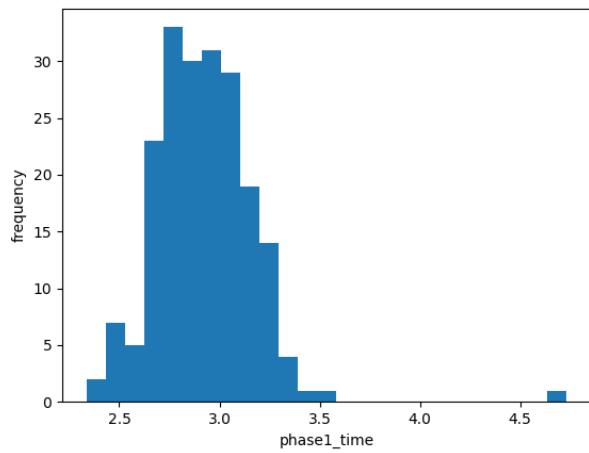


図 654 pht/experiment-win/1215-2/phase1/1215-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50193.eps

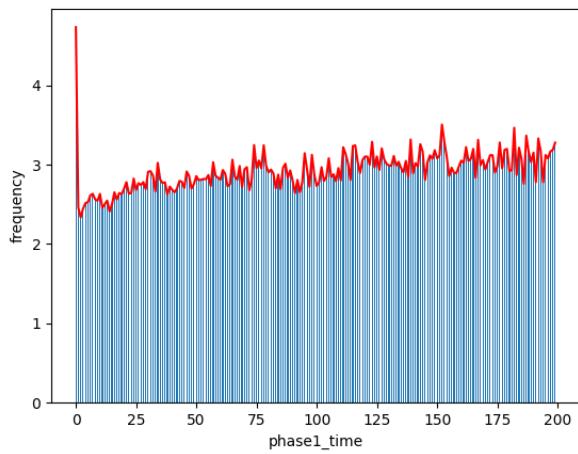


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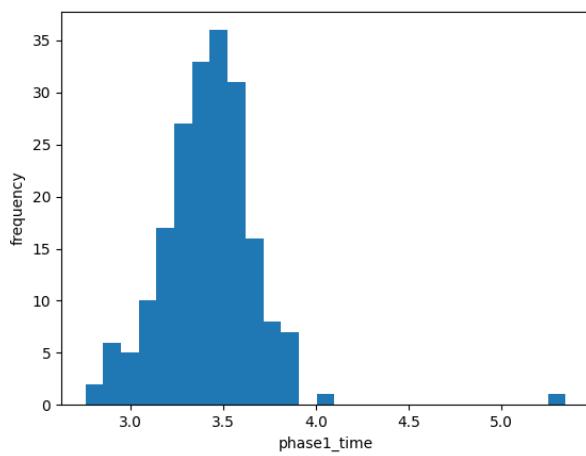


図 656 pht/experiment-win/1215-2/phase1/1215-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50195.eps

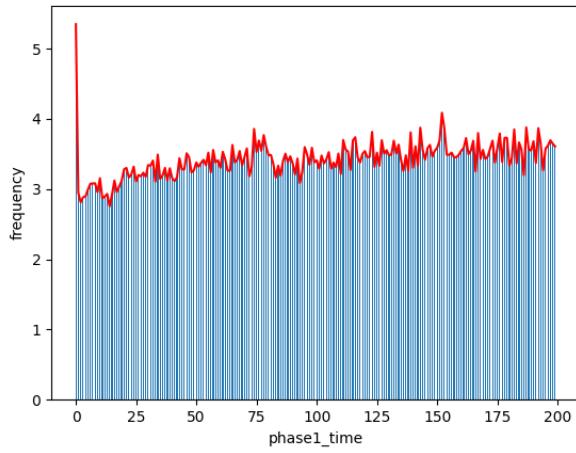


図 657 pht/experiment-win/1215-2/phase1/1215-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50195-in-chronological-order.eps

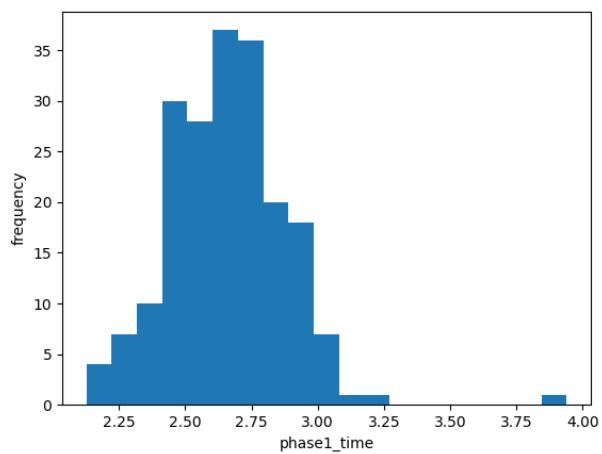


図 658 pht/experiment-win/1215-2/phase1/1215-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50197.eps

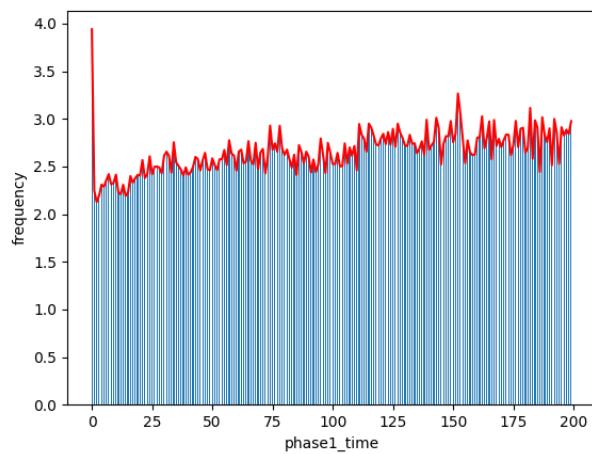


図 659 pht/experiment-win/1215-2/phase1/1215-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50197-in-chronological-order.eps

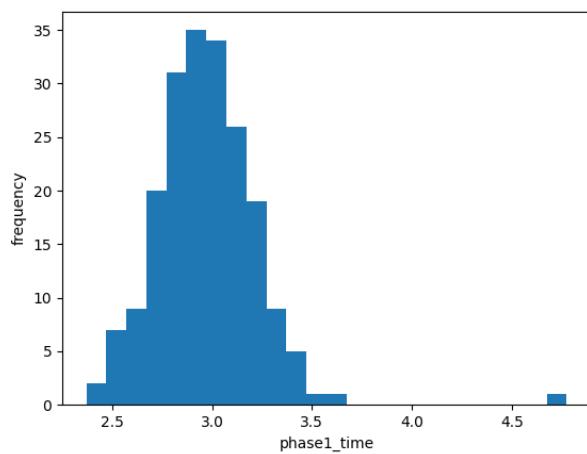


図 660 pht/experiment-win/1215-2/phase1/1215-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50199.eps

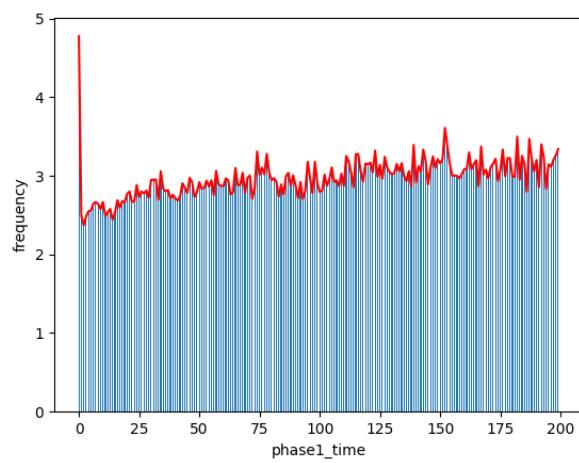


図 661 pht/experiment-win/1215-2/phase1/1215-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50199-in-chronological-order.eps

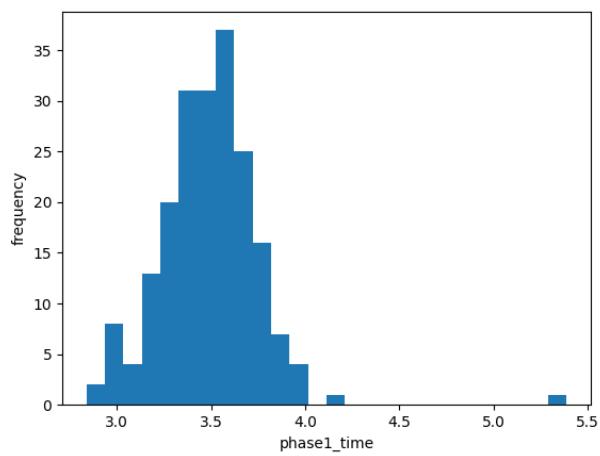


図 662 pht/experiment-win/1215-2/phase1/1215-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50201.eps

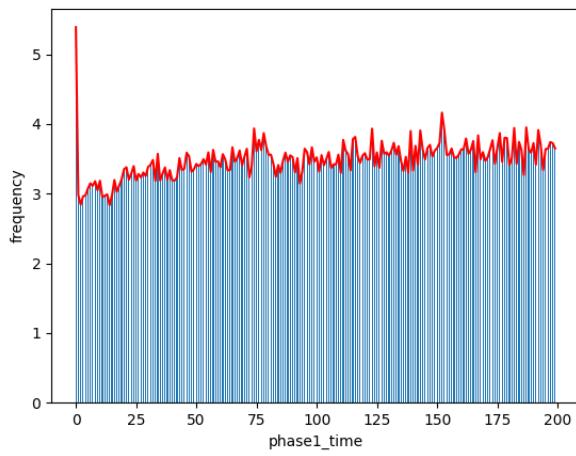


図 663 pht/experiment-win/1215-2/phase1/1215-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50201-in-chronological-order.eps

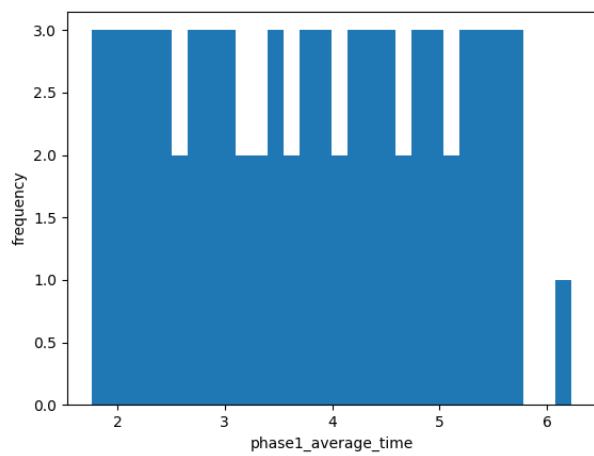


図 664 pht/experiment-win/1215-2/phase1/1215-2-75D-PC3ALL-DIF1-INT1-30analyze-phase1-average-time.eps

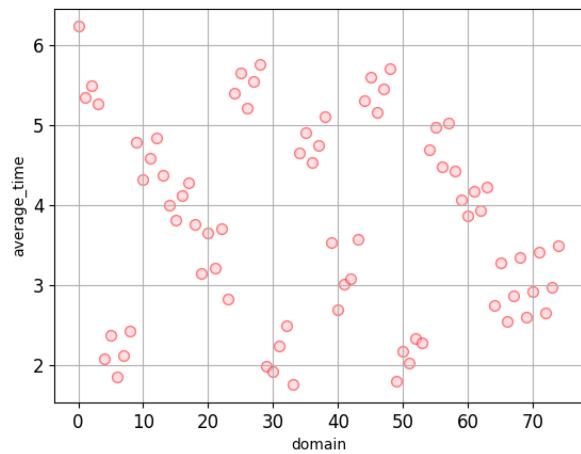


図 665 pht/experiment-win/1215-2/phase1/1215-2-75D-PC3ALL-DIF1-INT1-30analyze-phase1-average-time-scatter.eps

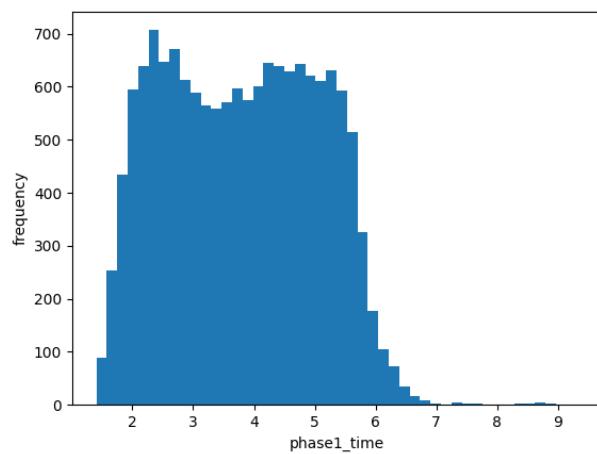


図 666 pht/experiment-win/1215-2/phase1/1215-2-75D-PC3ALL-DIF1-INT1-30analyze-phase1-time-auto.eps

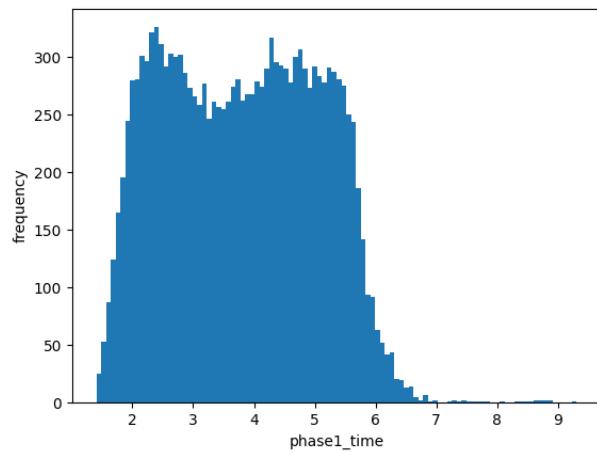


図 667 pht/experiment-win/1215-2/phase1/1215-2-75D-PC3ALL-DIF1-INT1-30analyze-phase1-time-bins100.eps

21 1216-1-75D-PC3ALL-DIF1-INT1-30

21 章にて報告する条件を表 20 に示す。

表 20 設定値の一覧

設定項目	設定値
起動ノード数 (ドメイン数 D)	70 [ノード]
履歴交差間隔	90 [秒]
履歴交差回数	200 [回]
ブロック不生成間隔	0.5[秒]
ブロック生成難易度	1 [0x]
PC 内同時起動台数	7 [台]
使用台数	10 [台]
ブロック不生成間隔 : 30 秒	70 [ノード]
ブロック不生成間隔 : 1 秒	0 [ノード]

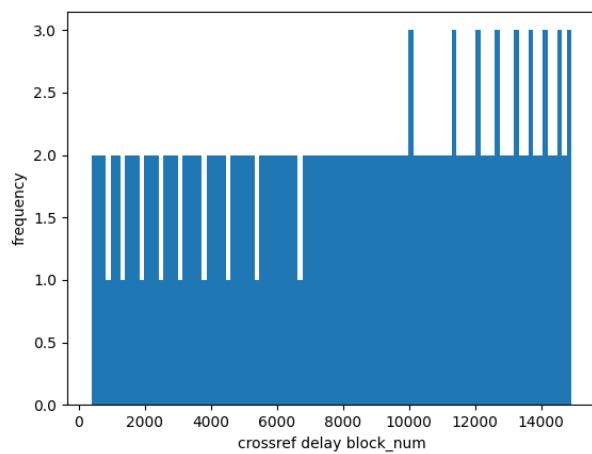


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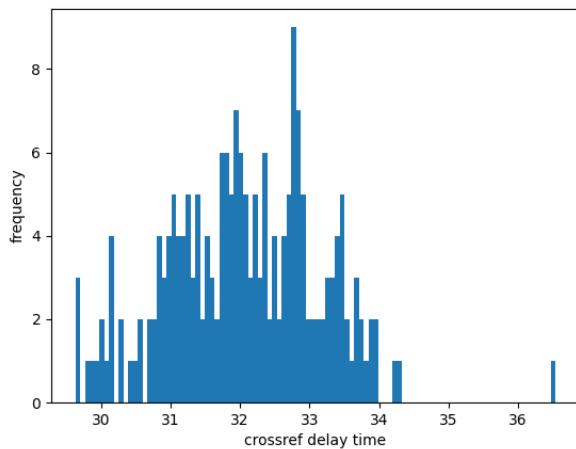


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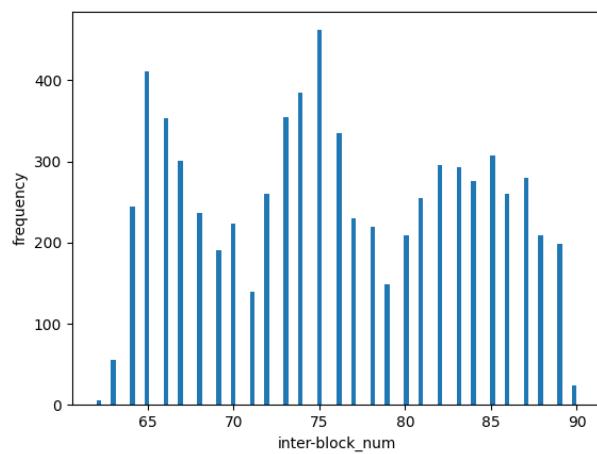


図 670 pht/experiment-win/1216-1/BC/1/1216-1-75D-PC3ALL-DIF1-INT1-30histogram-inter-blocknum.eps

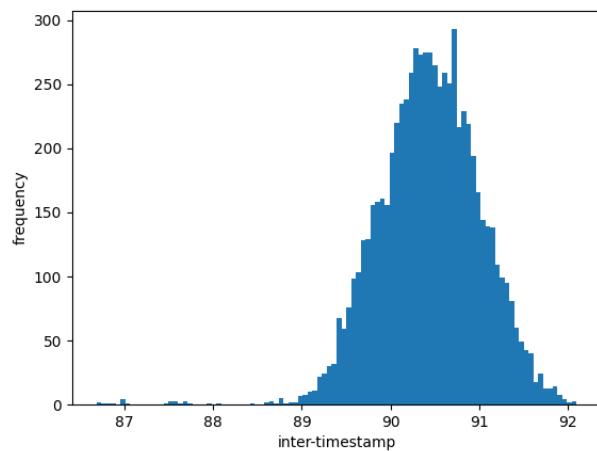


図 671 pht/experiment-win/1216-1/BC/1/1216-1-75D-PC3ALL-DIF1-INT1-30histogram-inter-timestamp.eps

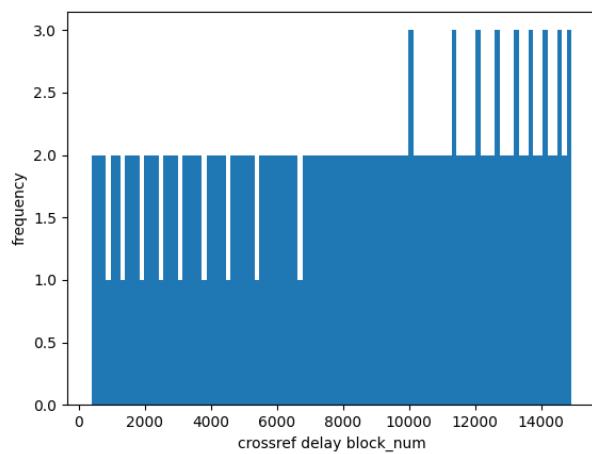


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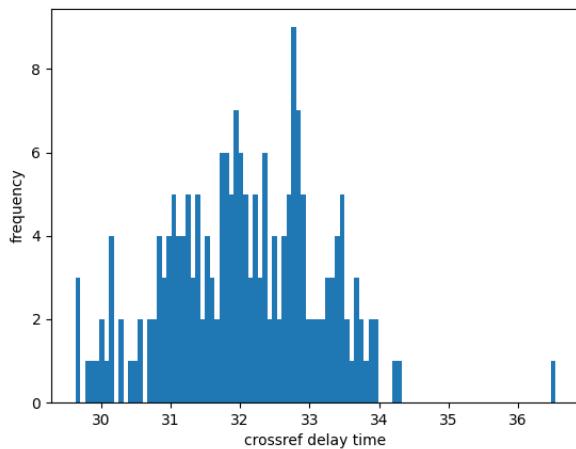


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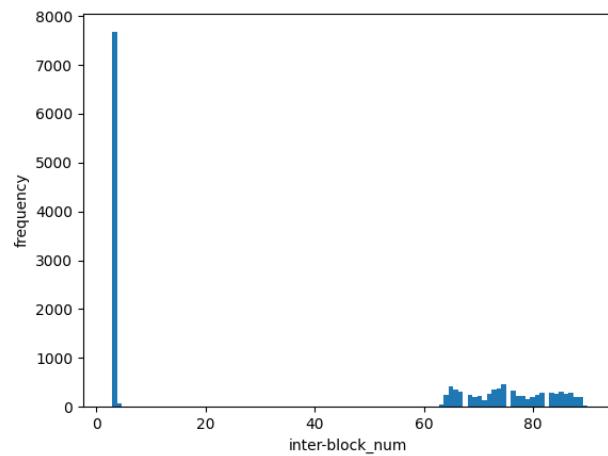


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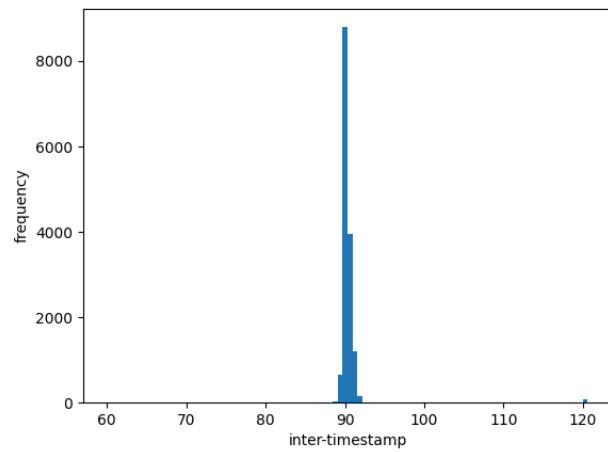


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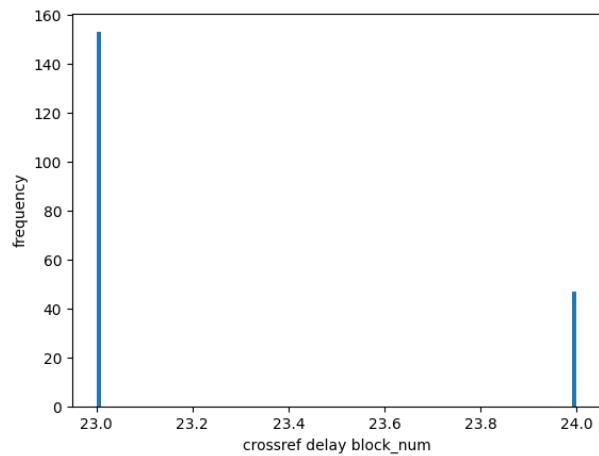


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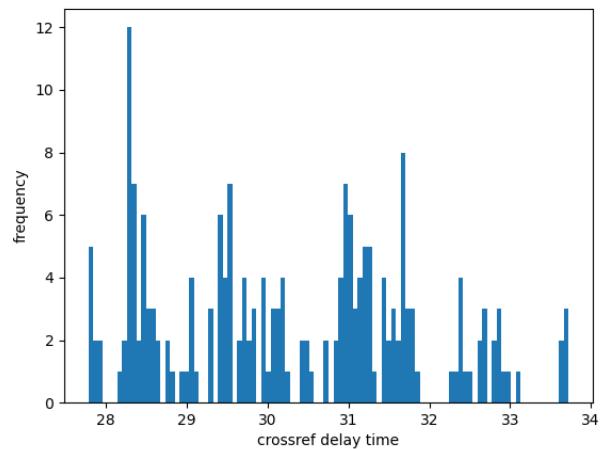


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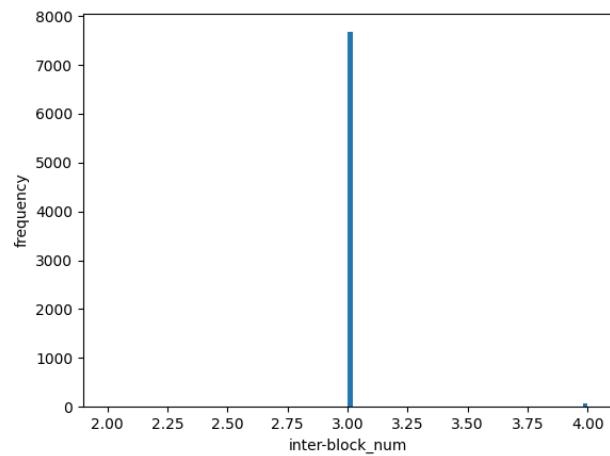


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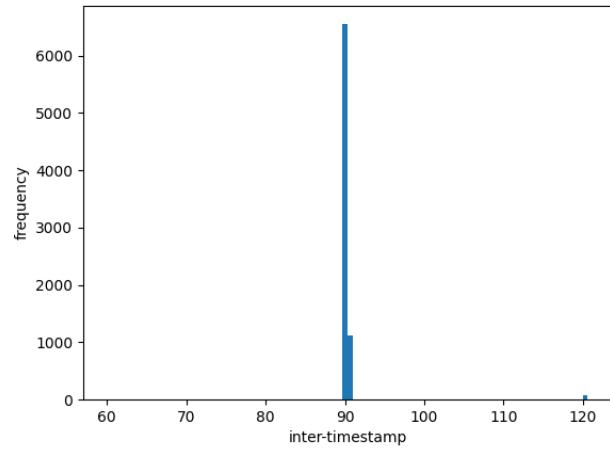


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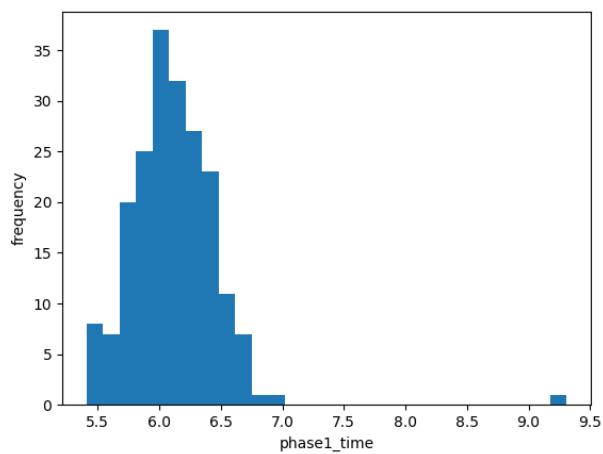


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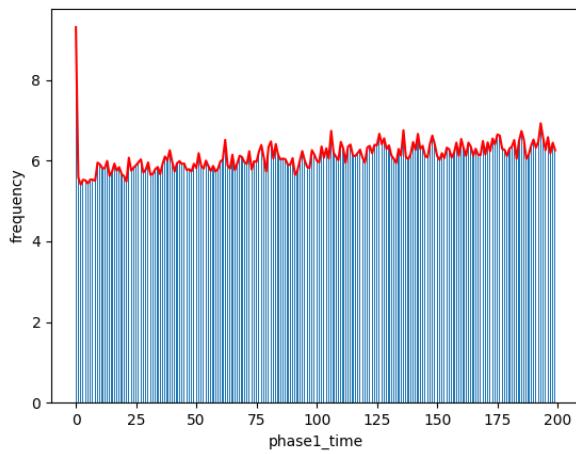


図 681 pht/experiment-win/1216-1/phase1/1216-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50051-in-chronological-order.eps

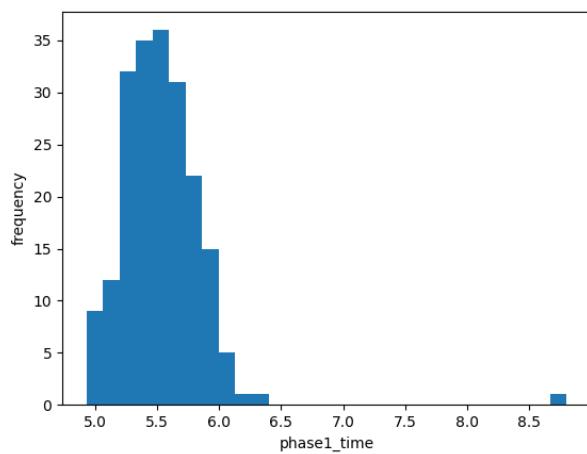


図 682 pht/experiment-win/1216-1/phase1/1216-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50053.eps

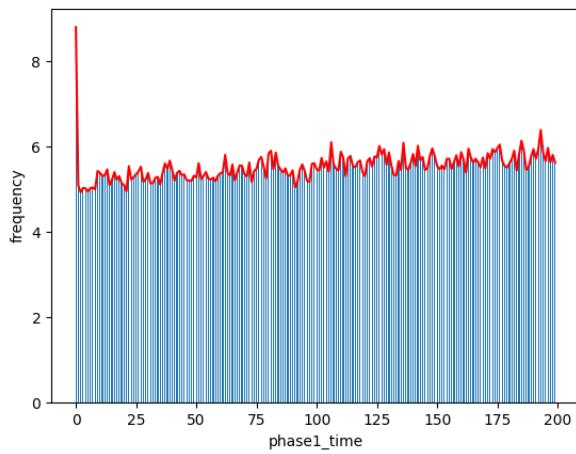


図 683 pht/experiment-win/1216-1/phase1/1216-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50053-in-chronological-order.eps

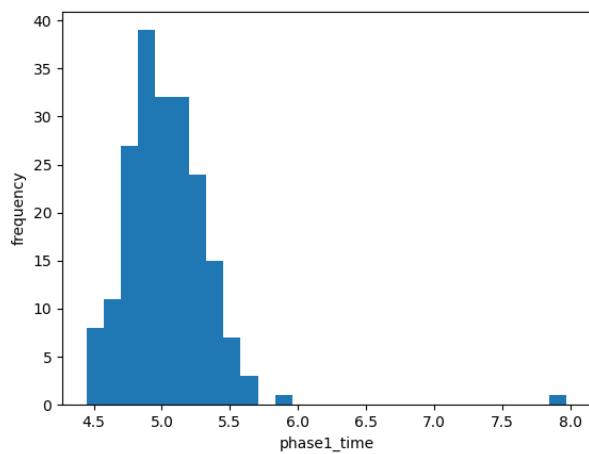


図 684 pht/experiment-win/1216-1/phase1/1216-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50055.eps

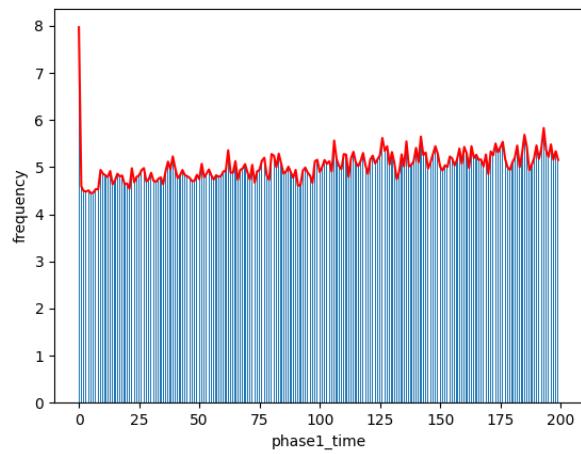


図 685 pht/experiment-win/1216-1/phase1/1216-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50055-in-chronological-order.eps

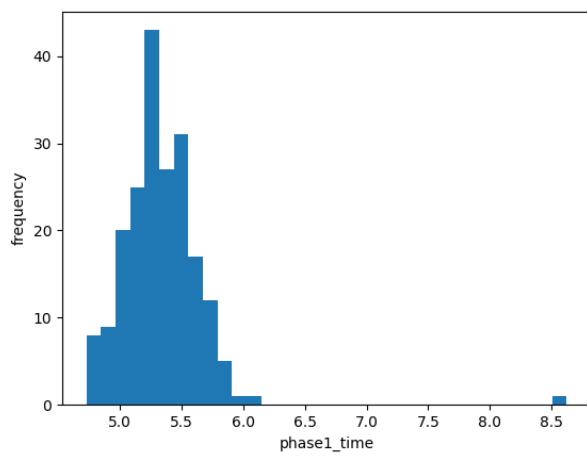


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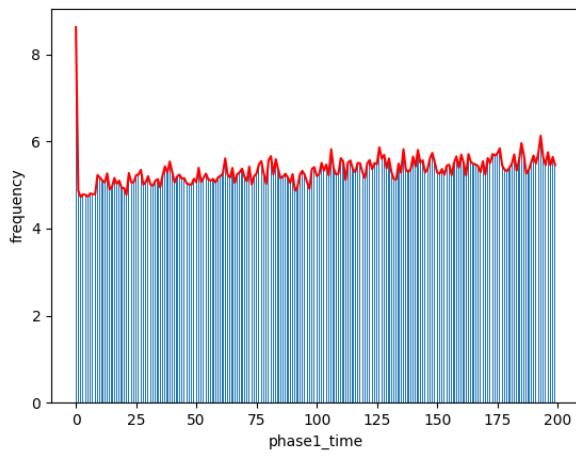


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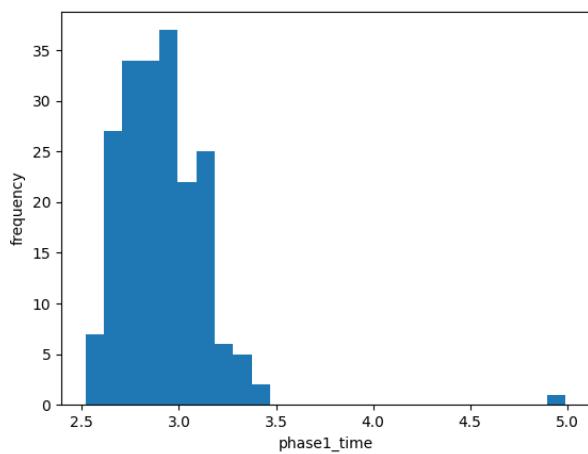


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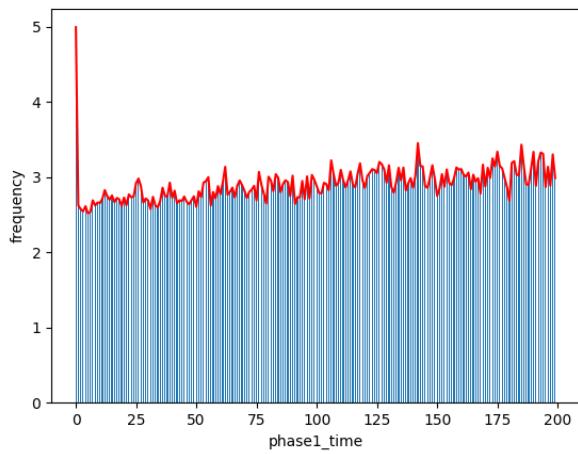


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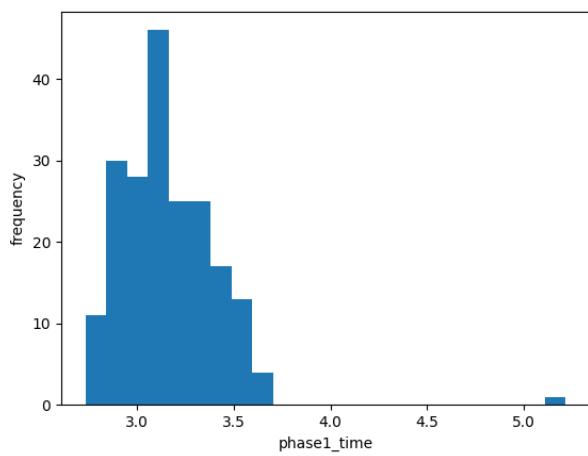


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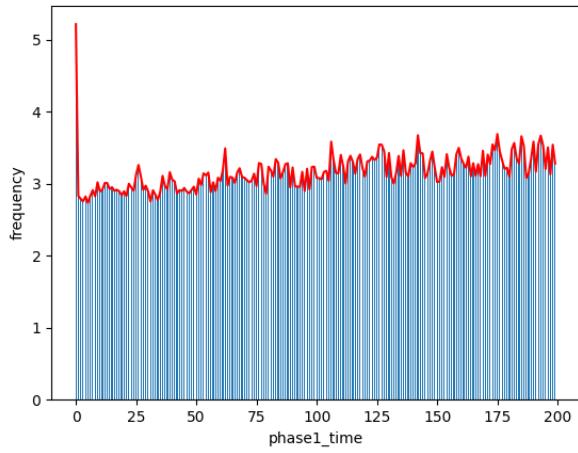


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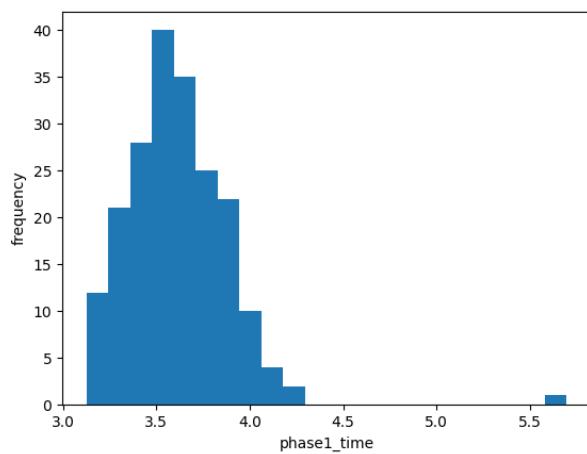


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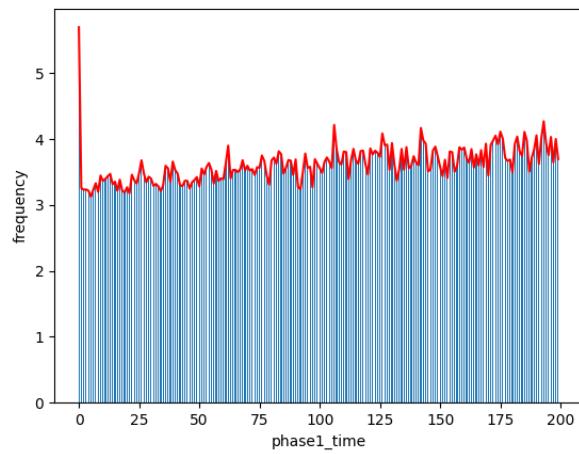


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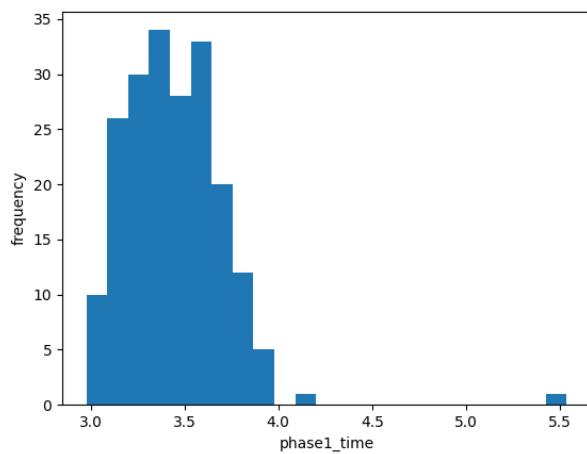


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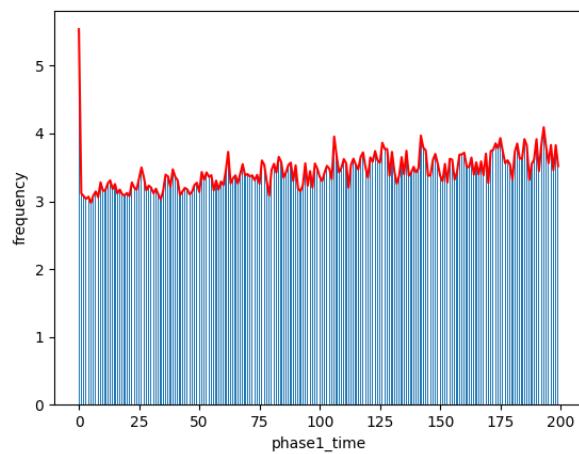


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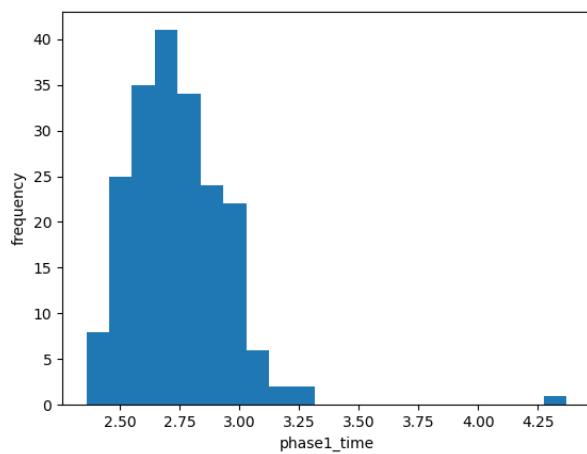


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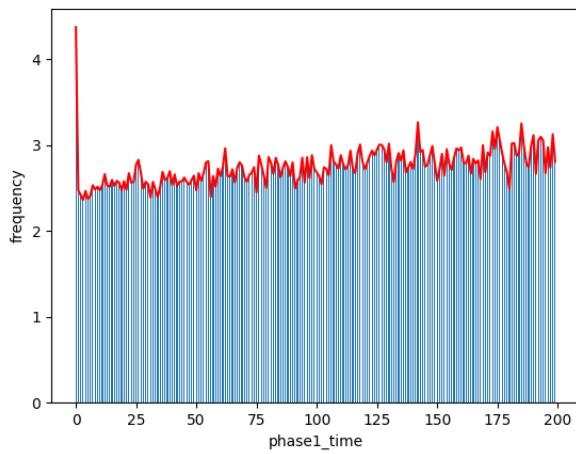


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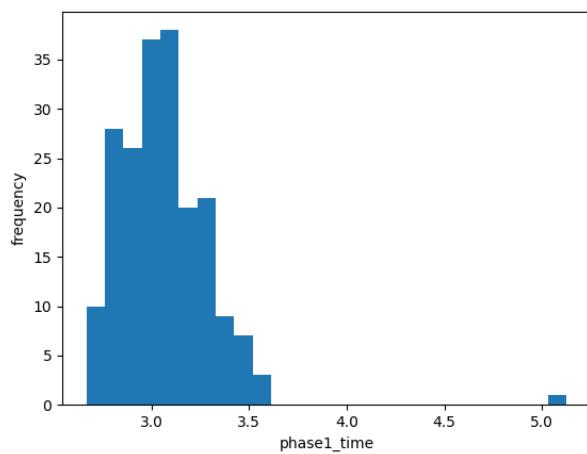
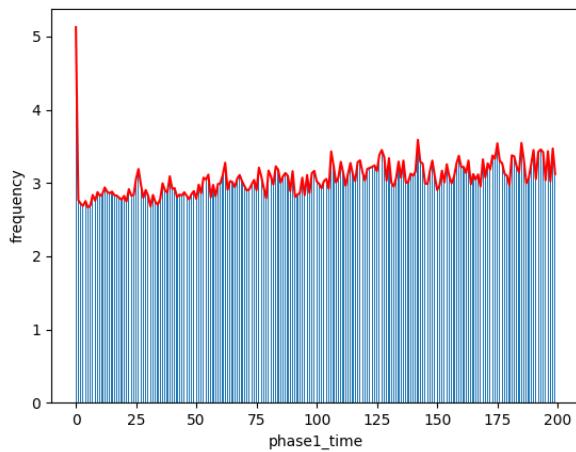


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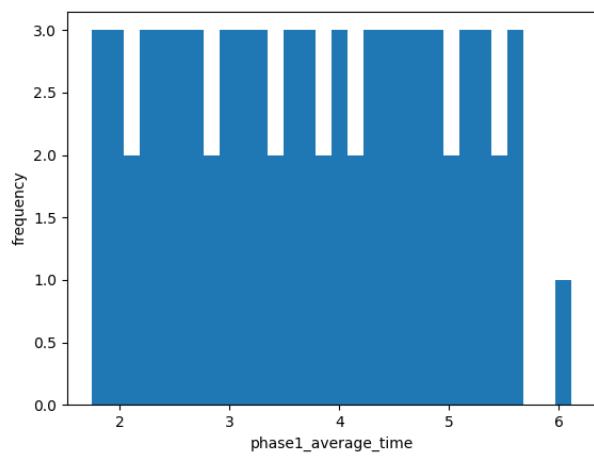


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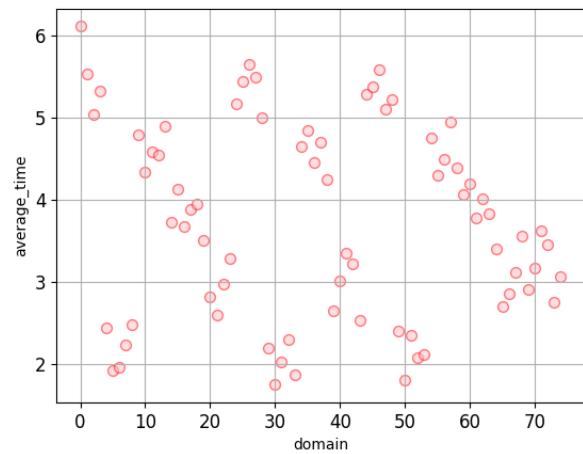


図 701 pht/experiment-win/1216-1/phase1/1216-1-75D-PC3ALL-DIF1-INT1-30analyze-phase1-average-time-scatter.eps

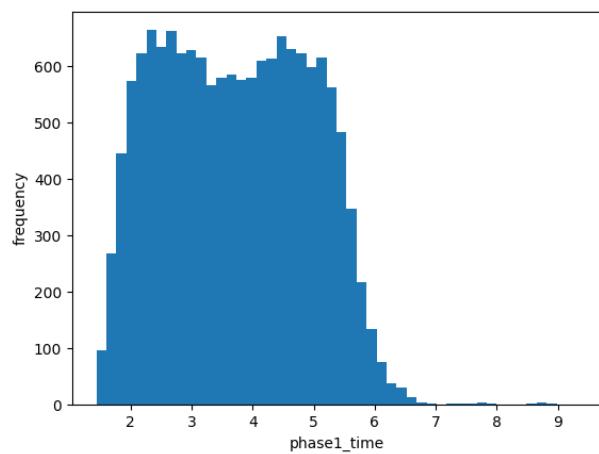


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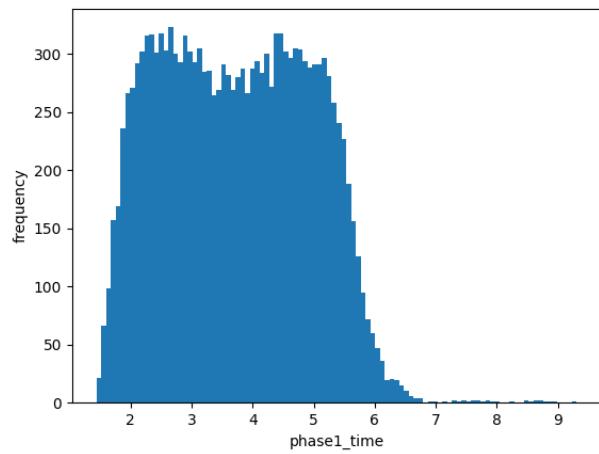


図 703 pht/experiment-win/1216-1/phase1/1216-1-75D-PC3ALL-DIF1-INT1-30analyze-phase1-time-bins100.eps

22 1216-2-75D-PC3ALL-DIF1-INT1-30

22 章にて報告する条件を表 21 に示す。

表 21 設定値の一覧

設定項目	設定値
起動ノード数 (ドメイン数 D)	70 [ノード]
履歴交差間隔	90 [秒]
履歴交差回数	200 [回]
ブロック不生成間隔	0.5[秒]
ブロック生成難易度	1 [0x]
PC 内同時起動台数	7 [台]
使用台数	10 [台]
ブロック不生成間隔 : 30 秒	70 [ノード]
ブロック不生成間隔 : 1 秒	0 [ノード]

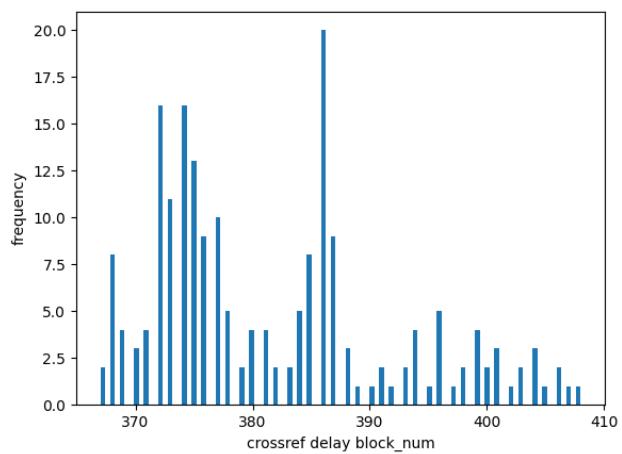


図 704 pht/experiment-win/1216-2/BC/1/1216-2-75D-PC3ALL-DIF1-INT1-30histogram-delay-blocknum.eps

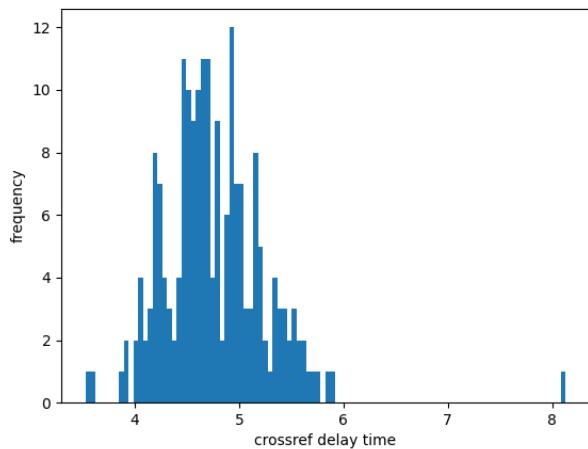


図 705 pht/experiment-win/1216-2/BC/1/1216-2-75D-PC3ALL-DIF1-INT1-30histogram-delay-time.eps

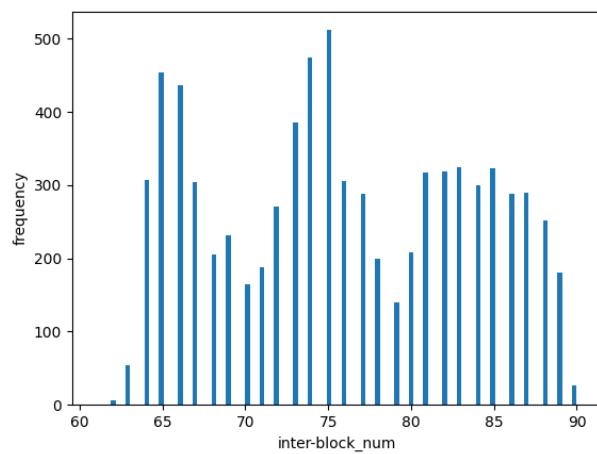


図 706 pht/experiment-win/1216-2/BC/1/1216-2-75D-PC3ALL-DIF1-INT1-30histogram-inter-blocknum.eps

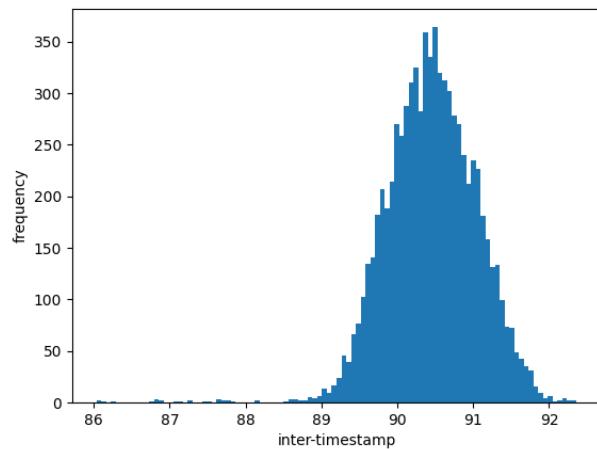


図 707 pht/experiment-win/1216-2/BC/1/1216-2-75D-PC3ALL-DIF1-INT1-30histogram-inter-timestamp.eps

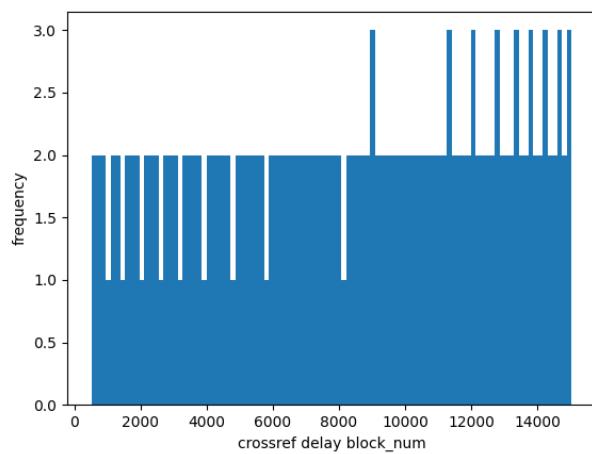


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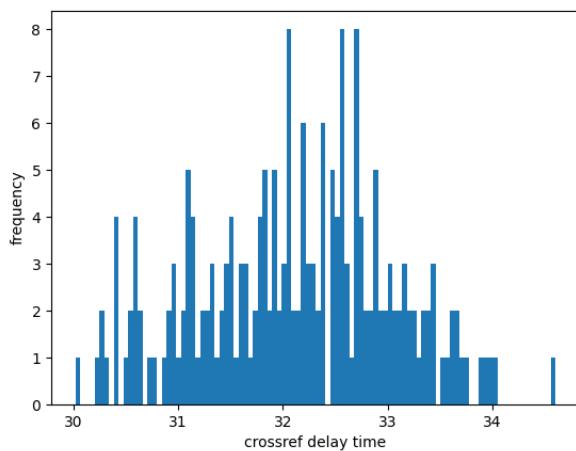


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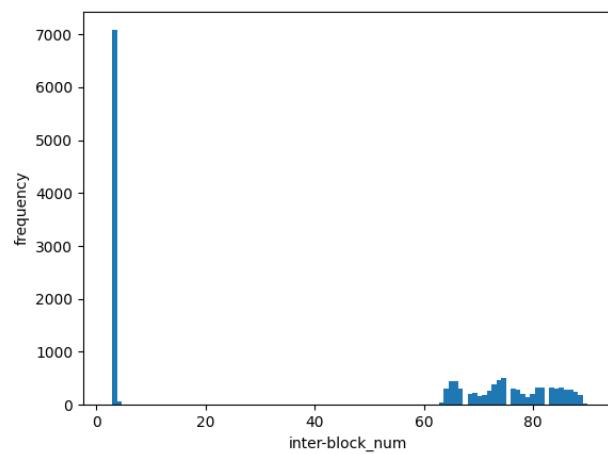


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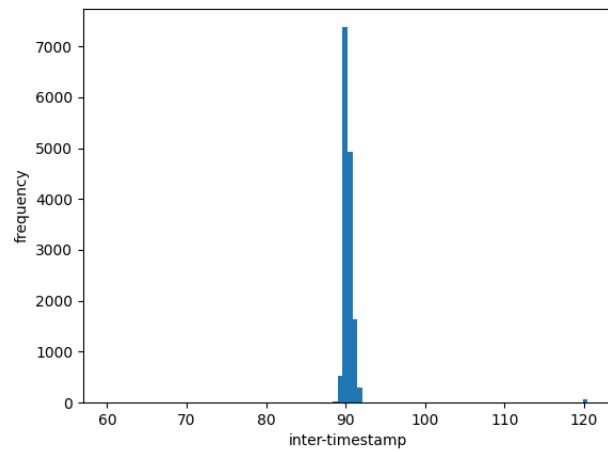


図 711 pht/experiment-win/1216-2/BC/1216-2-75D-PC3ALL-DIF1-INT1-30histogram-inter-timestamp.eps

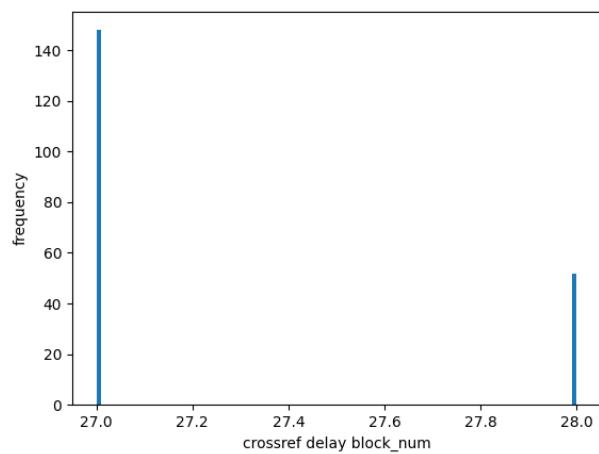


図 712 pht/experiment-win/1216-2/BC/30/1216-2-75D-PC3ALL-DIF1-INT1-30histogram-delay-blocknum.eps

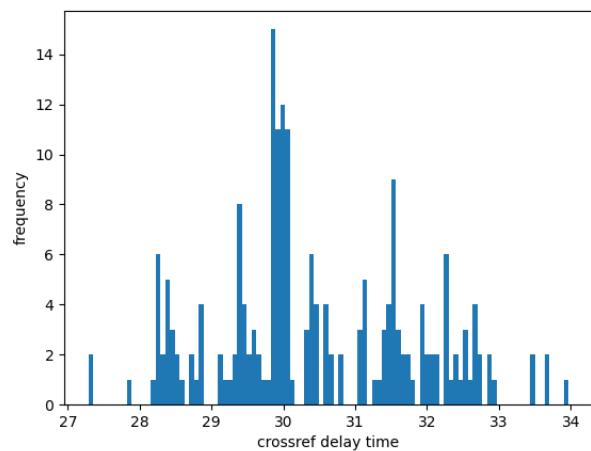


図 713 pht/experiment-win/1216-2/BC/30/1216-2-75D-PC3ALL-DIF1-INT1-30histogram-delay-time.eps

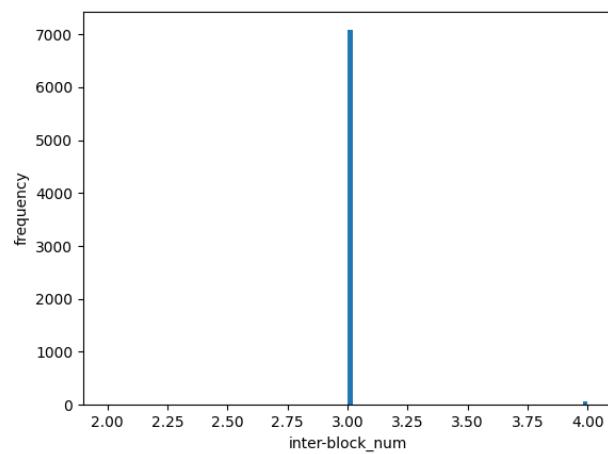


図 714 pht/experiment-win/1216-2/BC/30/1216-2-75D-PC3ALL-DIF1-INT1-30histogram-inter-blocknum.eps

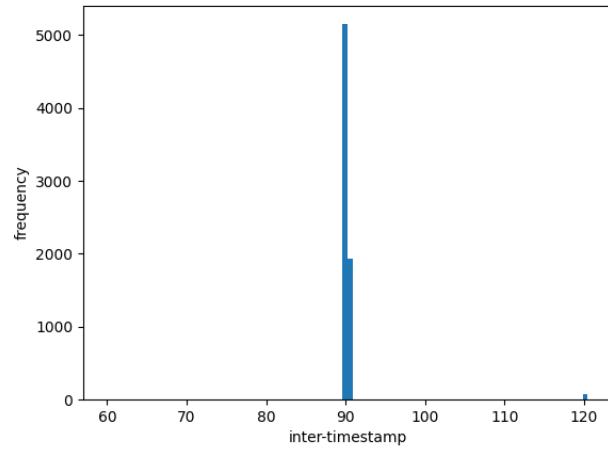


図 715 pht/experiment-win/1216-2/BC/30/1216-2-75D-PC3ALL-DIF1-INT1-30histogram-inter-timestamp.eps

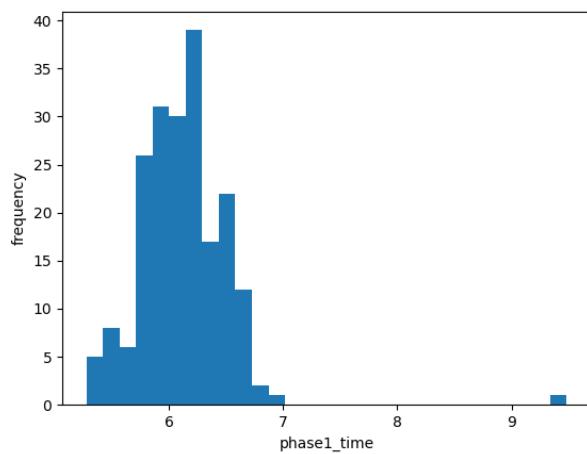


図 716 pht/experiment-win/1216-2/phase1/1216-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50051.eps

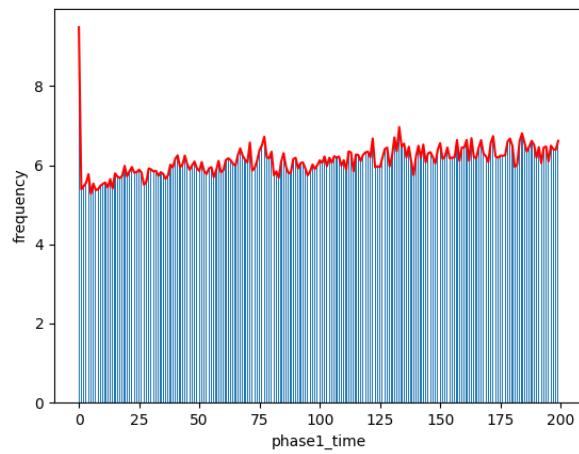


図 717 pht/experiment-win/1216-2/phase1/1216-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50051-in-chronological-order.eps

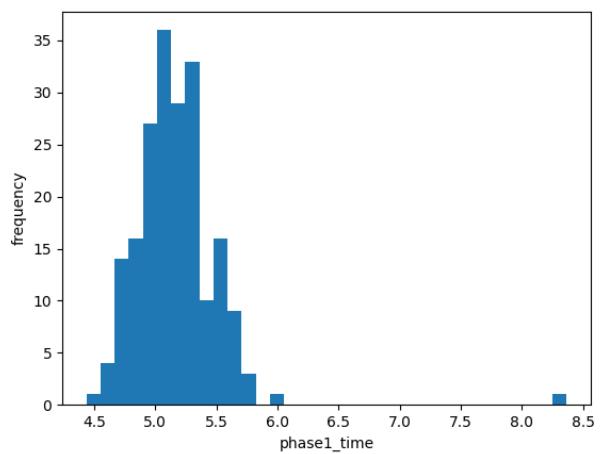


図 718 pht/experiment-win/1216-2/phase1/1216-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50053.eps

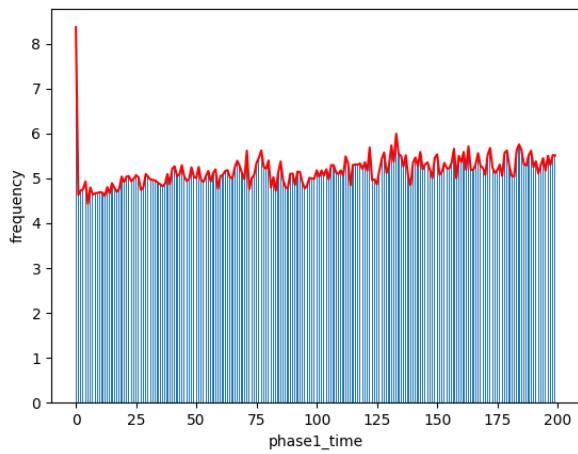


図 719 pht/experiment-win/1216-2/phase1/1216-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50053-in-chronological-order.eps

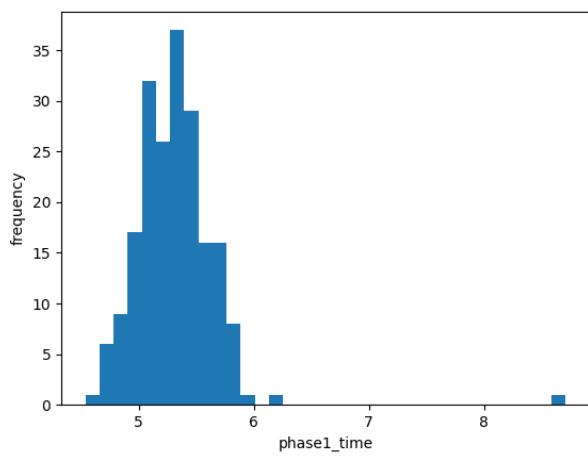


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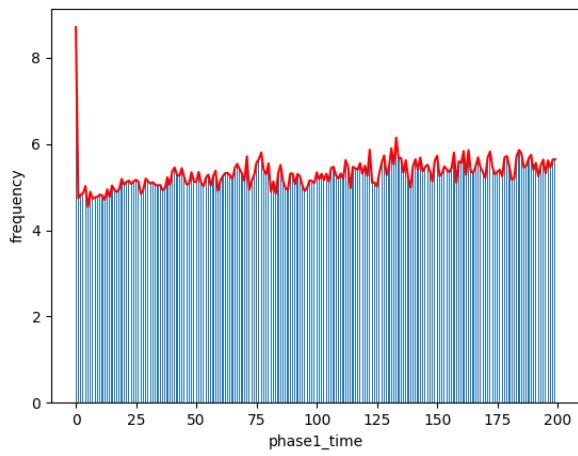


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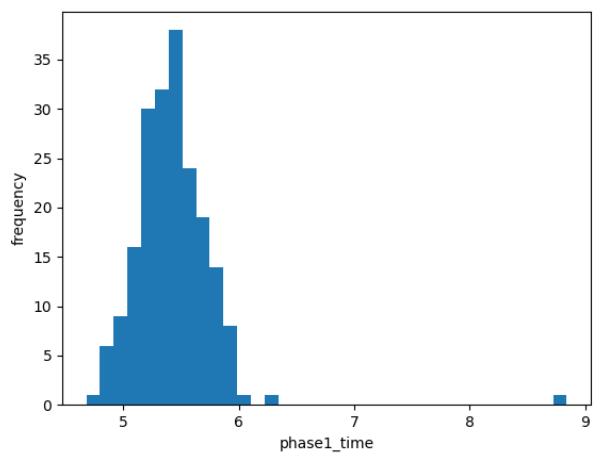


図 722 pht/experiment-win/1216-2/phase1/1216-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50059.eps

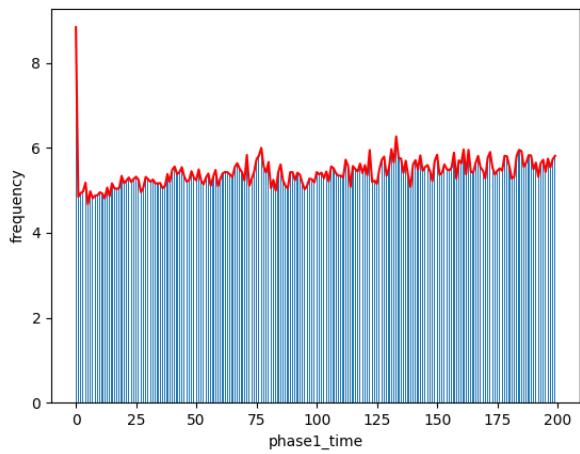


図 723 pht/experiment-win/1216-2/phase1/1216-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50059-in-chronological-order.eps

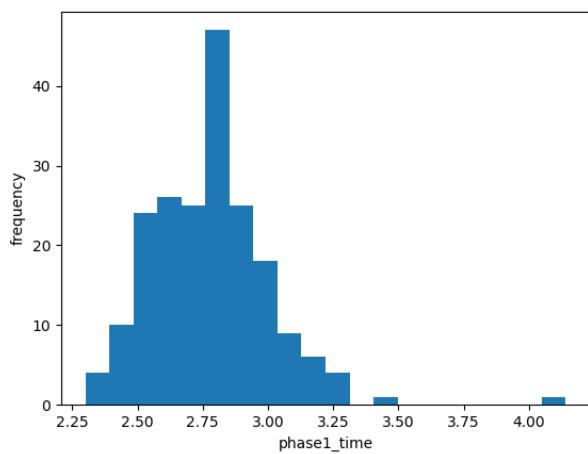


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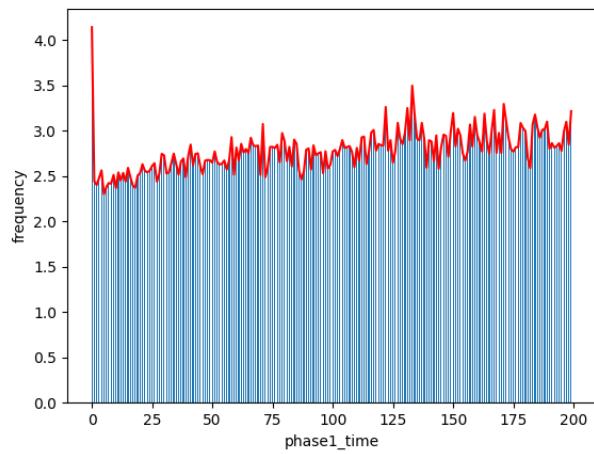


図 725 pht/experiment-win/1216-2/phase1/1216-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50191-in-chronological-order.eps

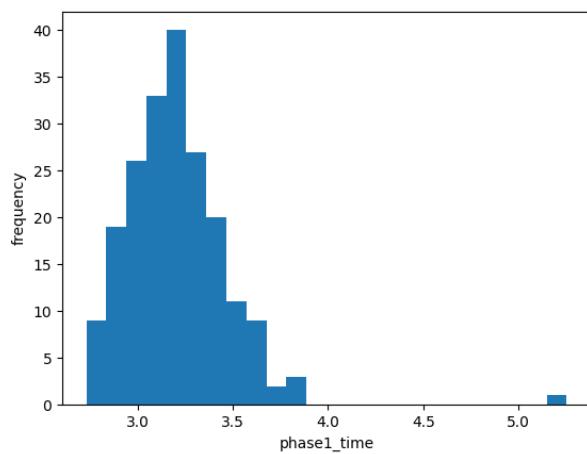


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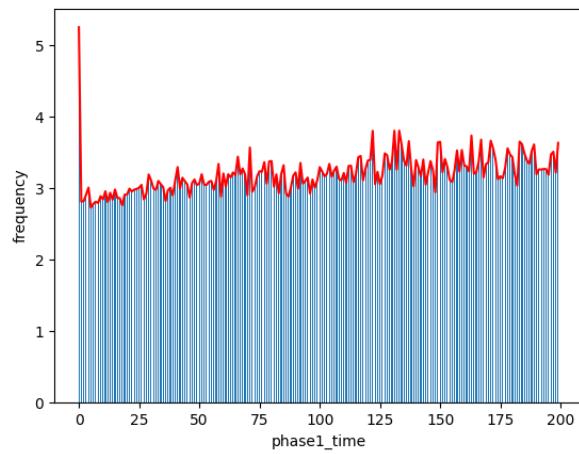


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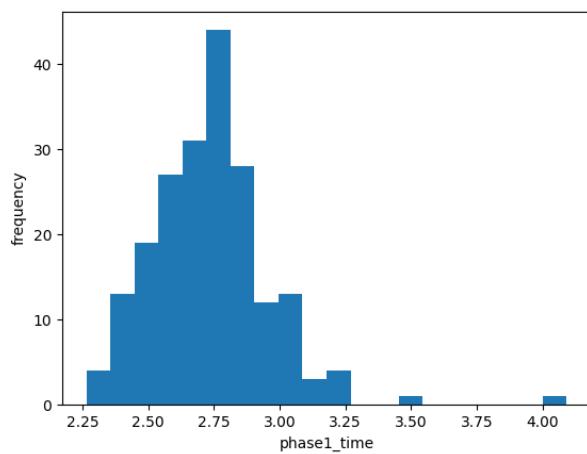


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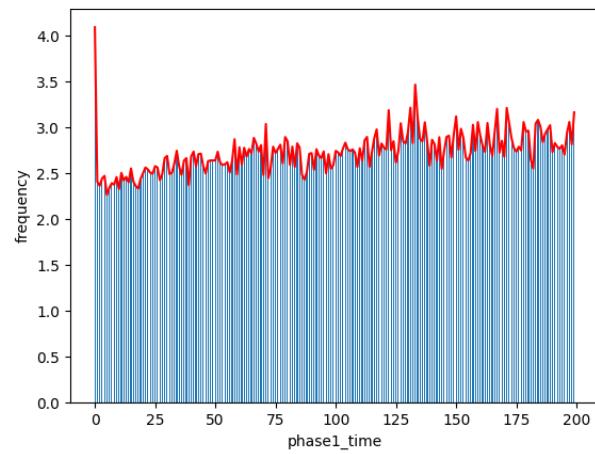


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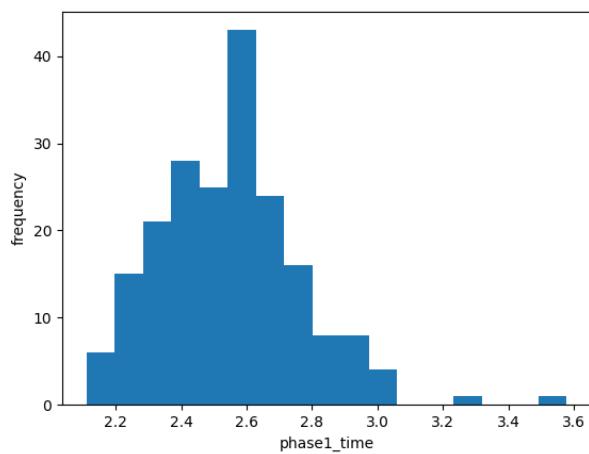


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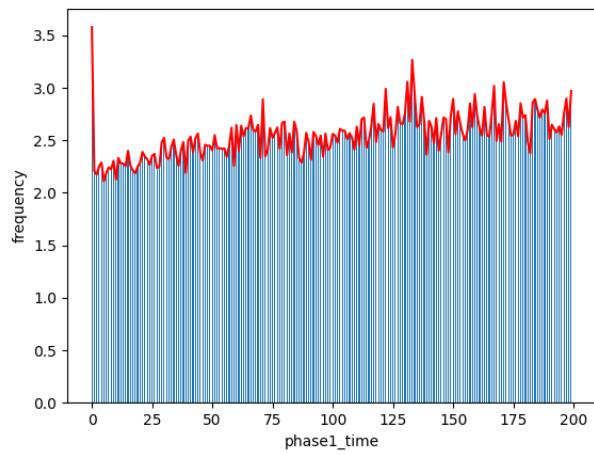


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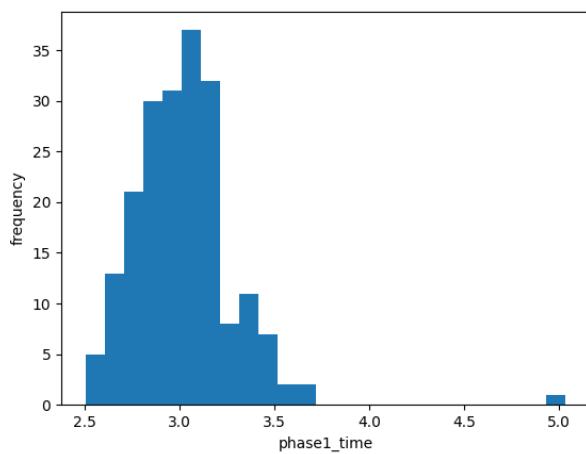


図 732 pht/experiment-win/1216-2/phase1/1216-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50199.eps

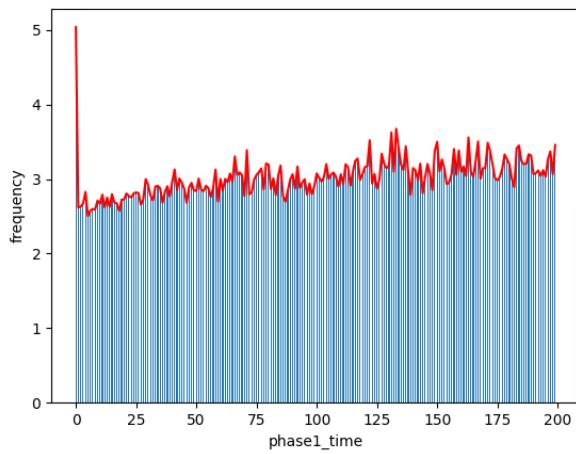


図 733 pht/experiment-win/1216-2/phase1/1216-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50199-in-chronological-order.eps

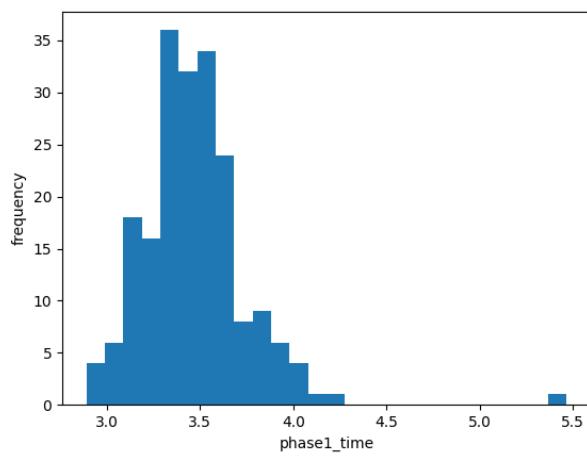


図 734 pht/experiment-win/1216-2/phase1/1216-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50201.eps

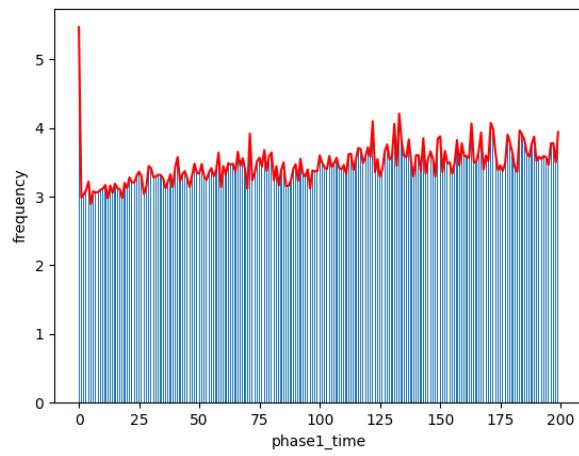


図 735 pht/experiment-win/1216-2/phase1/1216-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50201-in-chronological-order.eps

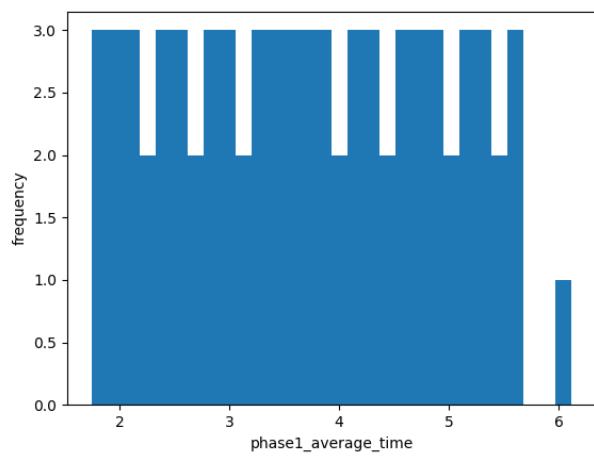


図 736 pht/experiment-win/1216-2/phase1/1216-2-75D-PC3ALL-DIF1-INT1-30analyze-phase1-average-time.eps

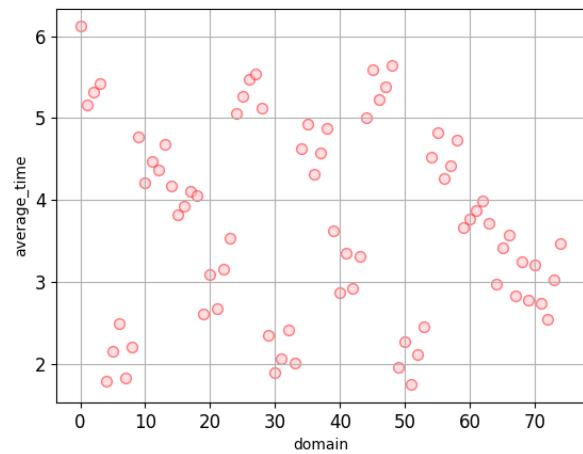


図 737 pht/experiment-win/1216-2/phase1/1216-2-75D-PC3ALL-DIF1-INT1-30analyze-phase1-average-time-scatter.eps

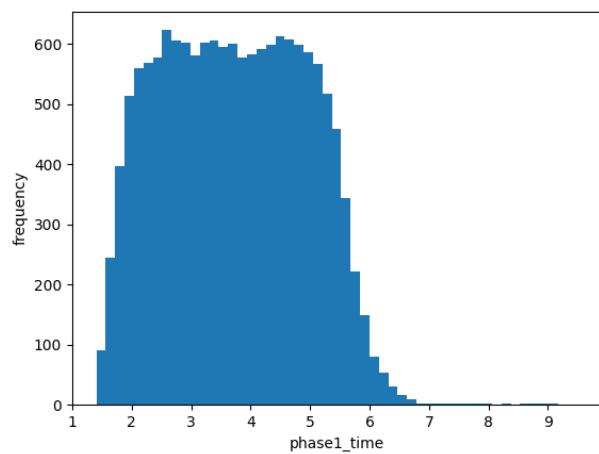


図 738 pht/experiment-win/1216-2/phase1/1216-2-75D-PC3ALL-DIF1-INT1-30analyze-phase1-time-auto.eps

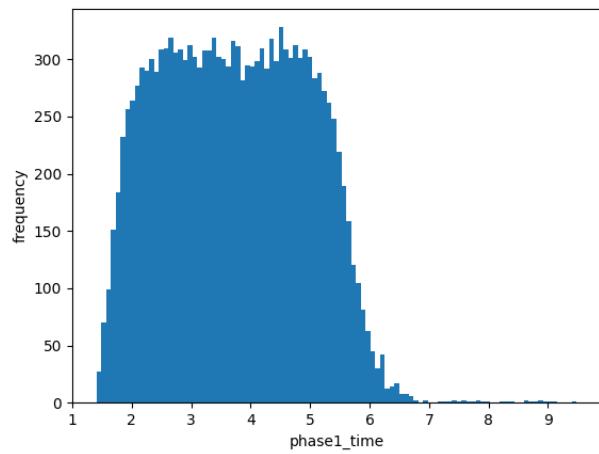


図 739 pht/experiment-win/1216-2/phase1/1216-2-75D-PC3ALL-DIF1-INT1-30analyze-phase1-time-bins100.eps

23 1217-2-75D-PC3ALL-DIF1-INT1-30

23 章にて報告する条件を表 22 に示す.

表 22 設定値の一覧

設定項目	設定値
起動ノード数 (ドメイン数 D)	70 [ノード]
履歴交差間隔	90 [秒]
履歴交差回数	200 [回]
ブロック不生成間隔	0.5[秒]
ブロック生成難易度	1 [0x]
PC 内同時起動台数	7 [台]
使用台数	10 [台]
ブロック不生成間隔 : 30 秒	70 [ノード]
ブロック不生成間隔 : 1 秒	0 [ノード]

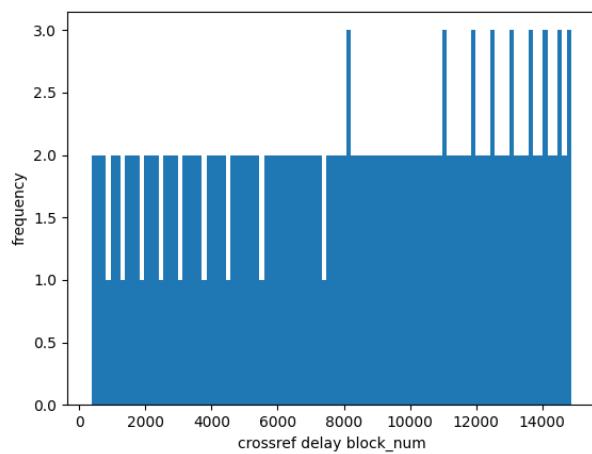


図 740 pht/experiment-win/1217-2/BC/1/1217-2-75D-PC3ALL-DIF1-INT1-30histogram-delay-blocknum.eps

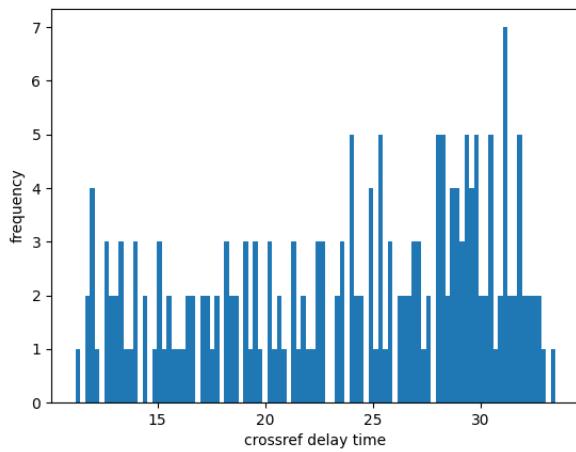


図 741 pht/experiment-win/1217-2/BC/1/1217-2-75D-PC3ALL-DIF1-INT1-30histogram-delay-time.eps

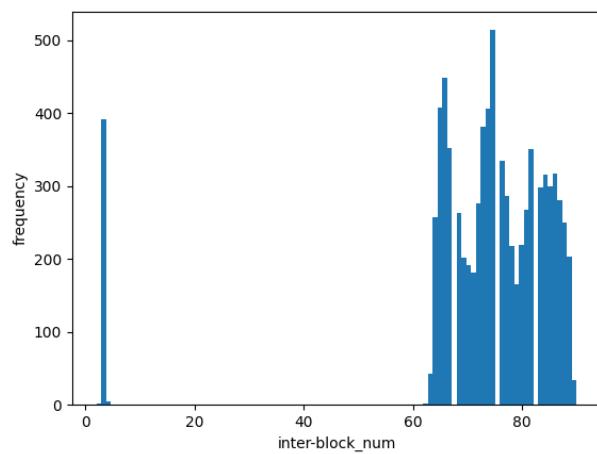


図 742 pht/experiment-win/1217-2/BC/1/1217-2-75D-PC3ALL-DIF1-INT1-30histogram-inter-blocknum.eps

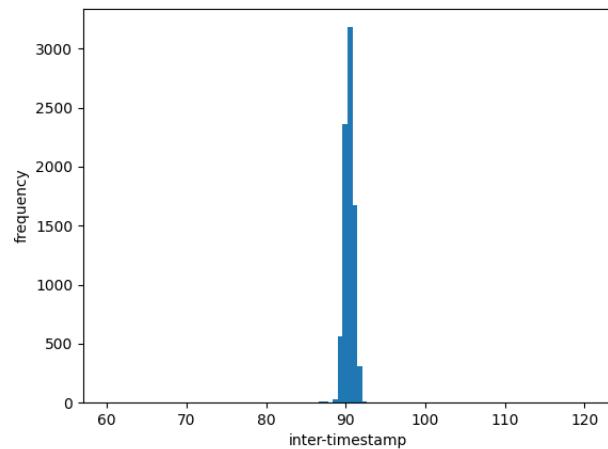


図 743 pht/experiment-win/1217-2/BC/1/1217-2-75D-PC3ALL-DIF1-INT1-30histogram-inter-timestamp.eps

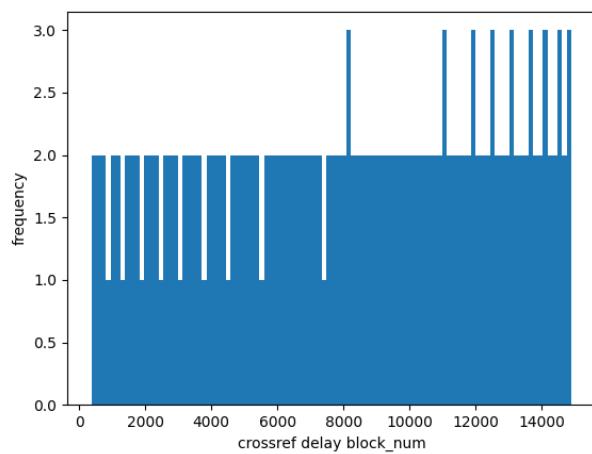


図 744 pht/experiment-win/1217-2/BC/1217-2-75D-PC3ALL-DIF1-INT1-30histogram-delay-blocknum.eps

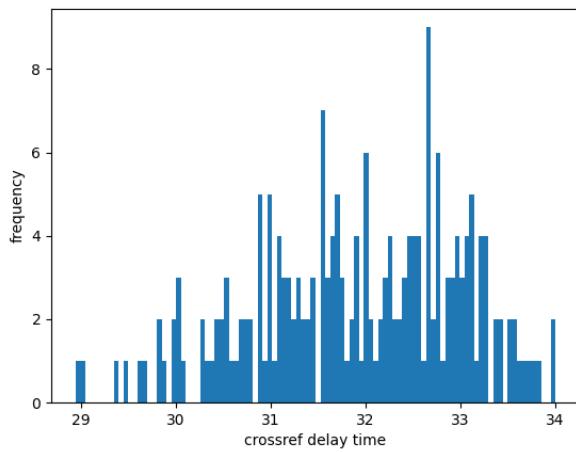


図 745 pht/experiment-win/1217-2/BC/1217-2-75D-PC3ALL-DIF1-INT1-30histogram-delay-time.eps

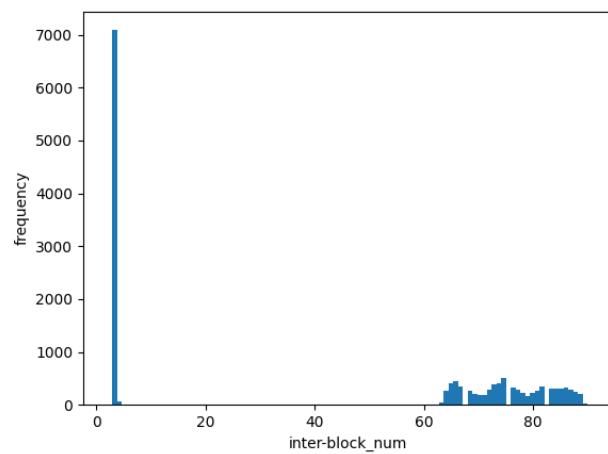


図 746 pht/experiment-win/1217-2/BC/1217-2-75D-PC3ALL-DIF1-INT1-30histogram-inter-blocknum.eps

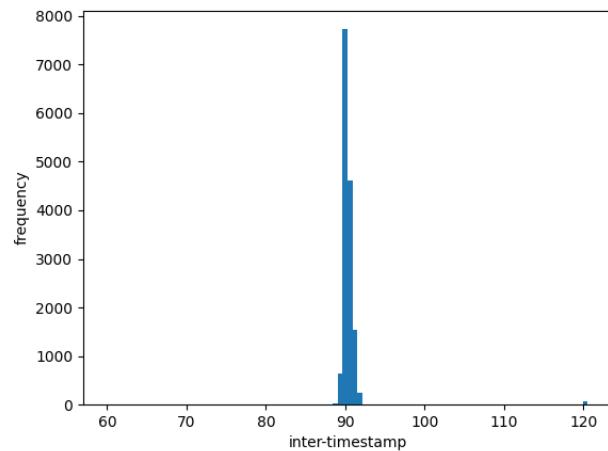


図 747 pht/experiment-win/1217-2/BC/1217-2-75D-PC3ALL-DIF1-INT1-30histogram-inter-timestamp.eps

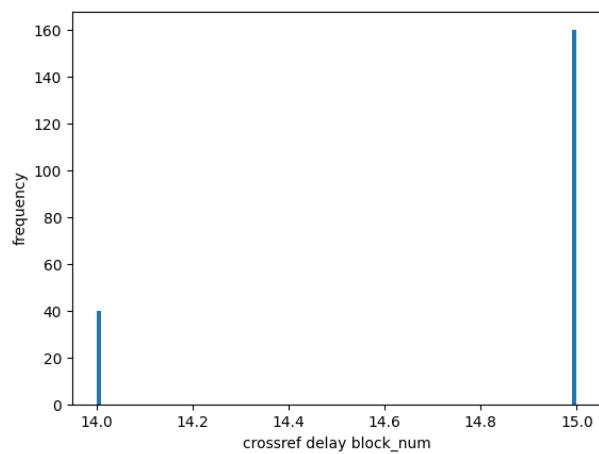


図 748 pht/experiment-win/1217-2/BC/30/1217-2-75D-PC3ALL-DIF1-INT1-30histogram-delay-blocknum.eps

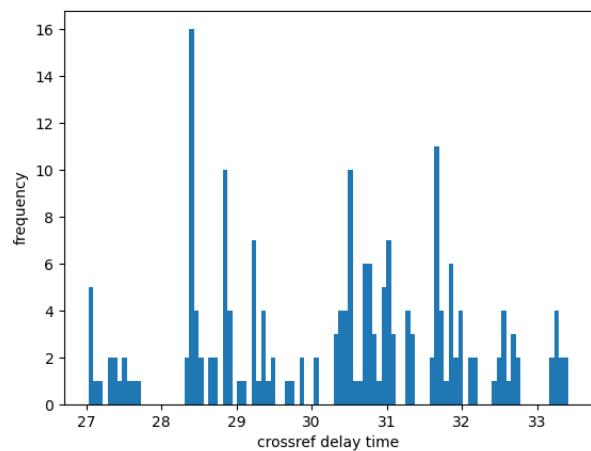


図 749 pht/experiment-win/1217-2/BC/30/1217-2-75D-PC3ALL-DIF1-INT1-30histogram-delay-time.eps

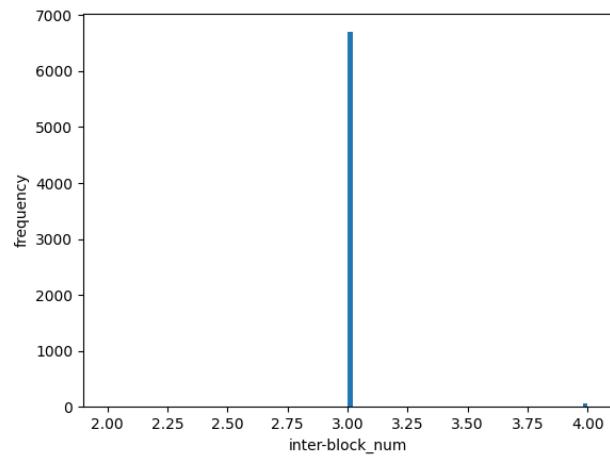


図 750 pht/experiment-win/1217-2/BC/30/1217-2-75D-PC3ALL-DIF1-INT1-30histogram-inter-blocknum.eps

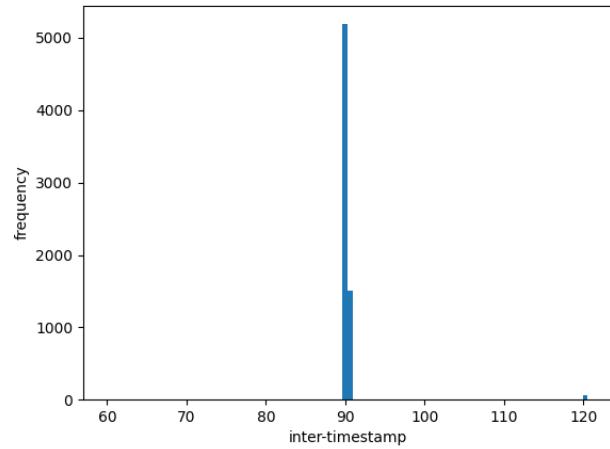


図 751 pht/experiment-win/1217-2/BC/30/1217-2-75D-PC3ALL-DIF1-INT1-30histogram-inter-timestamp.eps

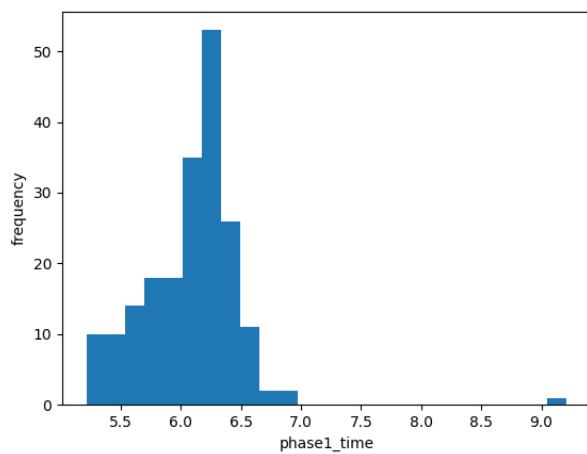


図 752 pht/experiment-win/1217-2/phase1/1217-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50051.eps

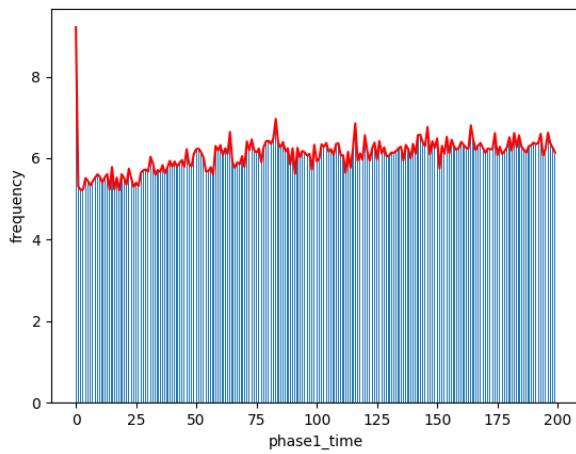


図 753 pht/experiment-win/1217-2/phase1/1217-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50051-in-chronological-order.eps

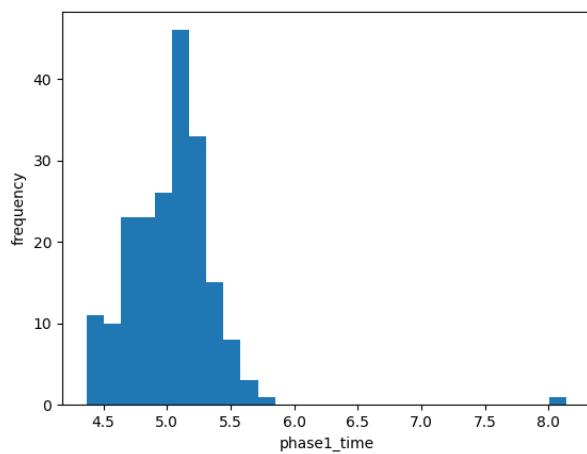


図 754 pht/experiment-win/1217-2/phase1/1217-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50053.eps

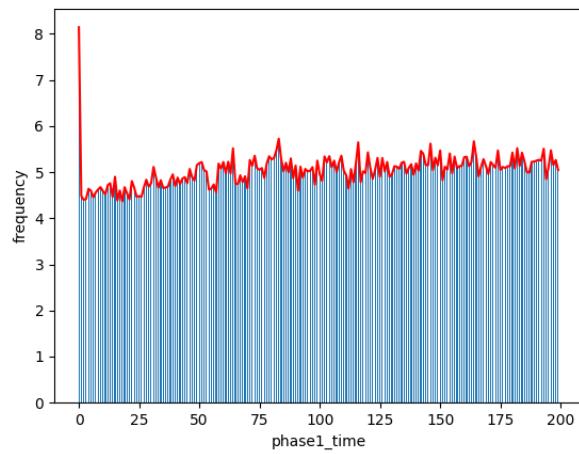


図 755 pht/experiment-win/1217-2/phase1/1217-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50053-in-chronological-order.eps

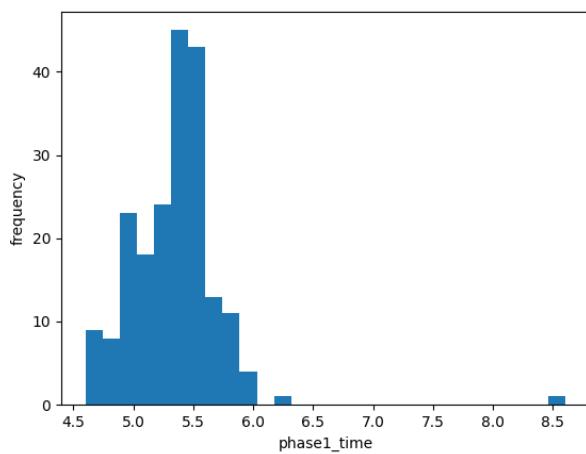


図 756 pht/experiment-win/1217-2/phase1/1217-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50055.eps

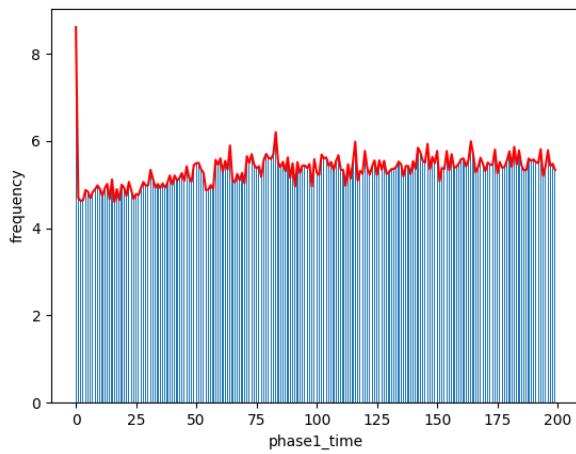


図 757 pht/experiment-win/1217-2/phase1/1217-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50055-in-chronological-order.eps

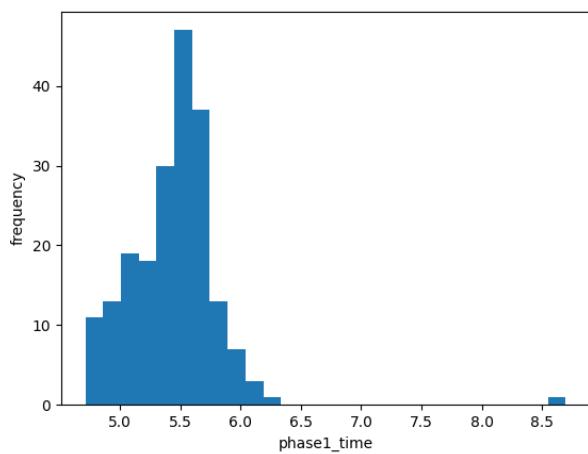


図 758 pht/experiment-win/1217-2/phase1/1217-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50059.eps

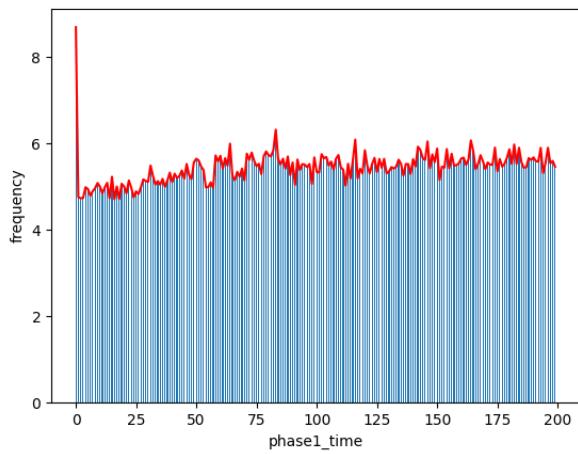


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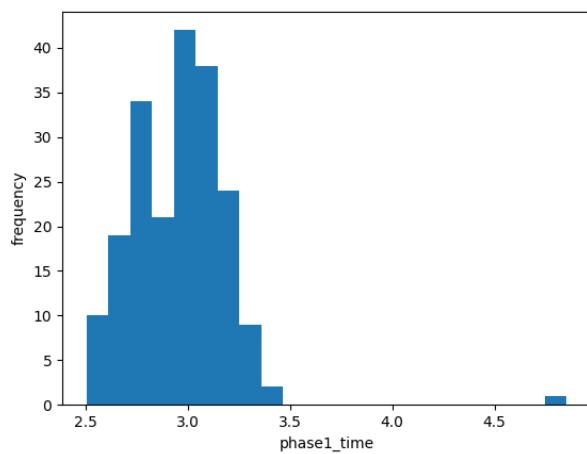


図 760 pht/experiment-win/1217-2/phase1/1217-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50191.eps

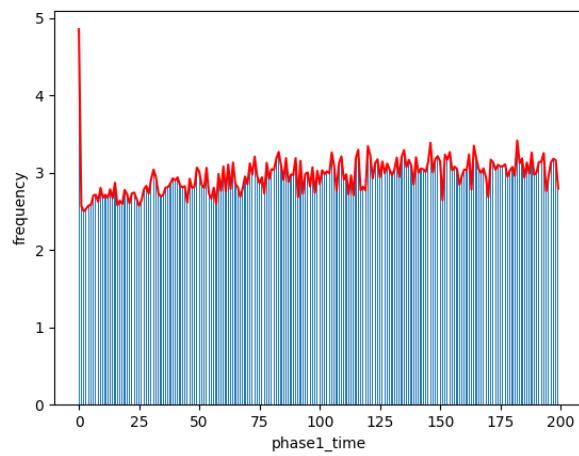


図 761 pht/experiment-win/1217-2/phase1/1217-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50191-in-chronological-order.eps

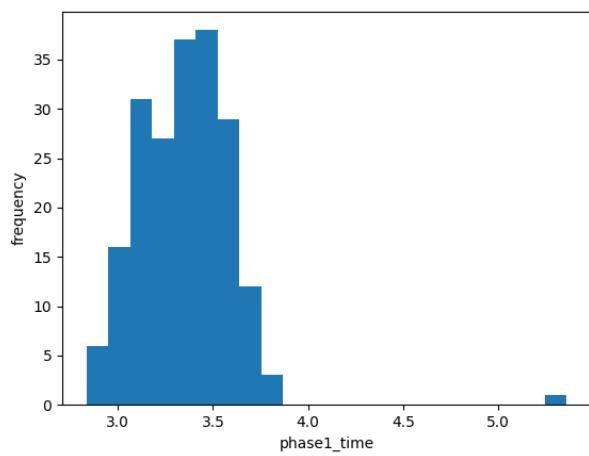


図 762 pht/experiment-win/1217-2/phase1/1217-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50193.eps

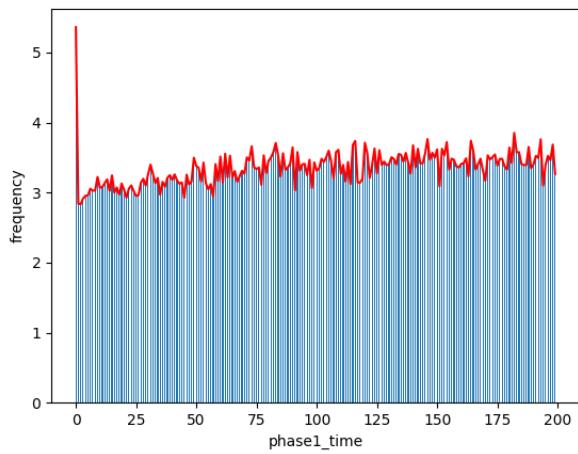


図 763 pht/experiment-win/1217-2/phase1/1217-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50193-in-chronological-order.eps

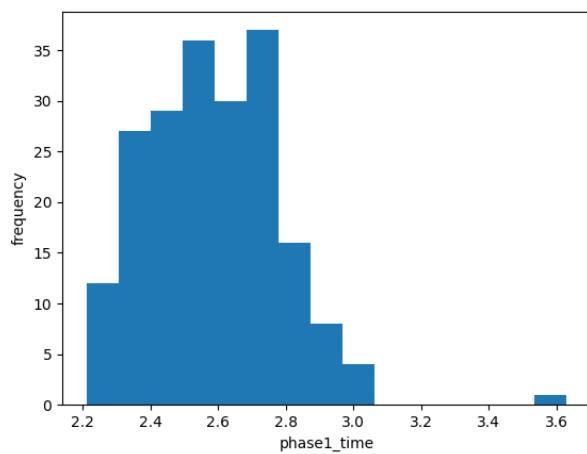


図 764 pht/experiment-win/1217-2/phase1/1217-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50195.eps

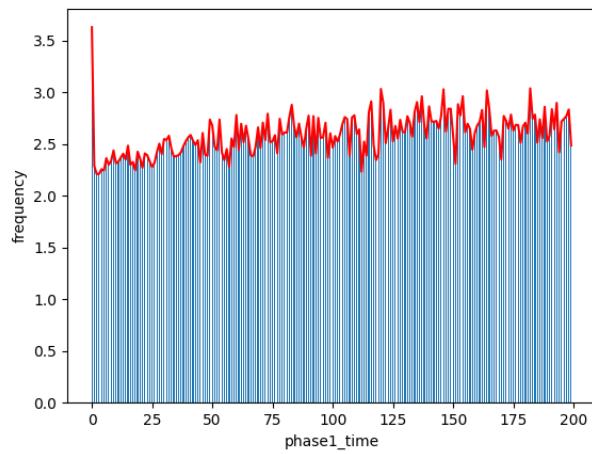


図 765 pht/experiment-win/1217-2/phase1/1217-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50195-in-chronological-order.eps

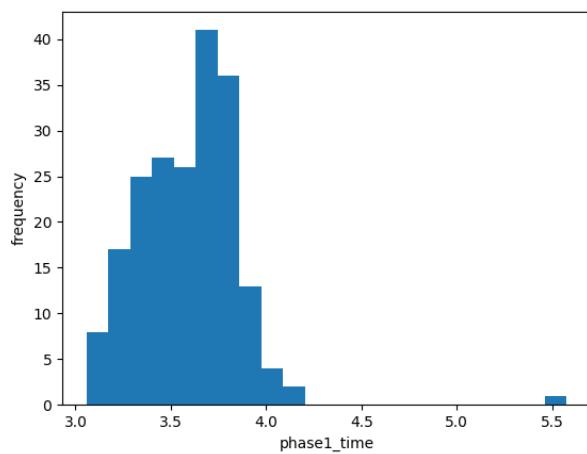


図 766 pht/experiment-win/1217-2/phase1/1217-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50197.eps

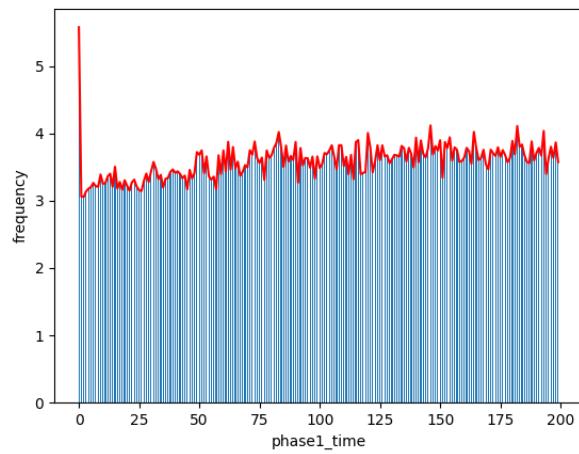


図 767 pht/experiment-win/1217-2/phase1/1217-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50197-in-chronological-order.eps

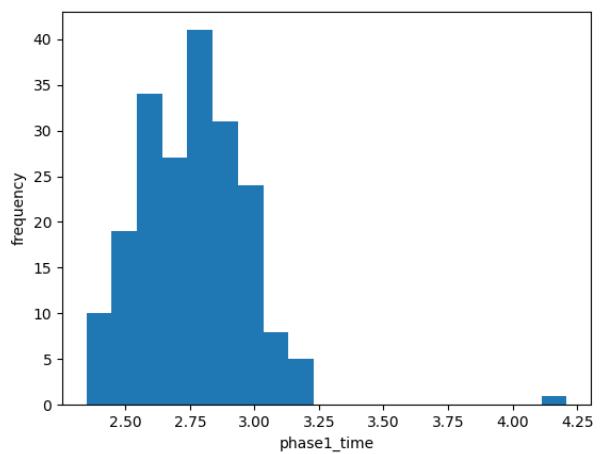


図 768 pht/experiment-win/1217-2/phase1/1217-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50199.eps

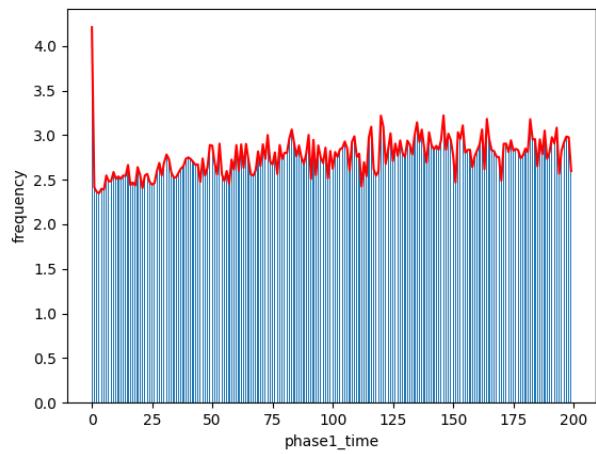


図 769 pht/experiment-win/1217-2/phase1/1217-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50199-in-chronological-order.eps

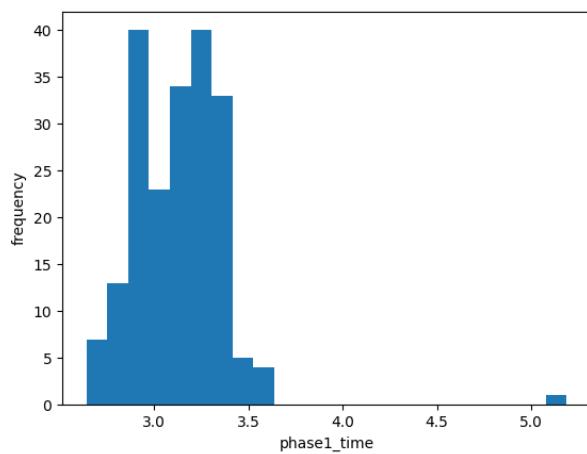


図 770 pht/experiment-win/1217-2/phase1/1217-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50201.eps

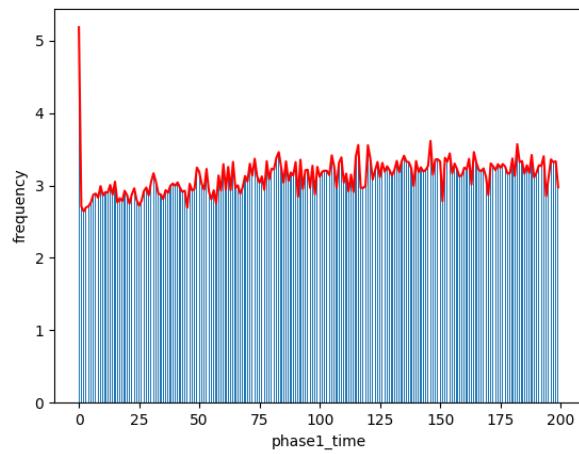


図 771 pht/experiment-win/1217-2/phase1/1217-2-75D-PC3ALL-DIF1-INT1-30Phase1-time50201-in-chronological-order.eps

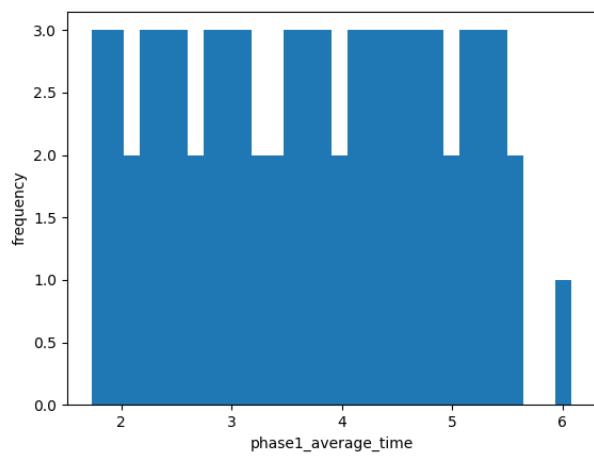


図 772 pht/experiment-win/1217-2/phase1/1217-2-75D-PC3ALL-DIF1-INT1-30analyze-phase1-average-time.eps

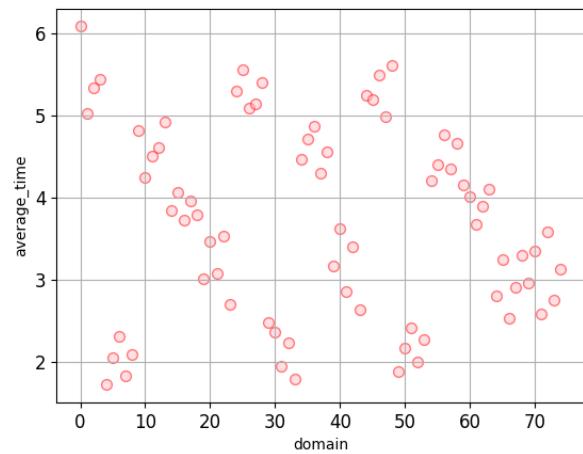


図 773 pht/experiment-win/1217-2/phase1/1217-2-75D-PC3ALL-DIF1-INT1-30analyze-phase1-average-time-scatter.eps

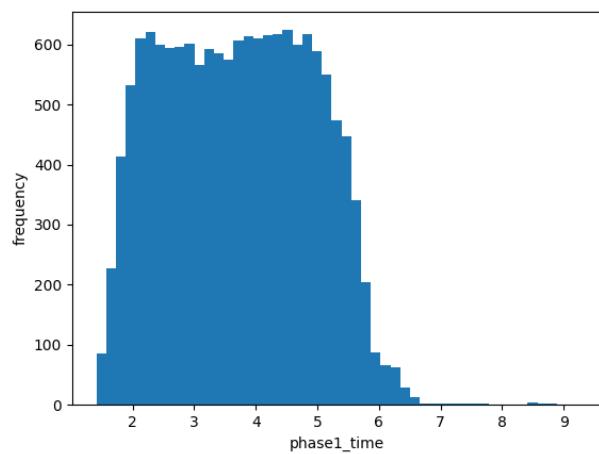


図 774 pht/experiment-win/1217-2/phase1/1217-2-75D-PC3ALL-DIF1-INT1-30analyze-phase1-time-auto.eps

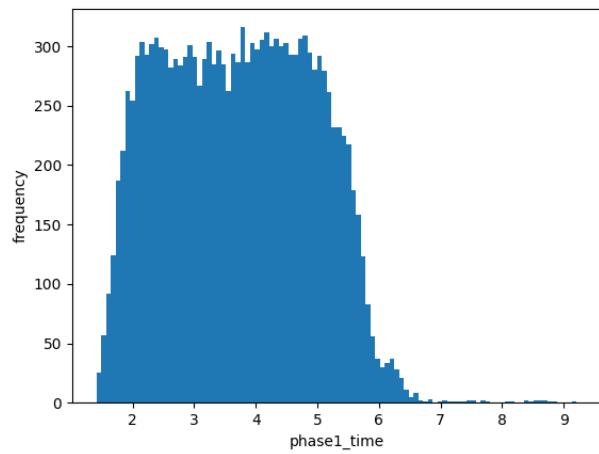


図 775 pht/experiment-win/1217-2/phase1/1217-2-75D-PC3ALL-DIF1-INT1-30analyze-phase1-time-bins100.eps

24 1218-1-75D-PC3ALL-DIF1-INT1-30

24 章にて報告する条件を表 23 に示す.

表 23 設定値の一覧

設定項目	設定値
起動ノード数 (ドメイン数 D)	70 [ノード]
履歴交差間隔	90 [秒]
履歴交差回数	200 [回]
ブロック不生成間隔	0.5[秒]
ブロック生成難易度	1 [0x]
PC 内同時起動台数	7 [台]
使用台数	10 [台]
ブロック不生成間隔 : 30 秒	70 [ノード]
ブロック不生成間隔 : 1 秒	0 [ノード]

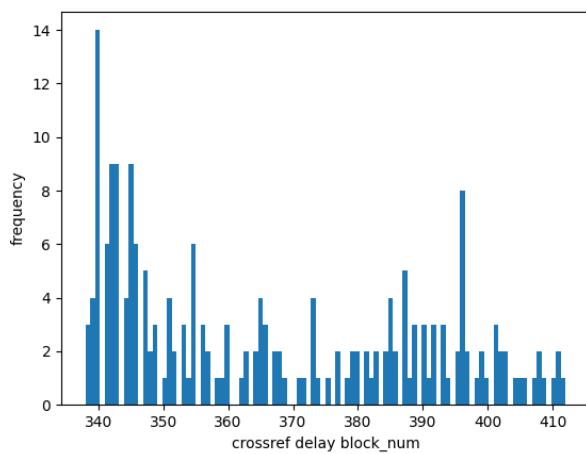


図 776 pht/experiment-win/1218/BC/1/1218-1-75D-PC3ALL-DIF1-INT1-30histogram-delay-blocknum.eps

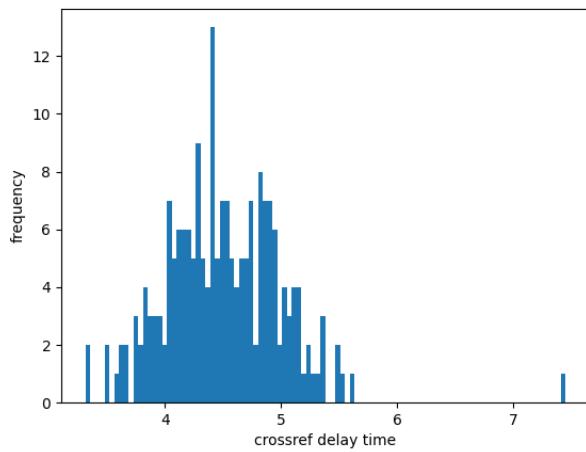


図 777 pht/experiment-win/1218/BC/1/1218-1-75D-PC3ALL-DIF1-INT1-30histogram-delay-time.eps

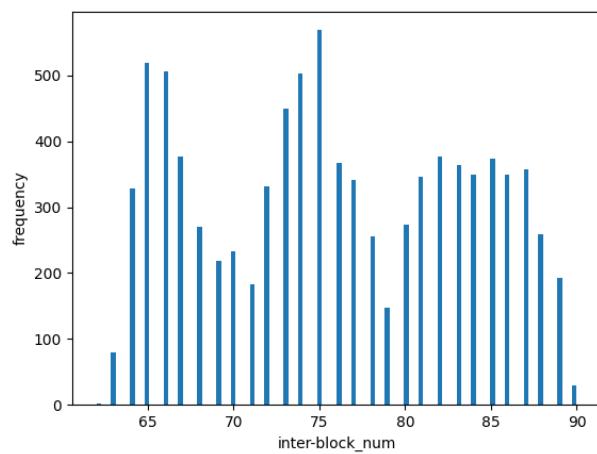


図 778 pht/experiment-win/1218/BC/1/1218-1-75D-PC3ALL-DIF1-INT1-30histogram-inter-blocknum.eps

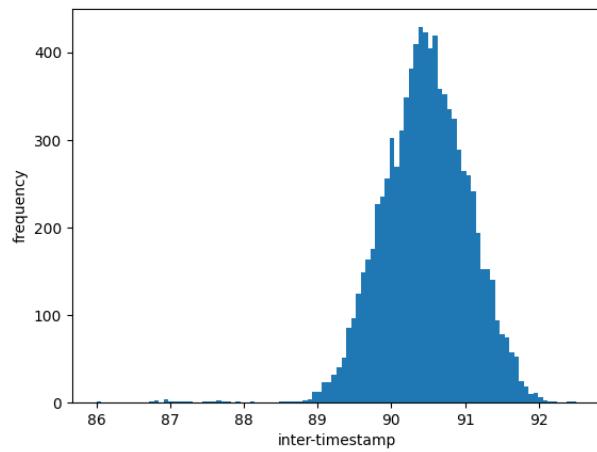


図 779 pht/experiment-win/1218/BC/1/1218-1-75D-PC3ALL-DIF1-INT1-30histogram-inter-timestamp.eps

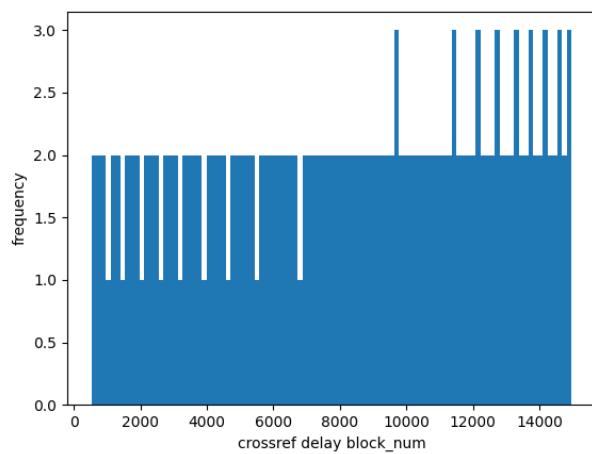


図 780 pht/experiment-win/1218/BC/1218-1-75D-PC3ALL-DIF1-INT1-30histogram-delay-blocknum.eps

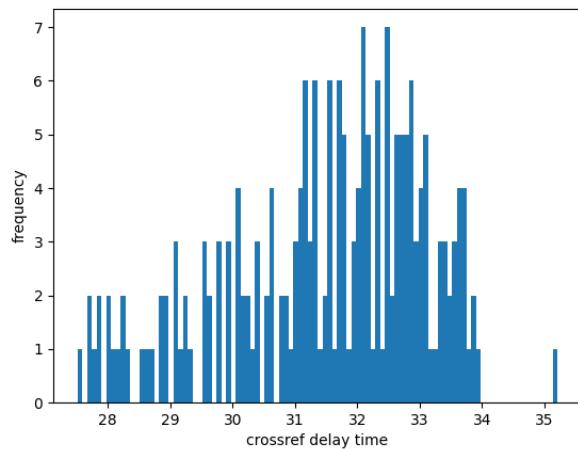


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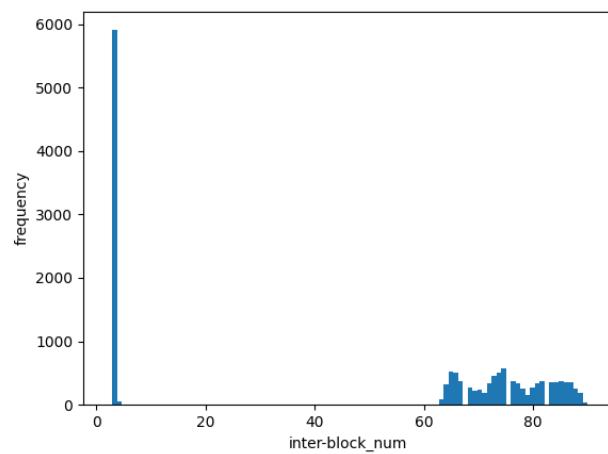


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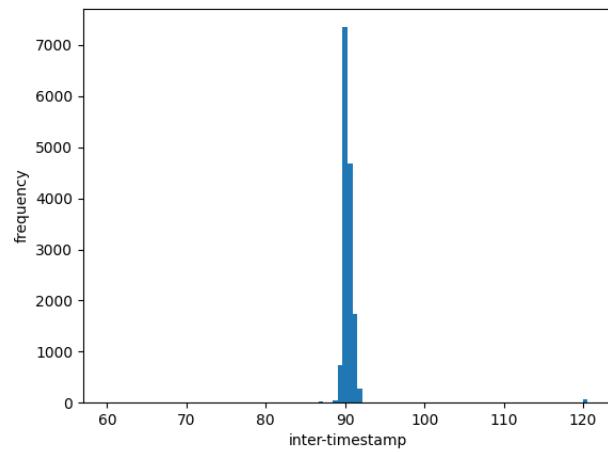


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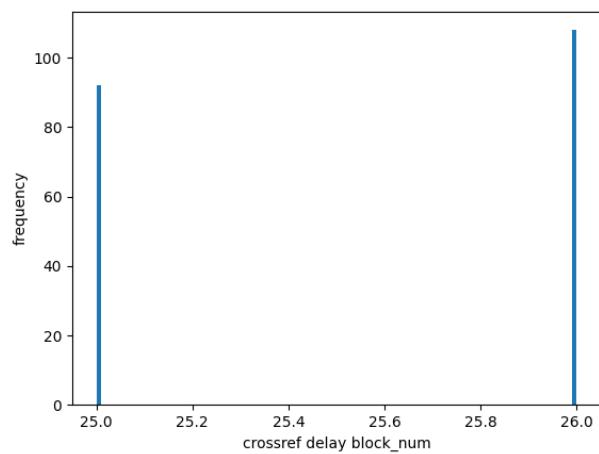


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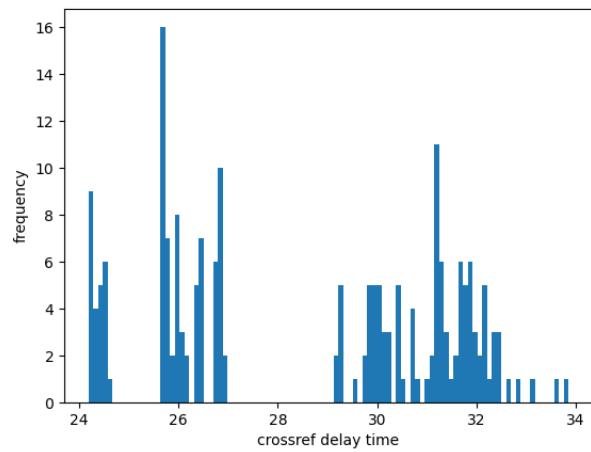


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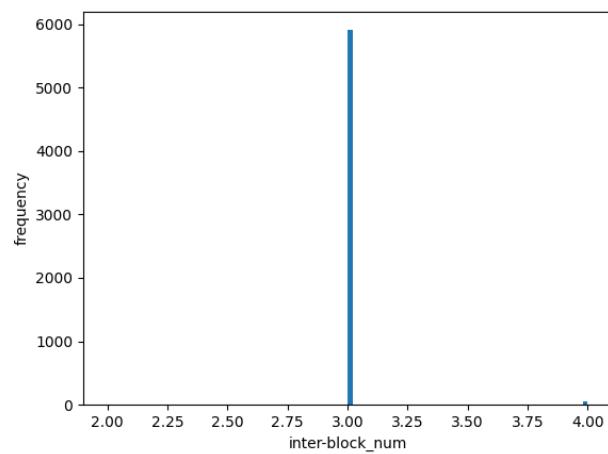


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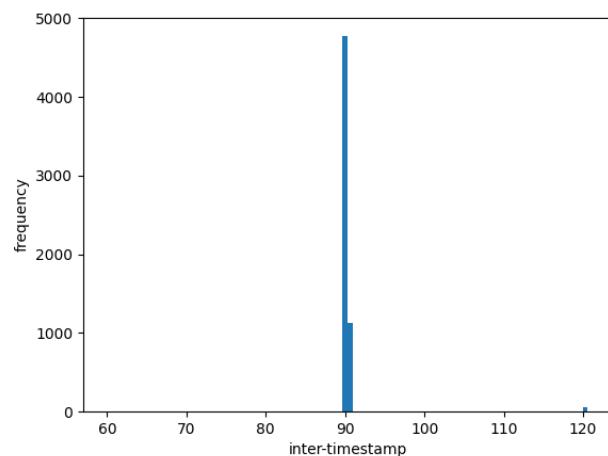


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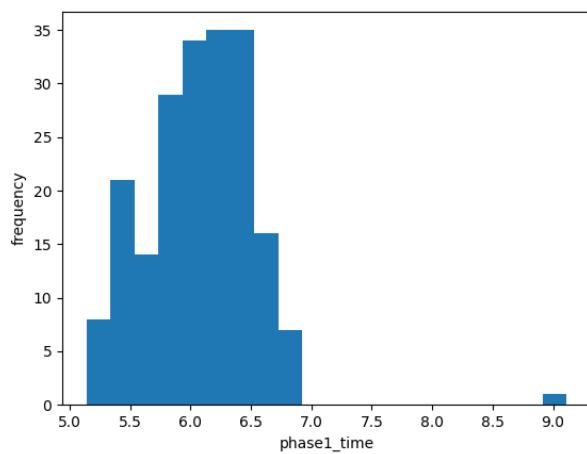


図 788 pht/experiment-win/1218/phase1/1218-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50051.eps

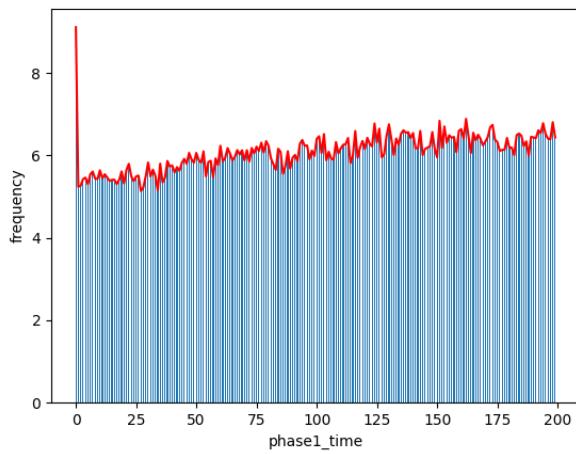


図 789 pht/experiment-win/1218/phase1/1218-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50051-in-chronological-order.eps

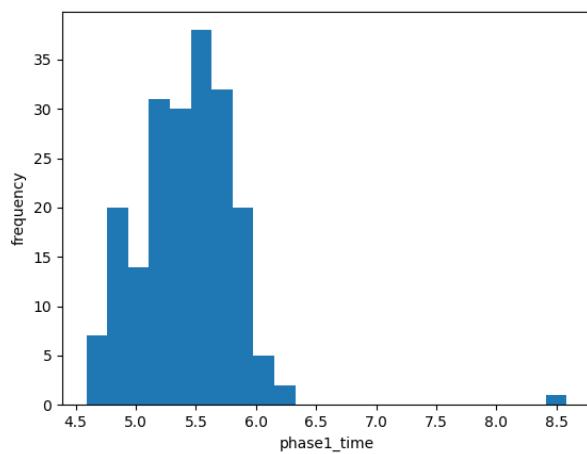


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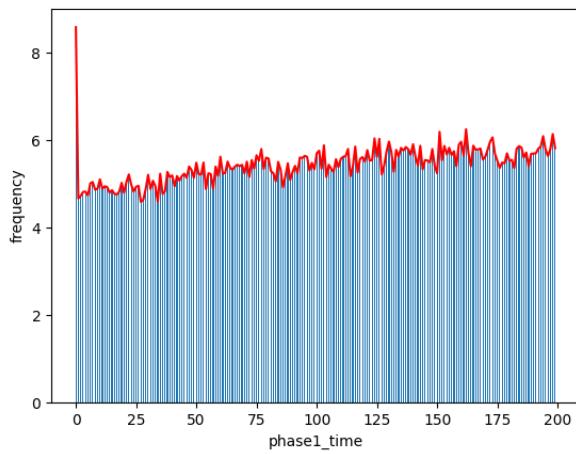


図 791 pht/experiment-win/1218/phase1/1218-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50053-in-chronological-order.eps

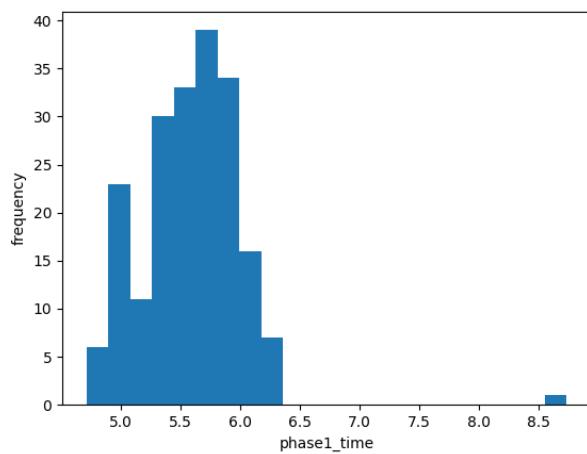


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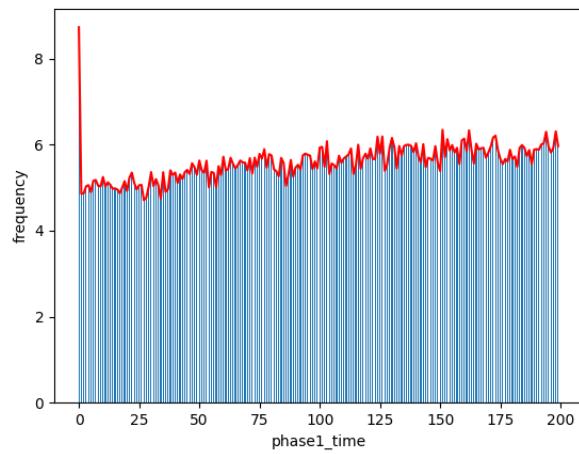


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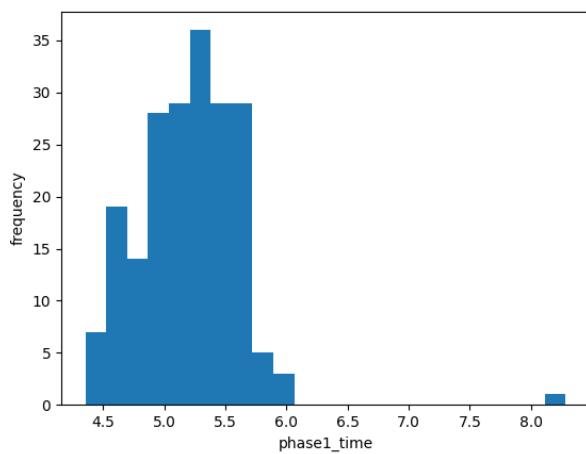


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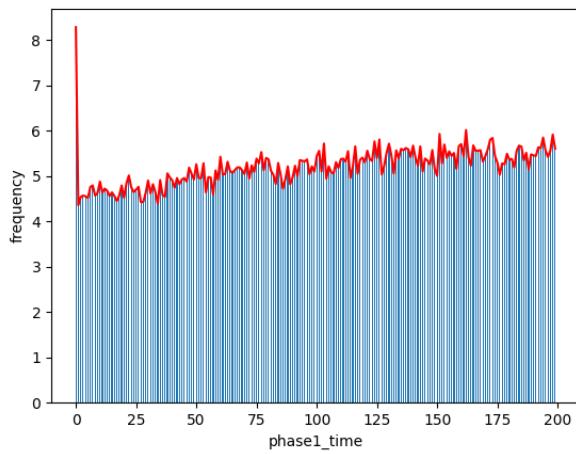


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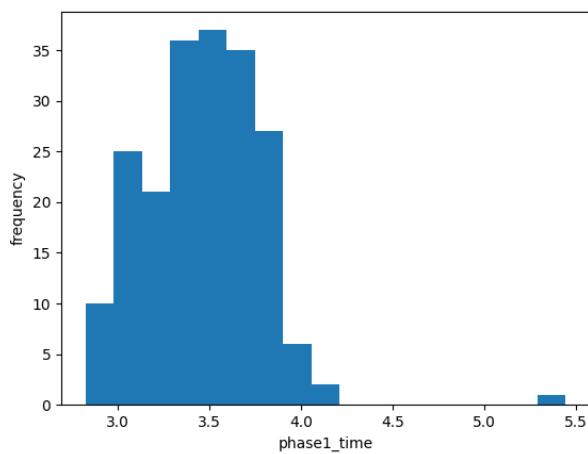


図 796 pht/experiment-win/1218/phase1/1218-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50191.eps

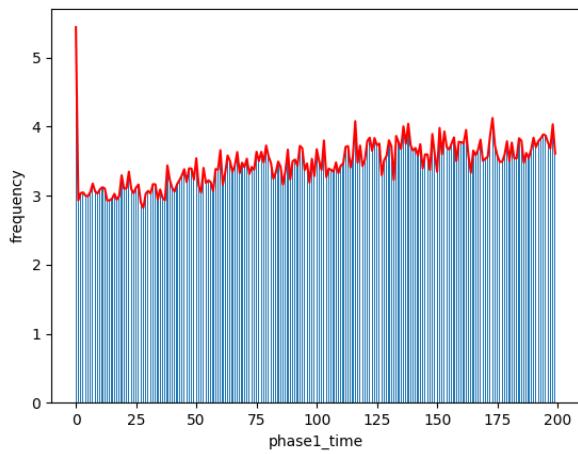


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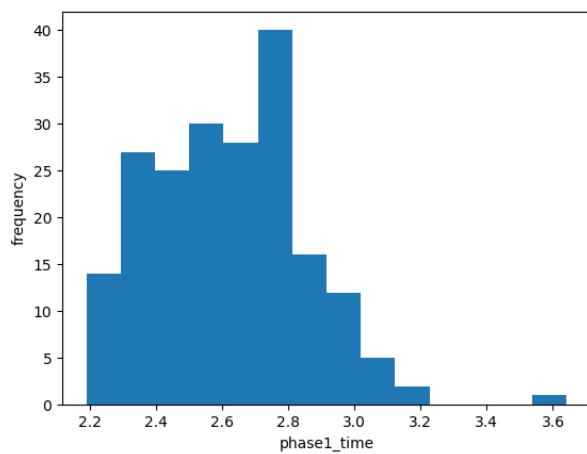


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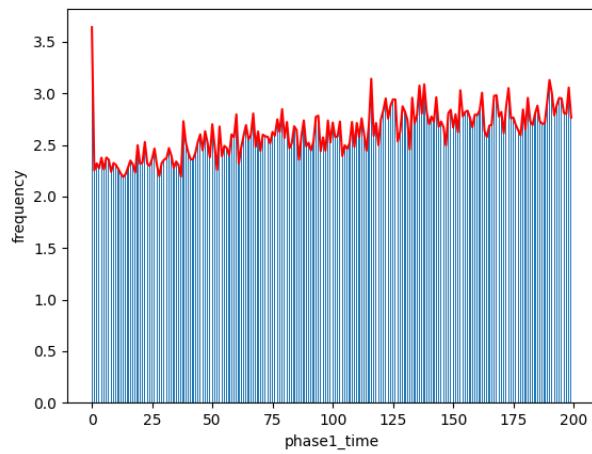


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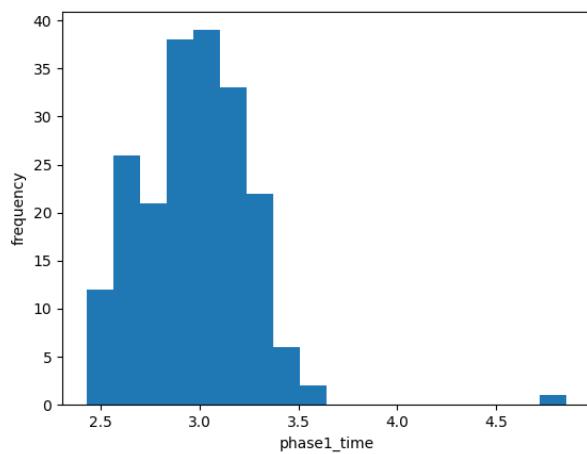


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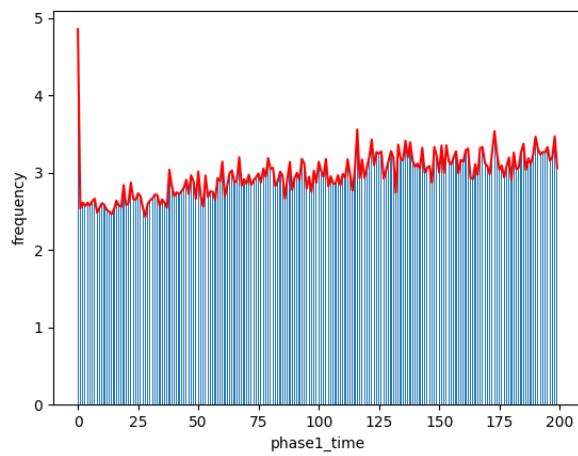


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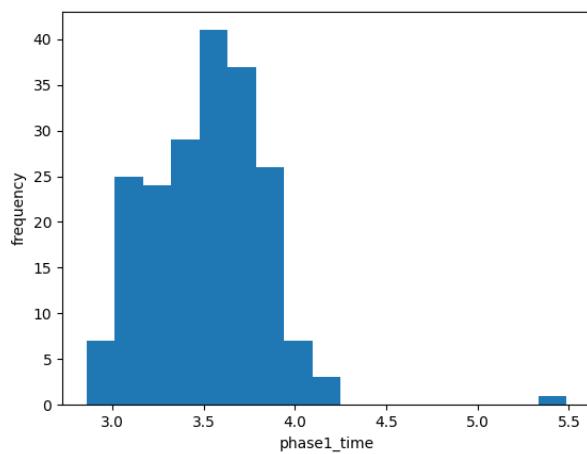


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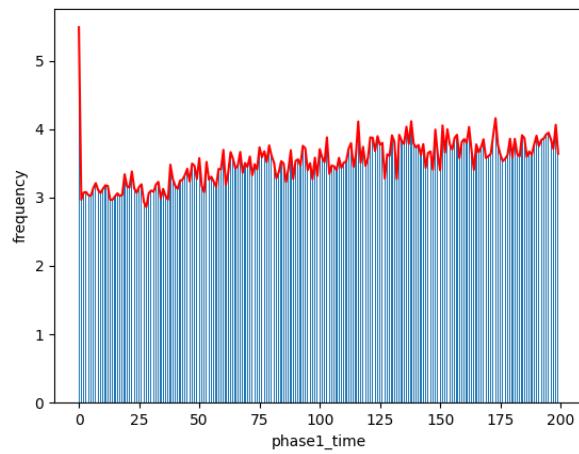


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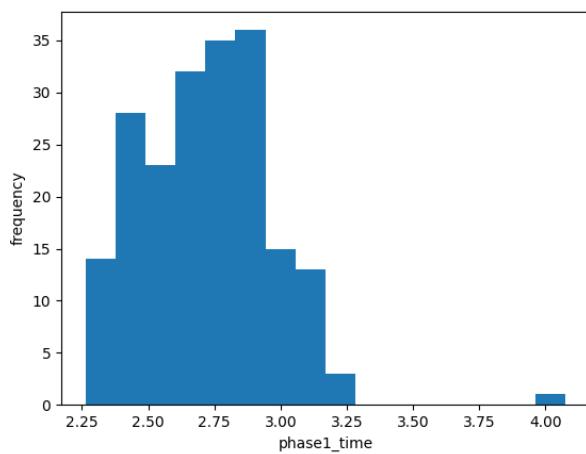


図 804 pht/experiment-win/1218/phase1/1218-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50199.eps

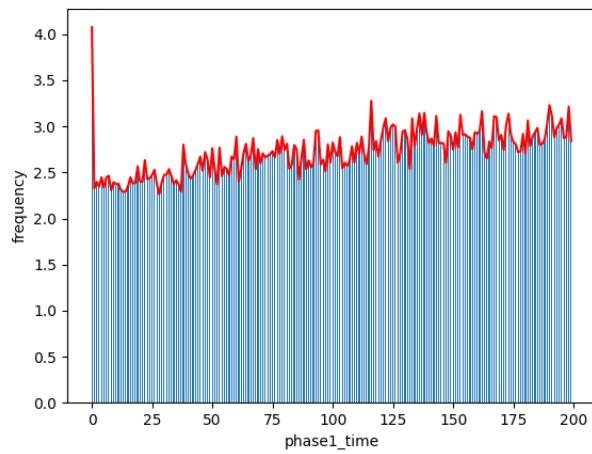


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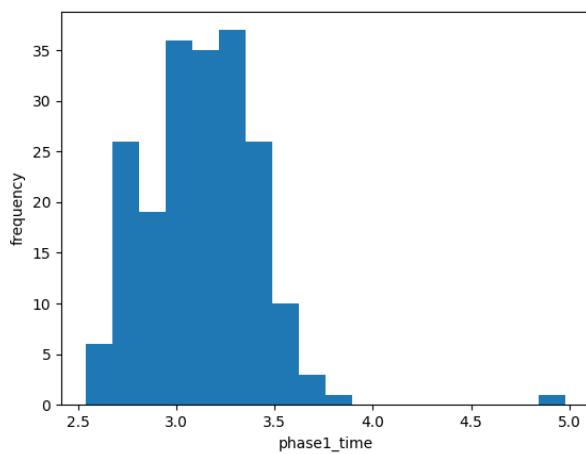


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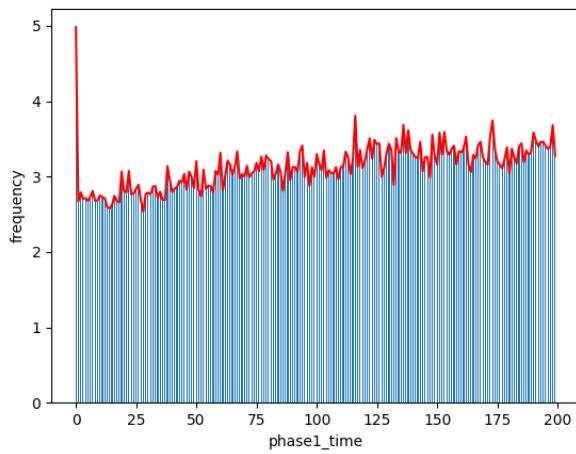


図 807 pht/experiment-win/1218/phase1/1218-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50201-in-chronological-order.eps

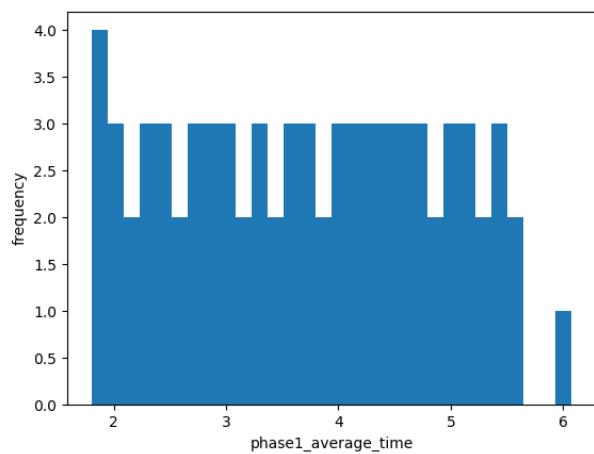


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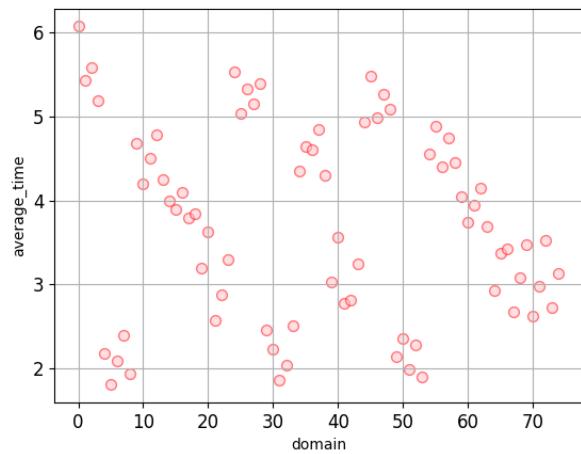


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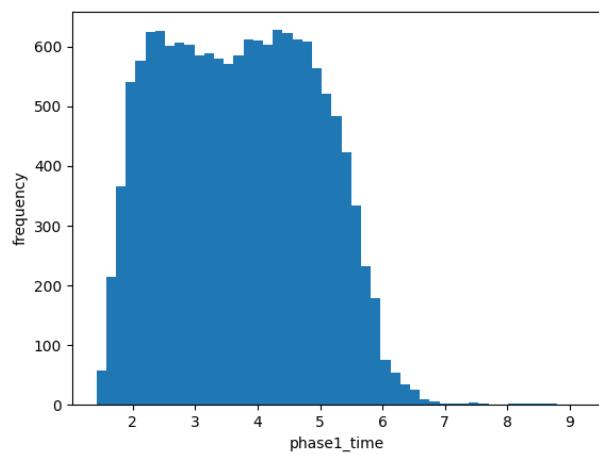


図 810 pht/experiment-win/1218/phase1/1218-1-75D-PC3ALL-DIF1-INT1-30analyze-phase1-time-auto.eps

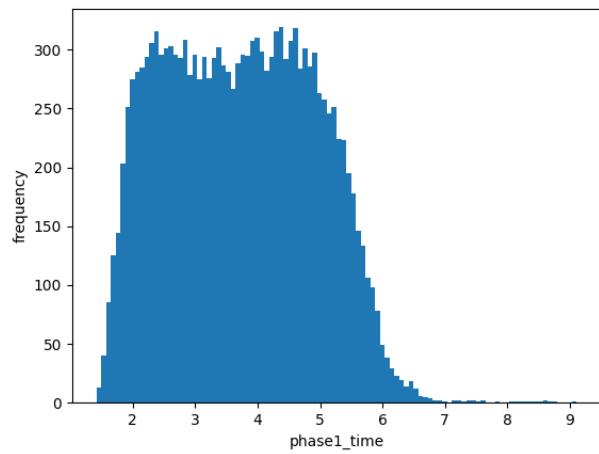


図 811 pht/experiment-win/1218/phase1/1218-1-75D-PC3ALL-DIF1-INT1-30analyze-phase1-time-bins100.eps

25 1220-1-75D-PC3ALL-DIF1-INT1-30

25 章にて報告する条件を表 24 に示す.

表 24 設定値の一覧

設定項目	設定値
起動ノード数 (ドメイン数 D)	70 [ノード]
履歴交差間隔	90 [秒]
履歴交差回数	200 [回]
ブロック不生成間隔	0.5[秒]
ブロック生成難易度	1 [0x]
PC 内同時起動台数	7 [台]
使用台数	10 [台]
ブロック不生成間隔 : 30 秒	70 [ノード]
ブロック不生成間隔 : 1 秒	0 [ノード]

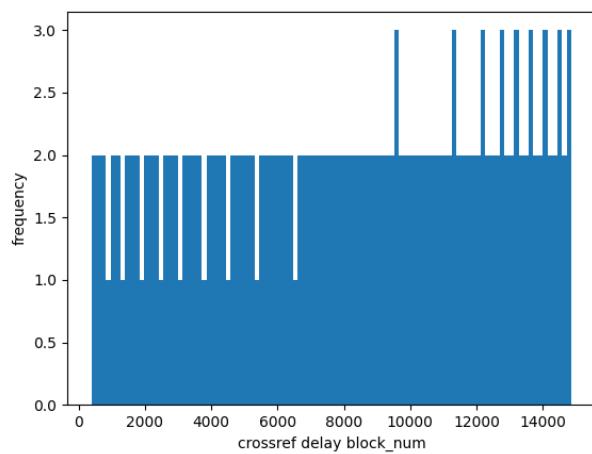


図 812 pht/experiment-win/1220-1/BC/1220-1-75D-PC3ALL-DIF1-INT1-30histogram-delay-blocknum.eps

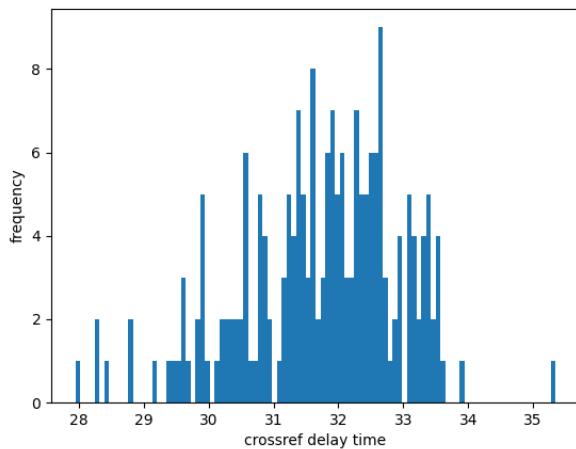


図 813 pht/experiment-win/1220-1/BC/1220-1-75D-PC3ALL-DIF1-INT1-30histogram-delay-time.eps

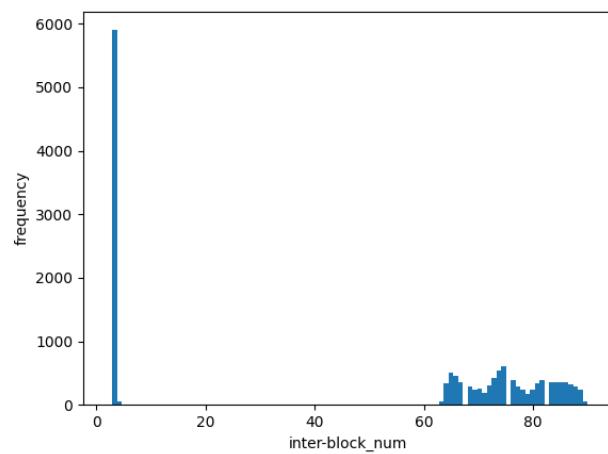


図 814 pht/experiment-win/1220-1/BC/1220-1-75D-PC3ALL-DIF1-INT1-30histogram-inter-blocknum.eps

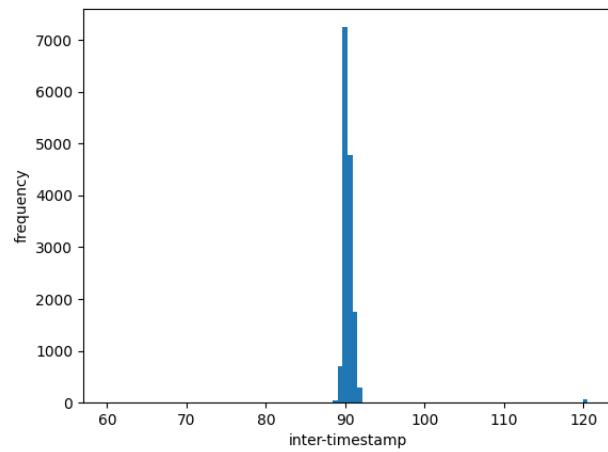


図 815 pht/experiment-win/1220-1/BC/1220-1-75D-PC3ALL-DIF1-INT1-30histogram-inter-timestamp.eps

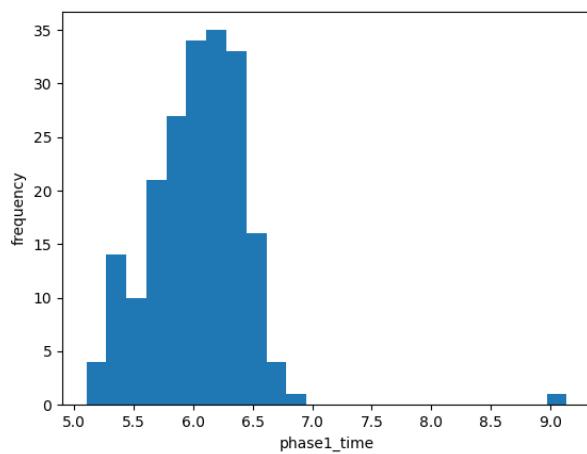


図 816 pht/experiment-win/1220-1/phase1/1220-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50051.eps

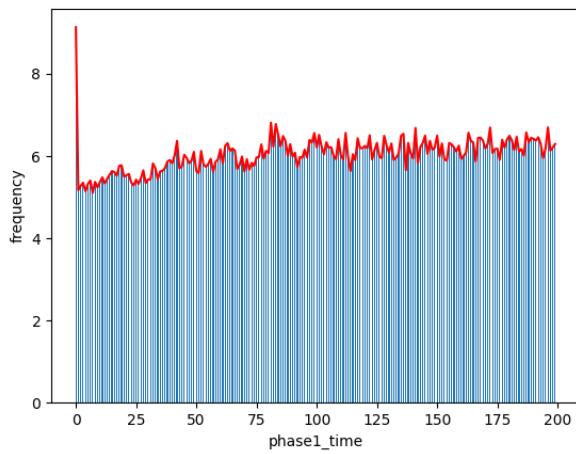


図 817 pht/experiment-win/1220-1/phase1/1220-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50051-in-chronological-order.eps

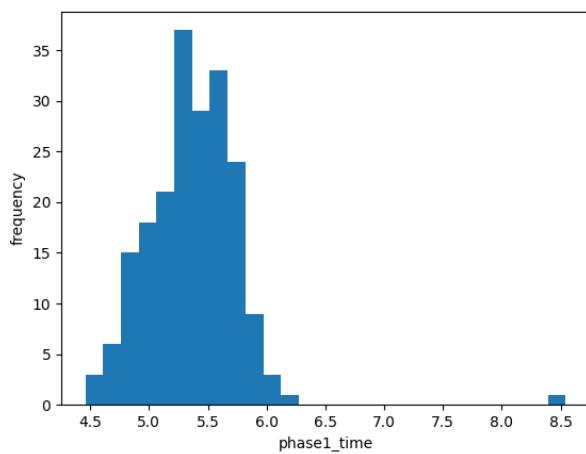


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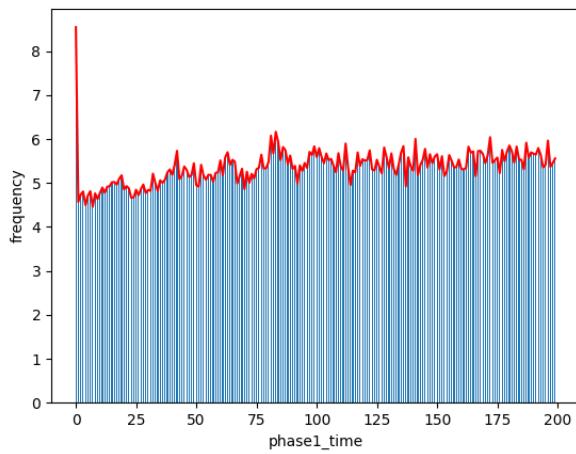


図 819 pht/experiment-win/1220-1/phase1/1220-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50053-in-chronological-order.eps

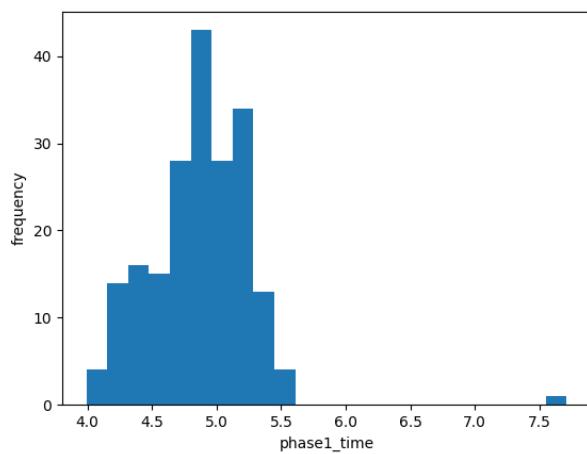


図 820 pht/experiment-win/1220-1/phase1/1220-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50055.eps

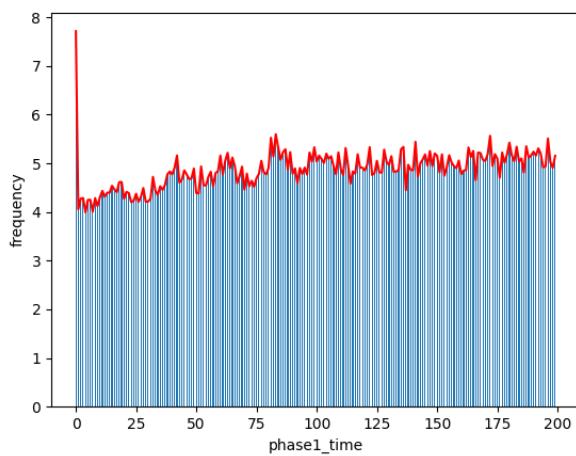


図 821 pht/experiment-win/1220-1/phase1/1220-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50055-in-chronological-order.eps

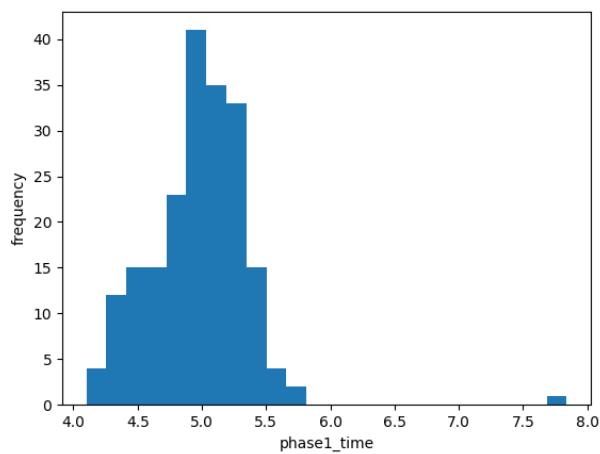


図 822 pht/experiment-win/1220-1/phase1/1220-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50059.eps

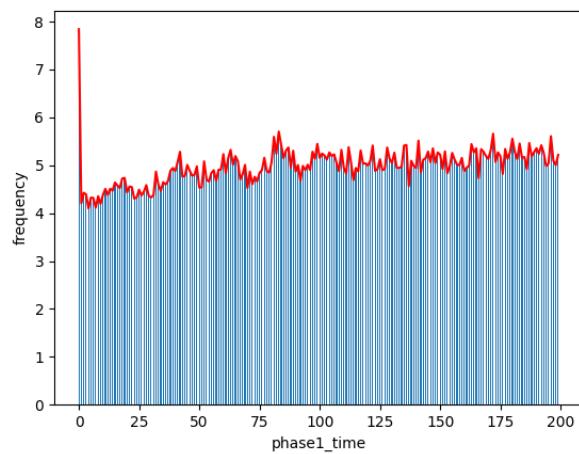


図 823 pht/experiment-win/1220-1/phase1/1220-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50059-in-chronological-order.eps

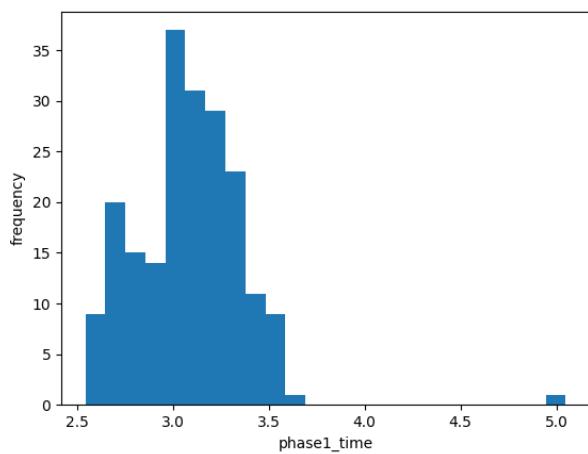


図 824 pht/experiment-win/1220-1/phase1/1220-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50191.eps

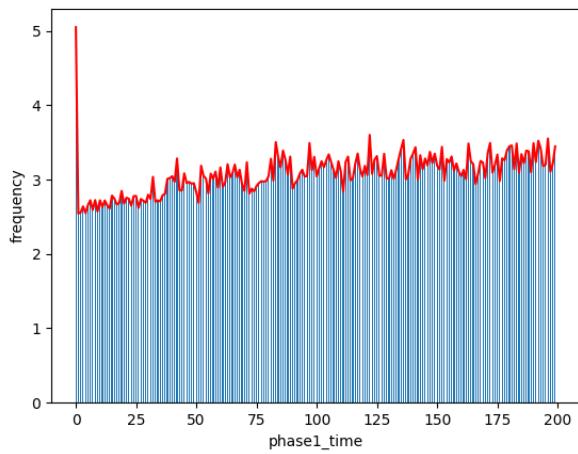


図 825 pht/experiment-win/1220-1/phase1/1220-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50191-in-chronological-order.eps

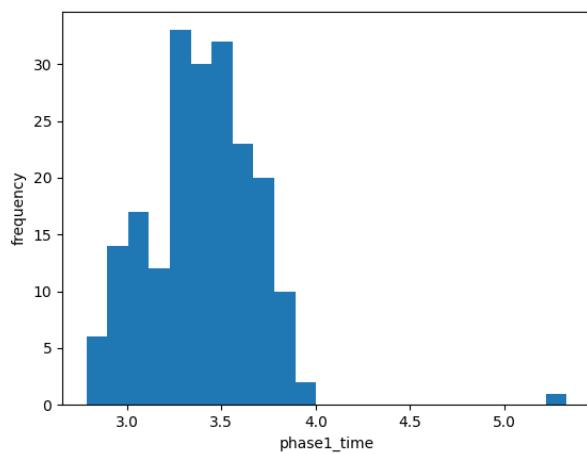


図 826 pht/experiment-win/1220-1/phase1/1220-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50193.eps

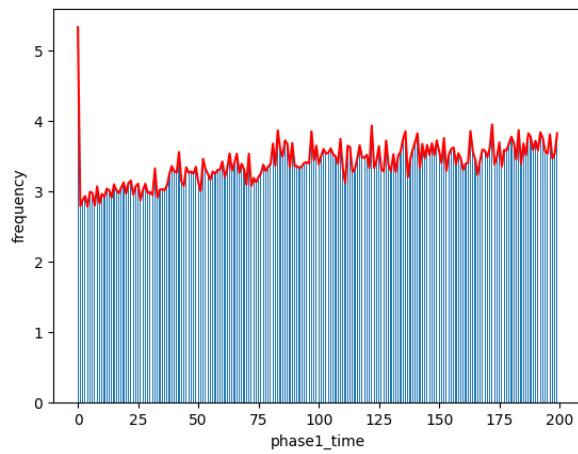


図 827 pht/experiment-win/1220-1/phase1/1220-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50193-in-chronological-order.eps

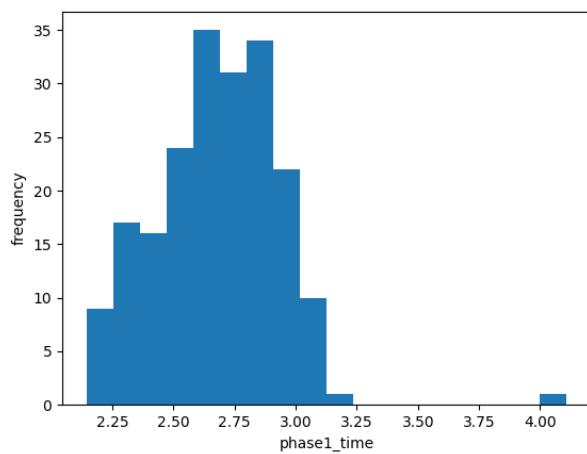


図 828 pht/experiment-win/1220-1/phase1/1220-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50195.eps

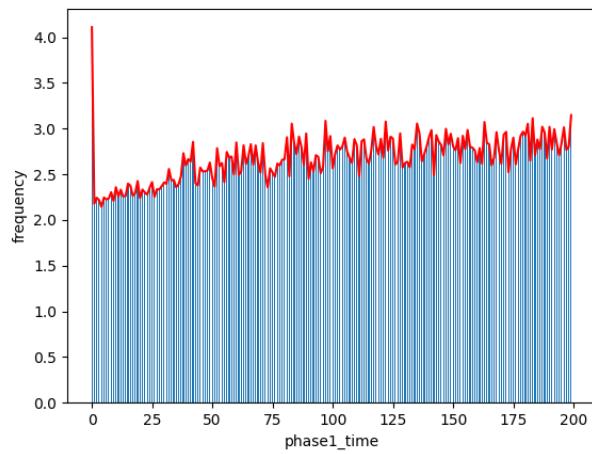


図 829 pht/experiment-win/1220-1/phase1/1220-1-75D-PC3ALL-DIF1-INT1-30Phase1-time50195-in-chronological-order.eps

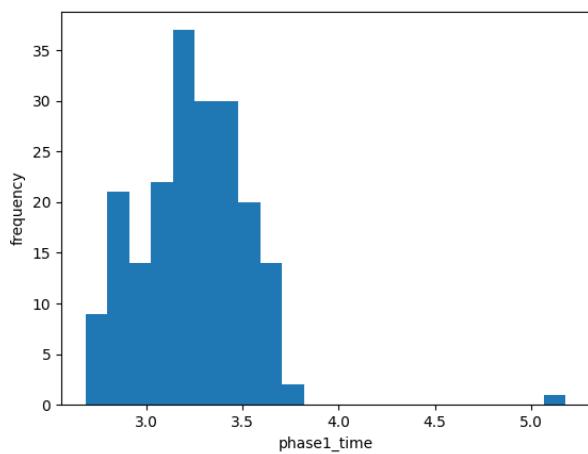


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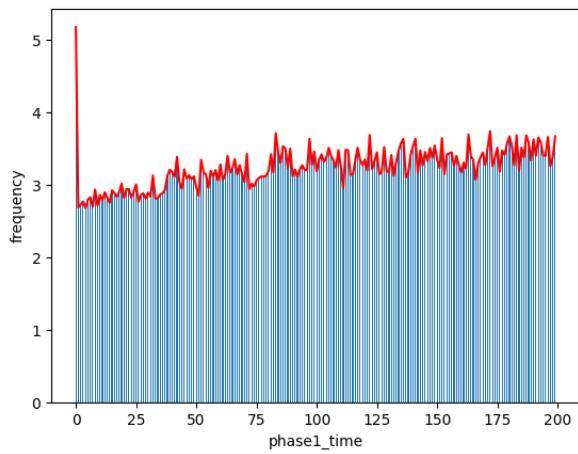


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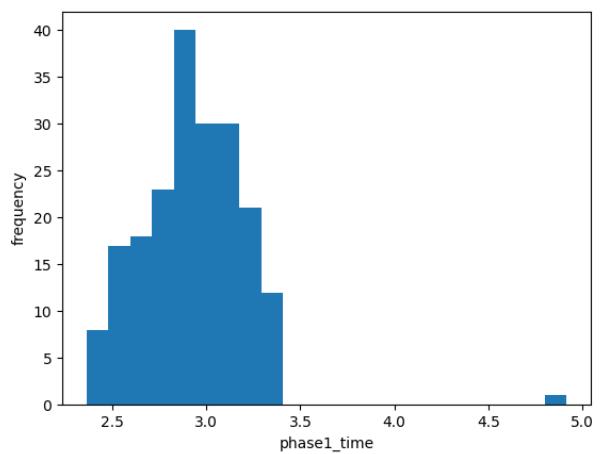


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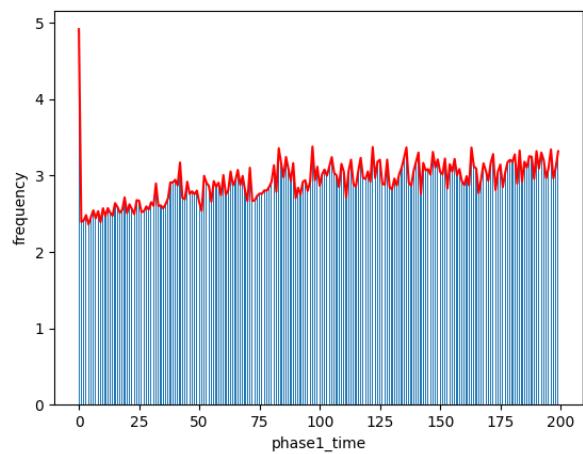


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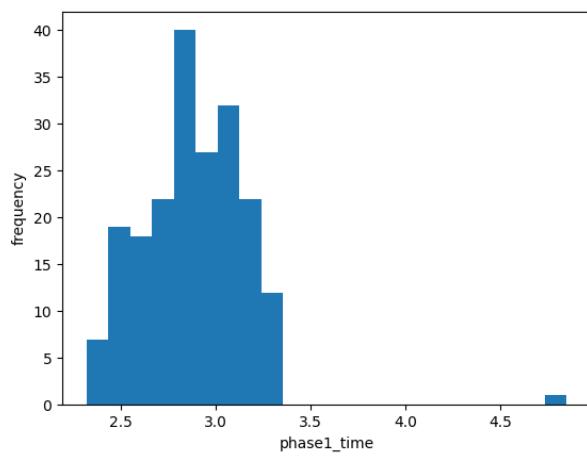


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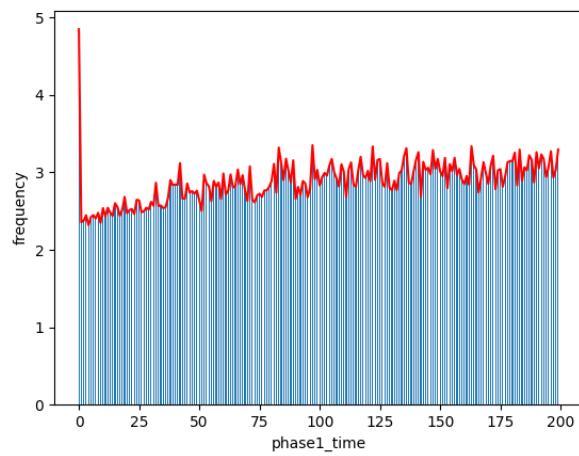


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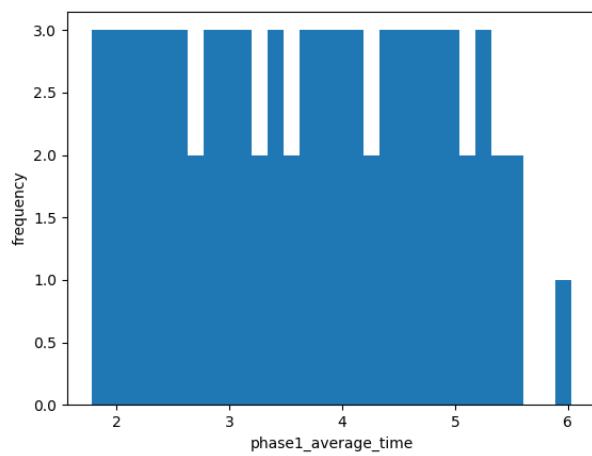


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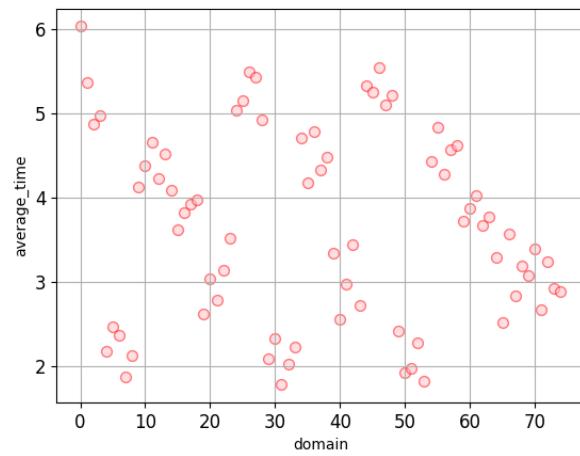


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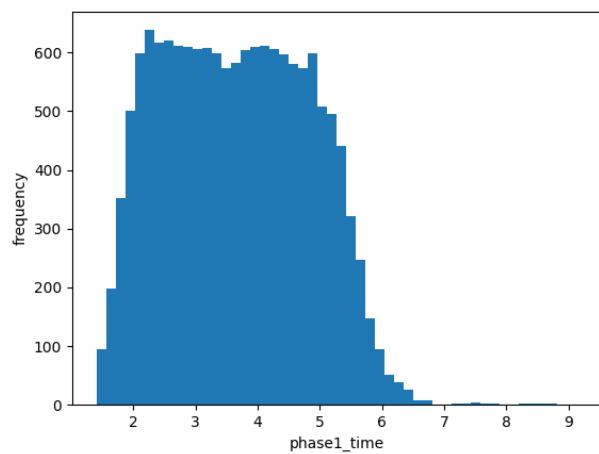


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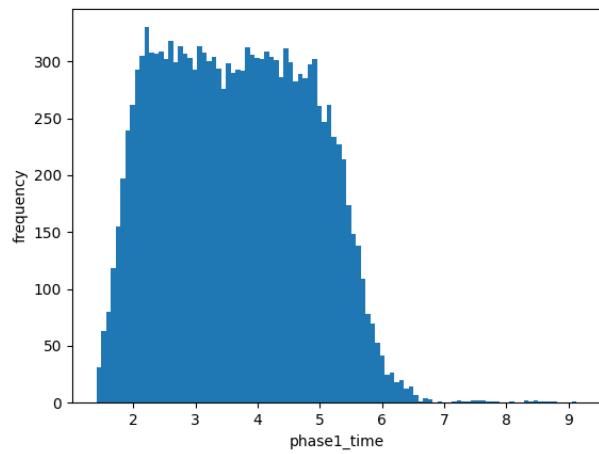


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