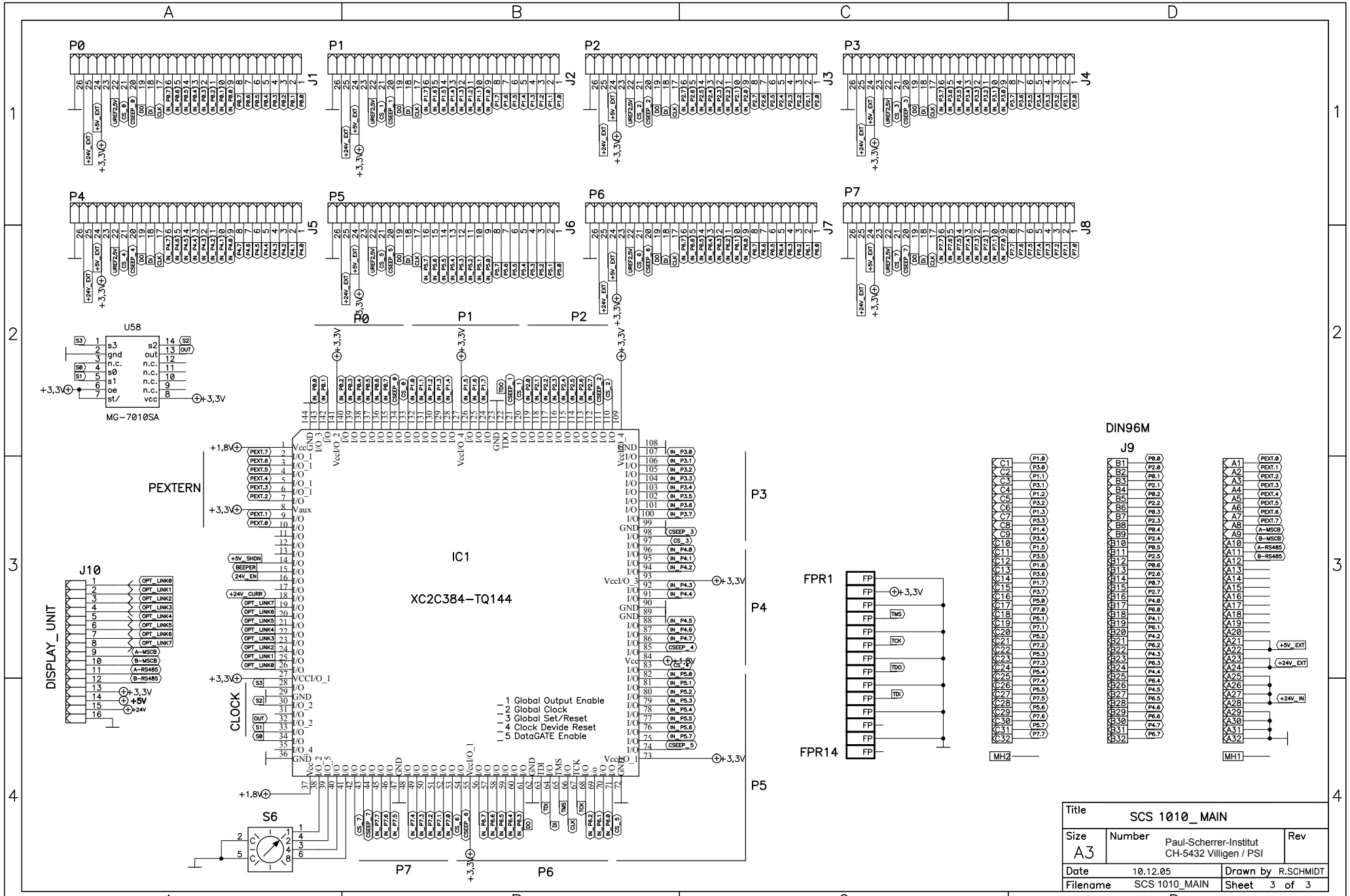
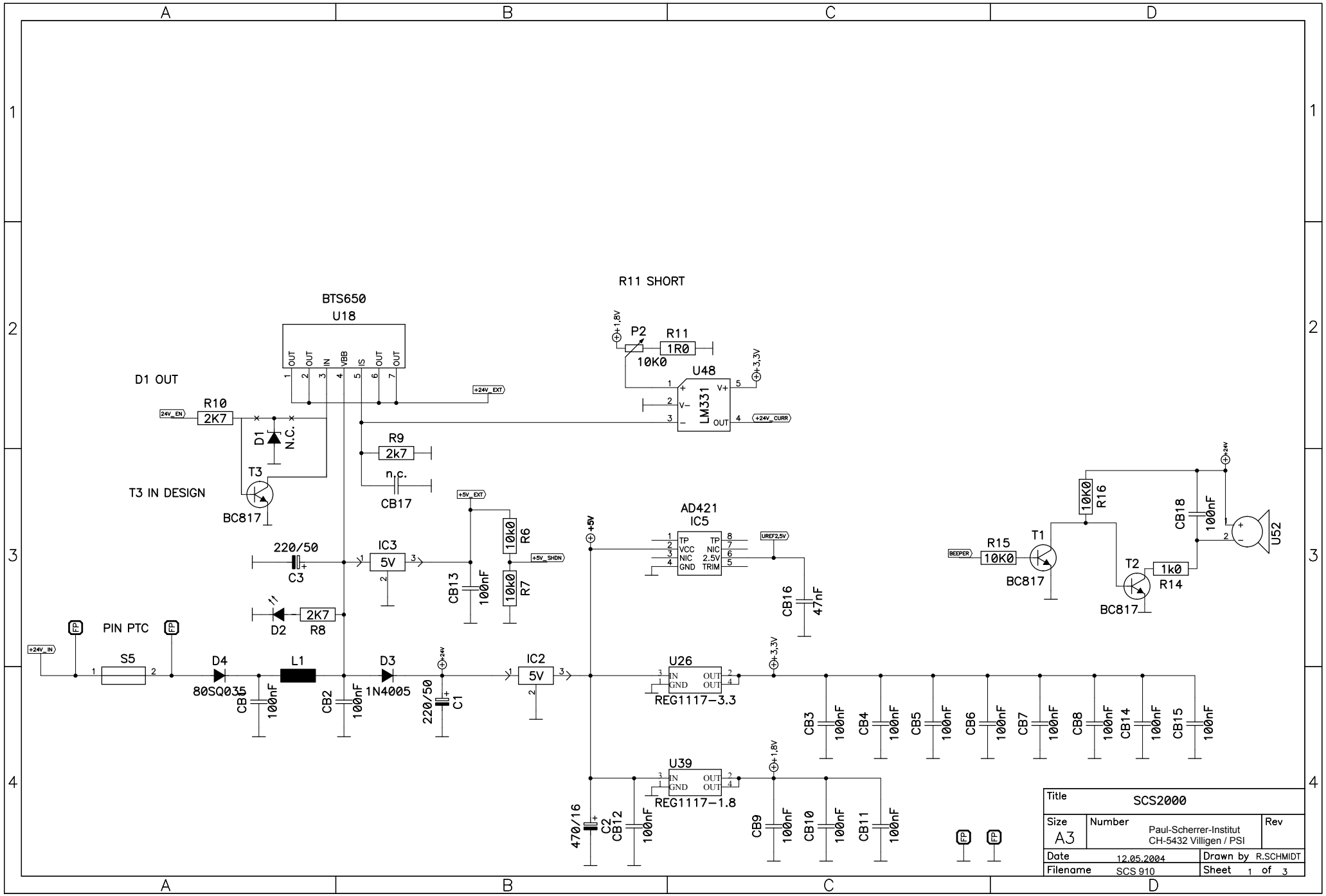

P0
P1
P2
P3
P4
P5
P6
P7
J1
J2
J3
J4
J5
J6
J7
J8
U58
MG-7010SA
IC1
XC2C384-TQ144
PEXTERN
DISPLAY_UNIT
J10
CLOCK
S6
FPR1
FPR14
DIN96M
J9
MH1
MH2
_1 Global Output Enable
_2 Global Clock
_3 Global Set/Reset
_4 Clock Devide Reset
_5 DataGATE Enable
Title
SCS 1010_ MAIN
Size
A3
Number
Paul-Scherrer-Institut
CH-5432 Villigen / PSI
Rev
Date
10.12.05
Drawn by
R.SCHMIDT
Filename
SCS 1010_MAIN
Sheet
3 of 3


R11 SHORT
BTS650
U18
OUT OUT IN VBB IS OUT OUT
D1 OUT
R10
2K7
24V_EN
D1
N.C.
T3
T3 IN DESIGN
BC817
R9
2k7
n.c.
CB17
+24V_EXT
+5V_EXT
+5V_SHDN
R6
10k0
R7
10k0
CB13
100nF
IC3
5V
220/50
C3
D2
2K7
R8
PIN PTC
S5
+24V_IN
D4
80SQ035
CB
100nF
L1
CB2
100nF
D3
1N4005
220/50
C1
IC2
5V
+1,8V
P2
10K0
R11
1R0
U48
LM331
V+
V-
OUT
+3,3V
+24V_CURR
+5V
AD421
IC5
TP VCC NIC GND
TP NIC 2.5V TRIM
UREF2,5V
CB16
47nF
U26
IN GND
OUT OUT
REG1117-3.3
+3,3V
CB3 100nF
CB4 100nF
CB5 100nF
CB6 100nF
CB7 100nF
CB8 100nF
CB14 100nF
CB15 100nF
U39
IN GND
OUT OUT
REG1117-1.8
+1,8V
470/16
C2
CB12
100nF
CB9 100nF
CB10 100nF
CB11 100nF
BEEPER
R15
10K0
T1
BC817
10K0
R16
T2
BC817
1k0
R14
CB18
100nF
+24V
U52
Title
SCS2000
Size
A3
Number
Paul-Scherrer-Institut
CH-5432 Villigen / PSI
Rev
Date
12.05.2004
Drawn by
R.SCHMIDT
Filename
SCS 910
Sheet
1 of 3