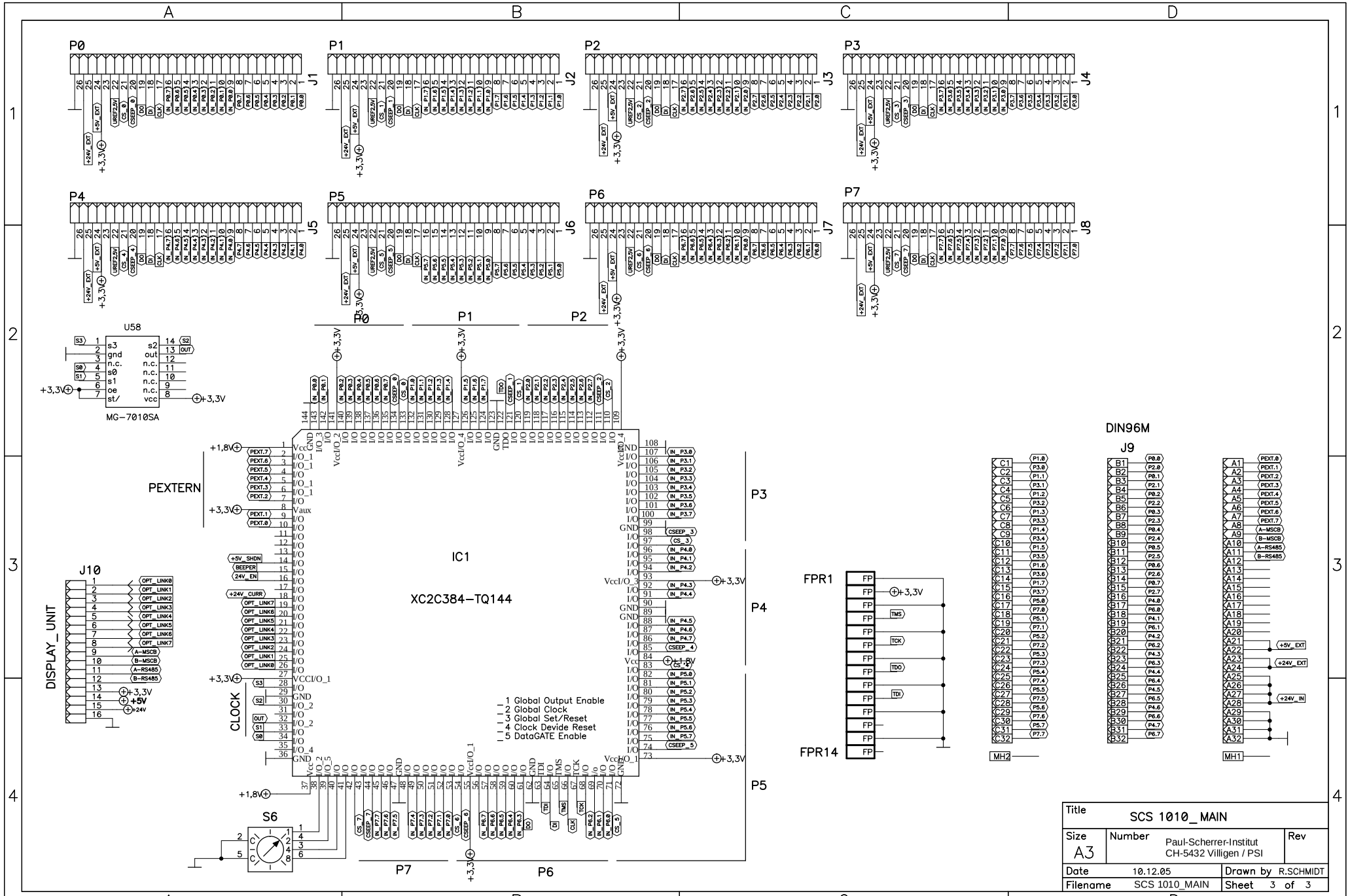
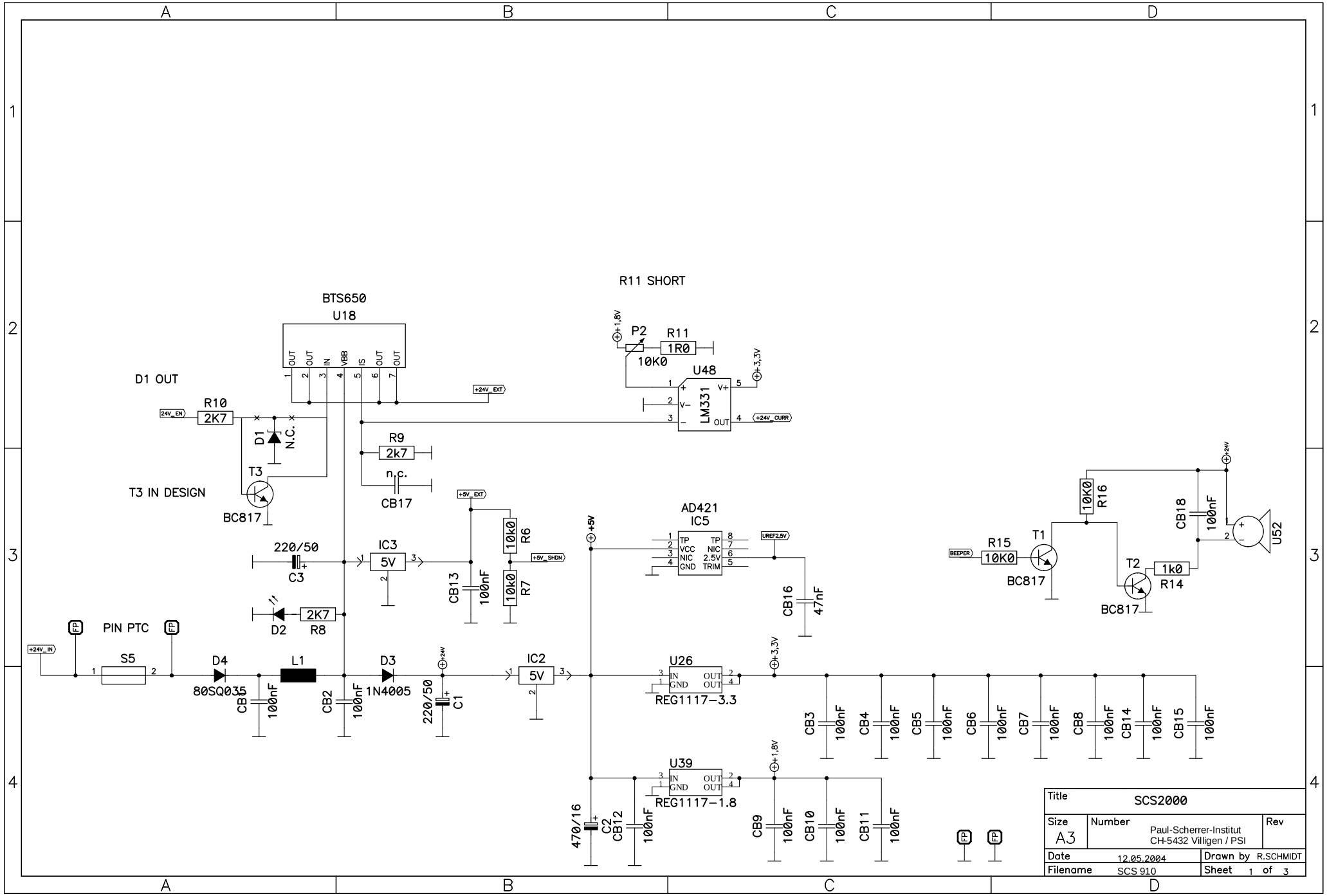

P0
P1
P2
P3
P4
P5
P6
P7
J1
J2
J3
J4
J5
J6
J7
J8
U58
MG-7010SA
IC1
XC2C384-TQ144
PEXTERN
DISPLAY_UNIT
J10
CLOCK
S6
FPR1
FPR14
DIN96M
J9
MH1
MH2
1 Global Output Enable
2 Global Clock
3 Global Set/Reset
4 Clock Devide Reset
5 DataGATE Enable
Title
SCS 1010_ MAIN
Size
A3
Number
Paul-Scherrer-Institut
CH-5432 Villigen / PSI
Rev
Date
10.12.05
Drawn by
R.SCHMIDT
Filename
SCS 1010_MAIN
Sheet
3 of 3


R11 SHORT
BTS650
U18
OUT
OUT
IN
VBB
IS
OUT
OUT
D1 OUT
R10
2K7
24V_EN
D1
N.C.
T3
T3 IN DESIGN
BC817
R9
2k7
n.c.
CB17
+24V_EXT
+1,8V
P2
10K0
R11
1R0
U48
LM331
V+
V-
OUT
+3,3V
+24V_CURR
+5V_EXT
R6
10k0
R7
10k0
+5V_SHDN
220/50
C3
IC3
5V
CB13
100nF
D2
2K7
R8
PIN PTC
+24V_IN
S5
D4
80SQ035
CB1
100nF
L1
CB2
100nF
D3
1N4005
220/50
C1
+24V
IC2
5V
+5V
AD421
IC5
TP
VCC
NIC
GND
TP
NIC
2.5V
TRIM
UREF2,5V
CB16
47nF
U26
IN
GND
OUT
OUT
REG1117-3.3
+3,3V
U39
IN
GND
OUT
OUT
REG1117-1.8
+1,8V
470/16
C2
CB12
100nF
CB9
100nF
CB10
100nF
CB11
100nF
CB3
100nF
CB4
100nF
CB5
100nF
CB6
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CB7
100nF
CB8
100nF
CB14
100nF
CB15
100nF
BEEPER
R15
10K0
T1
BC817
10K0
R16
T2
BC817
1k0
R14
CB18
100nF
+24V
U52
Title
SCS2000
Size
A3
Number
Paul-Scherrer-Institut
CH-5432 Villigen / PSI
Rev
Date
12.05.2004
Drawn by
R.SCHMIDT
Filename
SCS 910
Sheet
1
of
3