







FPGA BANK 3 | Correct | Corr XC6SLX45 - BANK 3 R102 NLR SDRAM_VDD SDRAM_VDDPULLUP R103 ₹20K R107 49.9 R108 49.9 R113 49.9 R104 R105 R106 49.9 R109 49.9 R110 49.9 R111 49.9 R112 SDRAM_VREF SDRAM_VDDQ SDRAM_VDD R114 C150 20K 0.1uF 20K R117 R118 R119 49.9 R120 49.9 R121 R123 R124 U32 MT47H64M16HR-3 UDQS B7 mcb3 dram dqs A8 mcb3 dram dqs n L8 K7 L7 K3 CS# RAS# CAS# WE# SDRAM | D. | Lap | MSD013 | D. | Lap | MSD014 | D. | Lap | MSD013 | D. | 64Mx16 R125 R126 R127 4.7K 4.7K 100 R E 33 E SDRAM_VDD FB12 BLM15AG100SN1D SDRAM VDDQ C151 C152 C153 C154 C155 C156 C157 C158 C159 C160 C161 C162 C163 C164 C165 C166 10nF 10nF 10nF 10nF 10nF 10nF C167 C168 C169 C170 C171 C172 mcb3_dram_a11 47nF 47nF 47nF 47nF 47nF 47nF SDRAM_VDDPULLUP SDRAM_VREF C187 C188 C189 C190 C183 C184 C2 F4 F6 G2 J5 L2 L7 N5 R2 U5 W2 W5 AA3 DIG 1.8V SDRAM VREF FB13) [BI M15AG100SN1D C191 C192 C193 C194 C195 C196 C197 C198 100F 100F 22uF 22uF 22uF 0.1uF 0.1uF Loctronix Corporation Model 2300 040-00102 SDRAM + FPGA BANK3 Α





