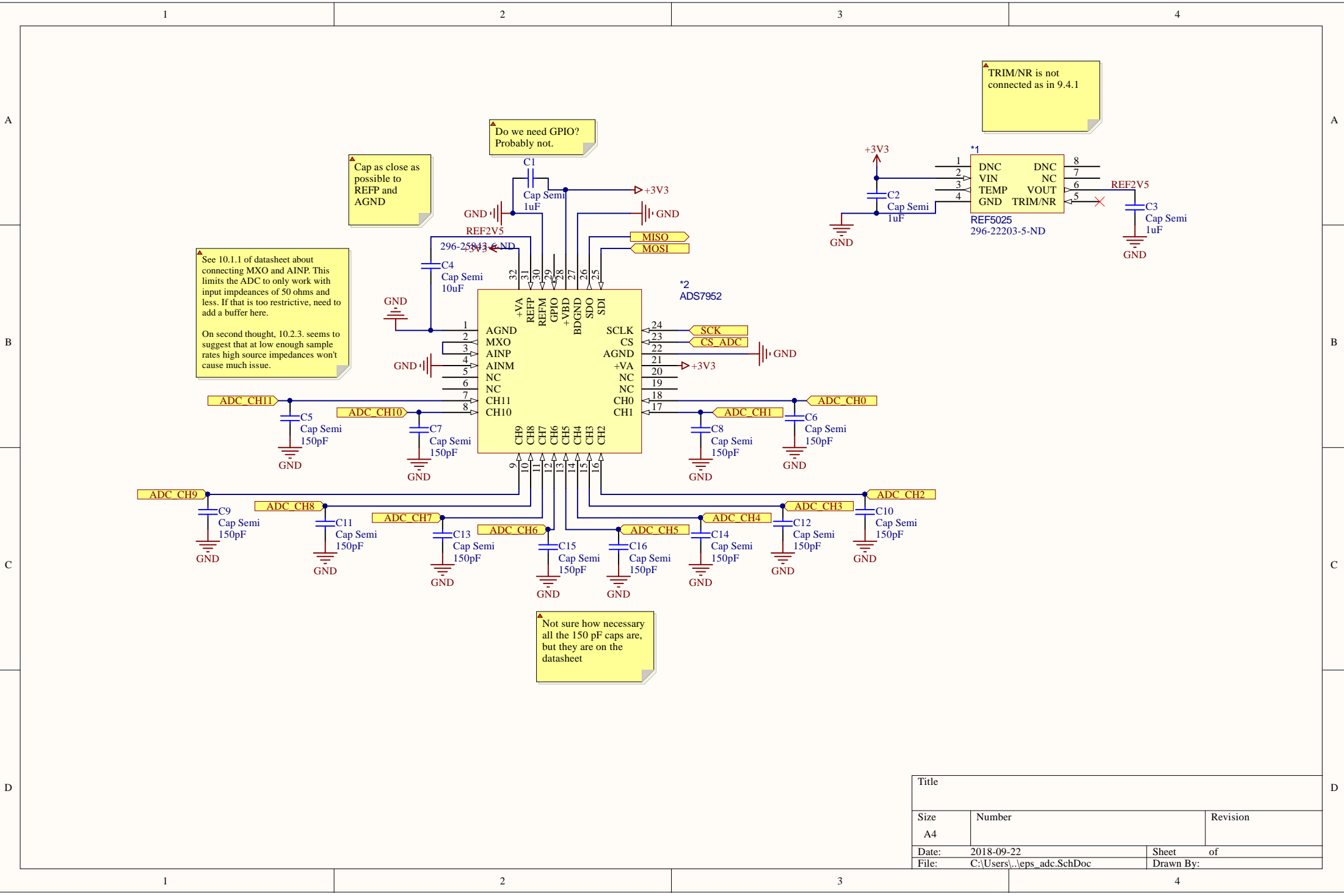
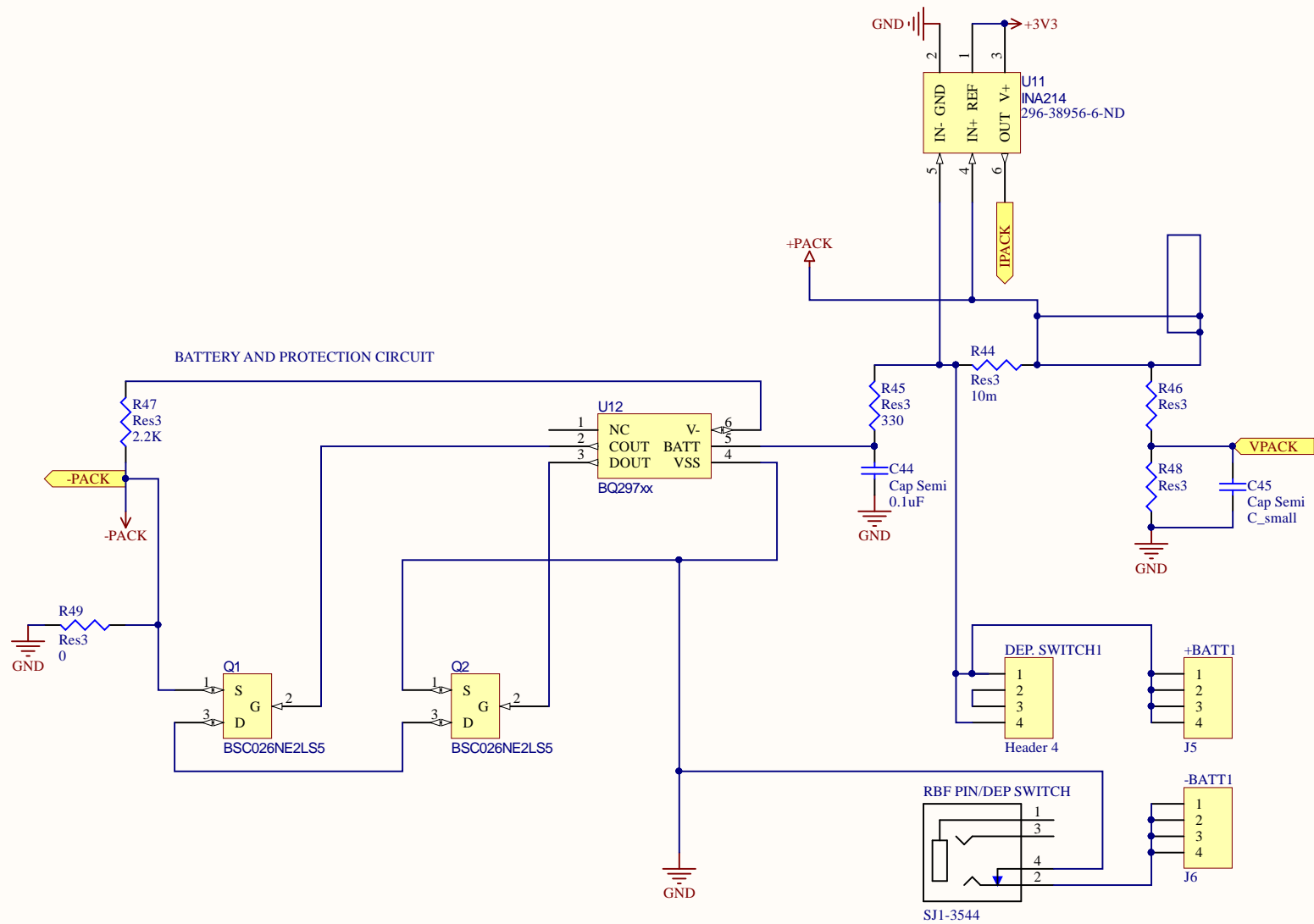


Connecting the voltage outputs of these two together for now, same for the boost. Can't tell if one fails.

+PACK is the internal plane, it is after the battery protection circuit and the batteries. +BATT is between the protection and the batteries

Title			
Size	Number		Revision
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Date:	2018-09-22		Sheet of
File:	C:\Users\...\eps_6.0.SchDoc		Drawn By:





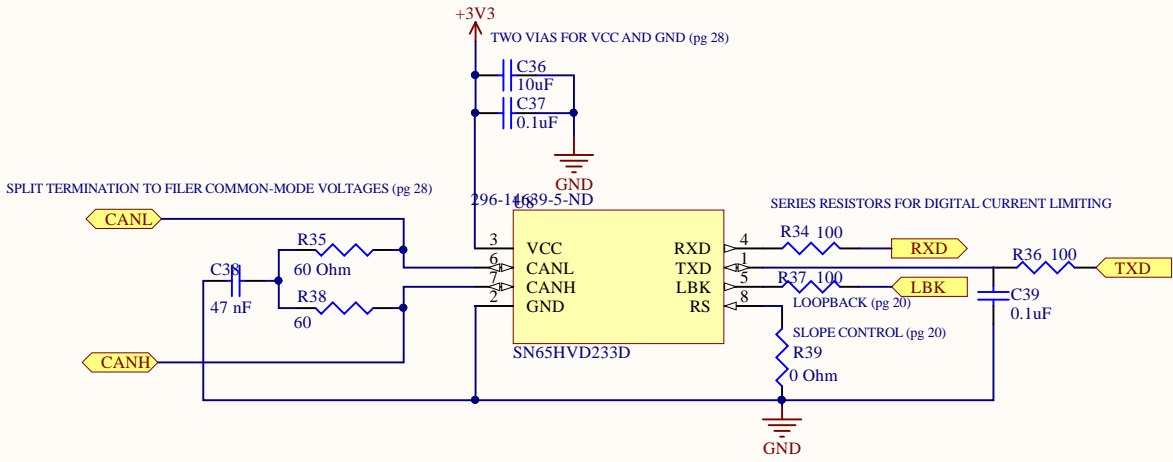
DEP. SWITCH: PINS 1,2  
 RBF: PINS 3,4 (PIN 3 HERE IS PIN 1 AND PIN 4 IS PIN 2 ON KICAD SCHEMATIC)

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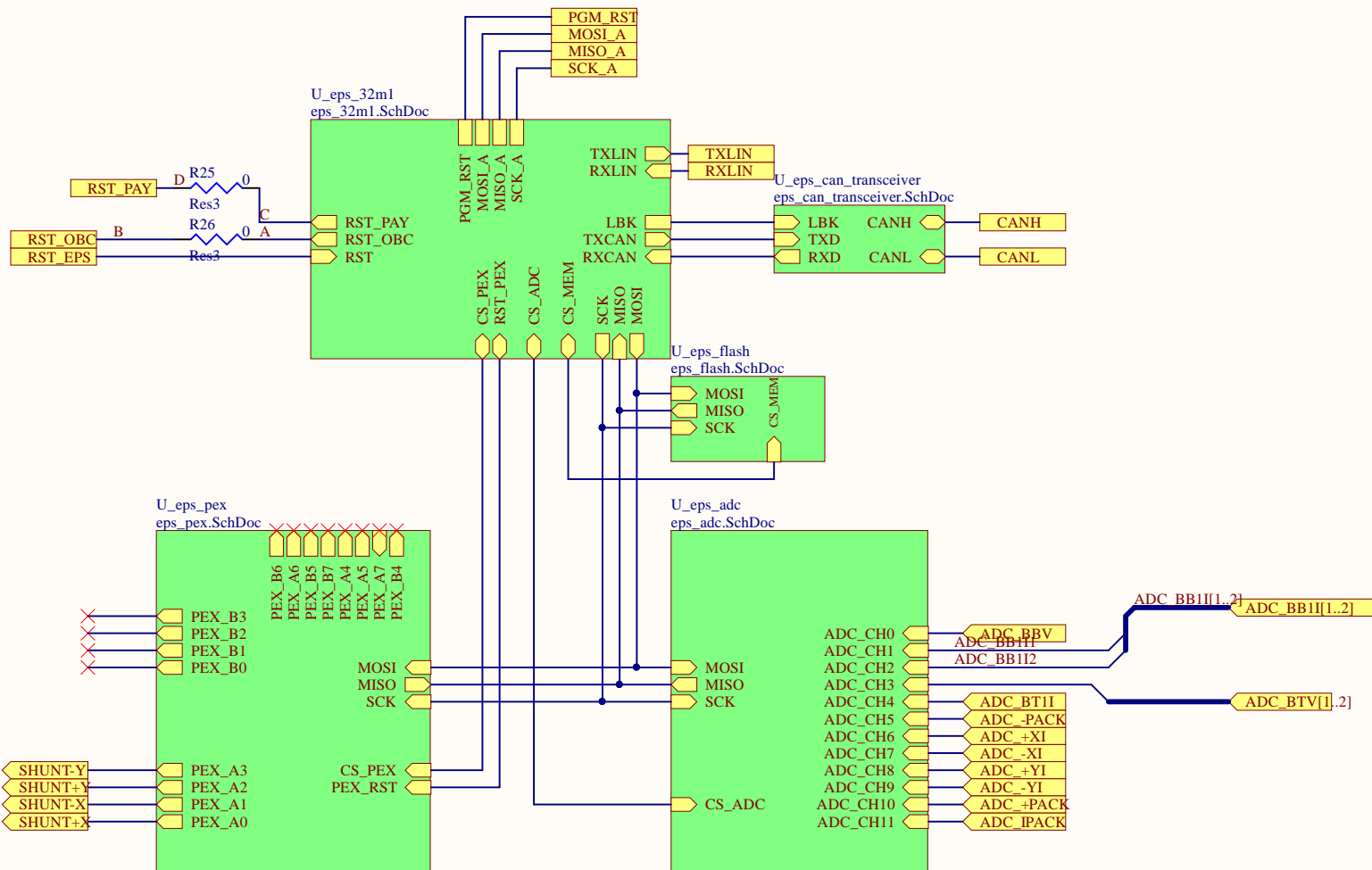








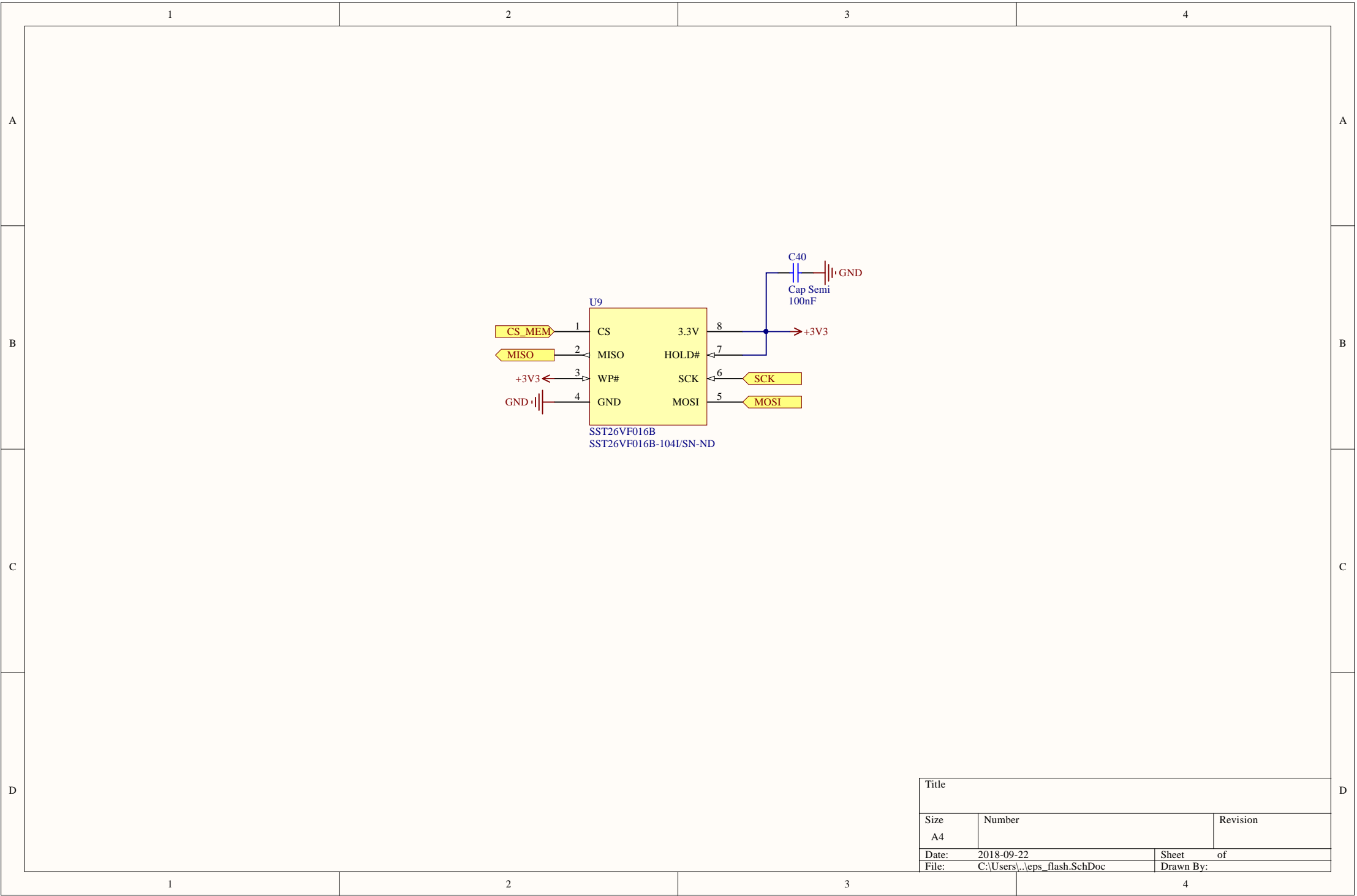
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Date:	2018-09-22	Sheet	of
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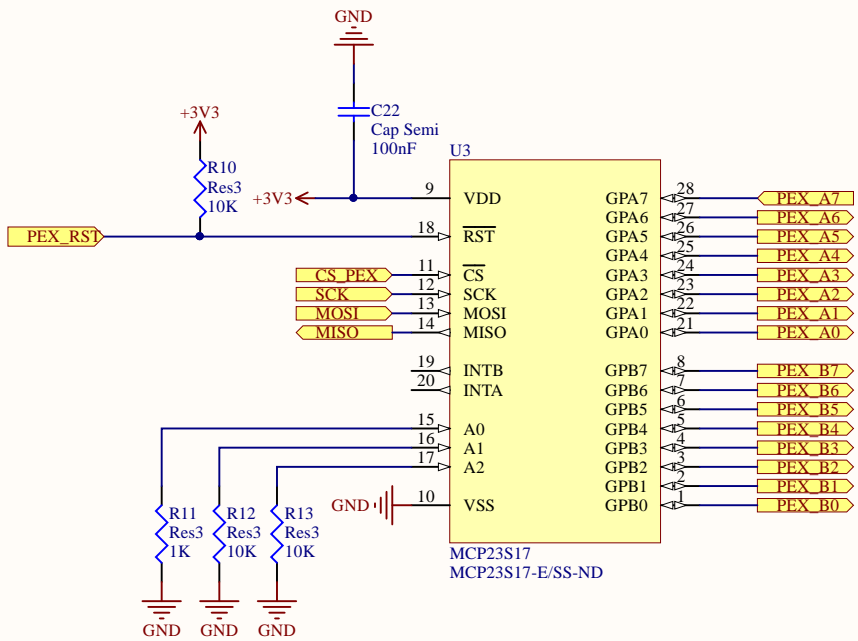


▲ For this to work, need the PEX to start in high impedance on startup to prevent a loop where the system cant start because the PEX isn't on  
 UPDATE: PEX Defaults to input on startup, so should be fine

▲ TBH this is a software hazard. why do we need this? im gonna slap on the 0 ohm resistors here

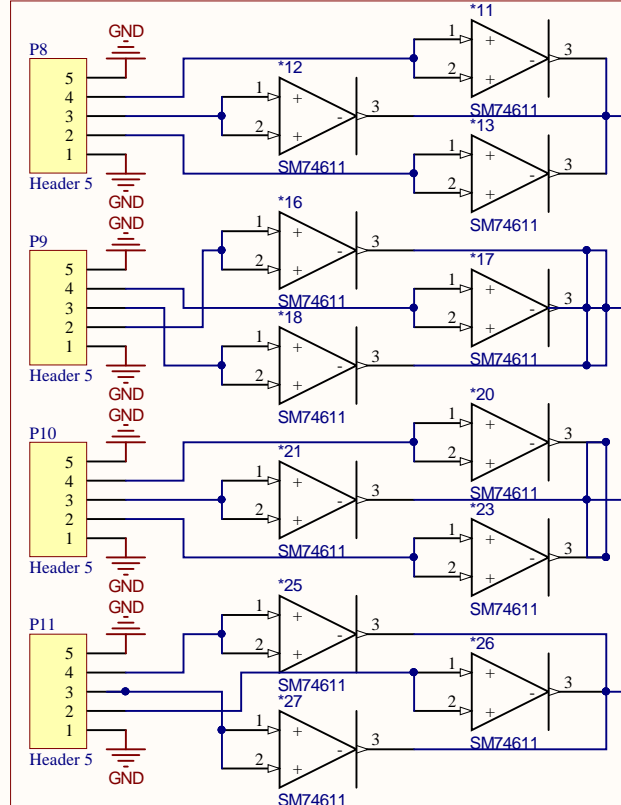
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File:	C:\Users\...\eps_digital.SchDoc	Drawn By:



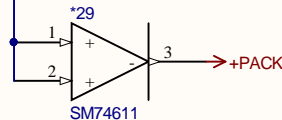
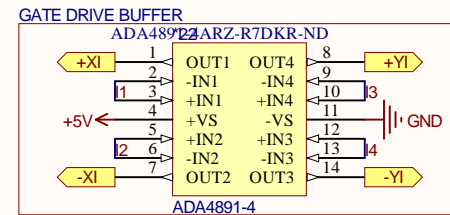
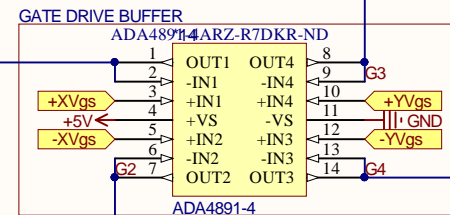
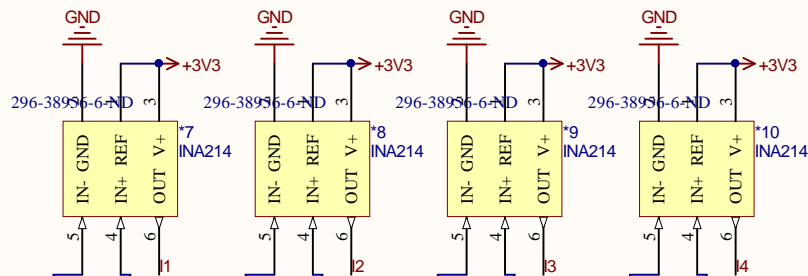
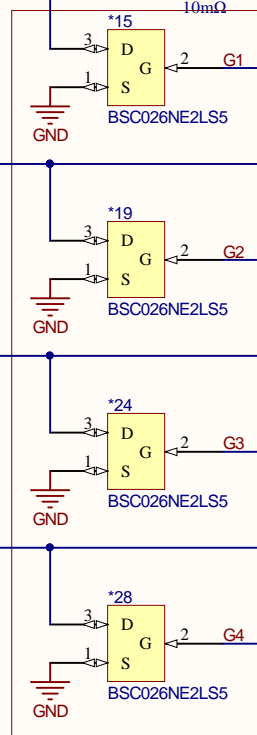


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Size A4	Number	Revision
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File:	C:\Users\...\eps_pex.SchDoc	Drawn By:

# SOLAR PANEL INTERFACE AND DIODES



## SHUNT SWITCHES



Title		
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A4		
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File:	C:\Users\...\eps_pv.SchDoc	Drawn By:

[illegible]