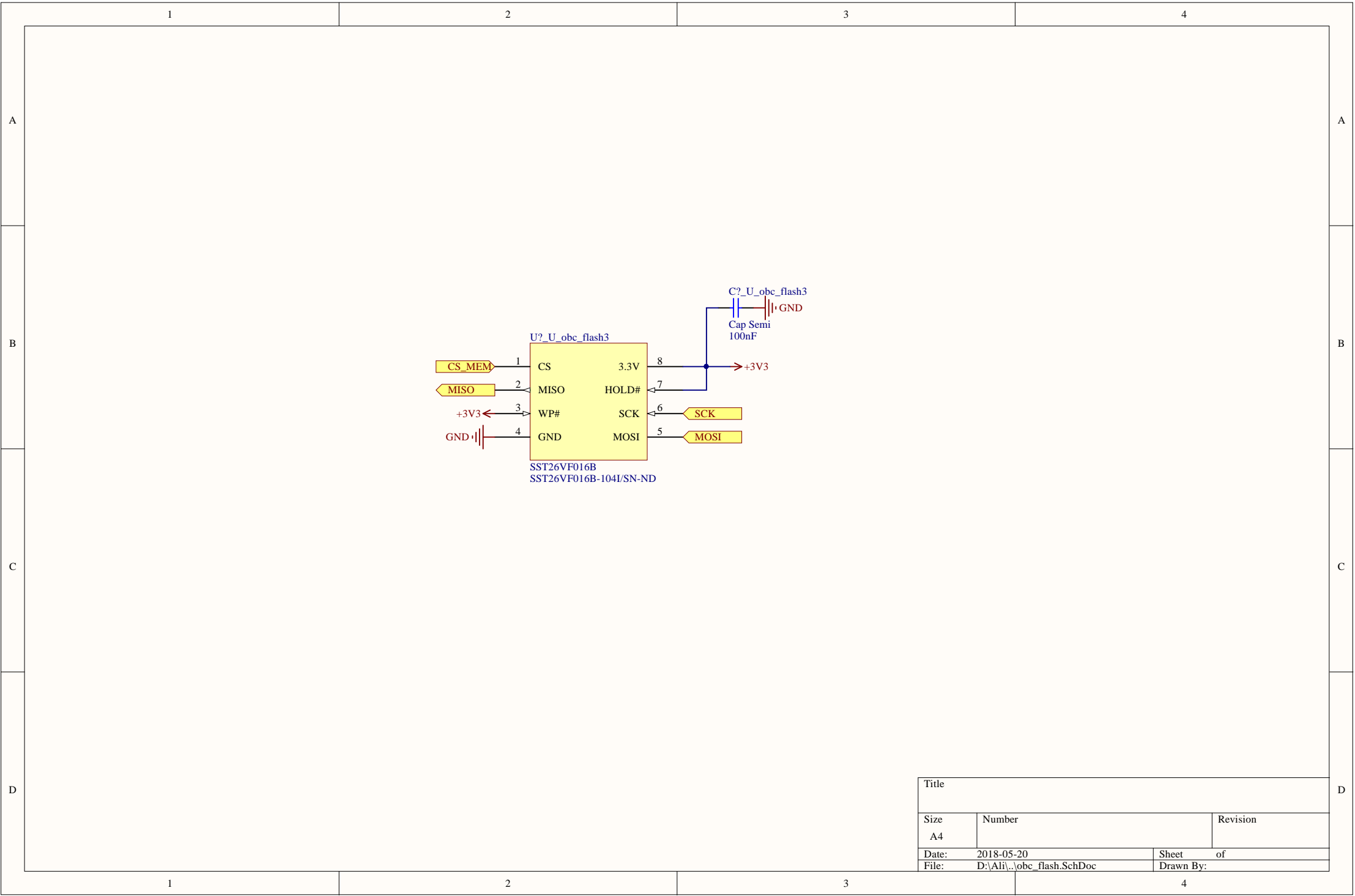
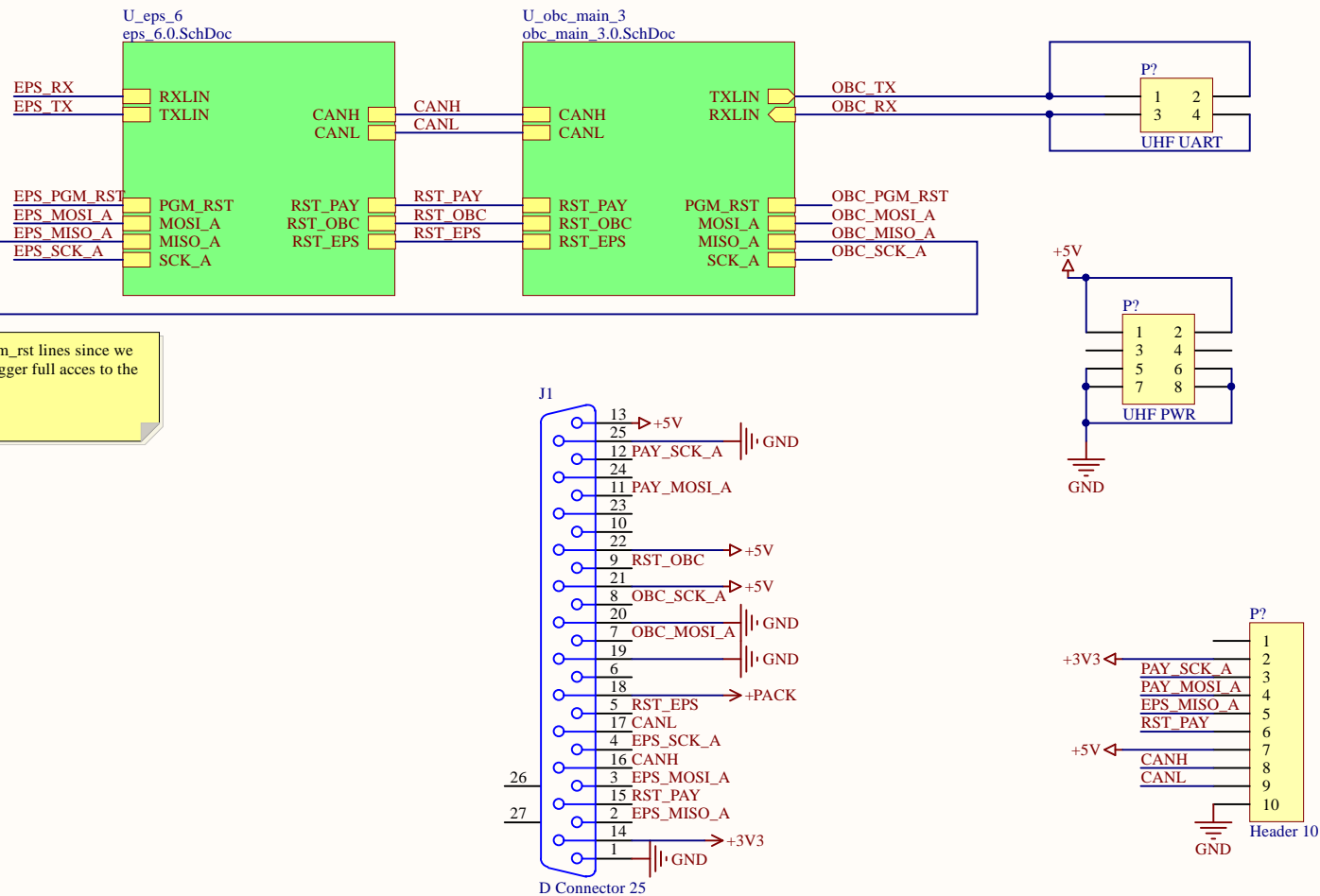


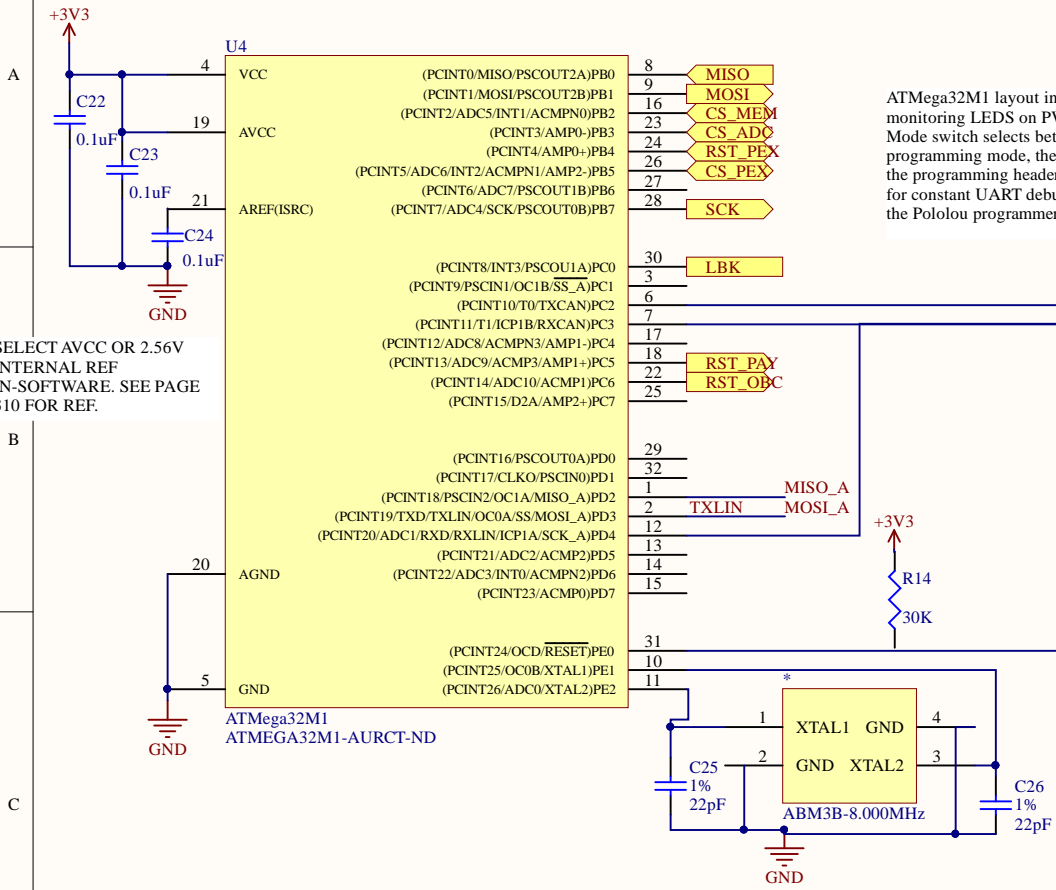
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Date:	2018-05-20	Sheet of
File:	D:\Ali\...\obc_flash.SchDoc	Drawn By:





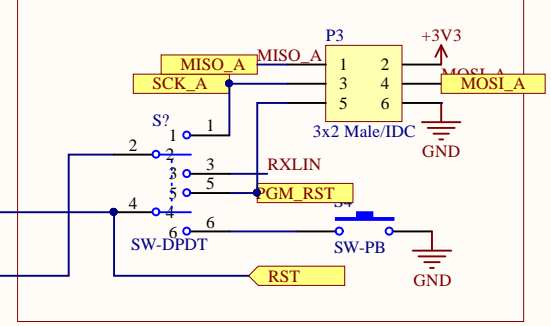
Title		
Size	Number	Revision
A4		
Date:	2018-05-20	Sheet of
File:	D:\Ali\bus-main.SchDoc	Drawn By:

SELECT AVCC OR 2.56V
INTERNAL REF
IN-SOFTWARE. SEE PAGE
310 FOR REF.

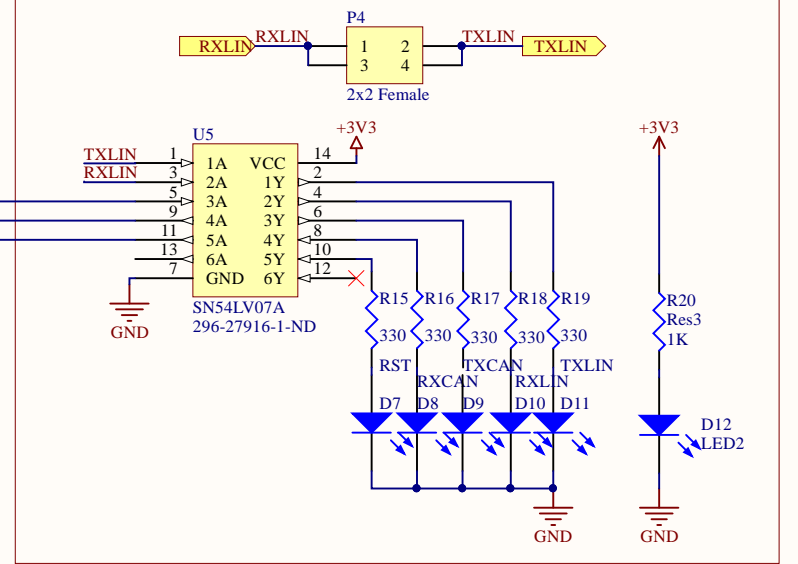


ATmega32M1 layout includes uController, 8MHz crystal, power connections, monitoring LEDs on PWR, TXCAN, RXCAN, RST, TXLIN and RXLIN. Mode switch selects between programming the board and "run mode". In programming mode, the RST and UART lines are disconnected and routed to the programming header. This allows for DebugWire to be used and allows for constant UART debugging without having to disconnect the RX pin from the Pololu programmer.

PROGRAMMING & MODE SELECT

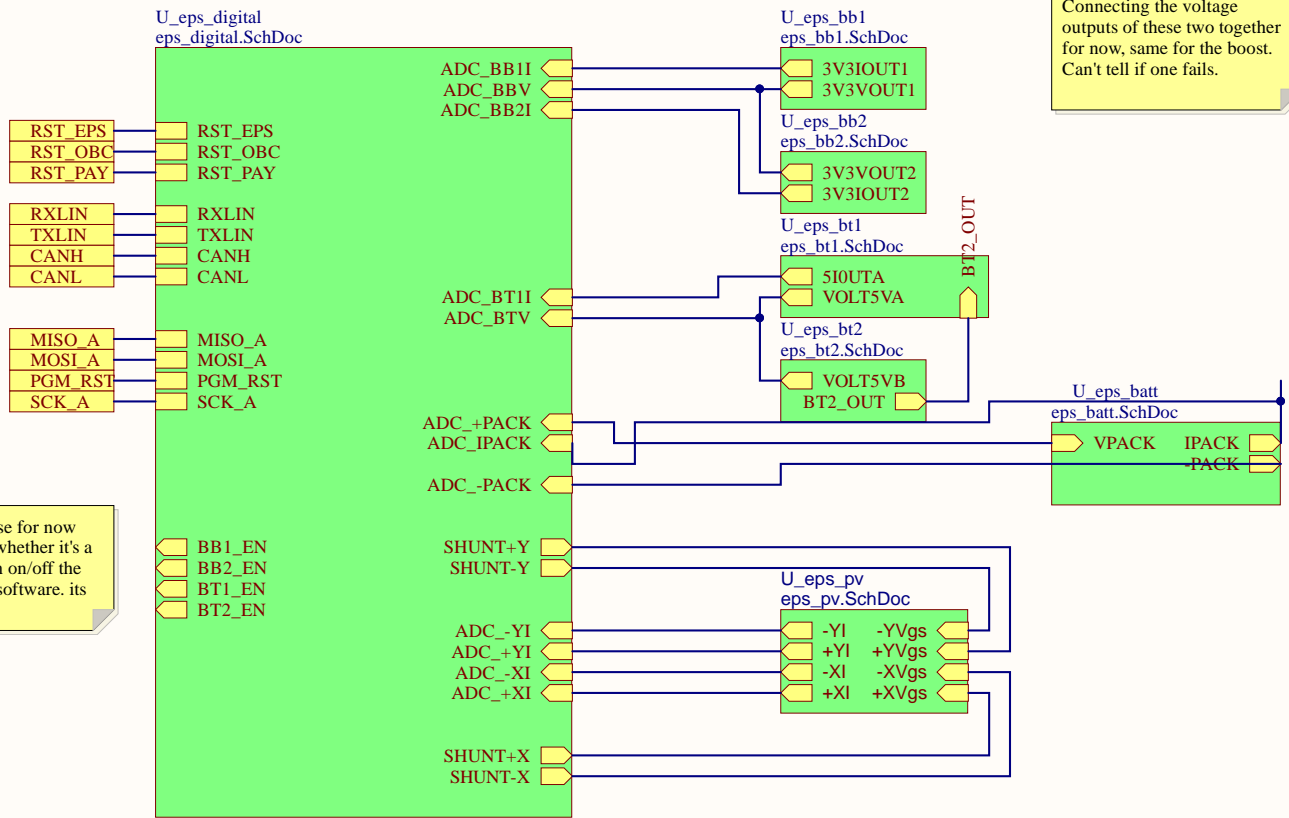


UART HEADER & MONITORING



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Size	Number	Revision
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Date:	2018-05-20	Sheet of
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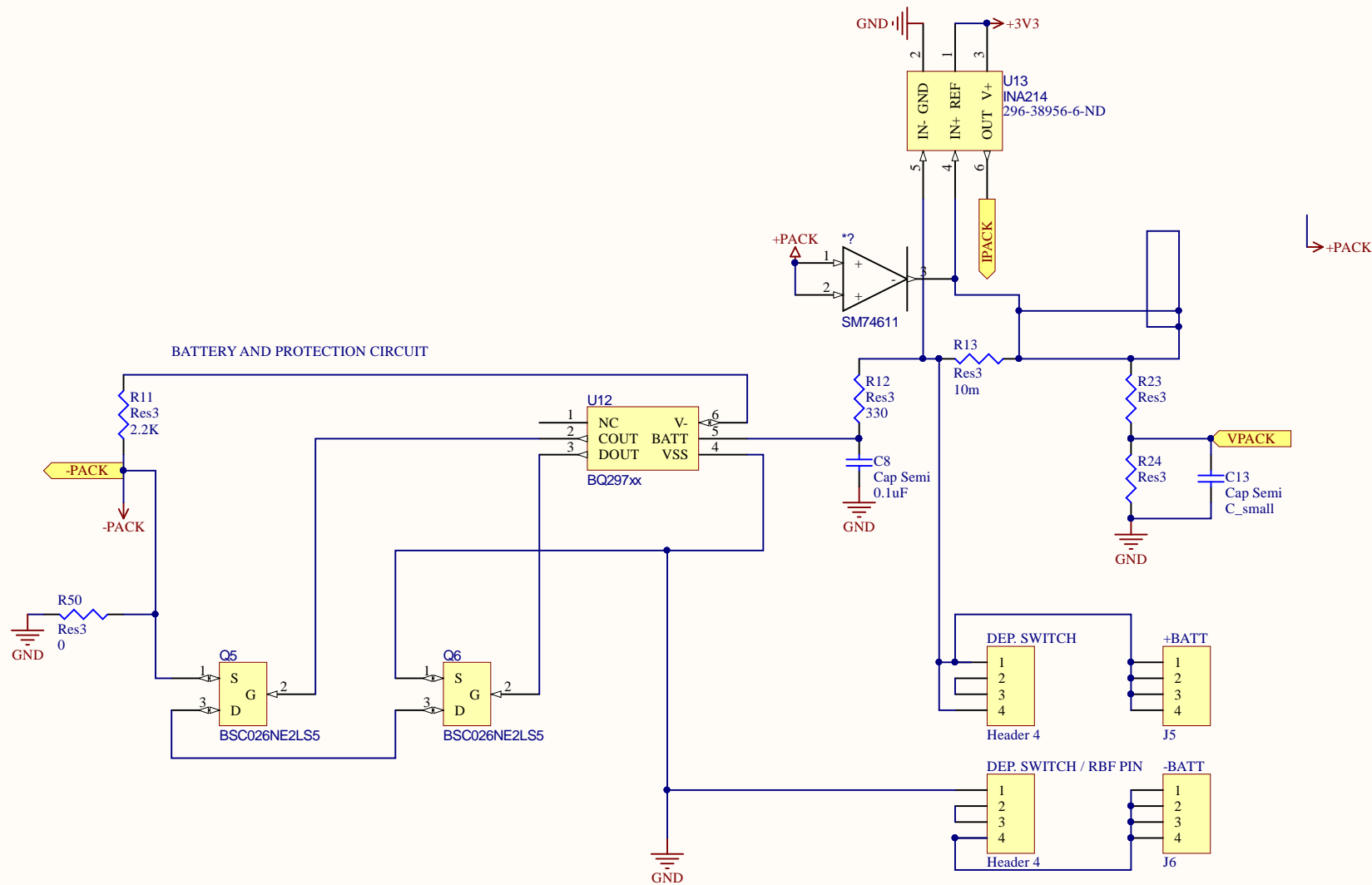
▲ Gonna leave these for now until we decide whether it's a good idea to turn on/off the converters with software. its probably not.



▲ Connecting the voltage outputs of these two together for now, same for the boost. Can't tell if one fails.

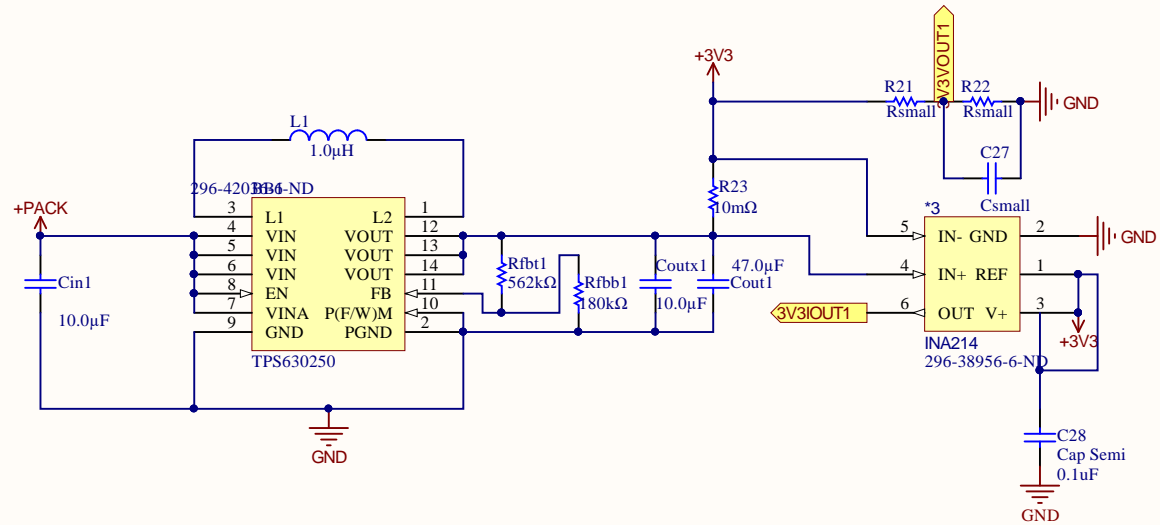
▲ +PACK is the internal plane, it is after the battery protection circuit and the batteries. +BATT is between the protection and the batteries

Title			
Size	Number		Revision
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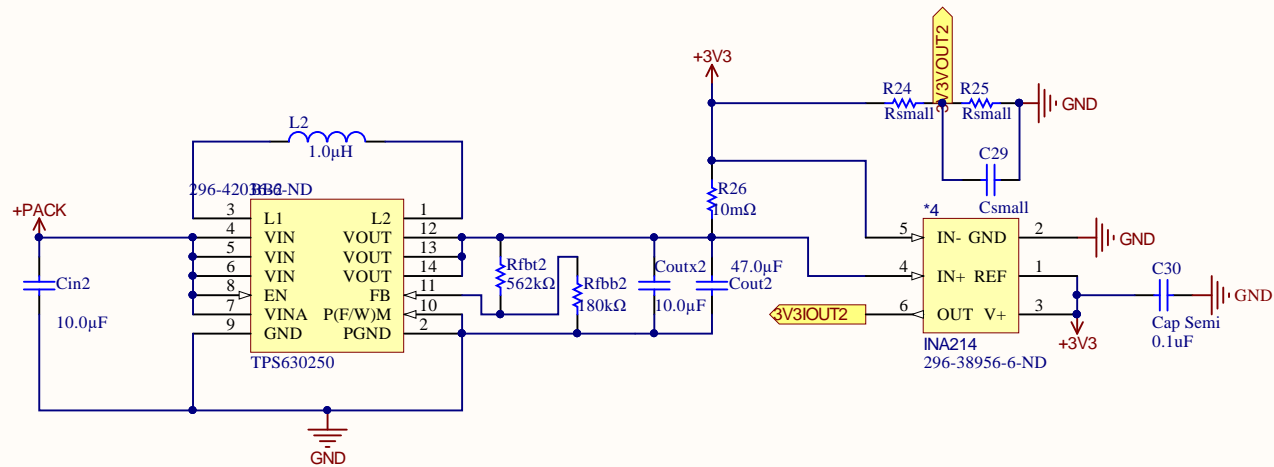


DEP. SWITCH: PINS 1,2
RBF: PINS 3,4 (PIN 3 HERE IS PIN 1 AND PIN 4 IS PIN 2 ON KICAD SCHEMATIC)

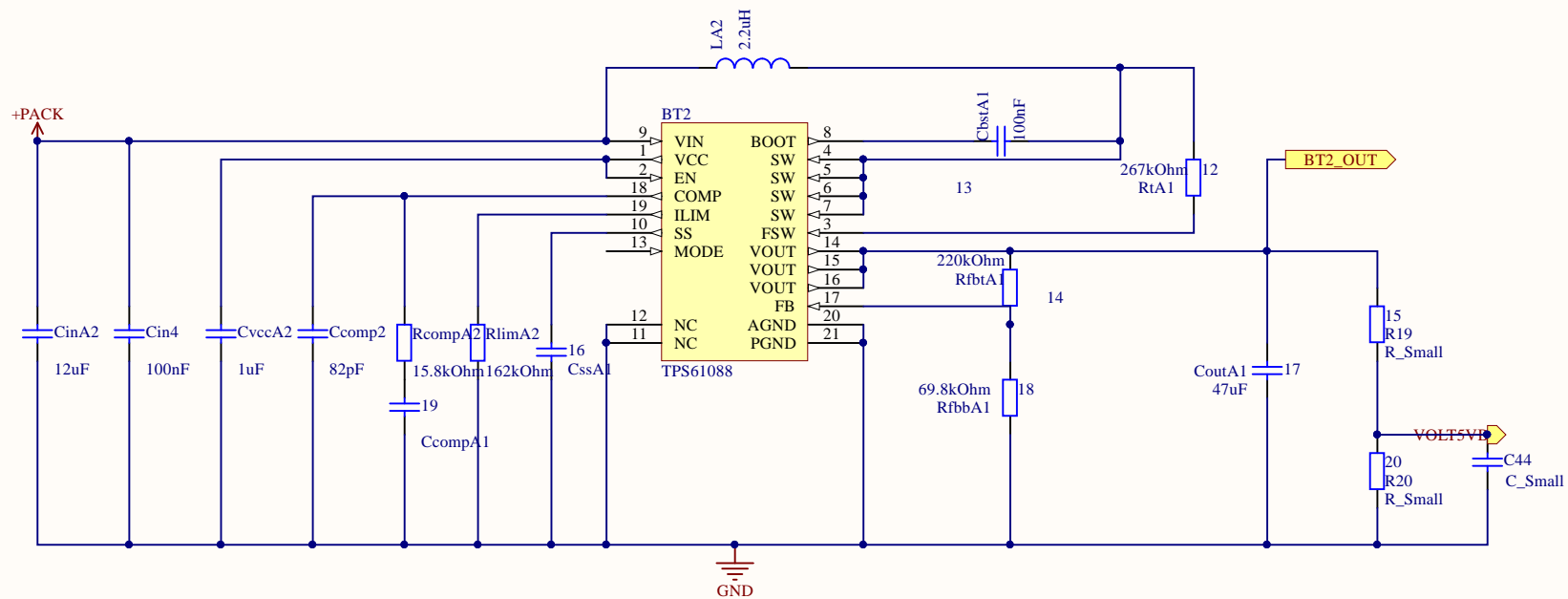
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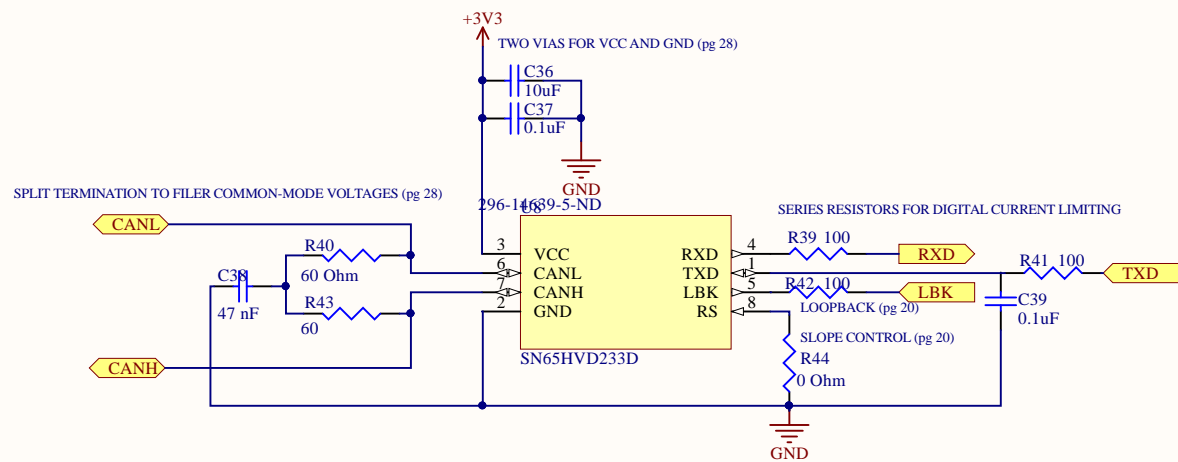
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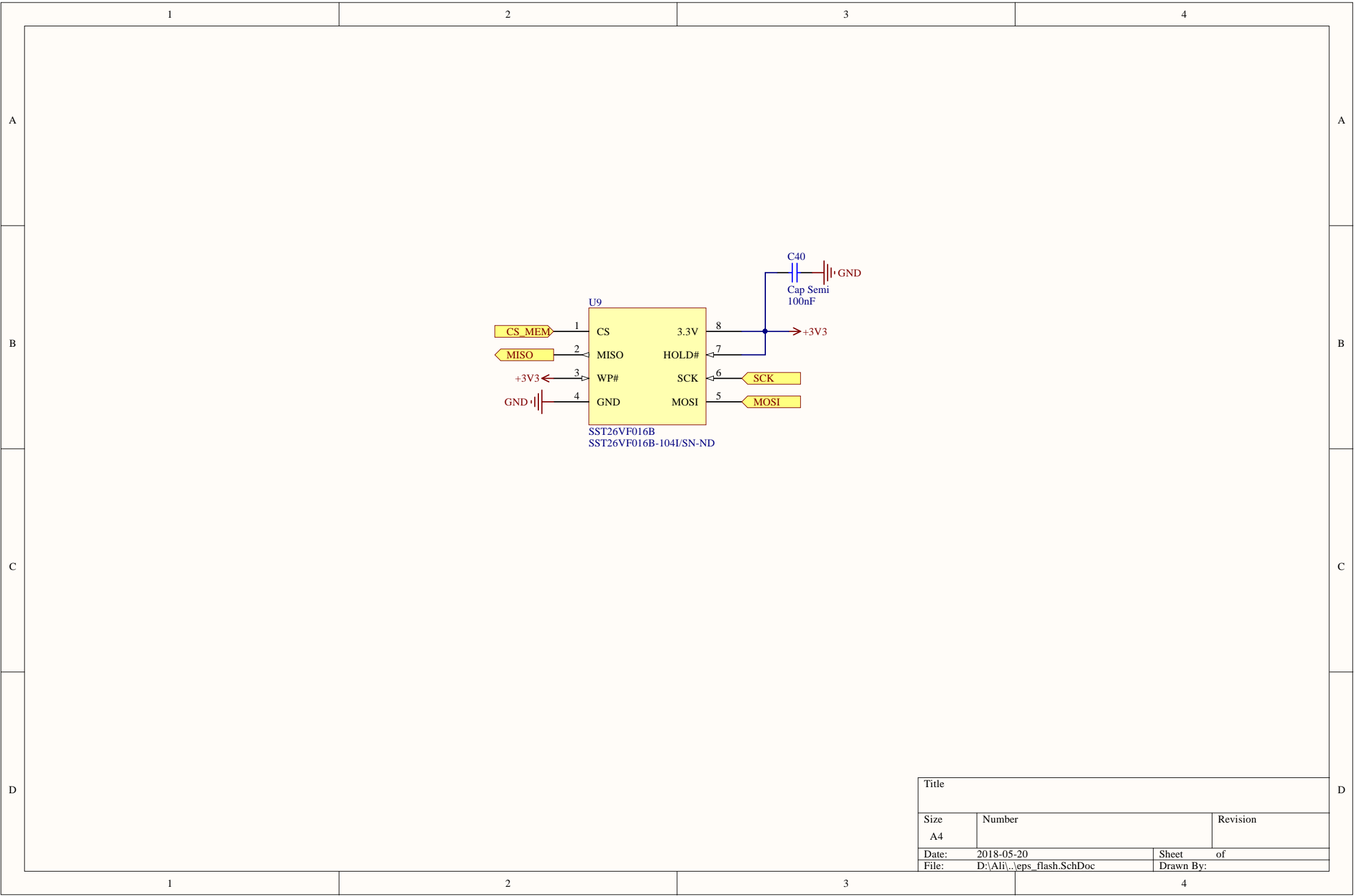
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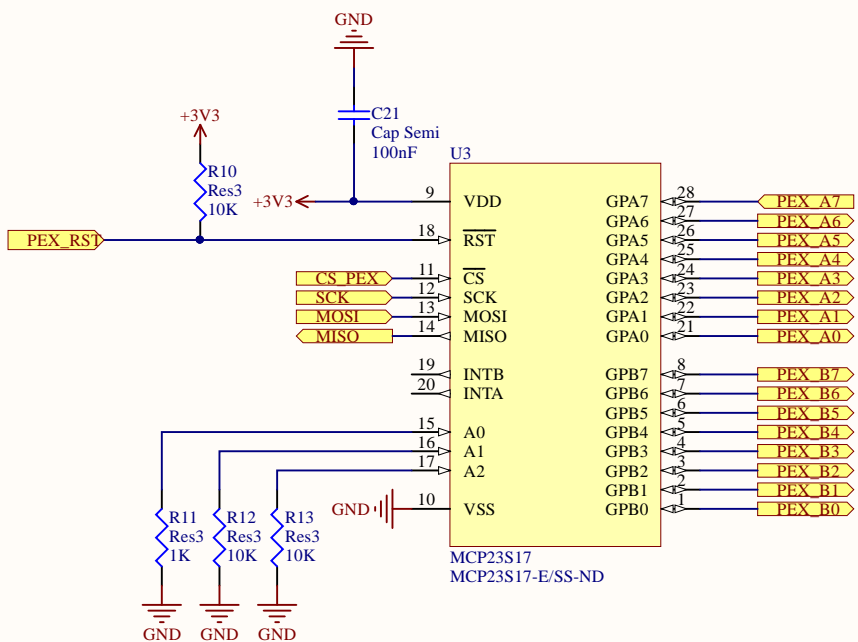


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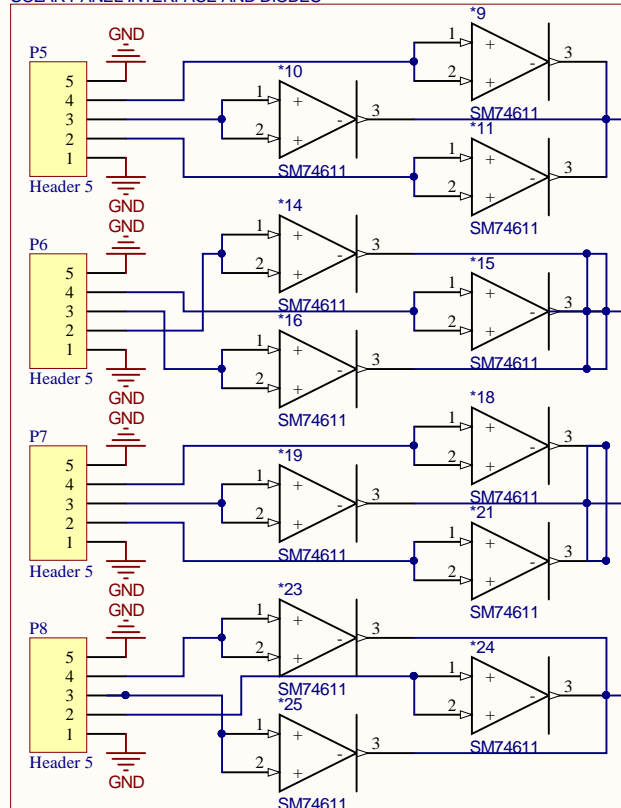
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Date:	2018-05-20	Sheet	of
File:	D:\Ali\...\eps_can_transceiver.SchDoc	Drawn By:	



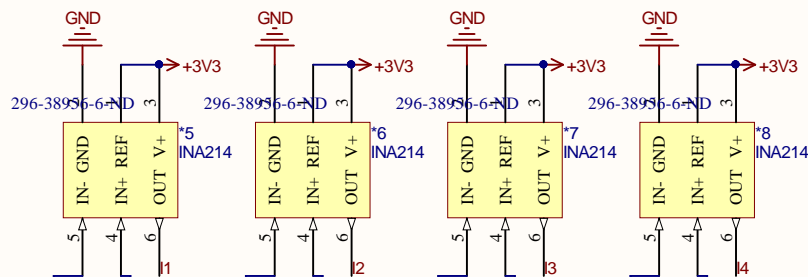
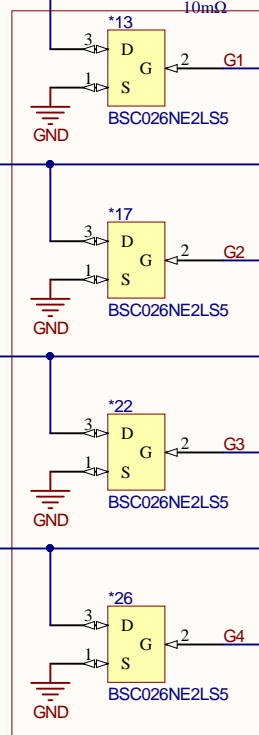


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Size A4	Number	Revision
Date:	2018-05-20	Sheet of
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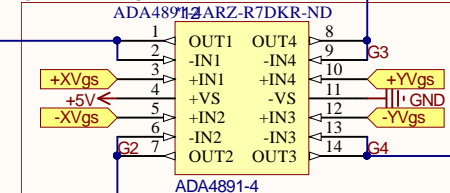
SOLAR PANEL INTERFACE AND DIODES



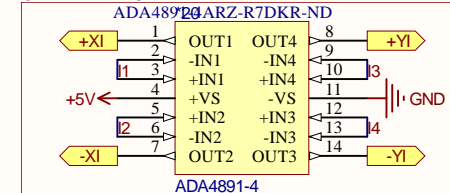
SHUNT SWITCHES



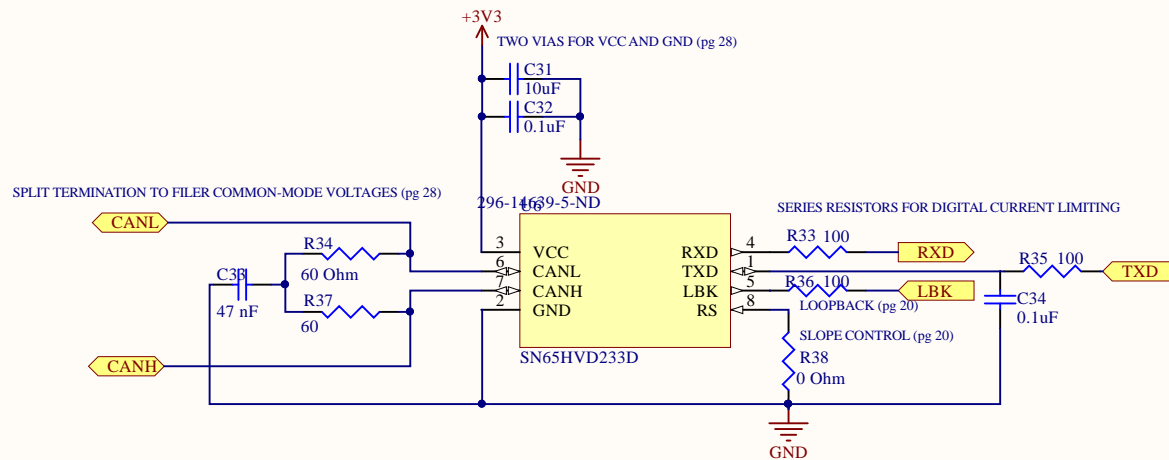
GATE DRIVE BUFFER



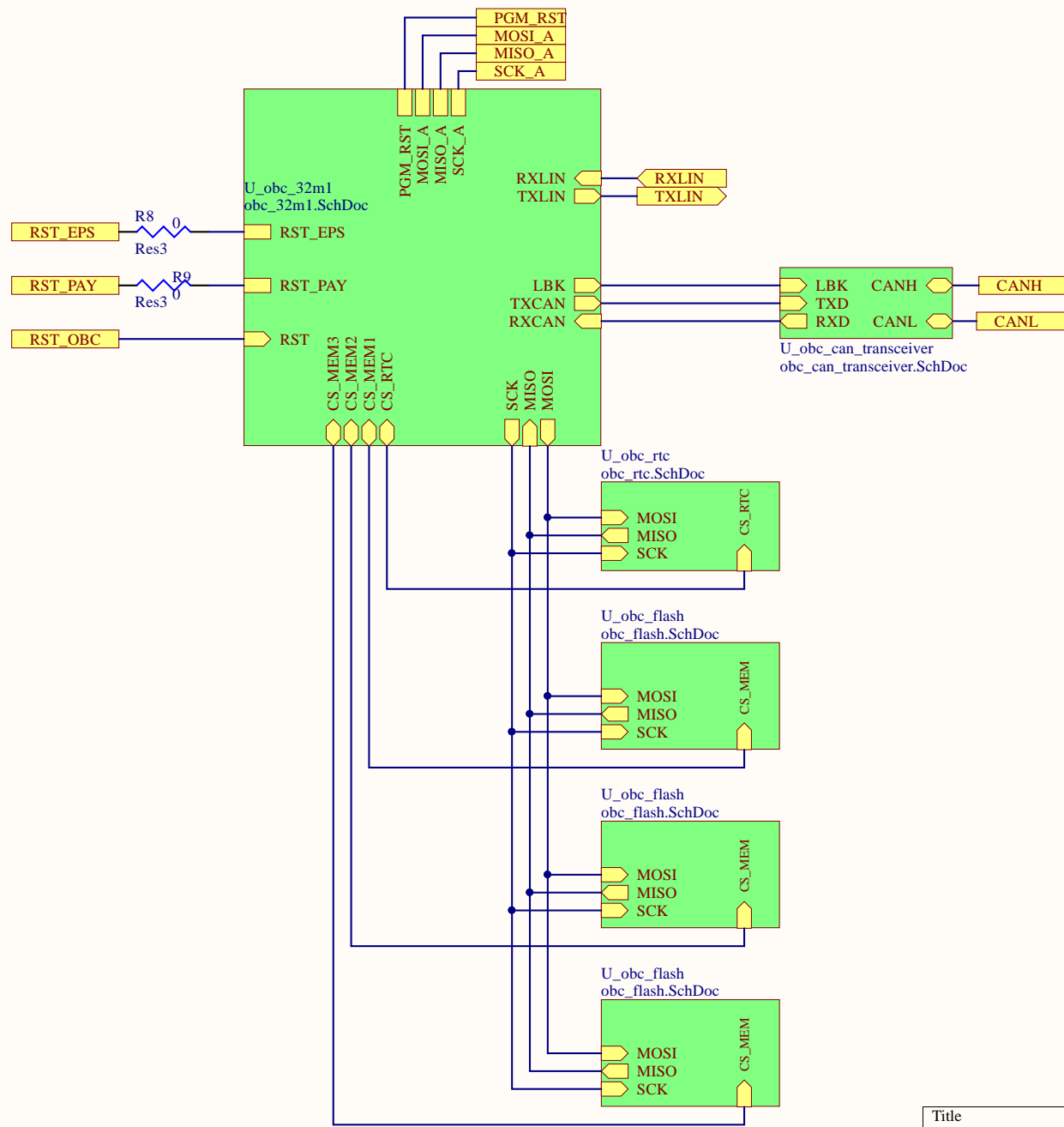
GATE DRIVE BUFFER



Title		
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A4		
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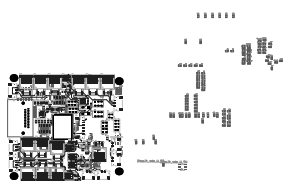


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Size	Number	Revision
A4		
Date:	2018-05-20	Sheet of
File:	D:\Ali\...\obc_can_transceiver.SchDoc	Drawn By:



▲ 120R Termination inside sheet

Title			
Size	Number		Revision
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Date:	2018-05-20		Sheet of
File:	D:\Ali\...\obc_main_3.0.SchDoc		Drawn By:



Board S