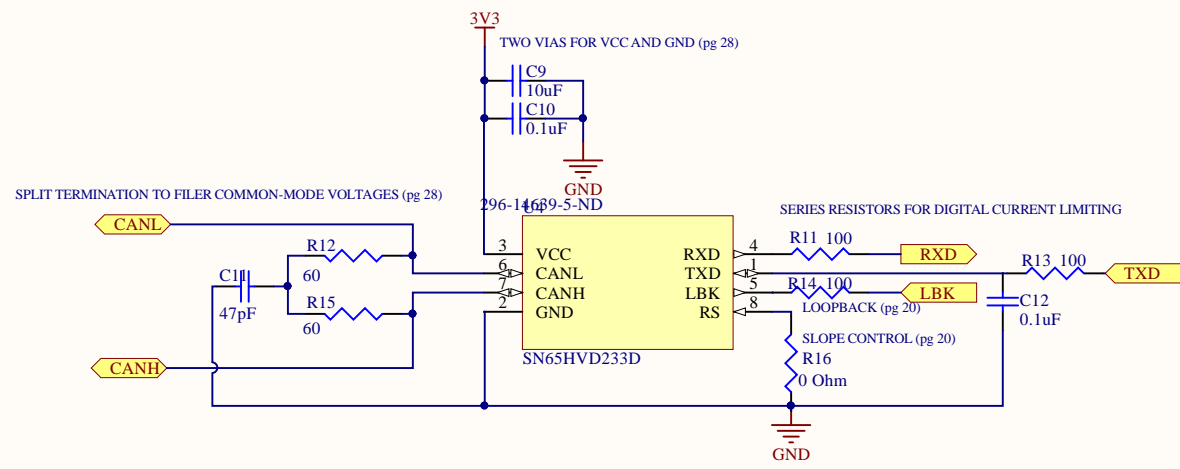
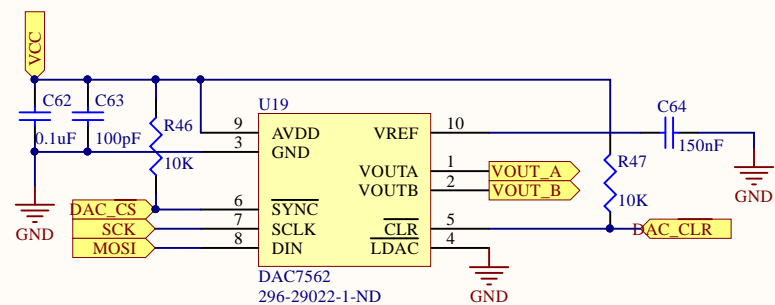


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Date:	2018-06-11	Sheet	of
File:	C:\Users\...\ATmega32M1.SchDoc	Drawn By:	

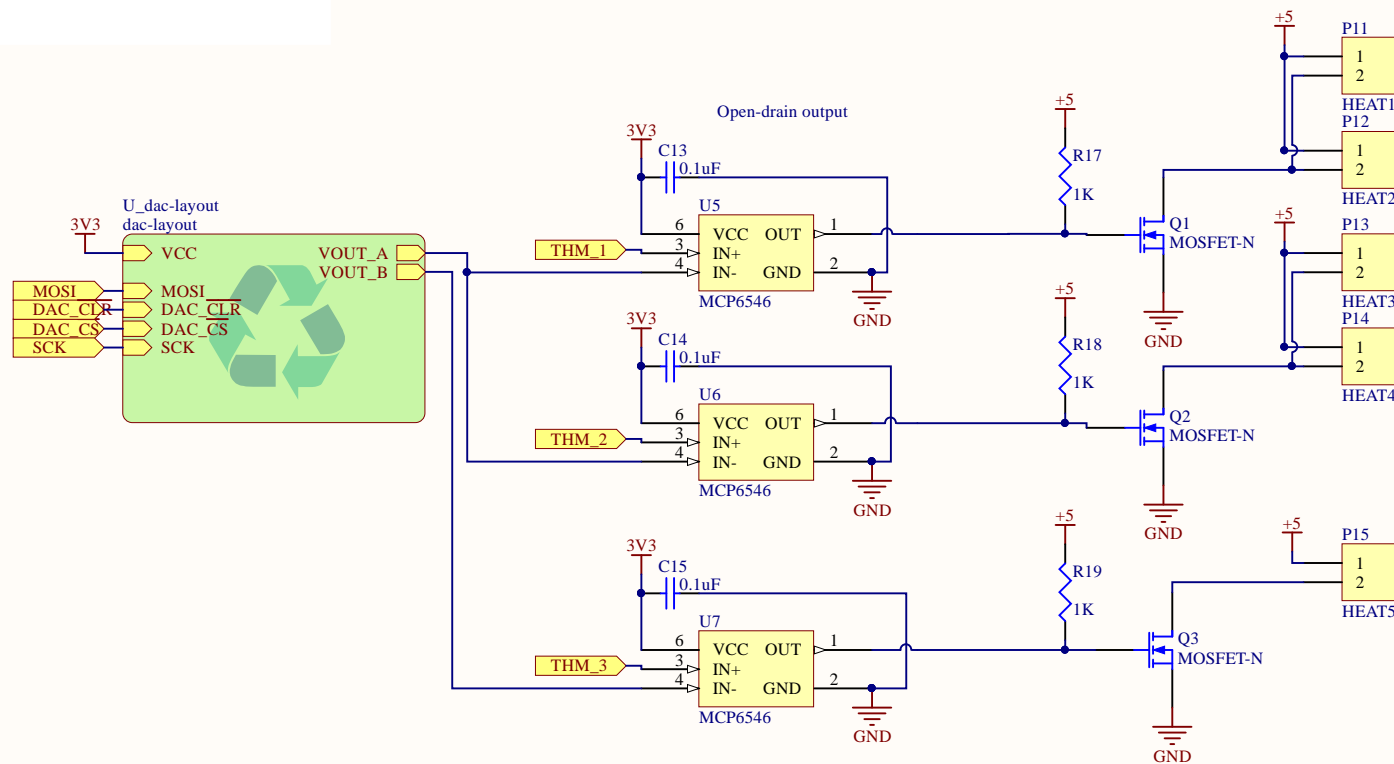


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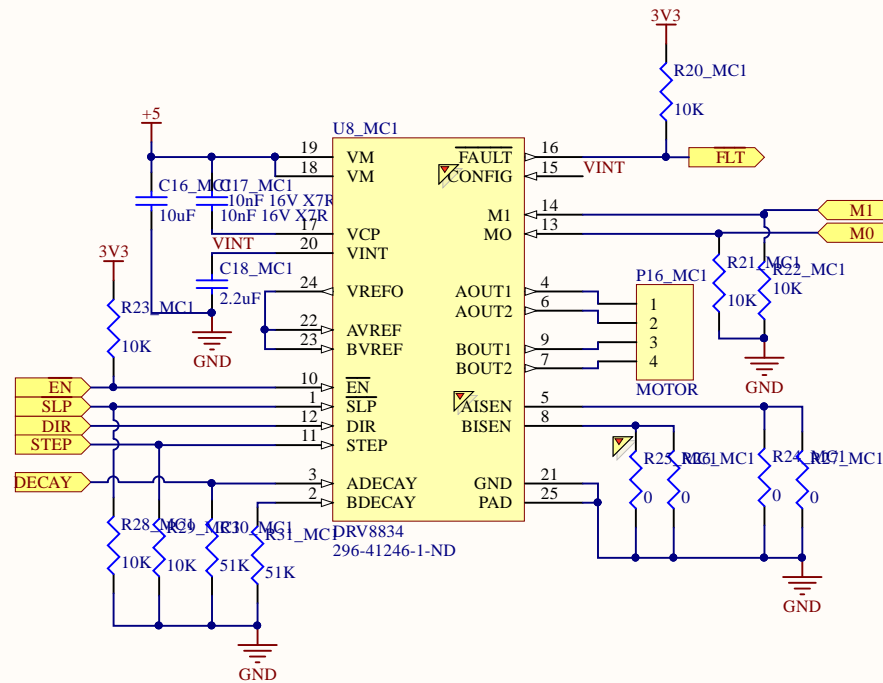


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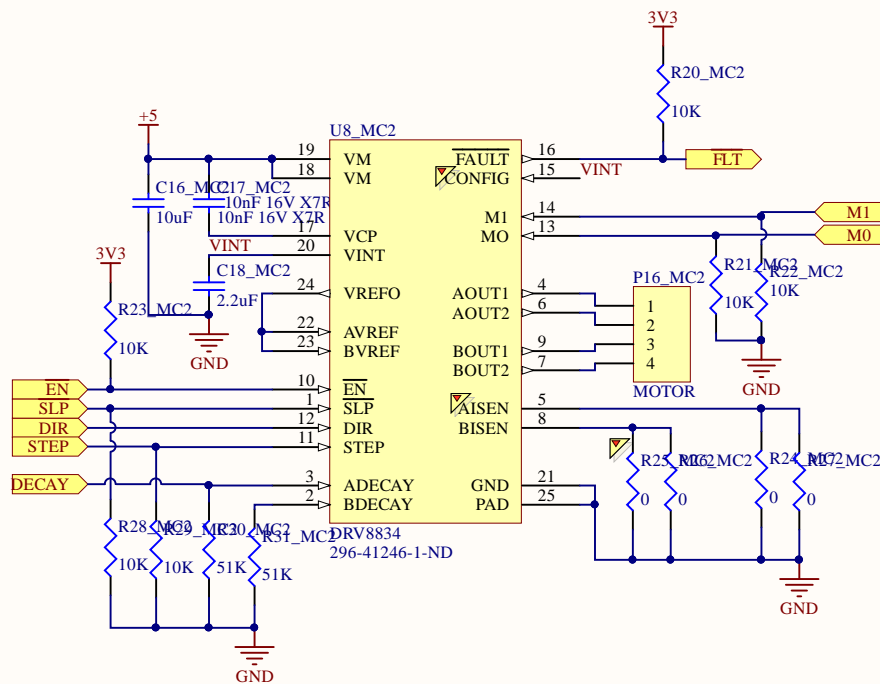
- Add DAC for voltage setting



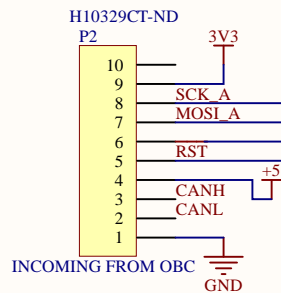
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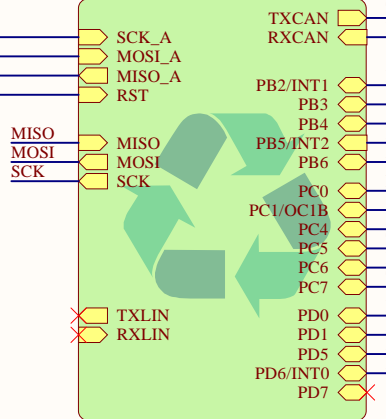
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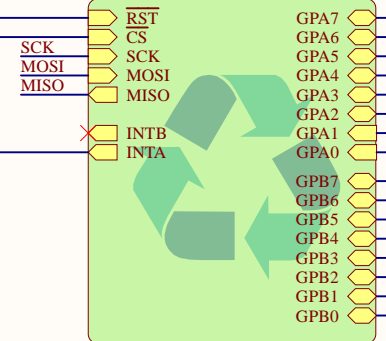
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Size A4	Number	Revision
Date:	2018-06-11	Sheet of
File:	C:\Users\...\motor_control.SchDoc	Drawn By:



U_ATmega32M1
ATmega32M1



U_portExpander
portExpander



U_power2V5
power2V5



U_can_transceiver
can_transceiver



U_sensors
sensors



U_heater_control
heater_control



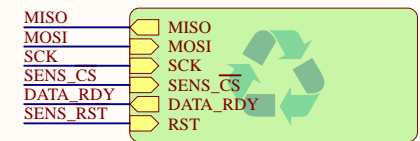
MC1
motor_control



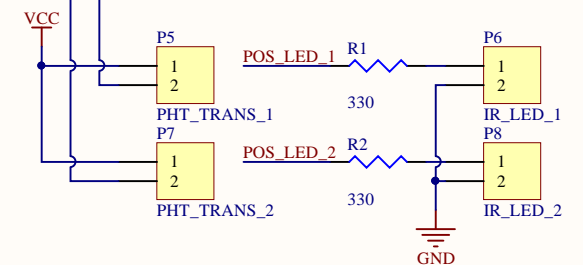
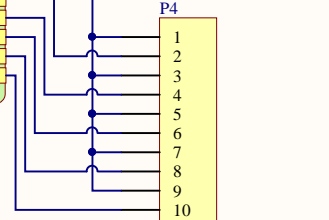
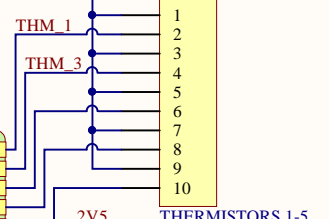
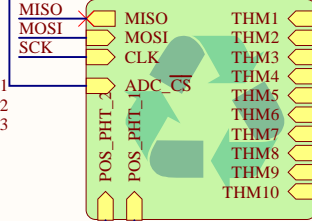
MC2
motor_control



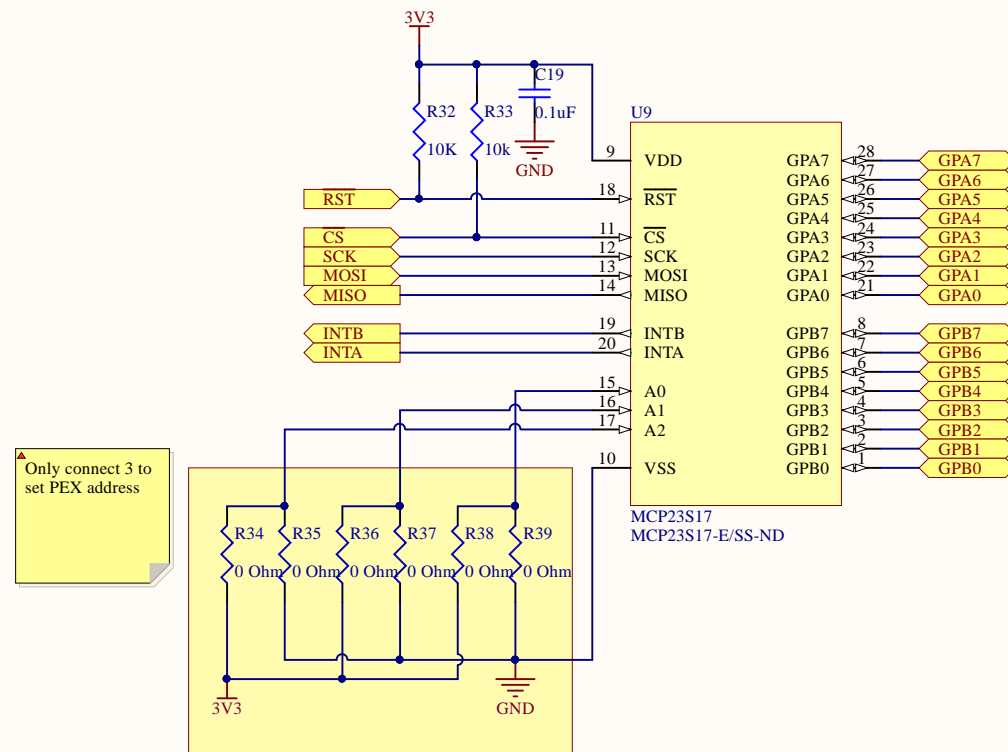
U_sensors-digital
sensors-digital



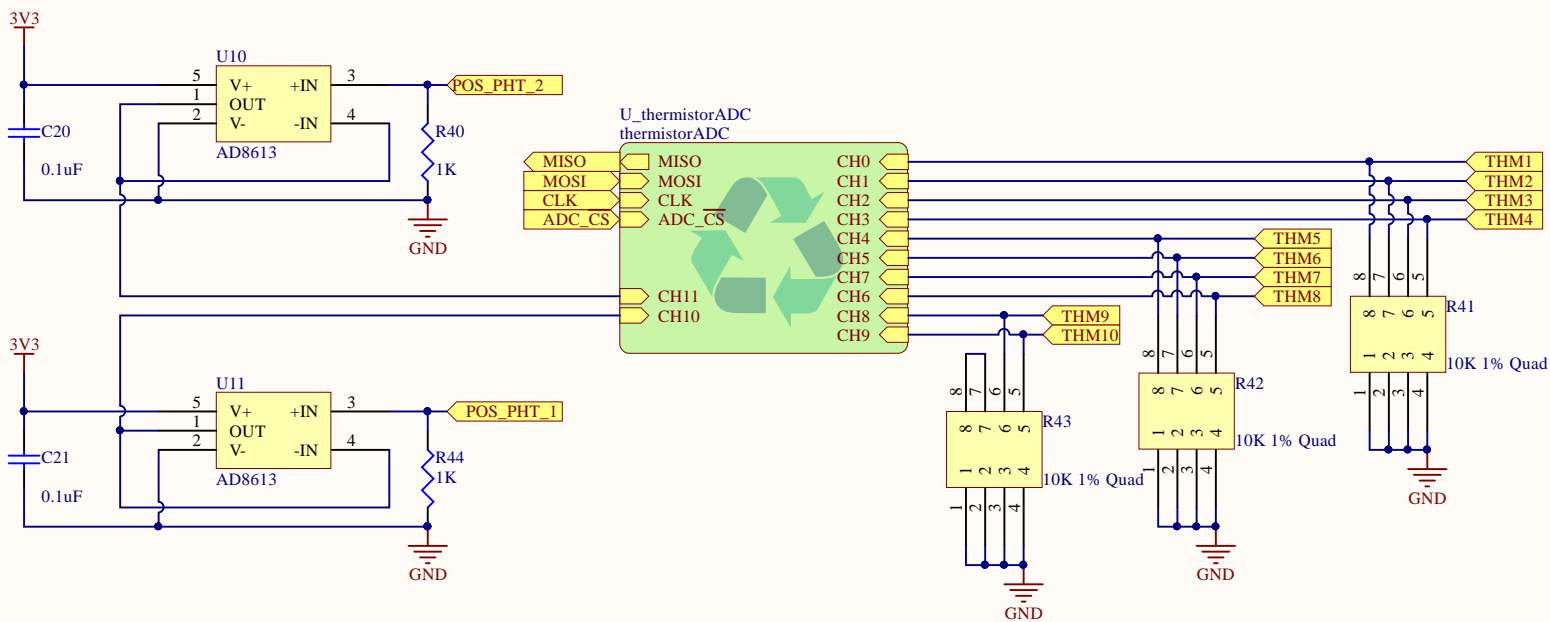
U_positioningAndThermistor
positioningAndThermistor



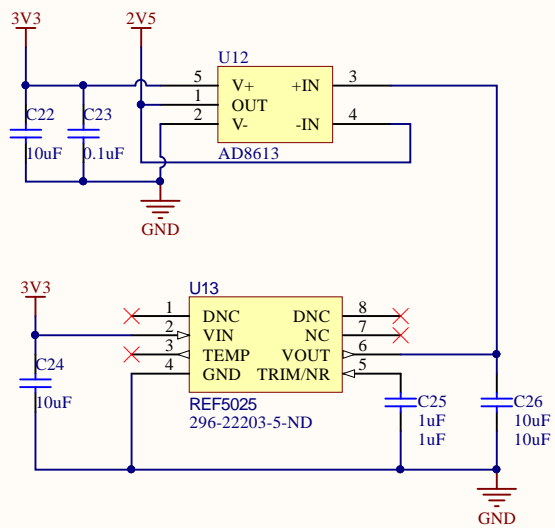
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A4		
Date:	2018-06-11	Sheet of
File:	C:\Users\...\pay-ssm.SchDoc	Drawn By:



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Date:	2018-06-11	Sheet of
File:	C:\Users\...\portExpander.SchDoc	Drawn By:

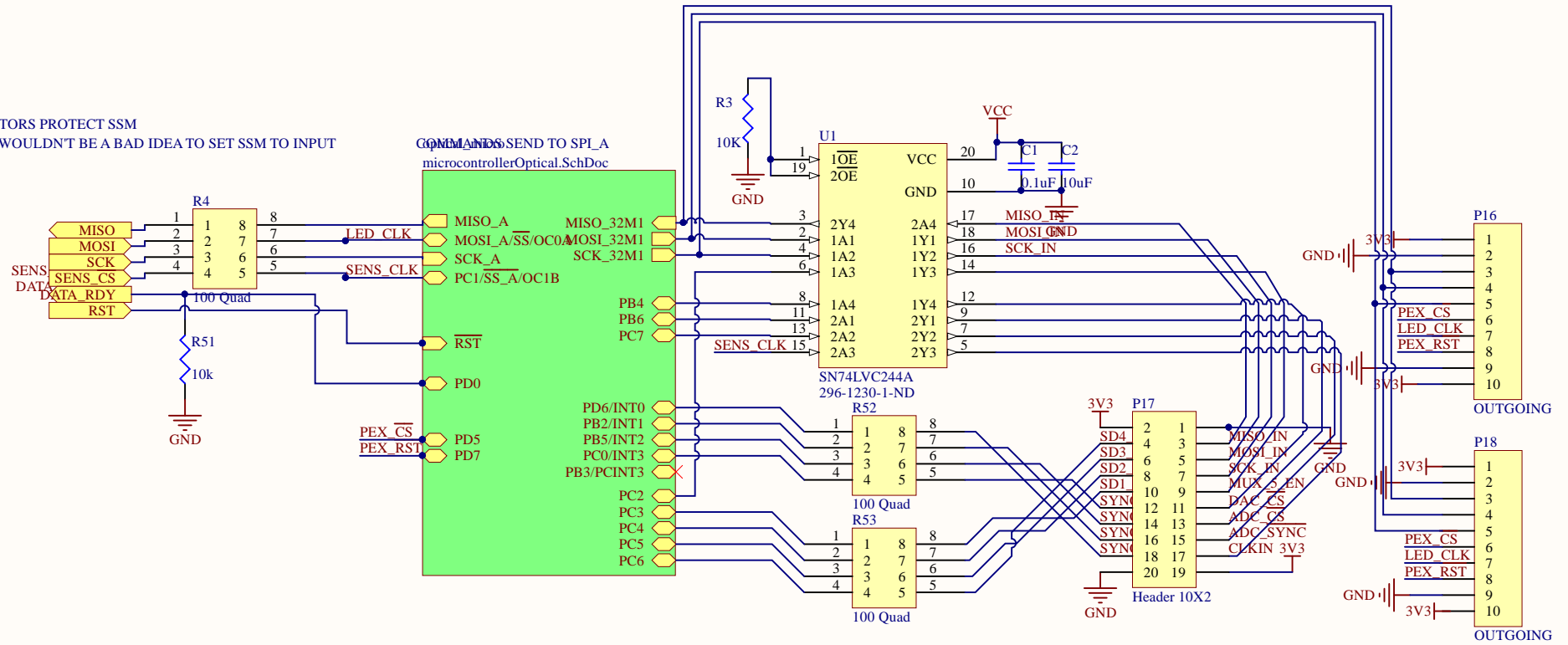


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Size A4	Number	Revision
Date:	2018-06-11	Sheet of
File:	C:\Users\...\positioningAndThermistor.SchDoc	



Title		
Size A4	Number	Revision
Date:	2018-06-11	Sheet of
File:	C:\Users\...\power2V5.SchDoc	Drawn By:

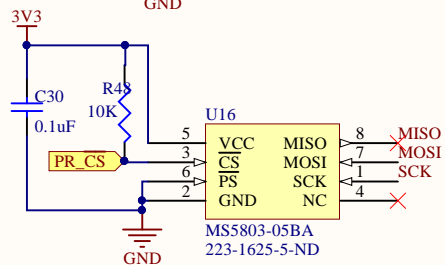
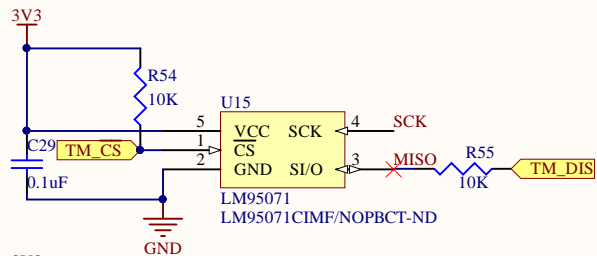
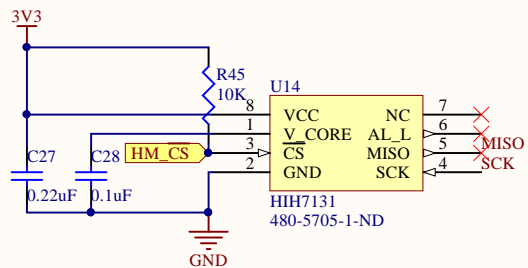
LIMIT RESISTORS PROTECT SSM
THAT SAID, WOULDN'T BE A BAD IDEA TO SET SSM TO INPUT



CHANGE THIS TO NET NAMES, ADD FOOTPRINT

Title		
Size	Number	Revision
A4		
Date:	2018-06-11	Sheet of
File:	C:\Users\...\sensors-digital.SchDoc	Drawn By:

MISO
MOSI
SCK



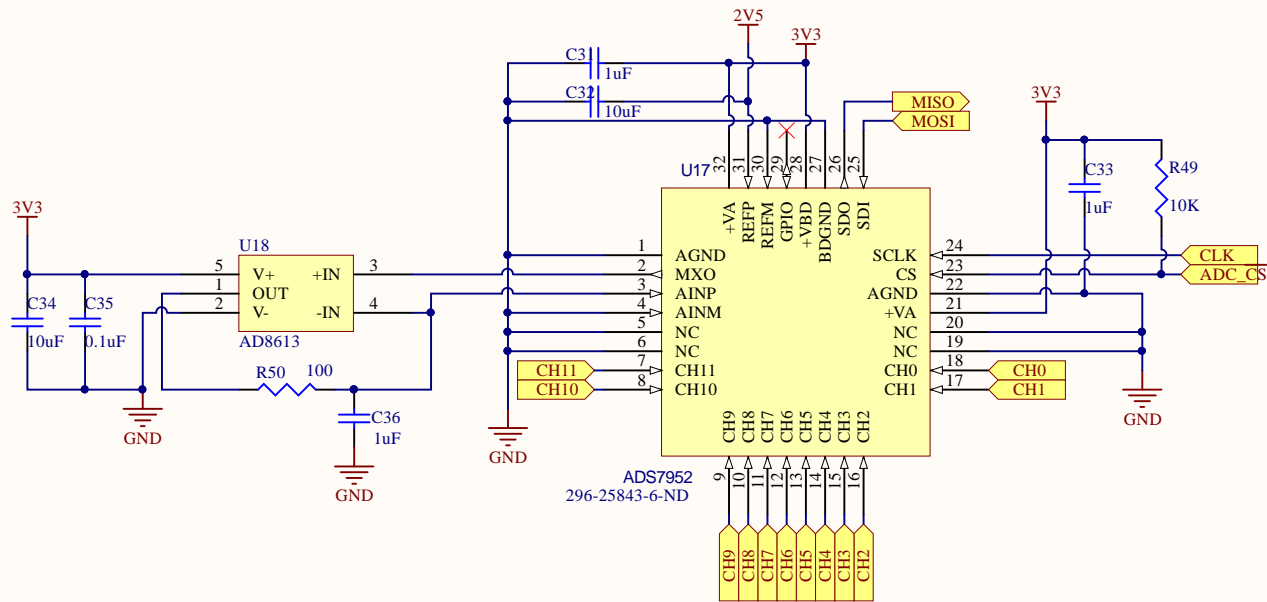
Title		
Size A4	Number	Revision
Date:	2018-06-11	Sheet of
File:	C:\Users\...\sensors.SchDoc	Drawn By:

▲ Cap as close as possible to REFP and AGND

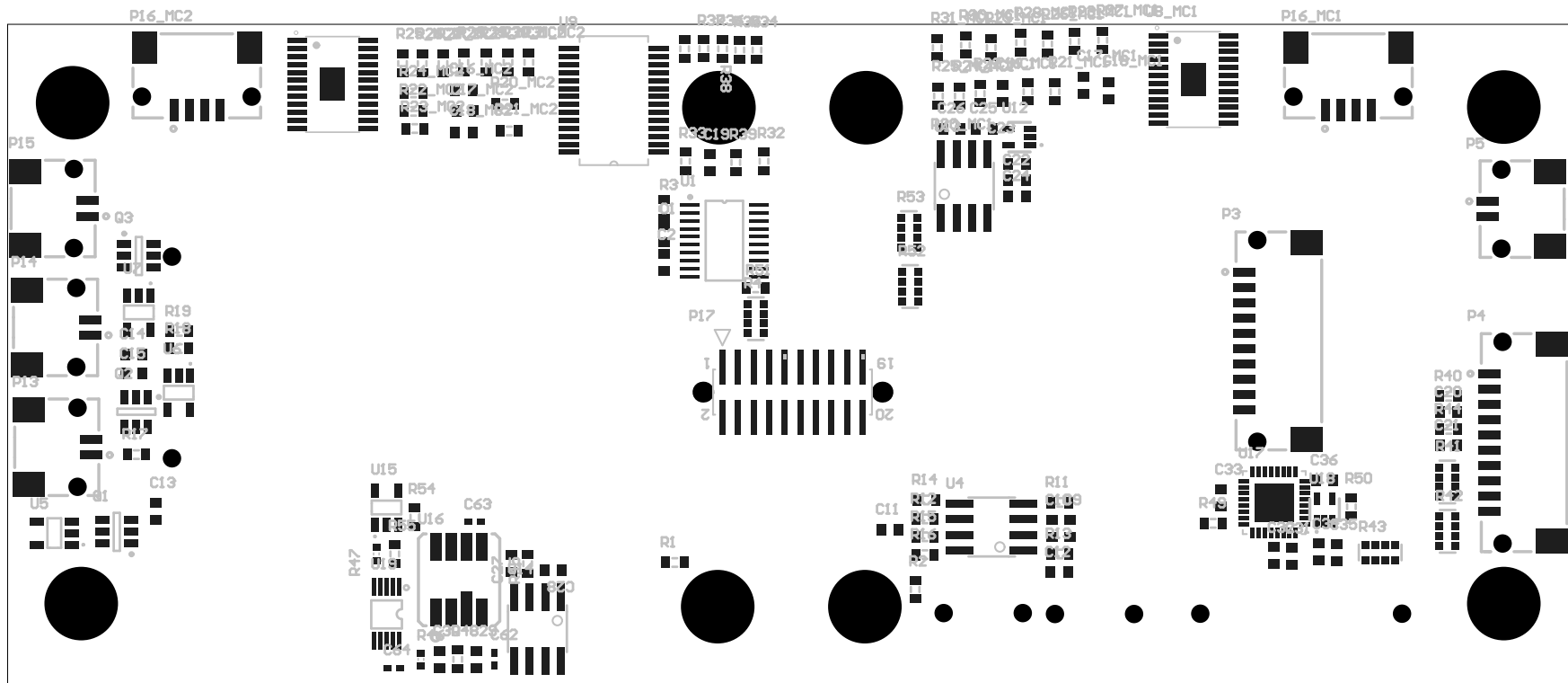
▲ See 10.1.1 of datasheet about connecting MXO and AINP. This limits the ADC to only work with input impedances of 50 ohms and less. If that is too restrictive, need to add a buffer here.

On second thought, 10.2.3. seems to suggest that at low enough sample rates high source impedances won't cause much issue.

▲ NR connected as in Figure 44 in 12.2



Title		
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Date:	2018-06-11	Sheet of
File:	C:\Users\...\thermistorADC.SchDoc	Drawn By:



A diagram showing a horizontal beam of light entering from the left, passing through a lens, and being focused onto a screen on the right. The beam is represented by a series of parallel lines that converge after passing through the lens.

D10 D11

