1. Logic:

- a. To check negative, we need to check the sign bit. Firstly, the EBX register is cleared, then the contents of the array is added to it. This makes sign bit 1 if it is negative number.
- b. We make jump if not signed and increment dx by 1 if it is signed (MSB is 1).
- c. We proceed to make the Most significant nibble 1. For that, first four bits are made 0, by consecutive SHL and SHR operations. Then it is XORed with 01000000 to make the first nibble as 1.
- d. Then, the result is moved to the location marked by SI, which is the newarray organized from 0400h.

2. Screenshots:

a. [DX Register after last operation: 0004]

b. [D:400]

```
-d 400
           02 89 67 45 88 67 07 78-3A 12 08 10 AB 78 56 34 ..gE.g.x:....xV4
0863:0400
0863:0410
           78 56 34 12 41
                          76 33 54-D4 C3 B1 12 32 65
                                                      34 28 xV4.Av3T....2e4(
0863:0420
           77 56 34 12
                          93 82
                                71-41 FF
                                          8B
                                            46 FE
                                                   23 CO 7F W4....gA..F.#..
                       A4
9863:0430
           03 E9 37
                          4E
                             FE
                                FF-4E
                                      08
                                         B8 B4 5E
                                                   50
                                                      FF
                                                         76
                                                             ..7..N..N...
           06 OE E8 BO FE
                                          ZA E4 96 59
                          8B 76 08-8A 04
                                                      59
                                                         F6
0863:0440
           84 B8 5E
                          D4
                             B8 B4-5E
                                      50 FF
                                             76 06 0E
                                                         94
                    20
                                                      E8
                       C5
                                                      A3 00
0863:0460
           FE 59 59 EB
                          B8 OD OO-50 FF
                                          76 06 0E
                                                   E8
                                                             .YY.....P.v..
0863:0470
           B8 0A 00 50 FF
                          76 06 0E-E8 98 00 8B 76 08 C6 04
                                                             ...P.v....v...
```

3. Code:

```
.model tiny
.386
.data
arr
      dd
45678902h,78076788h,09008123Ah,345678ABh,0D2345678h,54337641h,
0B2B1C3D4h,28346532h,092345677h,718293A4h
count db
             0ah
xorfac equ
             10000000h
.code
.startup
      lea
             di,arr
      lea
             si.newarr
      mov
             cl,count
             dx,dx
      sub
check: sub
             ebx,ebx
      add
             ebx,[di]
             moveto
      ins
```

```
dx
       inc
             ebx,4
      shl
      shr
             ebx,4
             ebx,xorfac
       xor
moveto:
             mov
                    [si],ebx
             si,4
      add
      add
             di,4
      dec
             cl
             check
      jnz
.exit
end
```