

1. Consider the following assembly code:

```

long loop(long x, int n)
x in %rdi, n in %esi
1  loop:
2      movl    %esi, %ecx
3      movl    $1, %edx
4      movl    $0, %eax
5      jmp     .L2
6  .L3:
7      movq    %rdi, %r8
8      andq    %rdx, %r8
9      orq     %r8, %rax
10     salq    %cl, %rdx
11  .L2:
12     testq   %rdx, %rdx
13     jne     .L3
14     rep; ret

```

The preceding code was generated by compiling C code that had the following overall form:

```

long loop(long x, long n) {
    long result = ____
    long mask;
    for (mask = ____; mask ____; mask ____) {

        result |= ____;
    }
    return result;
}

```

a. Which registers hold program values **x**, **n**, **result**, and **mask**?

```

%rax = result
%rdi = x
%esi = n
%rdx = mask

```

b. What are the initial values of **result** and **mask**?

```

result = 0
mask = 1

```

c. What is the test condition for **mask**?

If it is not equal/not zero

d. How does **mask** get updated?

After each iteration of the loop the value of mask shifts to the left by 1

e. How does **result** get updated?

After each iteration of the loop result gets updated using the | function with the result of x & mask

f. Fill in all the missing parts of the C code.

```

long loop(long x, long n) {
    long result = 0;
    long mask;
    for (mask = 1; mask <= n; mask <<= 1) {
        result |= (x & mask);
    }
    return result;
}

```

2. The code that follows shows an example of branching on an enumerated type value in a switch statement. Recall that enumerated types in C are simply a way to introduce a set of names having associated integer values. By default, the values assigned to the names count from zero upward. In our code, the actions associated with the different case labels have been omitted.

```

1  /* Enumerated type creates set of constants numbered 0 and upward */
2  typedef enum {MODE_A, MODE_B, MODE_C, MODE_D, MODE_E} mode_t;
3
4  long switch3(long *p1, long *p2, mode_t action)
5  {
6      long result = 0;
7      switch(action) {
8          case MODE_A:
9
10         case MODE_B:
11
12         case MODE_C:
13
14         case MODE_D:
15
16         case MODE_E:
17
18         default:
19
20     }
21     return result;
22 }

```

The part of the generated assembly code implementing the different actions is shown below. The annotations indicate the argument locations, the register values, and the case labels for the different jump destinations.

Fill in the missing parts of the C code. It contained one case that fell through to another—try to reconstruct this.

```

1  .L8:                                MODE_E
2  movl    $27, %eax
3  ret
4  .L3:                                MODE_A
5  movq    (%rsi), %rax
6  movq    (%rdi), %rdx
7  movq    %rdx, (%rsi)
8  ret
9  .L5:                                MODE_B
10 movq    (%rdi), %rax
11 addq    (%rsi), %rax
12 movq    %rax, (%rdi)
13 ret
14 .L6:                                MODE_C
15 movq    $59, (%rdi)
16 movq    (%rsi), %rax
17 ret
18 .L7:                                MODE_D
19 movq    (%rsi), %rax
20 movq    %rax, (%rdi)
21 movl    $27, %eax
22 ret
23 .L9:                                default
24 movl    $12, %eax
25 ret

```

```

long switch3(long *p1, long *p2, mode_t action){
    long result = 0;
    switch(action) {
        case MODE_A:
            result = *p2;
            action = *p1;
            *p2 = action;
            break;
        case MODE_B:
            result = *p1 + *p2;
            *p1 = result;
            break;
        case MODE_C:
            *p1 = 59;
            result = *p2;
            break;

```

```
        case MODE_D:
            result = *p2;
            *p1 = result;
            result = 27;
            break;
        case MODE_E:
            result = 27;
            break;
        default:
            result = 12;
            break;
    }
    return result;
}
```