

Ohm's Law

$$v_{AB} = iR$$

$$v_A = v_B + iR$$

$$i = \frac{v_{AB}}{R}$$

$$i = \frac{v_A - v_B}{R}$$

Conductance

$$v = \frac{i}{G}$$

$$i = vG$$

KCL

$$\sum \text{currents in} = \sum \text{currents out}$$

$$i_1 = i_2 + i_3$$

Parallel Resistors

$$R_{eq} = \left(\frac{1}{R_1} + \frac{1}{R_2} \right)^{-1} = \frac{R_1 R_2}{R_1 + R_2}$$

Parallel Conductance

$$G_{eq} = G_1 + G_2$$

Series Resistors

$$R_{eq} = R_1 + R_2$$

Voltage Divider

$$v_{AB} = v_{AC} \frac{R_1}{R_1 + R_2}$$

$$v_{BC} = v_{AC} \frac{R_2}{R_1 + R_2}$$

Current Divider

$$i_1 = i_s \frac{G_1}{G_1 + G_2}$$

$$i_2 = i_s \frac{G_2}{G_1 + G_2}$$

$\Delta - Y$ Transform

Denominator:

$$D = R_{AB} + R_{AC} + R_{BC}$$

$$R_{AY} = \frac{R_{AB} R_{AC}}{D}$$

$$R_{BY} = \frac{R_{AB} R_{BC}}{D}$$

$$R_{CY} = \frac{R_{AC} R_{BC}}{D}$$

$Y - \Delta$ Transform

Numerator:

$$N = R_{AY} R_{BY} + R_{AY} R_{CY} + R_{BY} R_{CY}$$

$$R_{AB} = \frac{N}{R_{CY}}$$

$$R_{BC} = \frac{N}{R_{AY}}$$

$$R_{AC} = \frac{N}{R_{BY}}$$

KVL

$$\sum \text{rises} = \sum \text{drops}$$

$$v_1 + v_2 = v_3 + v_4$$

Node-Voltage

Equation at node N:

$$\frac{v_A - v_N}{R_a} = \frac{v_N - v_B}{R_b} + \frac{v_N - v_C}{R_c}$$

Mesh Currents

KVL equation for mesh a:

$$R_1(i_a) + R_2(i_a) + R_3(i_a - i_b) + R_4(i_a) = 0$$

i_a overlaps i_b

KVL equation for mesh b:

$$R_5(i_b) + R_6(i_b) + R_3(i_b - i_a) + R_7(i_b) = 0$$

i_a overlaps i_b

Source Transformation

Super-Mesh

Use with mesh-current method

KVL wraps around two meshes to avoid current source.

KVL equation: $R_1 i_a + R_2 i_b + R_3 i_b + R_4 i_b + R_5 i_a + R_6 i_a = 0$

Mesh Constraint: $i_b - i_a = I_S$

Max Power Transfer

Load power is maximum when $R_L = R_{Th}$

Thevenin Equivalent Circuit

Thevenin Voltage

Short-Circuit Current Method

$$R_{Th} = \frac{V_{Th}}{I_{Sc}}$$

Test Voltage Source Method

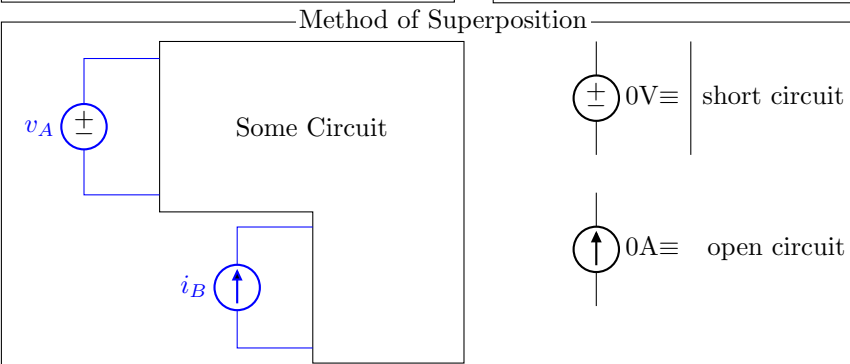
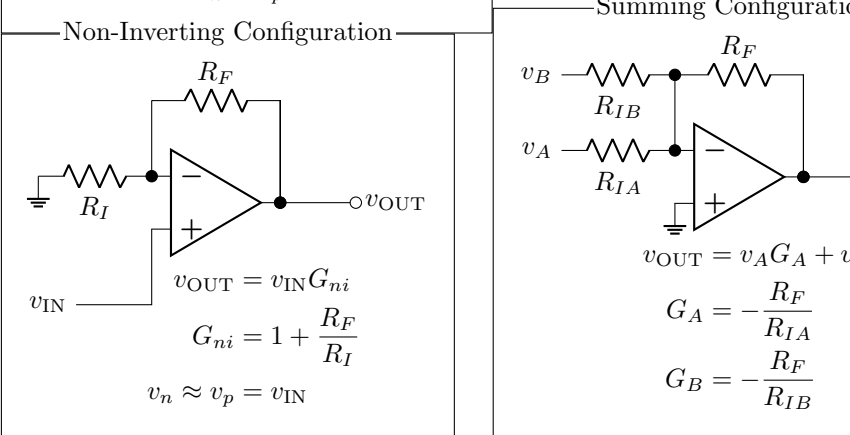
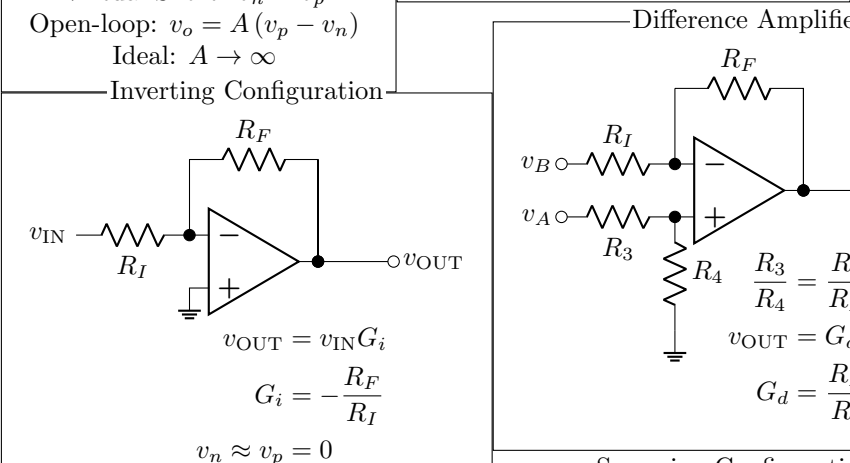
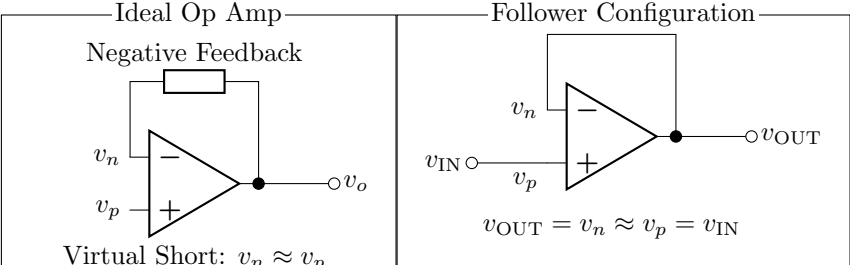
Set internal independent sources to zero.

$$R_{Th} = \frac{V_x}{I_x}$$

Thevenin Equivalent Circuit

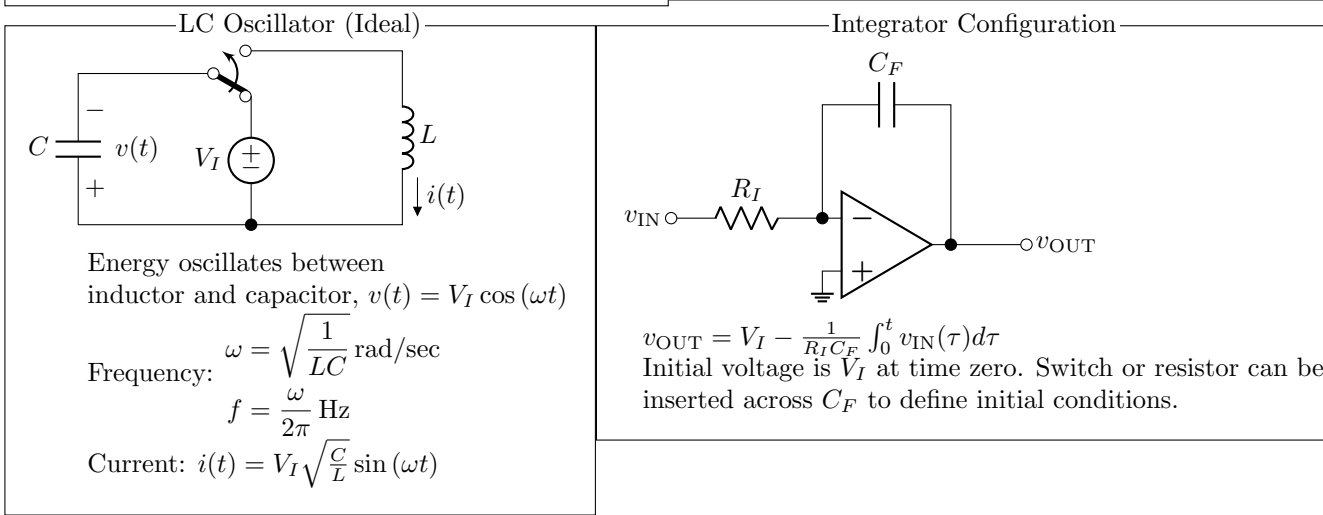
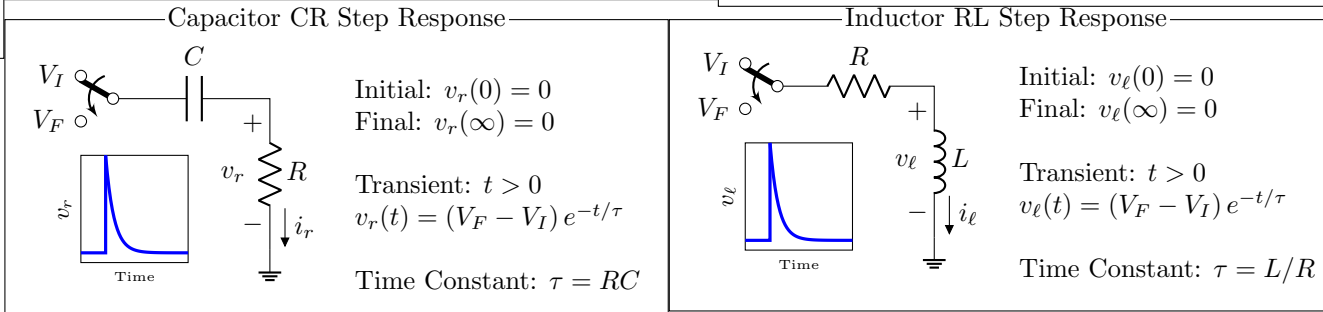
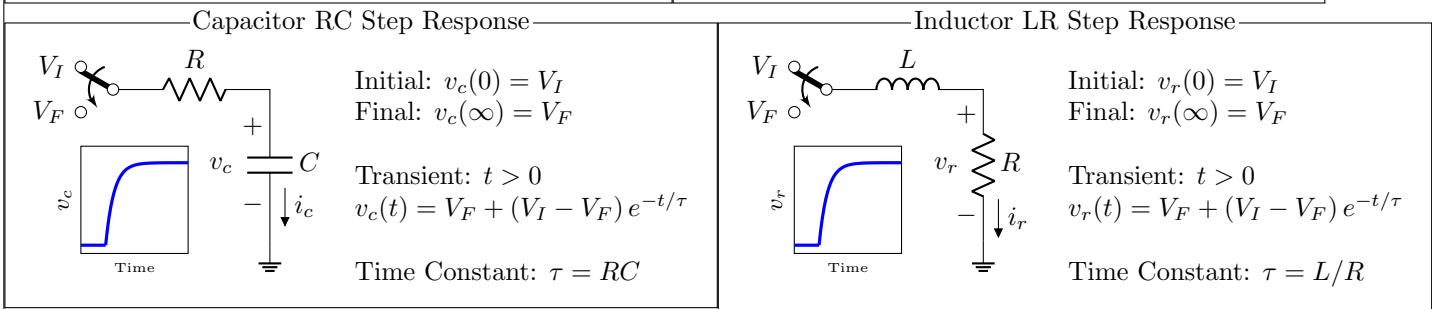
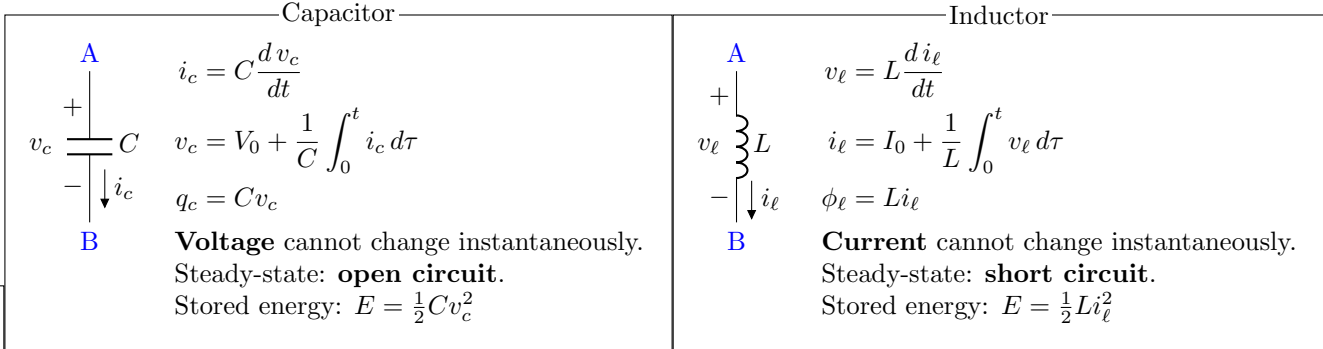
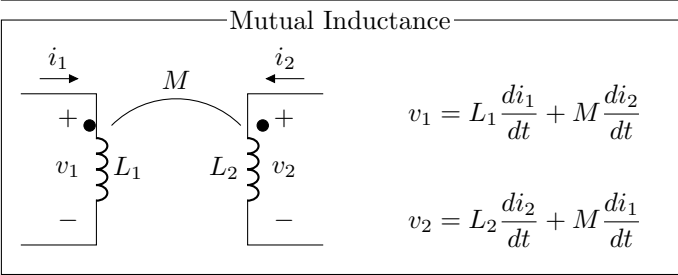
Signal Attenuation

Voltage across load is $V_L = V_{Th} \left(\frac{R_L}{R_L + R_{Th}} \right)$



Circuit With Multiple **Independent** Sources:
Solve **partial solutions** for one source at a time, set all other independent sources to zero.
Combined Solution is the sum of partial solutions for every node and branch.

Example:
First: Set $i_B = 0$, solve voltages v'_k and currents i'_j in the circuit.
Next: Set $v_A = 0$, solve voltages v''_k and currents i''_j in the circuit.
Combine: sum $v_k = v'_k + v''_k$ and $i_j = i'_j + i''_j$.



SI Prefixes		Basic Units		
SI Prefix	Scale	Thing	Unit	Equivalent Units
a “atto”	10^{-18}	Charge	Q or C “Coulomb”	
f “femto”	10^{-15}	Energy	J “Joule”	
p “pico”	10^{-12}	Power	W “Watt”	J/s
n “nano”	10^{-9}	Voltage	V “Volt”	J/Q
u or μ “micro”	10^{-6}	Current	A “Amp(ere)”	Q/s
m “milli”	10^{-3}	Flux Linkage	Wb “Weber”	V·s
c “centi”	10^{-2}	Frequency	Hz (cycles per second)	radians/sec = $2\pi \times$ Hz
d “deci”	10^{-1}	Resistance	Ω “Ohm”	V/A
da “deka”	10^1	Conductance	\mathcal{U} “Mho” or S “Siemens”	A/V
h “hecto”	10^2	Capacitance	F “Farad”	Q/V
k “kilo”	10^3	Inductance	H “Henry”	Wb/A
M “mega”	10^6			
G “giga”	10^9			
T “terra”	10^{12}			