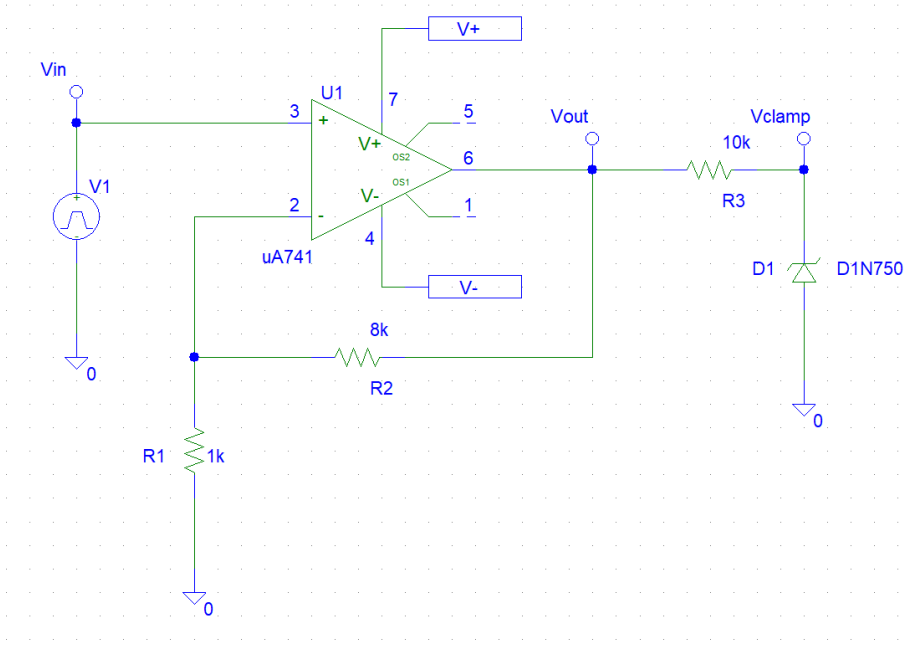
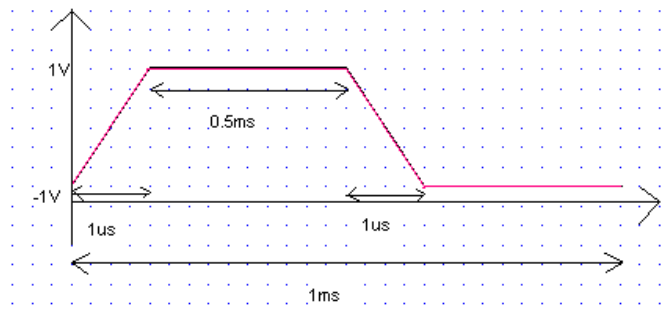
 POLITEKNIK MALAYSIA POLITEKNIK SULTAN ABDUL HALIM MU'ADZAM SHAH		ELECTRICAL ENGINEERING DEPARTMENT		
		SESSION	: 2 2023/2024	
		COURSE	: DEE30071 ELECTRONIC COMPUTER AIDED DESIGN	
CONTINUOUS ASSESSMENT		: END OF CHAPTER 2	Total Marks : 100	
TOPIC	TOPIC 2 : SIMULATION OF ANALOGUE CIRCUITS			
CLO	CLO1/DK3/DK4/DK6/DP1/DP3/DP5 : Apply the simulation results for the various types of simulation analysis based on the electronic circuit theory and operations. CLO2/DK3/DK4/DK6/DP1/DP3/DP5 : Construct the simulation and the PCB layout for digital and analogue circuits using a schematic capture software.			
NAME		REG. NUMBER		

CLO1 (C3, PLO1)	COGNITIVE ASSESSMENT (20 %)	
CLO2 (P4, PLO5)	PSYCHOMOTOR ASSESMENT (80 %)	
TOTAL MARKS (100%)		

*Refer to Rubric

1	<p>LEARNING OUTCOMES (LO):</p> <ol style="list-style-type: none"> 1. Apply the simulation results for the various types of simulation analysis based on the electronic circuit theory and operations (C3, PLO1)
2	<p>OBJECTIVE : Apply transient analysis to simulate analogue circuits</p>
3	<p>THEORY :</p>
4	<p>EQUIPMENT / TOOL / SOFTWARE :</p> <ol style="list-style-type: none"> 1. PC workstation 2. Related software
5	<p>PROCEDURE:</p> <p>Question 1</p> <p>Students need to design this circuit given below (Figure 1) using transient analysis, then simulate the circuit. Answer the questions in Result Section.</p>  <p style="text-align: center;">Figure 1</p> <p>(a) Set the attributes of the pulse source, V1, to produce the signal below.</p>



- i) From the Bar menu, select Analysis then setup. Check on the enable box for transient.
- ii) Click on the Transient Button. Set the specification as shown below
Print Step = 1ms
Final time = 15ms
- iii) Click the icon OK and simulate the circuit using the icon or menu bar Analysis / Run.
- iv) From PROBE, Trace the output for V_{in} , V_{out} and V_{clamp} .

Question 2

1. Draw the RF amplifier circuit as shown below (using AC Analysis).

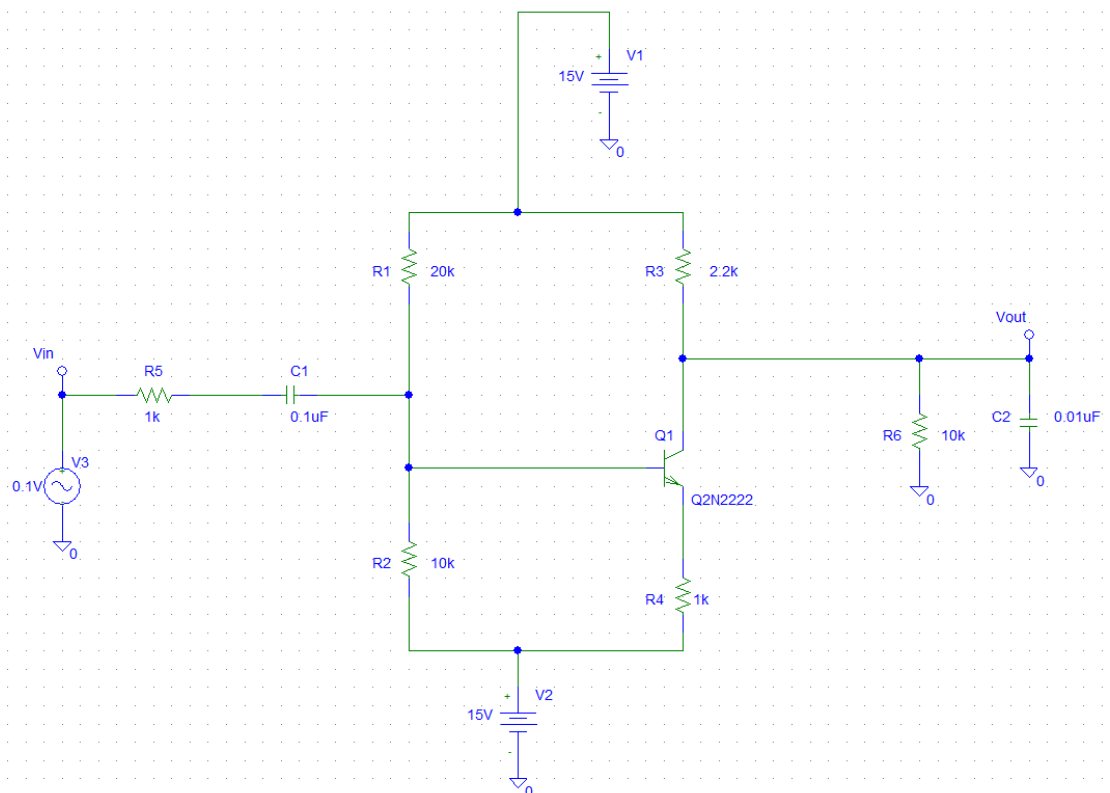


Figure 2

- a) Simulate the circuit from 100Hz to 1MHz.
- b) Produce the Bode Plot for the filter output. From the Bode plot, you are to determine (in dB) :
- c) Maximum gain, A (max) in dB.

i. =

d) cut-off frequency.

i. $f_{c1} = \dots\dots\dots$ $f_{c2} = \dots\dots\dots$

e) Bandwidth, BW

i. =

f) Print out the Bode plot.

g) From the curves obtained, determine the values of V_{in} and V_{out} at the input frequency 100Hz.

h) Calculate the voltage gain for the circuit by using the formula.

$$A_v = V_{out}/V_{in}$$

i) Compare the value that you get at (h) with the output from Bode plot.

6

Questions and Answers :

1. (a) By using the voltage gain formula (for figure 2), get the value for V_{out}

$$A_v = V_{out} / V_{in}$$

(b) Record the value for V_{in} , V_{out} and voltage gain in the table below :

Circuit	Voltage Value
Small Signal Amplifier	$V_{in} =$ $V_{out} =$ $A_v =$
RF amplifier	$V_{in} =$ $V_{out} =$ $A_v =$

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PRACTICAL END OF CHAPTER 2

Course Learning Outcomes(CLO)/ Learning Domain Cluster (CLS)	Circuit	Skills / Aspects	Very Poor	Satisfactory	Very Good	Marks
			1	2	3	
<p>CLO 1 : Apply the simulation results for the various types of simulation analysis based on the electronic circuit theory and operations.</p> <p>CLO 2: Construct the simulation and the PCB layout for digital and analogue circuits using a schematic capture software.</p> <p>CLS 1: Knowledge & Understanding</p> <p>CLS3a : Practical skill</p>	1	Construct circuit: Student able to construct the circuit same as given.	Able to construct the circuit with assistance.	Good to construct the circuit moderately with little assistance.	Excellent to construct the circuit effectively.	/3
		Circuit Simulation: Student able to simulate the circuit and observe the result correctly.	Able to simulate the circuit and observe the result correctly with assistance.	Good to simulate the circuit and observe the result correctly with little assistance.	Excellent to simulate the circuit and observe the result effectively.	/3
		Results : Student able to obtain the waveform and label the desired values in the circuit.	Able to obtain the waveform and label the desired values correctly with assistance	Good to obtain the waveform and label the desired values correctly with minimum assistance	Excellent to obtain waveform and label the desired values correctly and effectively.	/3
						/9
	2	Construct circuit: Student able to construct the circuit same as given.	Able to construct the circuit with assistance.	Good to construct the circuit moderately with little assistance.	Excellent to construct the circuit effectively.	/3
		Circuit Simulation: Student able to simulate the circuit and observe the result correctly.	Able to simulate the circuit and observe the result correctly with assistance.	Good to simulate the circuit and observe the result correctly with little assistance.	Excellent to simulate the circuit and observe the result effectively.	/3
		Results : Student able to obtain the waveform and label the desired values in the circuit.	Able to obtain the waveform and label the desired values correctly with assistance	Good to obtain the waveform and label the desired values correctly with minimum assistance	Excellent to obtain waveform and label the desired values correctly and effectively.	/3
						/9

1. PRACTICAL SKILLS PSYCHOMOTOR ASSESMENT - (80%)

NO.	STUDENT'S NAME	FIGURE 1	FIGURE 2	Total:	80%
				(18 marks)	
1		/9	/9	/18	/80

2. PRACTICAL WORK COGNITIVE ASSESSMENT - (20%)

MEASURE	QUESTION 1		TOTAL	20%
(c, d and e)	a)	b)		
/5	/5	/5	/15	/20

3. TOTAL MARKS

NO.	STUDENT'S NAME	COGNITIVE ASSESSMENT (20 %)	PSYCHOMOTOR ASSESSMENT (80 %)	Total:
				(100 %)
A		/20	/80	/100