

## PUBLICATIONS

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### CONFERENCE AND WORKSHOP PAPERS

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- [1] M. Aigner et al. “Fast, Multicore-Scalable, Low-Fragmentation Memory Allocation through Large Virtual Memory and Global Data Structures”. In: *Proc. ACM SIGPLAN Conference on Object-Oriented Programming, Systems, Languages, and Applications (OOPSLA)*. ACM, 2015. Click here for PDF file.
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- [1] A. Haas et al. “Scal: A Benchmarking Suite for Concurrent Data Structures”. In: *Proc. International Conference on Networked Systems (NETYS)*. LNCS. Springer, 2015. Click here for PDF file.
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## PROCEEDINGS AND SPECIAL ISSUES

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- [1] M. Dodds, A. Haas, and C.M. Kirsch. *Fairness vs. Linearizability in a Concurrent FIFO Queue*. Short Talk at the Joint Euro-TM/MEDIAN Workshop on Dependable Multicore and Transactional Memory Systems (DMTM). 2014.
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14. PURR: Parallel Unit Resulting Resolution, a concurrent first-order theorem prover with advanced indexing operations, see Master's Thesis.
15. ACID: A Collection of Indexing Data Structures, implemented in C and Prolog, with Peter Graf.

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## HARDWARE

1. The JAviator: A Quadrotor Helicopter and Software Laboratory for Time-Portable Java Programming, with Rainer Trummer. Web: <http://javiator.cs.uni-salzburg.at>