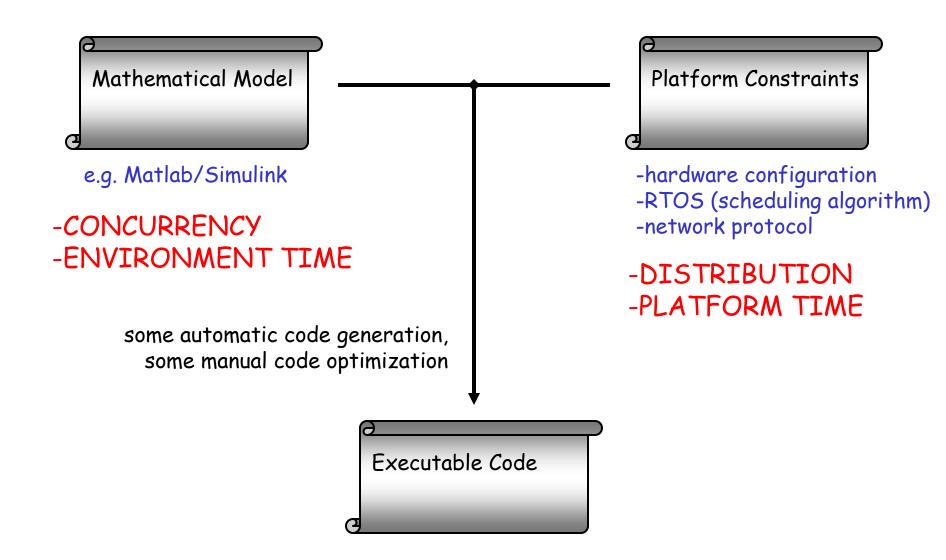
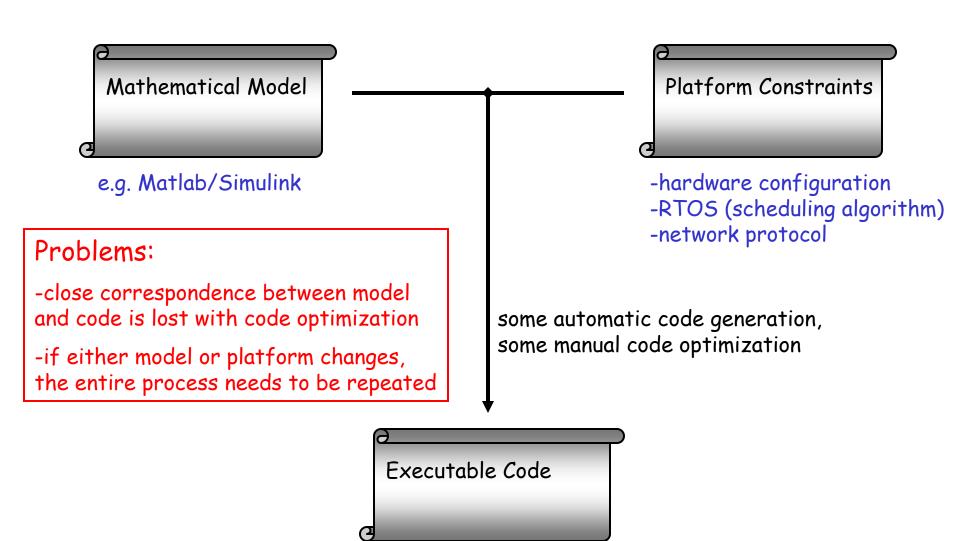
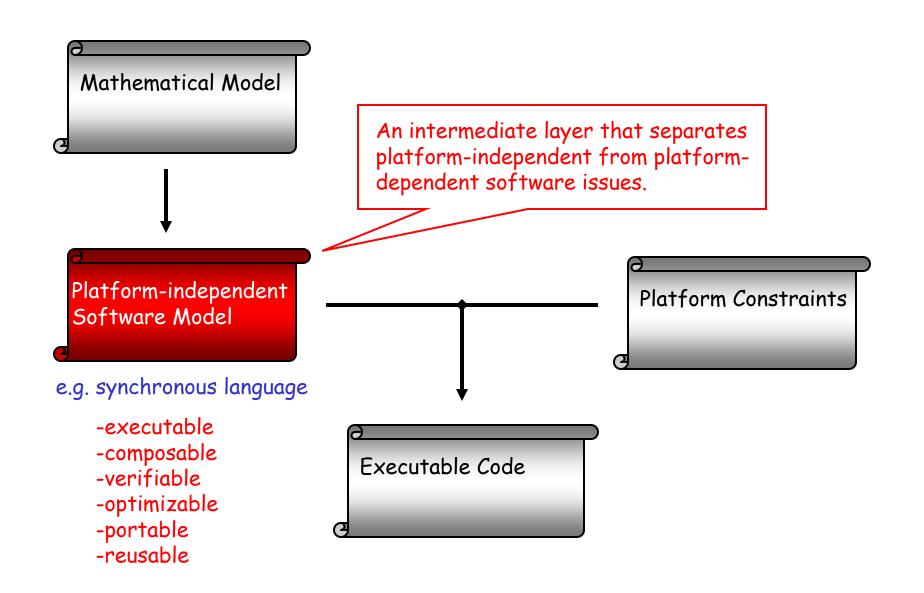
## Giotto

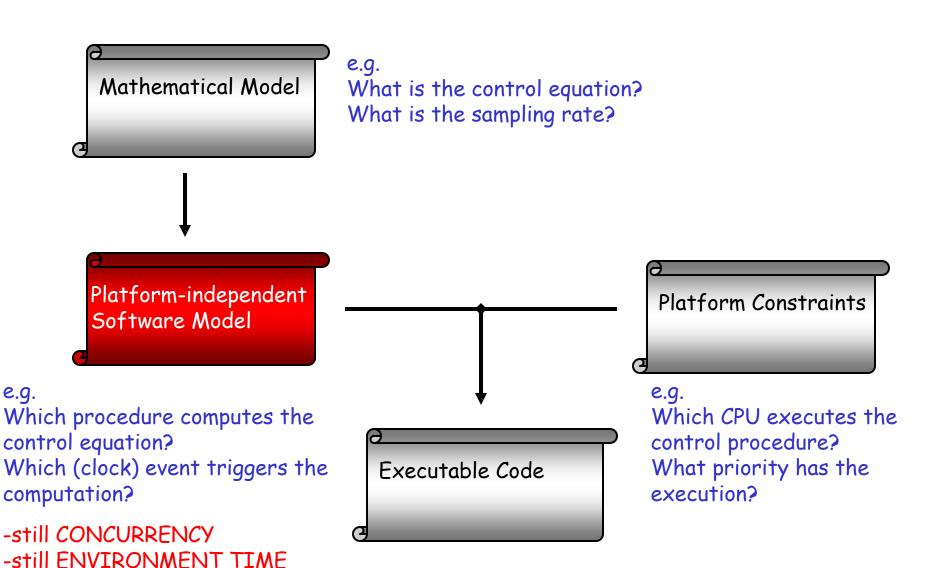
Thomas A. Henzinger, Benjamin Horowitz, Christoph Kirsch

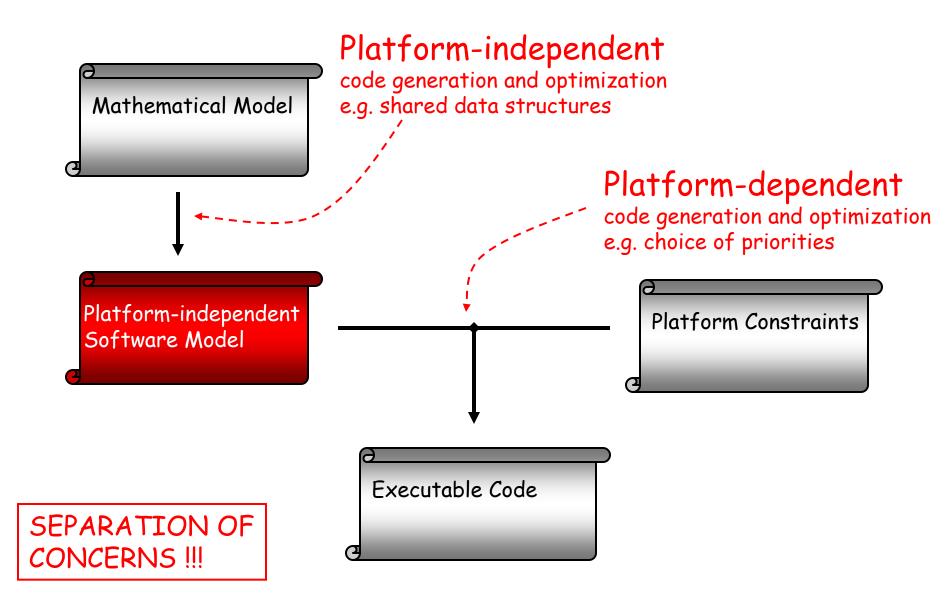
UC Berkeley









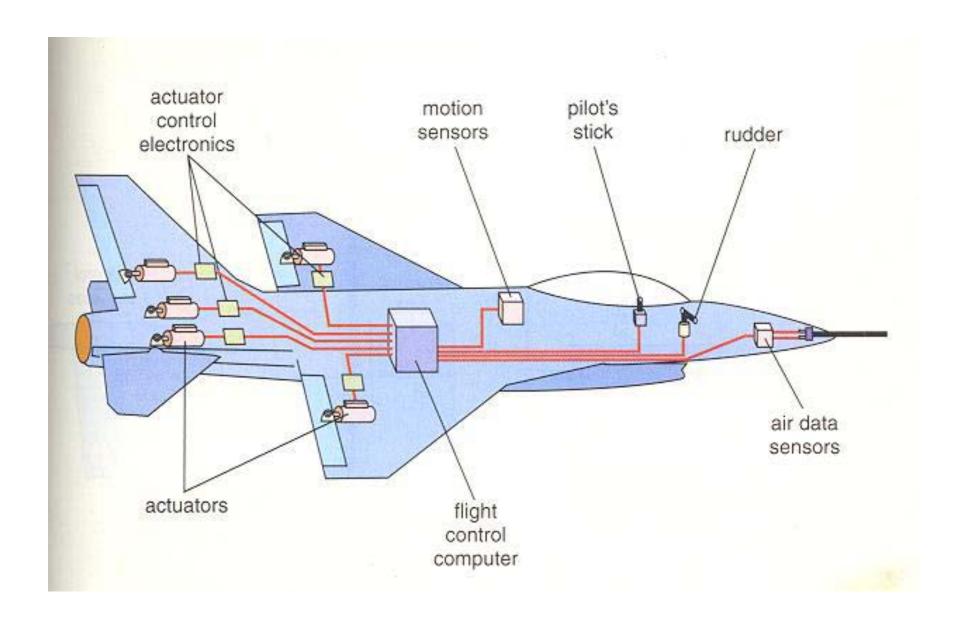


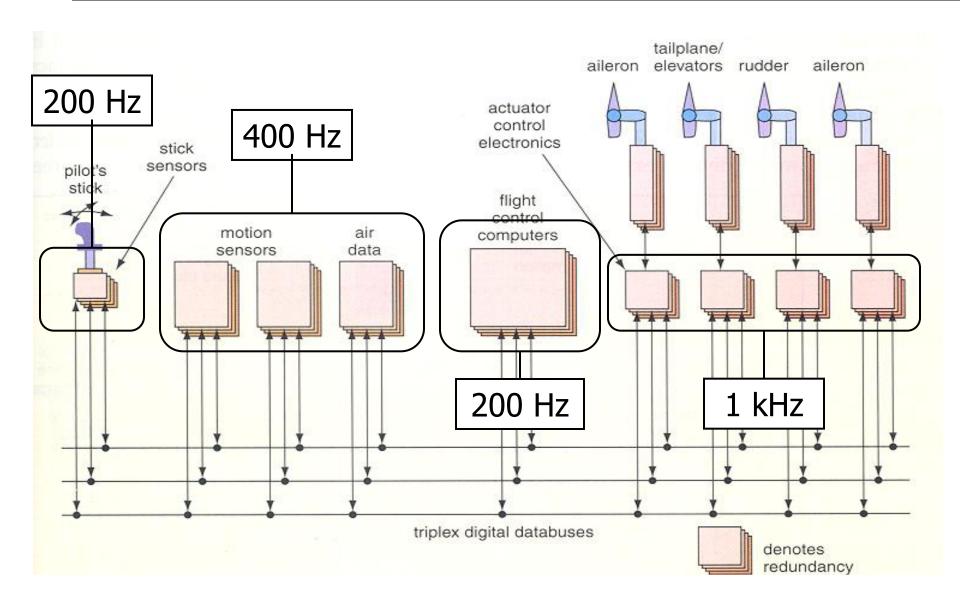


Single CPU.



Two or three connected CPUs.



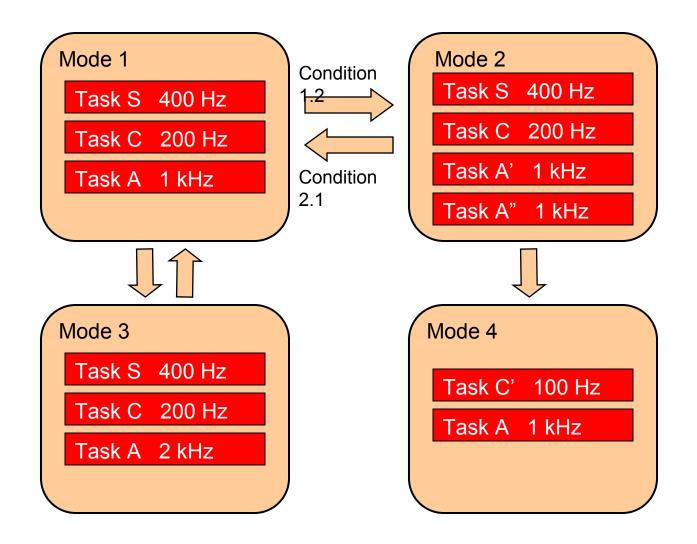


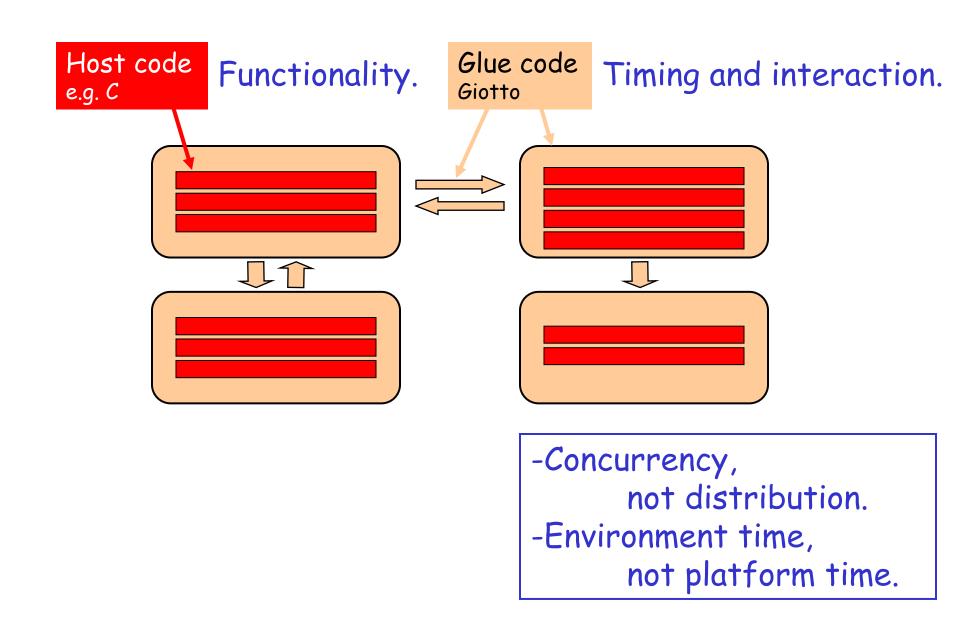
## 1. Concurrent periodic tasks:

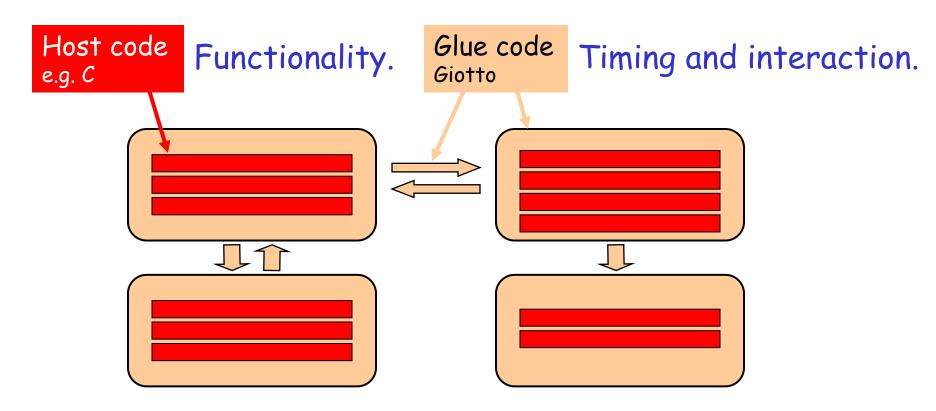
- -sensing
- -control law computation
- -actuating

## 2. Multiple modes of operation:

- -navigational modes (autopilot, manual, etc.)
- -maneuver modes (taxi, takeoff, cruise, etc.)
- -degraded modes (sensor, actuator, CPU failures)







This kind of software is understood:

Host code may be generated automatically.

The software complexity lies in the glue code:

Giotto enables requirements-driven rather than platform-driven glue-code programming.

## The Giotto Programmer's Model

## Programming in terms of environment time:

- -time-triggered task invocation
- -tasks have fixed duration (  $\geq$  WCET )
- -tasks are not preemptable

## Implementation in terms of platform time:

- -need access to (logical) global time, no other platform requirements
- -tasks may finish early, but outputs cannot be observed early
- -tasks may be preempted

Similar to the synchronous programmer's model, only simpler (no fixpoint issues).

## The Giotto Programmer's Model

#### Given:

1. Units of scheduled host code (application-level tasks). e.g. control law computation



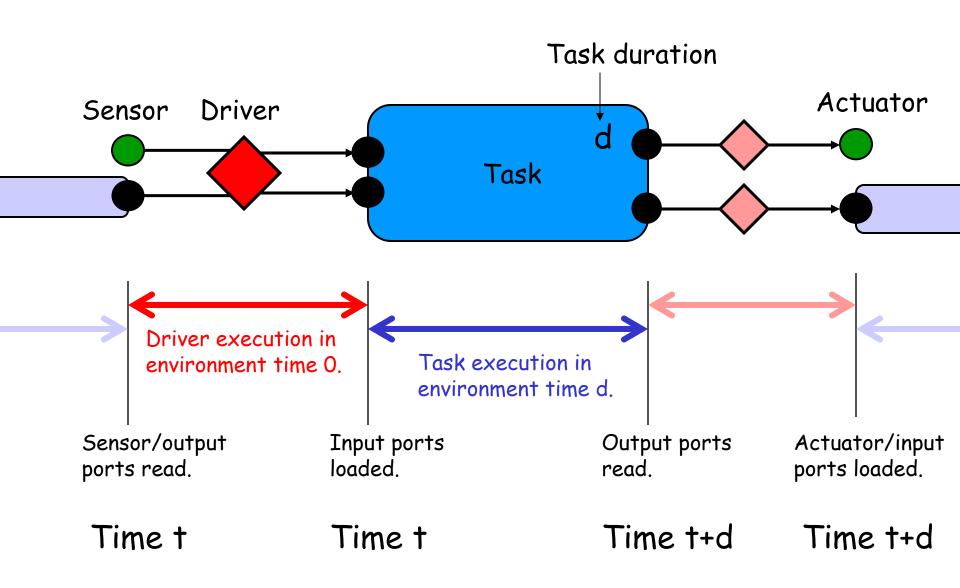
2. Units of synchronous host code (system-level drivers). e.g. device drivers



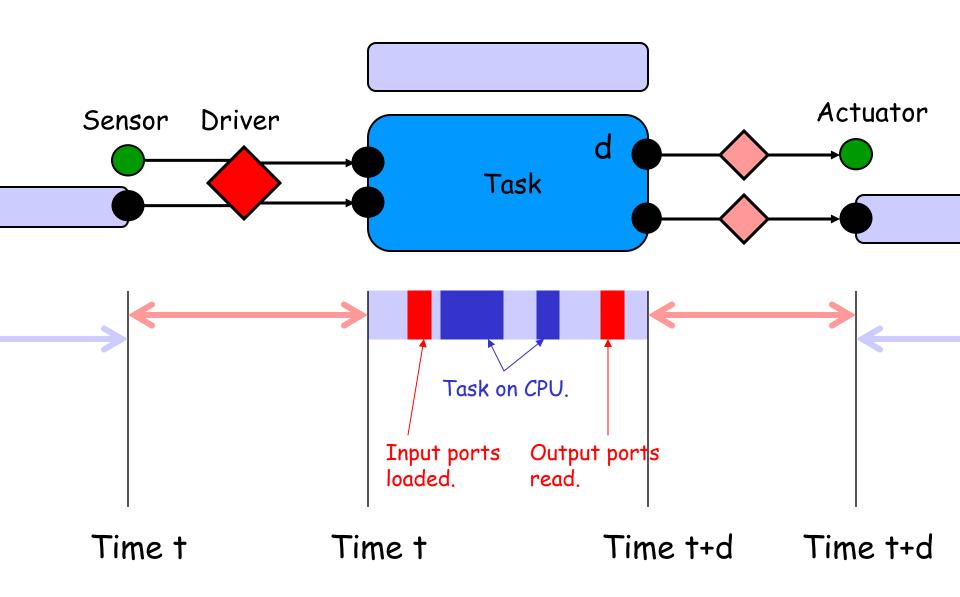
3. Real-time requirements and data flow between tasks.

Giotto: Glue code that calls 1. and 2. in order to realize 3.

## Environment Timeline (defined by Giotto semantics)



## Platform Timeline (chosen by Giotto compiler)



## Platform Independence ensures Predictability

## Input Determinism:

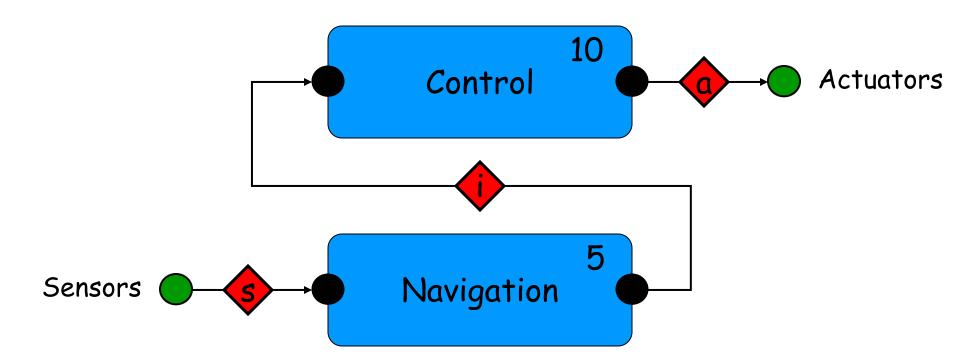
The Giotto compiler chooses for a given platform a platform timeline that is value equivalent to the environment timeline defined by the Giotto semantics.

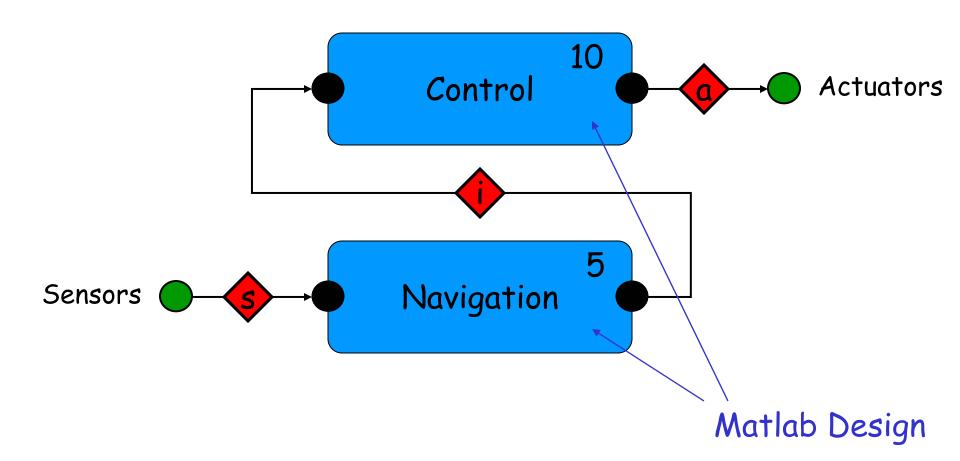


#### Time Determinism:

For a given time-triggered sequence of sensor readings, the corresponding time-triggered sequence of actuator settings is uniquely determined (i.e., there are no race conditions).

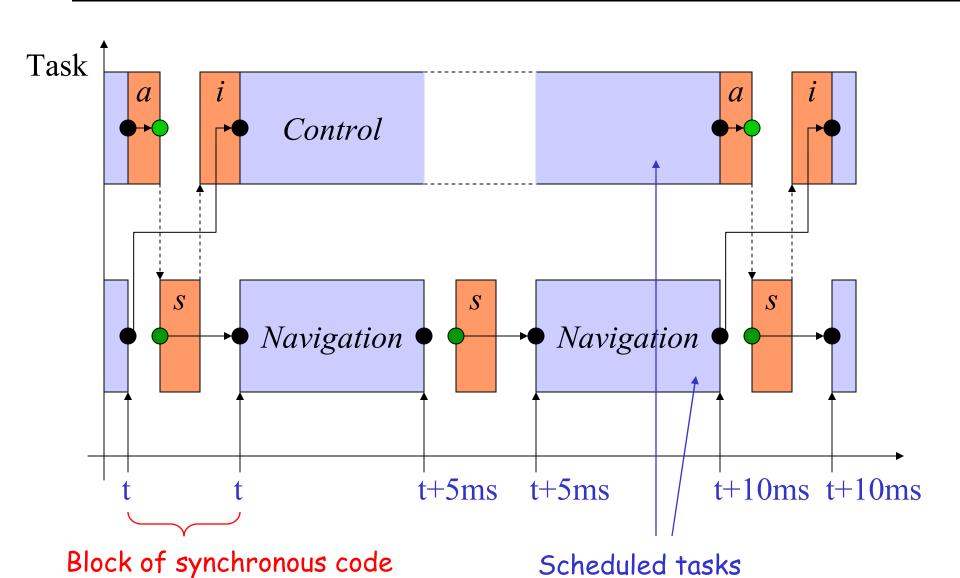
## Helicopter Software





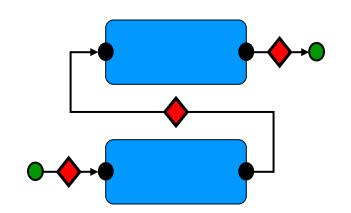
## Helicopter Software: Environment Timeline

(nonpreemptable)



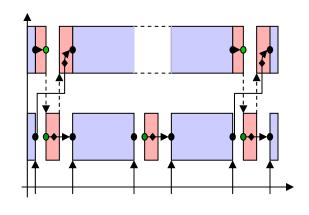
(preemptable)

## Helicopter Software: Giotto Syntax (Functionality)



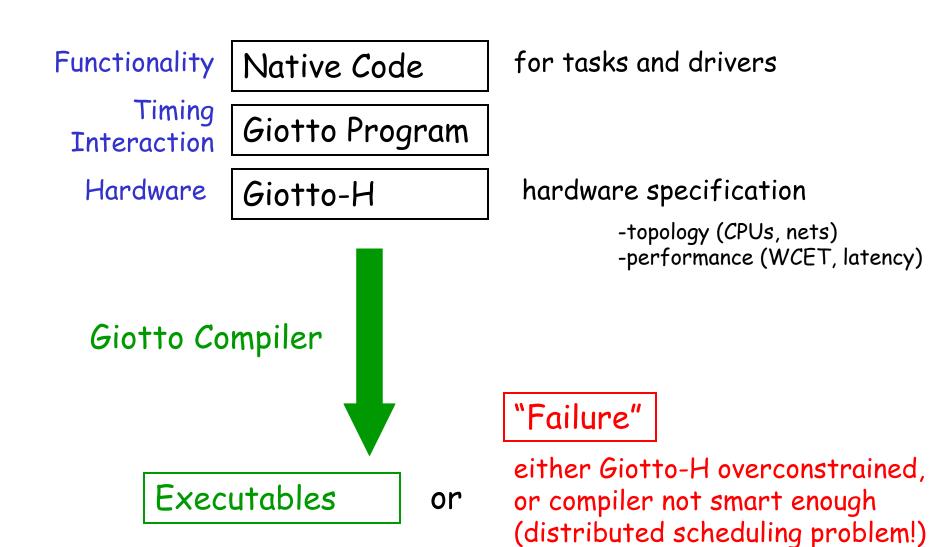
```
sensor gps type GPS uses c gps device;
actuator servo_type Servo := c_servo_init
        uses c servo device;
output
ctr type CtrOutput := c ctr init;
nav_type NavOutput := c_nav_init ;
driver sensing (GPS) output (gps_type gps)
{ c qps pre processing (GPS, qps ) }
task Navigation (gps_type gps) output (NavOutput)
{ c matlab navigation code ( gps, NavOutput ) }
```

## Helicopter Software: Giotto Syntax (Timing)

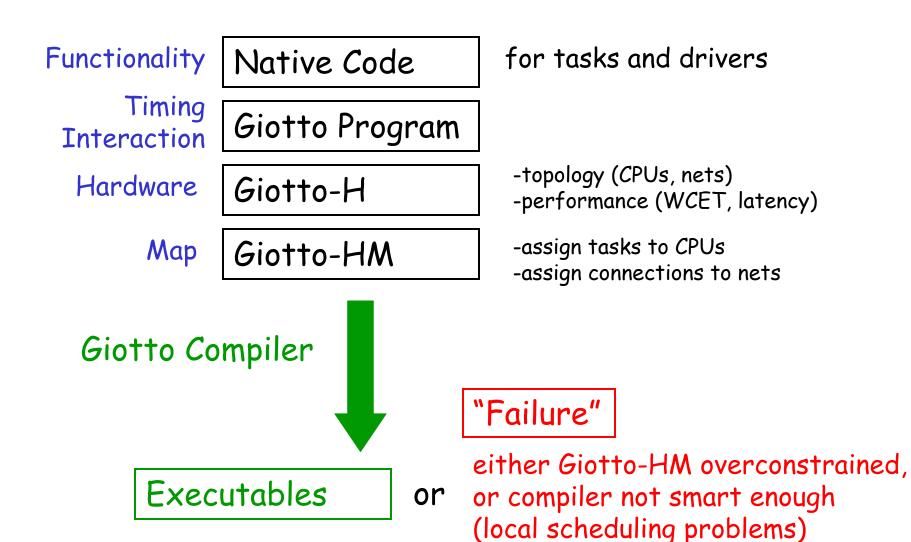


```
mode Flight ( ) period 10ms
{
    actfreq 1 do Actuator ( actuating ) ;
    taskfreq 1 do Control ( input ) ;
    taskfreq 2 do Navigation ( sensing ) ;
}
```

## The Giotto Compiler



## Closing the Gap: Annotated Giotto



## Closing the Gap: Annotated Giotto

Functionality Native Code for tasks and drivers Timing Giotto Program Interaction -topology (CPUs, nets) Hardware Giotto-H -performance (WCET, latency) -assign tasks to CPUs Map Giotto-HM -assign connections to nets Schedule -assign tasks to priorities (say) Giotto-HMS -assign connections to TDMA slots (say) Giotto Compiler

Executables

or

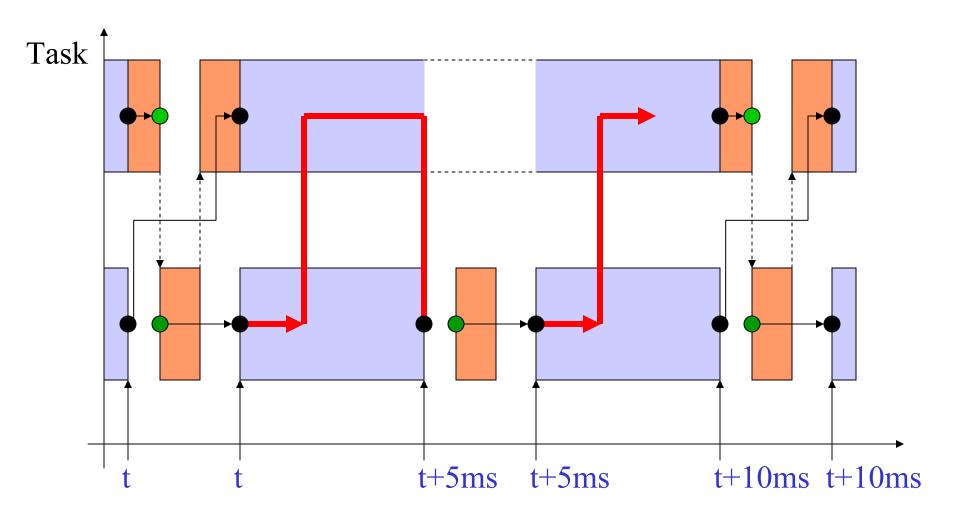
"Failure"

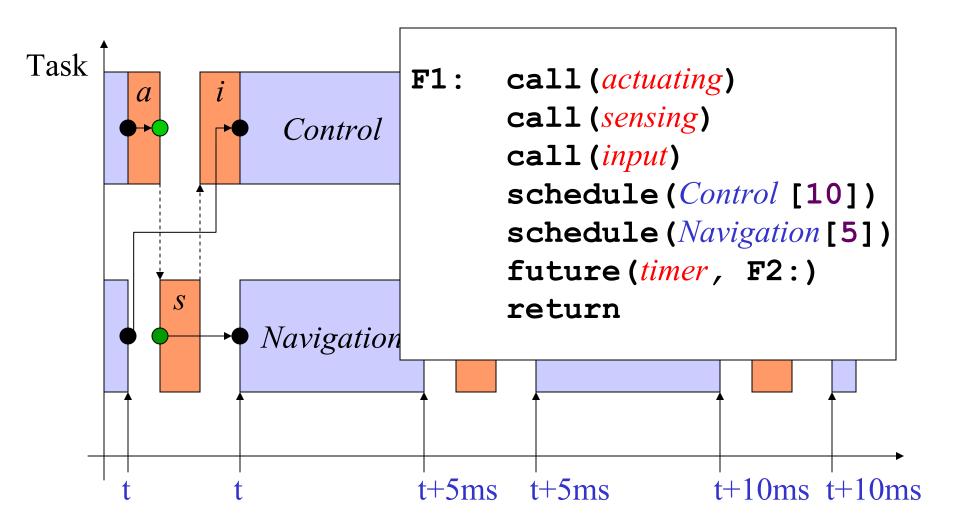
Giotto-HMS overconstrained

## Single-CPU Helicopter: Annotated Giotto

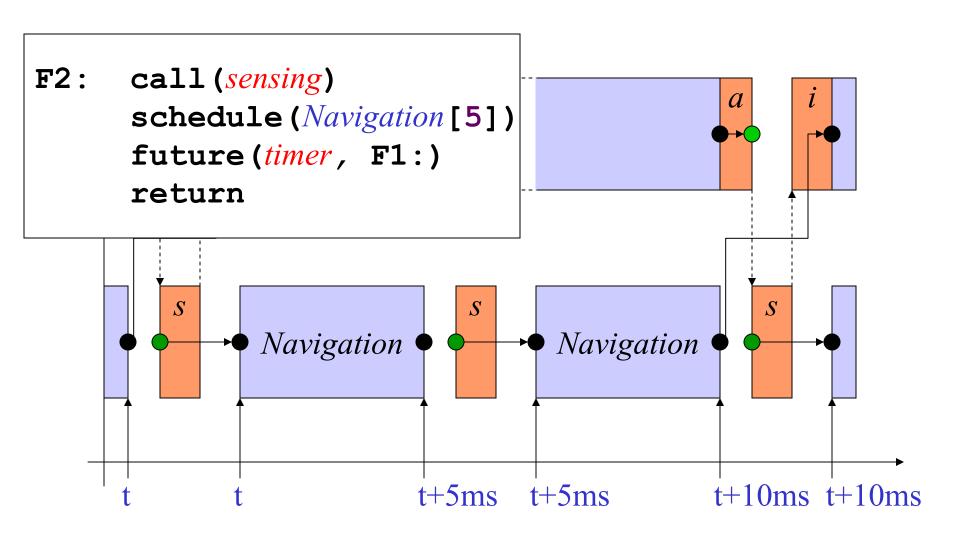
```
[ host Heli address 192.168.0.1 ] // Giotto-H Annotation
mode Flight ( ) period 10ms
     actfreq 1 do Actuator (actuating);
     taskfreq 1 do Control (input) [host Heli deadline 10]; // Giotto-MS
     taskfreq 2 do Navigation (sensing) [host Heli deadline 5];
```

## Single-CPU Helicopter: Platform Timeline (EDF)





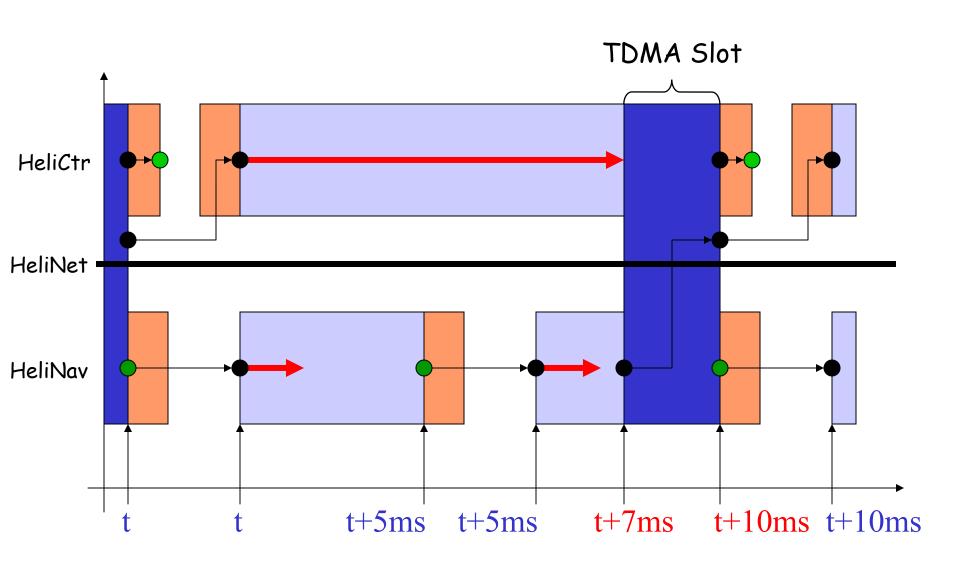
#### Code Generation



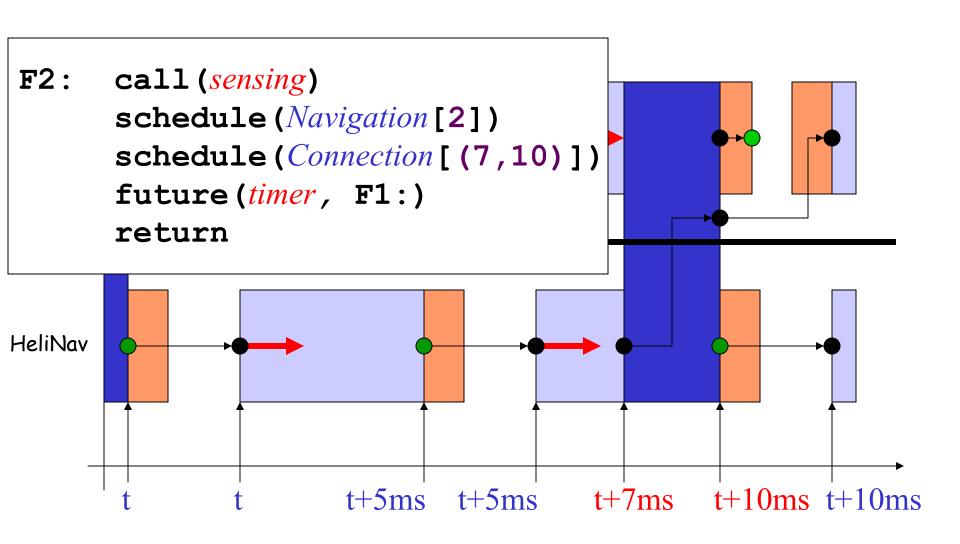
# Two-CPU Helicopter: Annotated Giotto (Time-triggered Communication)

```
[ host HeliCtr address 192.168.0.1;
 host HeliNav address 192.168.0.2;
 network HeliNet address 192.168.0.0 connects HeliCtr, HeliNav ]
mode Flight ( ) period 10ms
  {
     actfreq 1 do Actuator (actuating);
     taskfreq 1 do Control (input) [host HeliCtr deadline 7];
     taskfreq 2 do Navigation (sensing) [host HeliNav deadline 2;
     push (NavOutput) to (HeliCtr) in HeliNet slots (7,10) ];
  }
```

## Two-CPU Helicopter: Platform Timeline (Time-triggered Communication)



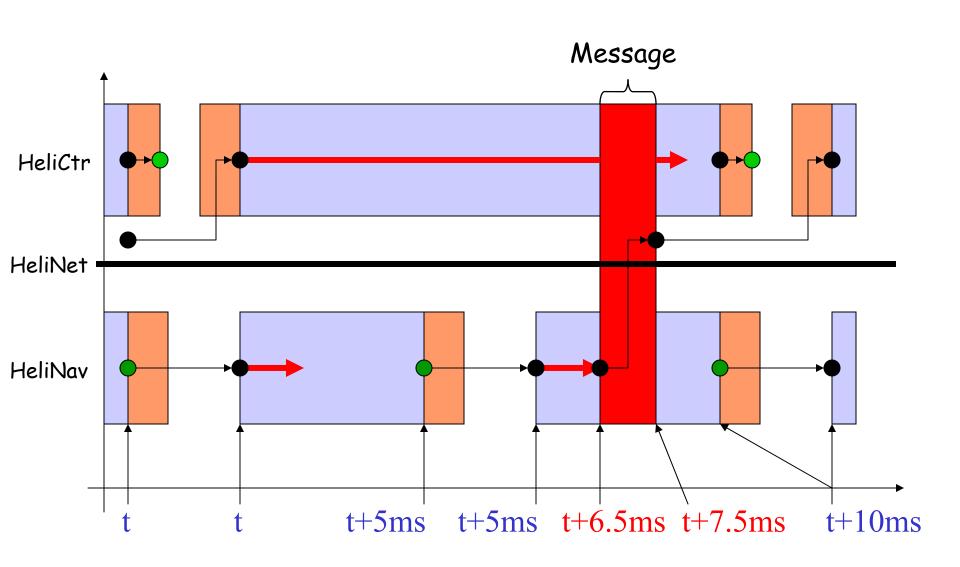
#### Code Generation for HeliNav

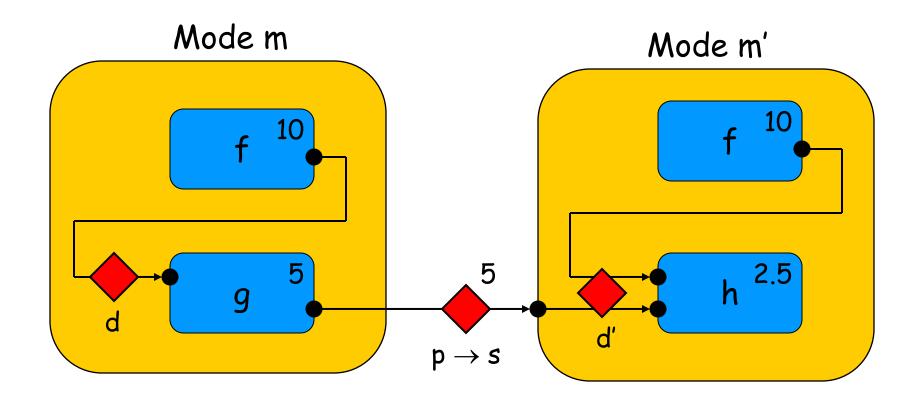


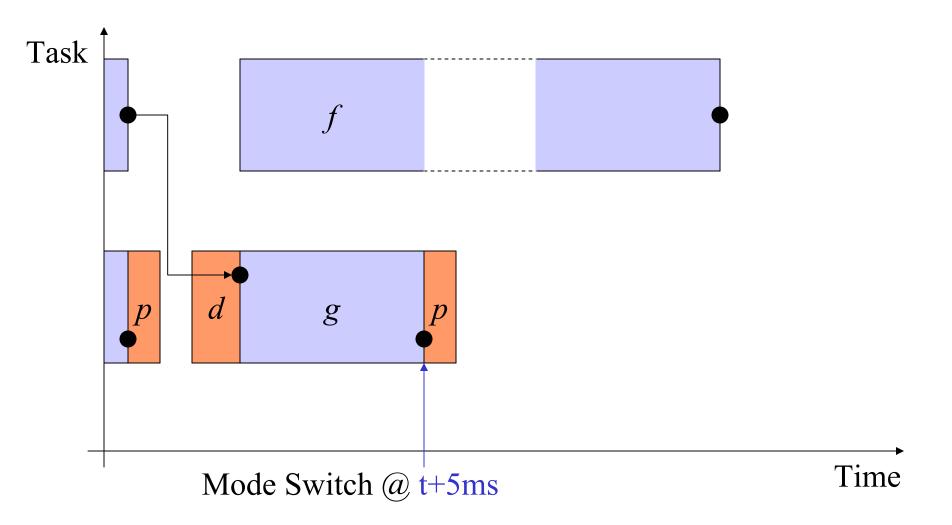
## Two-CPU Helicopter: Annotated Giotto (Event-triggered Communication)

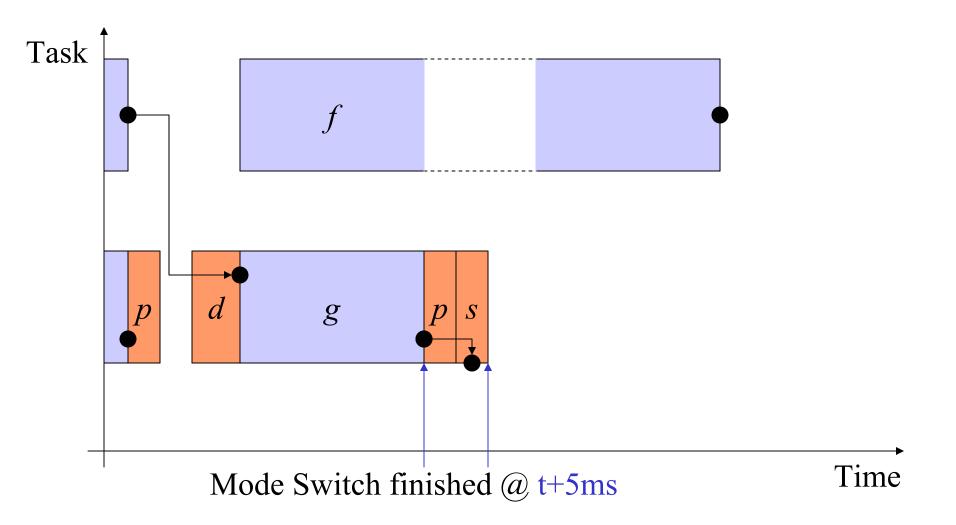
```
[ host HeliCtr address 192.168.0.1;
 host HeliNav address 192.168.0.2;
 network HeliNet address 192.168.0.0 connects HeliCtr, HeliNav ]
mode Flight ( ) period 10ms
     actfreq 1 do Actuator (actuating);
     taskfreq 1 do Control (input) [host HeliCtr deadline 10];
     taskfreq 2 do Navigation (sensing) [host HeliNav deadline 2;
     push ( NavOutput ) to ( HeliCtr ) in HeliNet deadline 3 ];
```

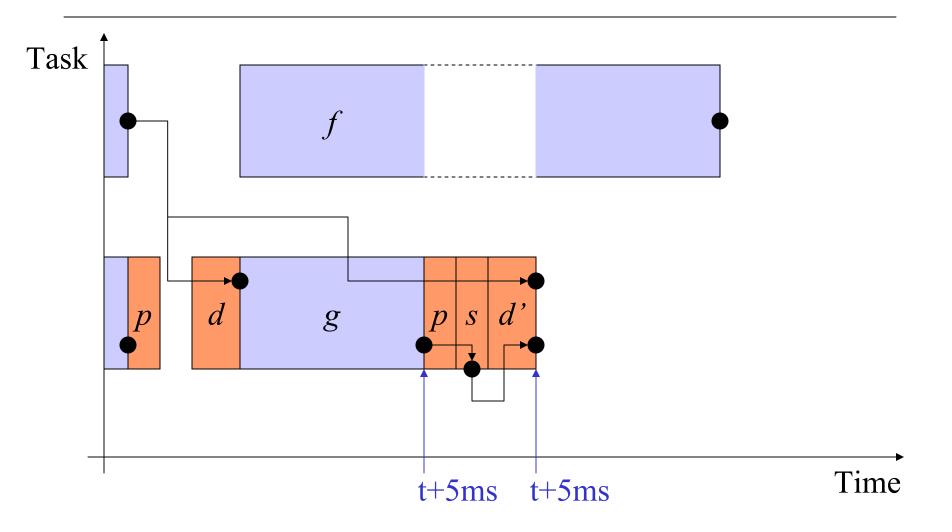
# Two-CPU Helicopter: Platform Timeline (Event-triggered Communication)

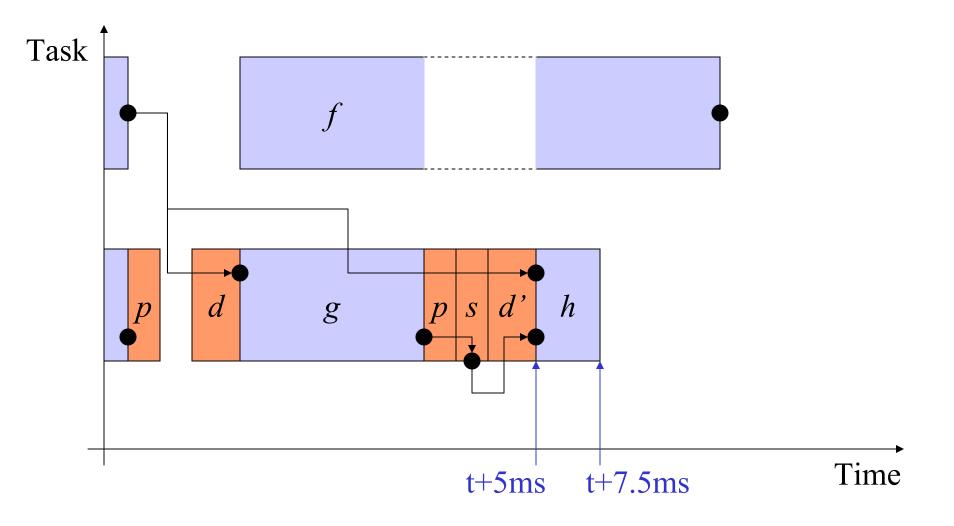


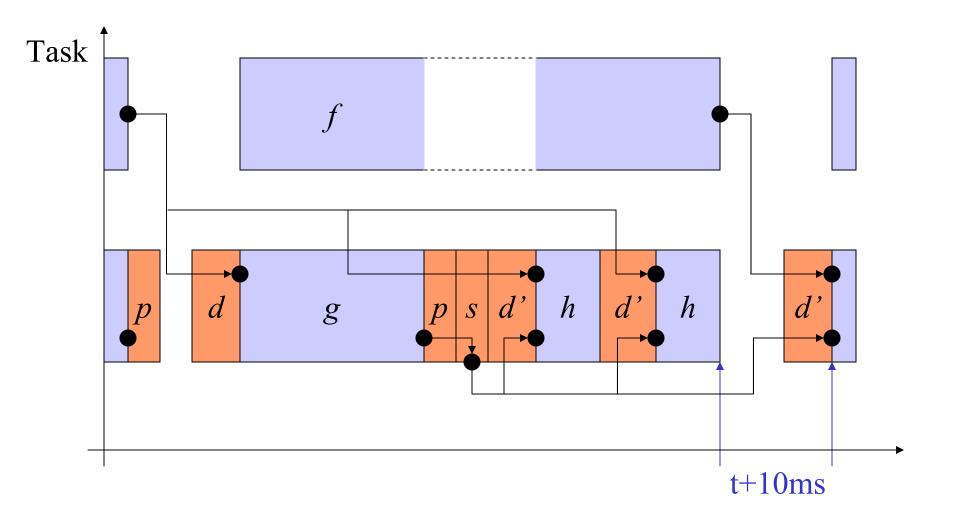












Try it out!

www.eecs.berkeley.edu/~fresco/giotto