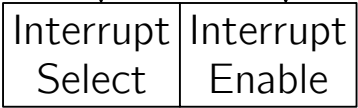
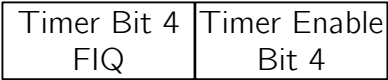
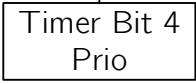




Control Register



Vector Address Register

