

Lab due By February 15, 2022

1. Submit screenshots using ModelSim tutorial

"Using the ModelSim-Intel FPGA

Simulator by Drawing Waveform" for majority.vhd and mymux2_1.vhd circuits for 1bit signals only.

What to submit: Screenshots that demonstrate waveforms for input and output signals. You have to describe how do you verify the truth table in each case.