

# Lab to be performed during class hours on 02/16/2022

## Sections

12:-1:40 PM and 2:-3:40 PM

1. Create components and2, or2, not1 as described in the tutorial on Page 11.
2. Create structural BUZZER. VHDL using these components as described on page 13 of the tutorial.
3. Simulate BUZZER.VHD using ModelSim and verify its correctness.
4. Submit the screenshots with waveforms.