Christopher Lall

Lab Experiment 4a, 4b, SRAM

CSc 342/343

Professor Gertner

Due 3/14/22
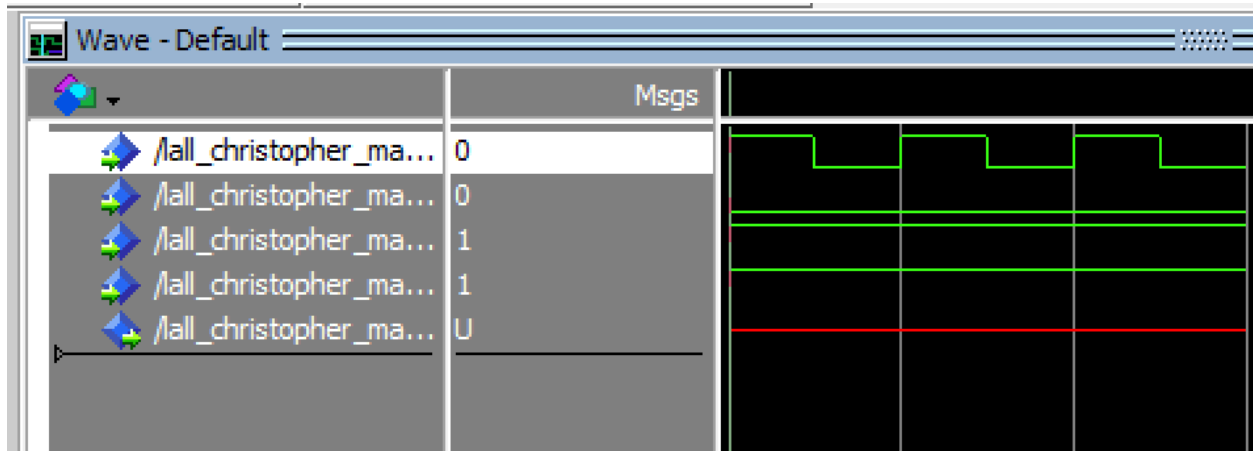
Table of Contents
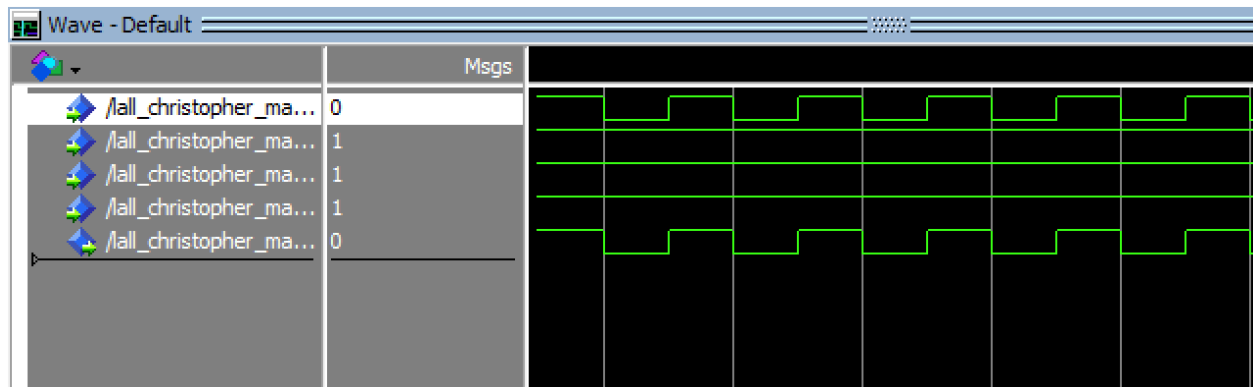
4A Part 1

Figure 1. D on and off, Q redline.

Figure 2. E forced to 1. Q follows D

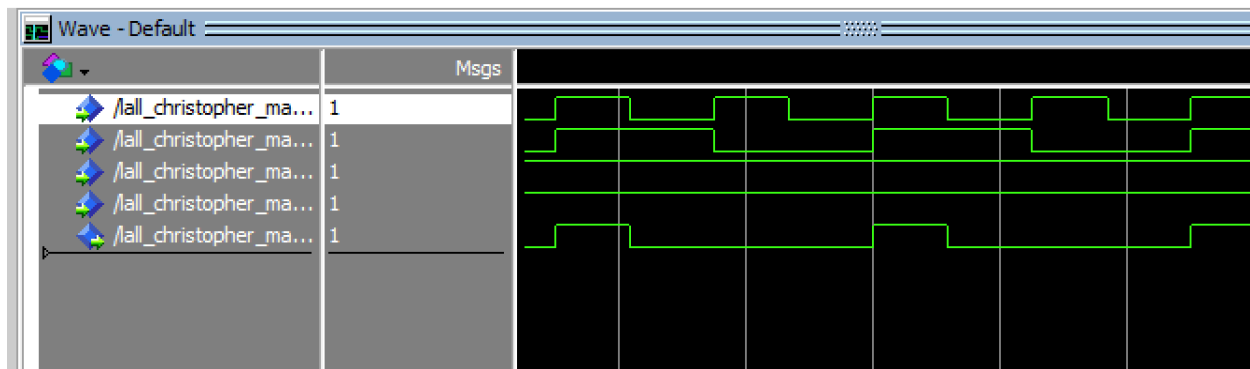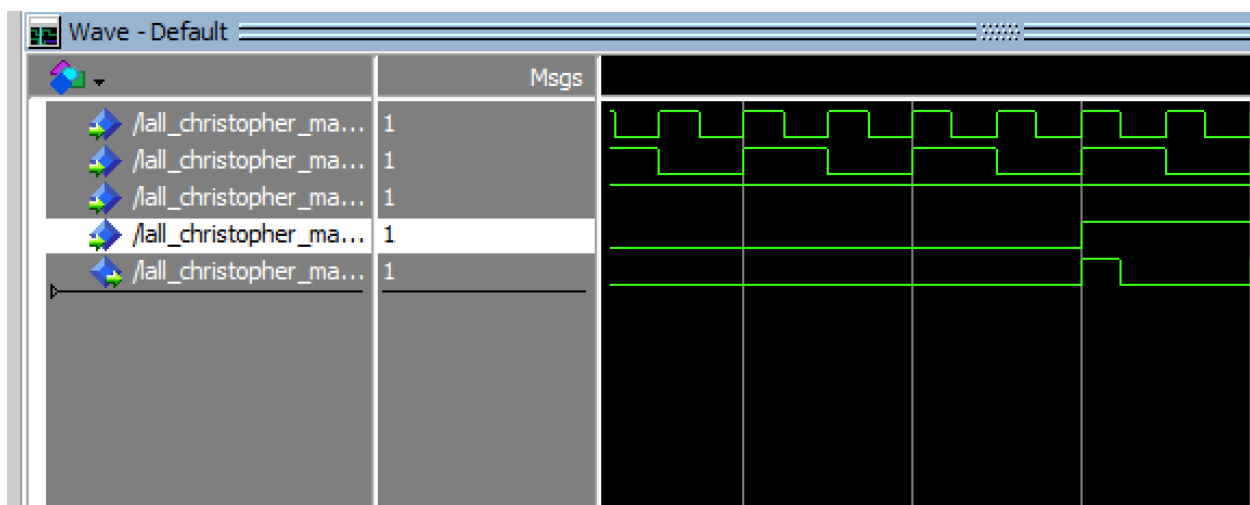*Figure 3. Q mirrors D*

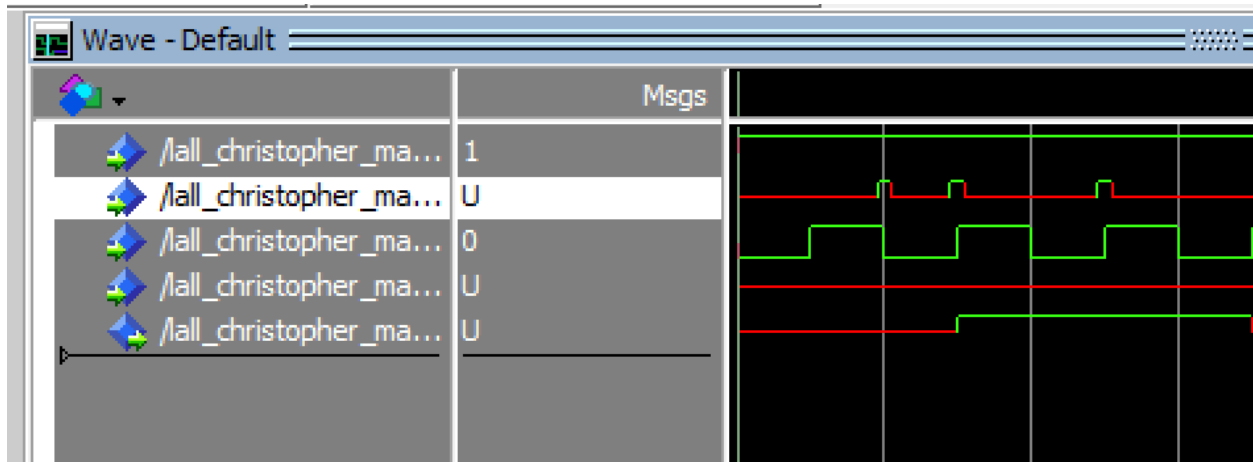*Figure 4. CLRN 0 to 1*

4A Part 2

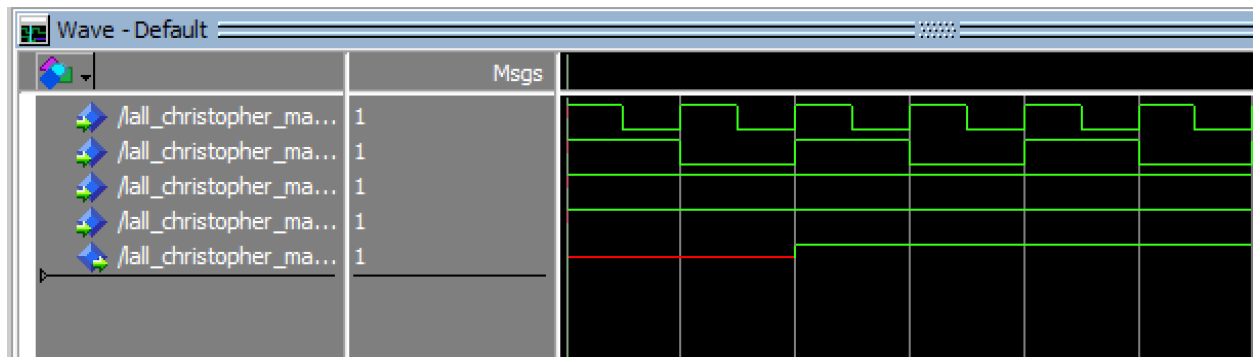*Figure 5. Master slave works exactly like d flip flop*

4A Part 3

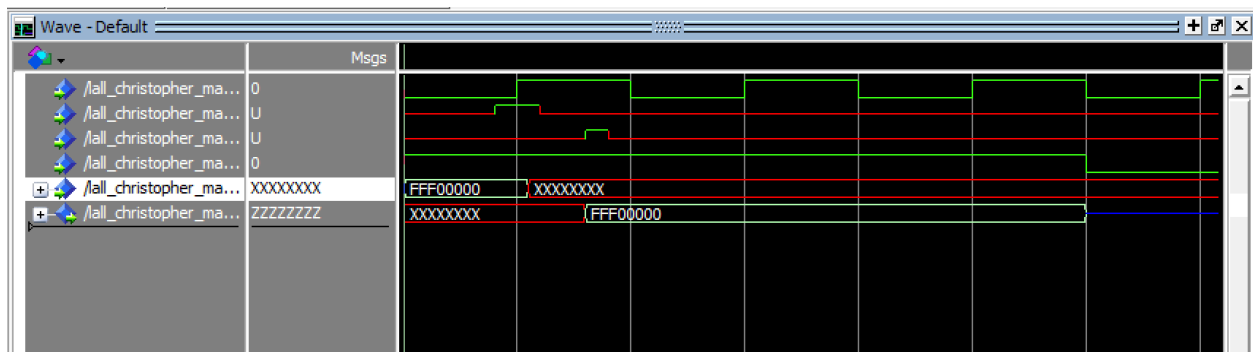*Figure 6. Edge twice the speed of D*

4B

Figure 7. Storage and output display, chip enabling moves from 1 to 0 which changed output to undefined.
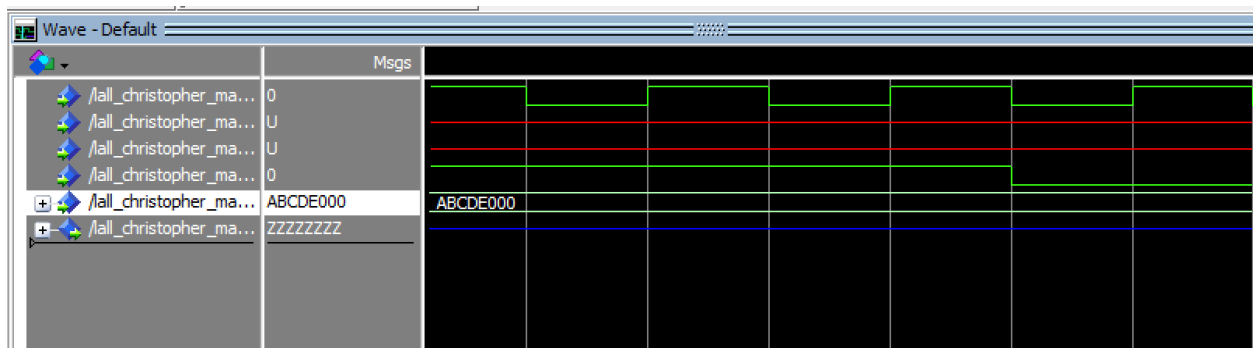
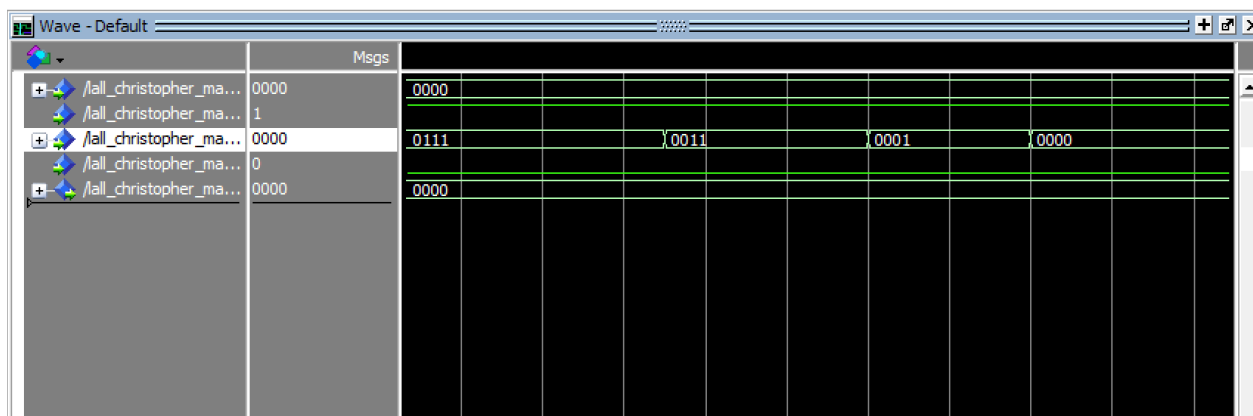*Figure 8. Same idea as example above, however, just my example of changing the hex value.*

SRAM

Figure 9. Sram waves