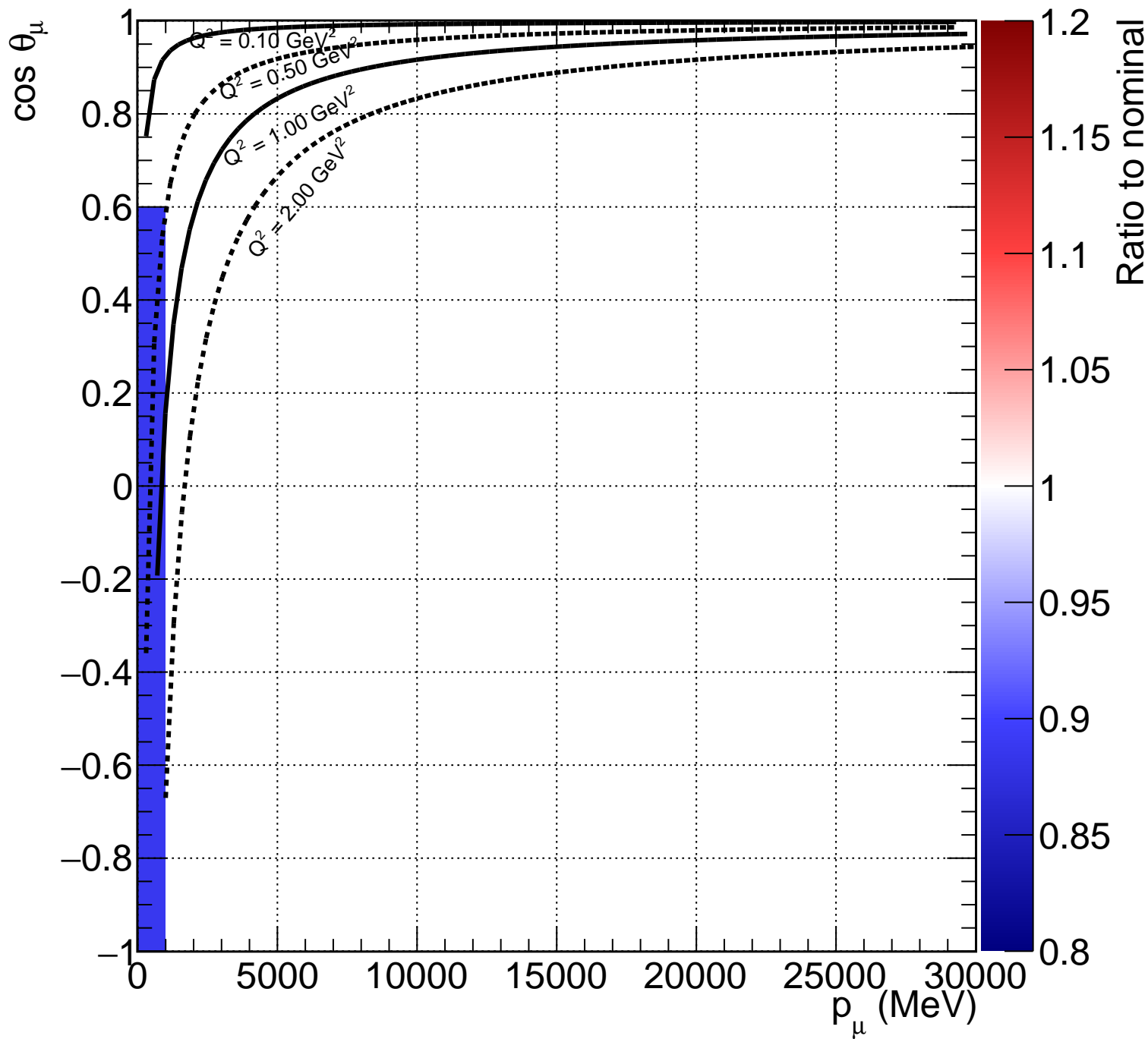
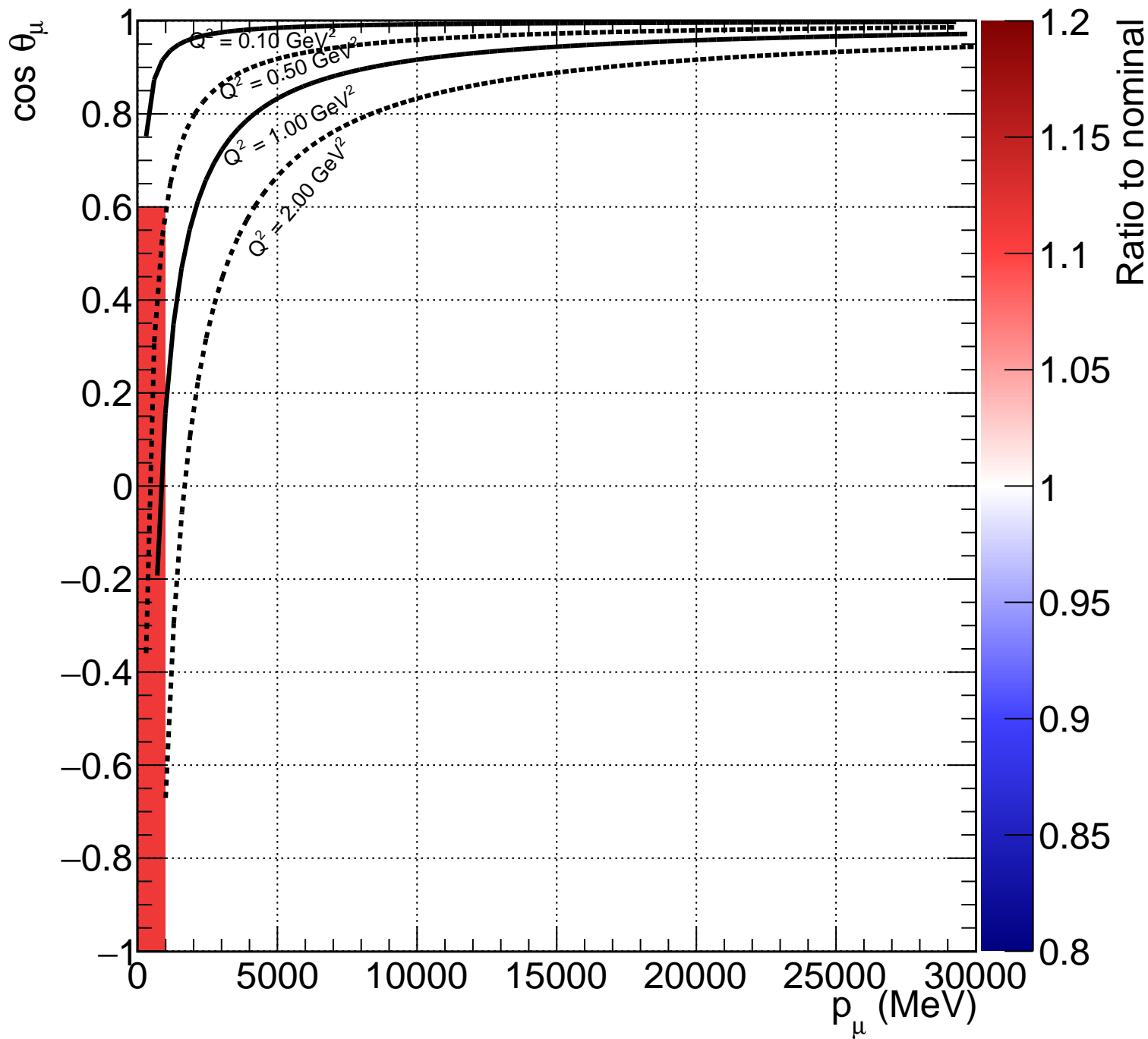


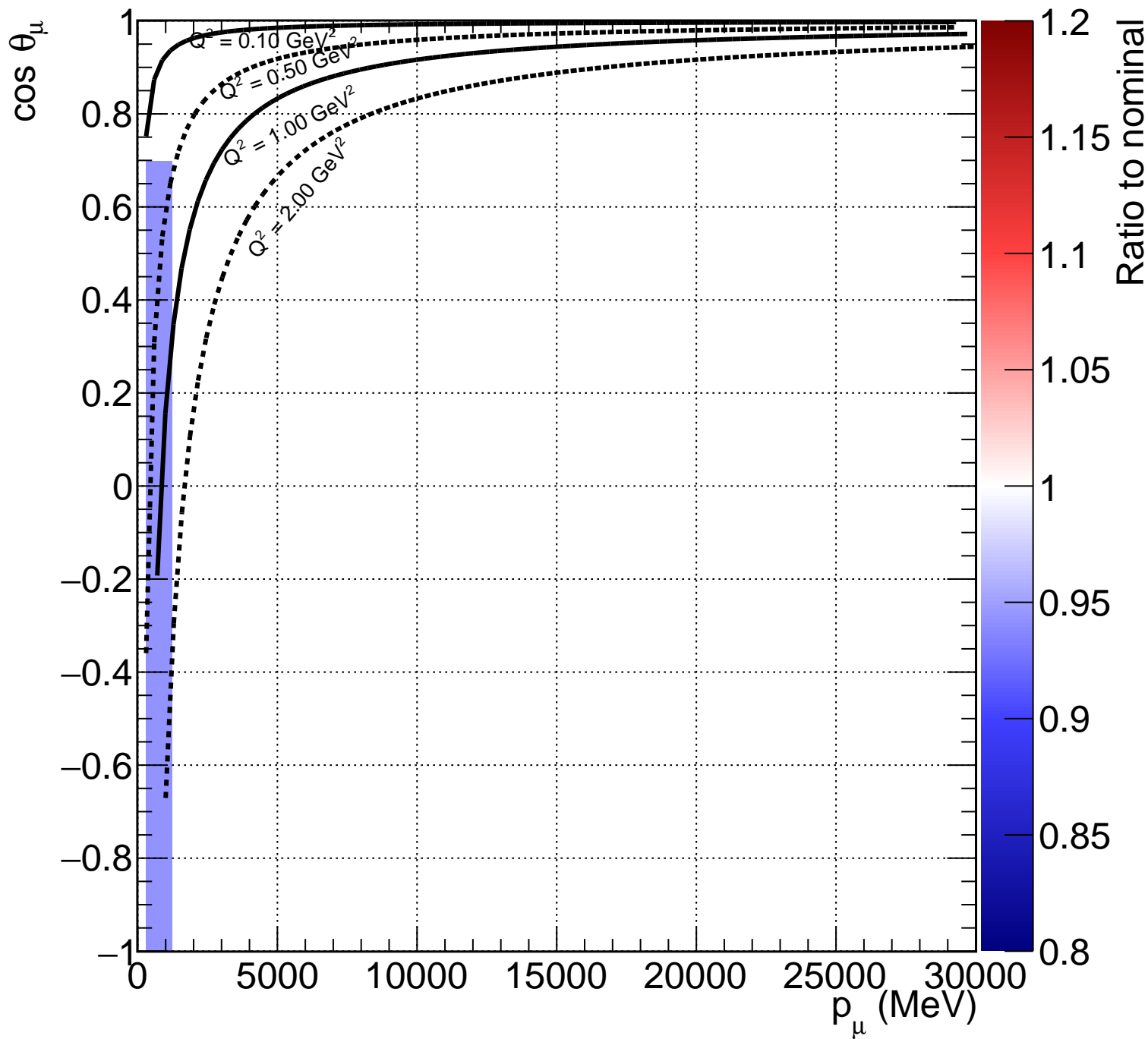
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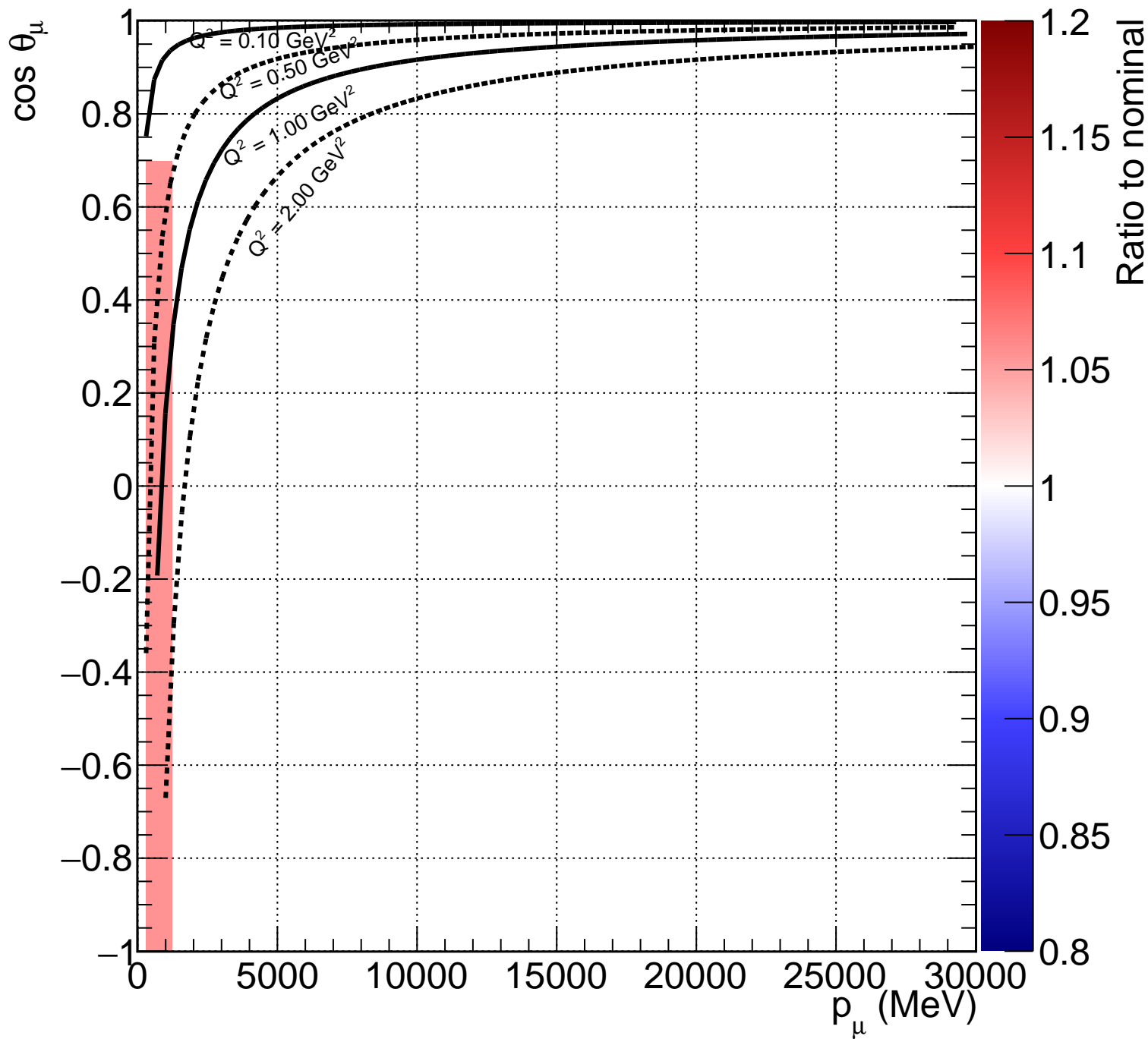
# FGD1\_numuCC\_0pi\_ndd\_0\_1.11262



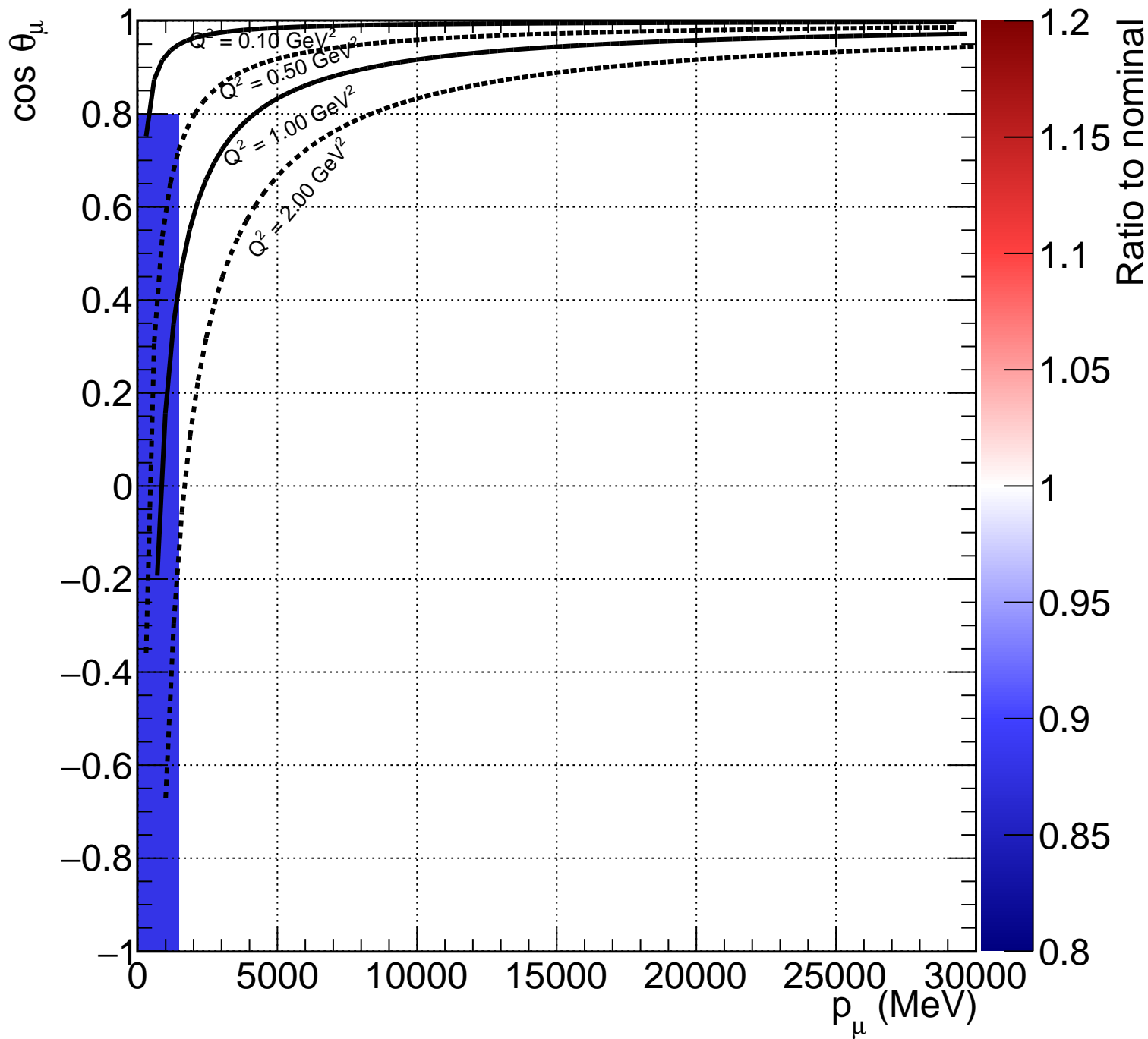
# FGD1\_numuCC\_1pi\_ndd\_50\_0.947826



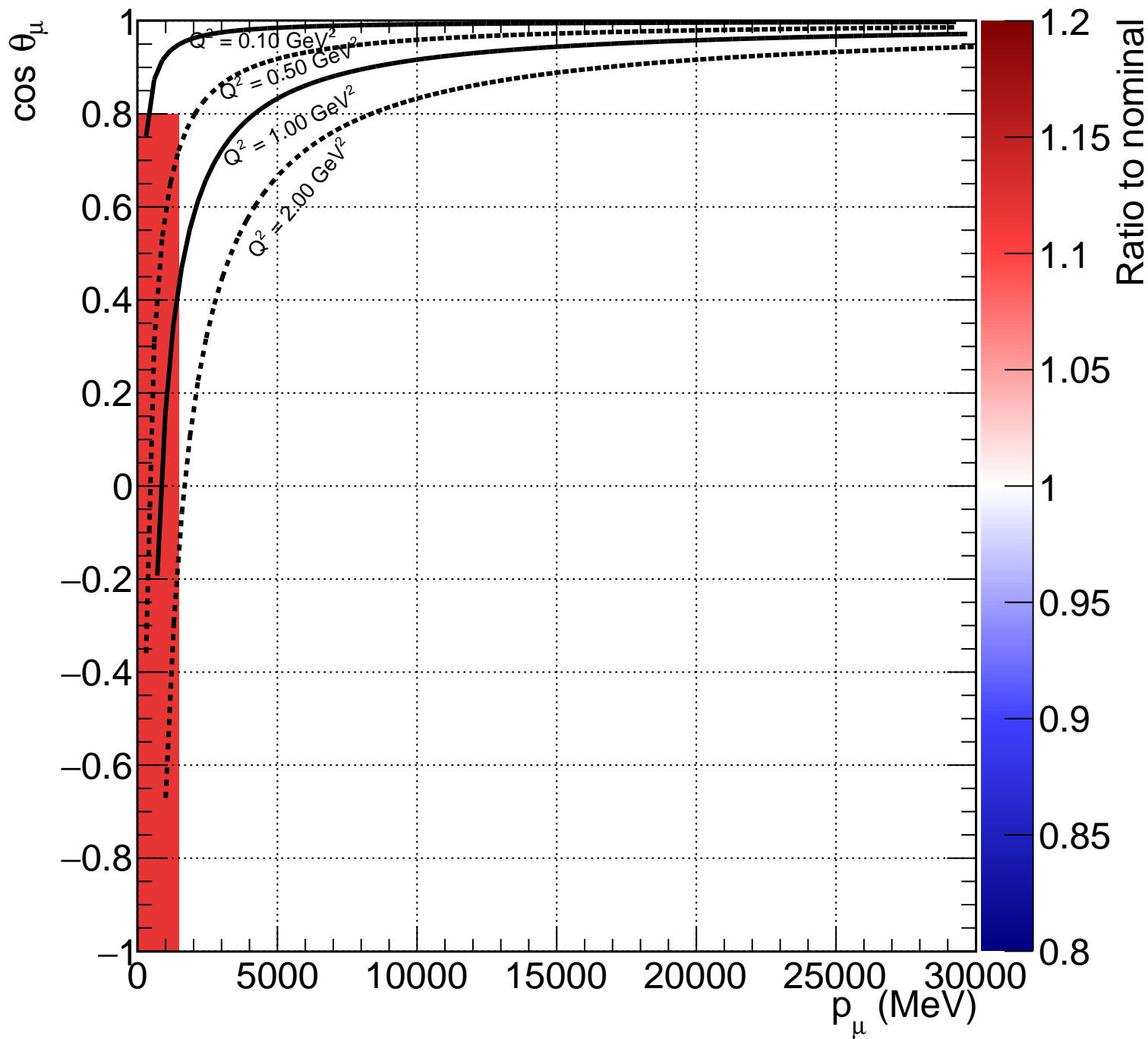
# FGD1\_numuCC\_1pi\_ndd\_50\_1.05978



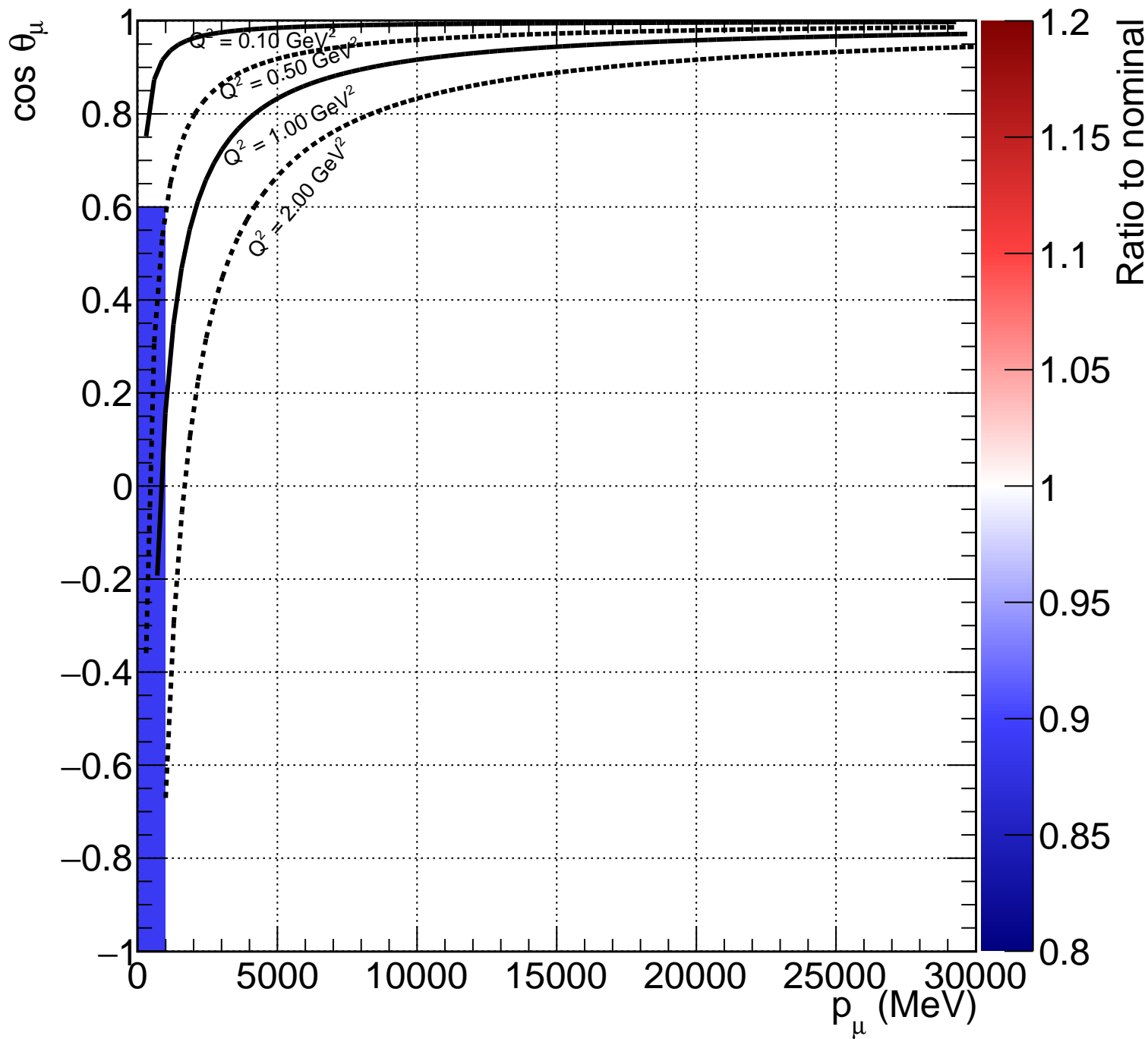
# FGD1\_numuCC\_other\_ndd\_82\_0.946546



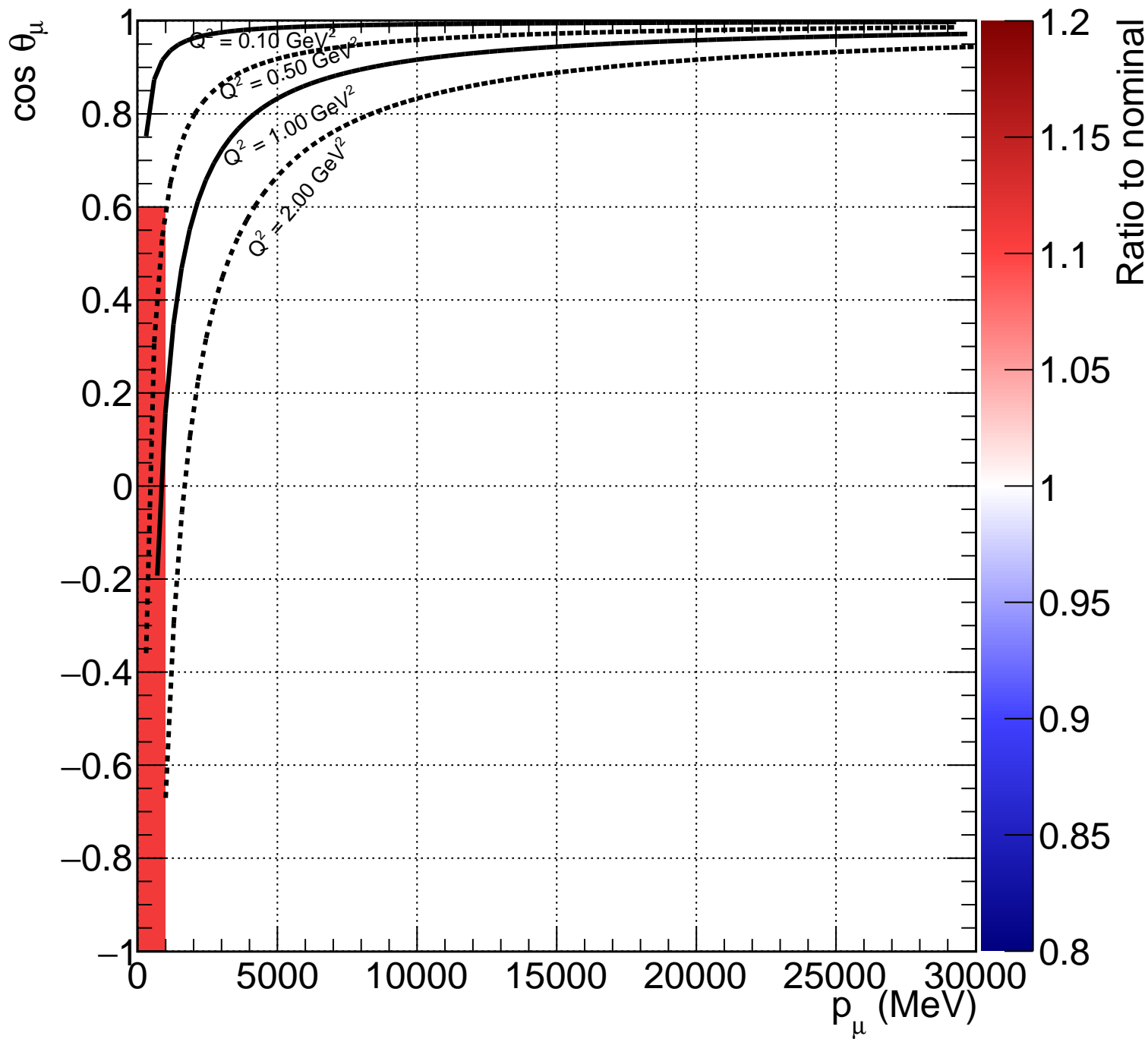
# FGD1\_numuCC\_other\_ndd\_82\_1.20303



# FGD2\_numuCC\_0pi\_ddd\_278\_0.887962

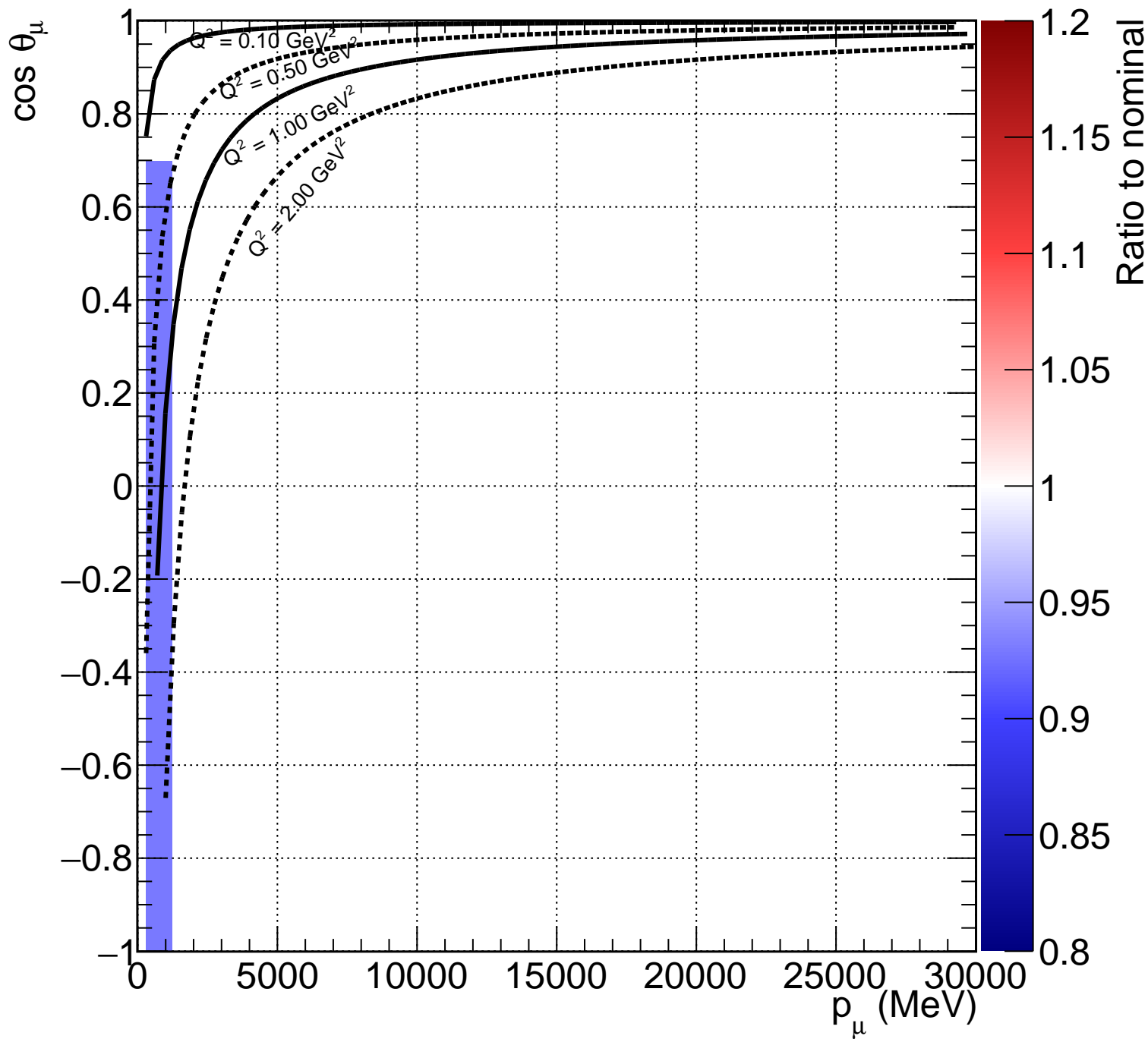


# FGD2\_numuCC\_0pi\_ndd\_278\_1.10824

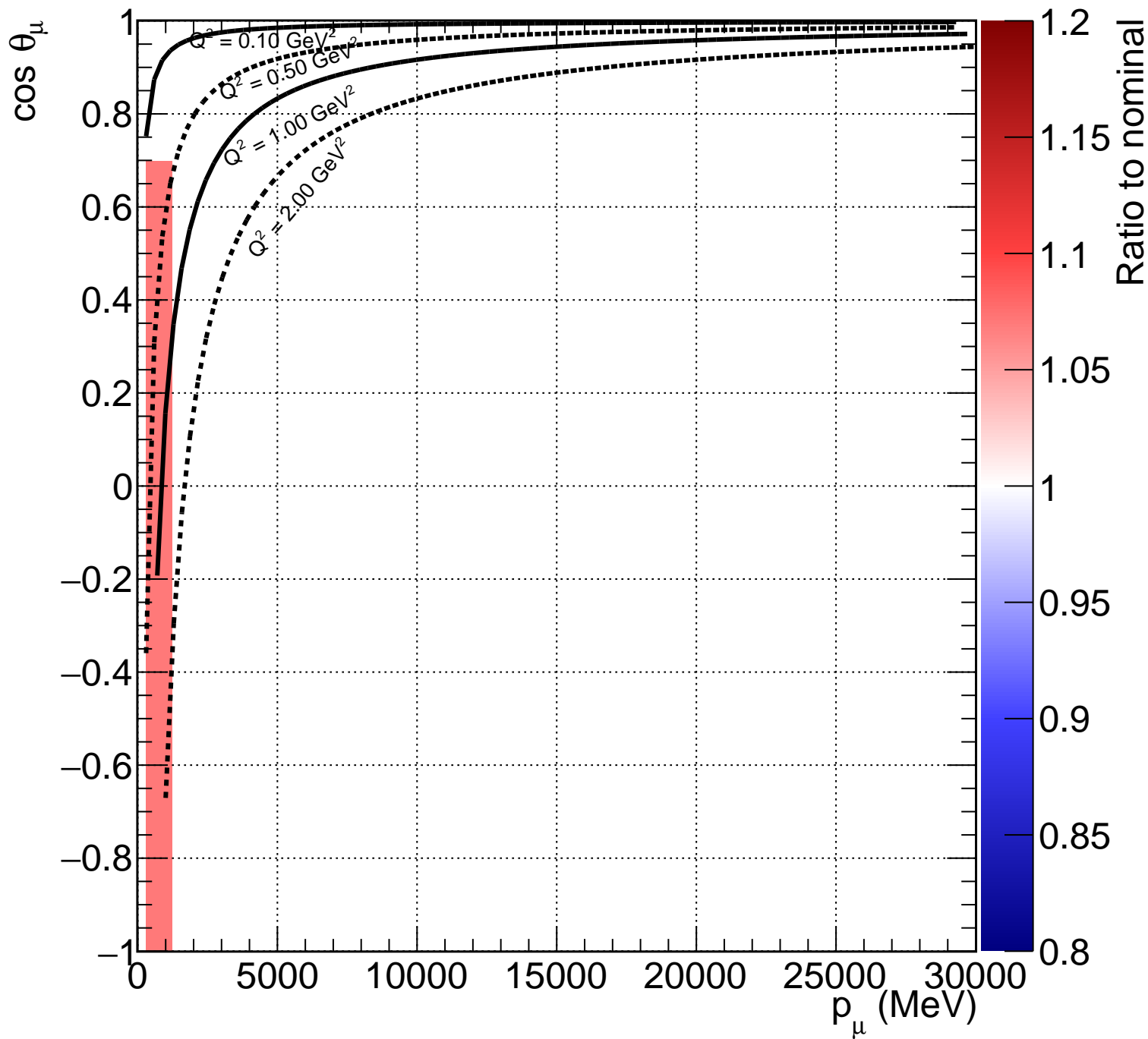




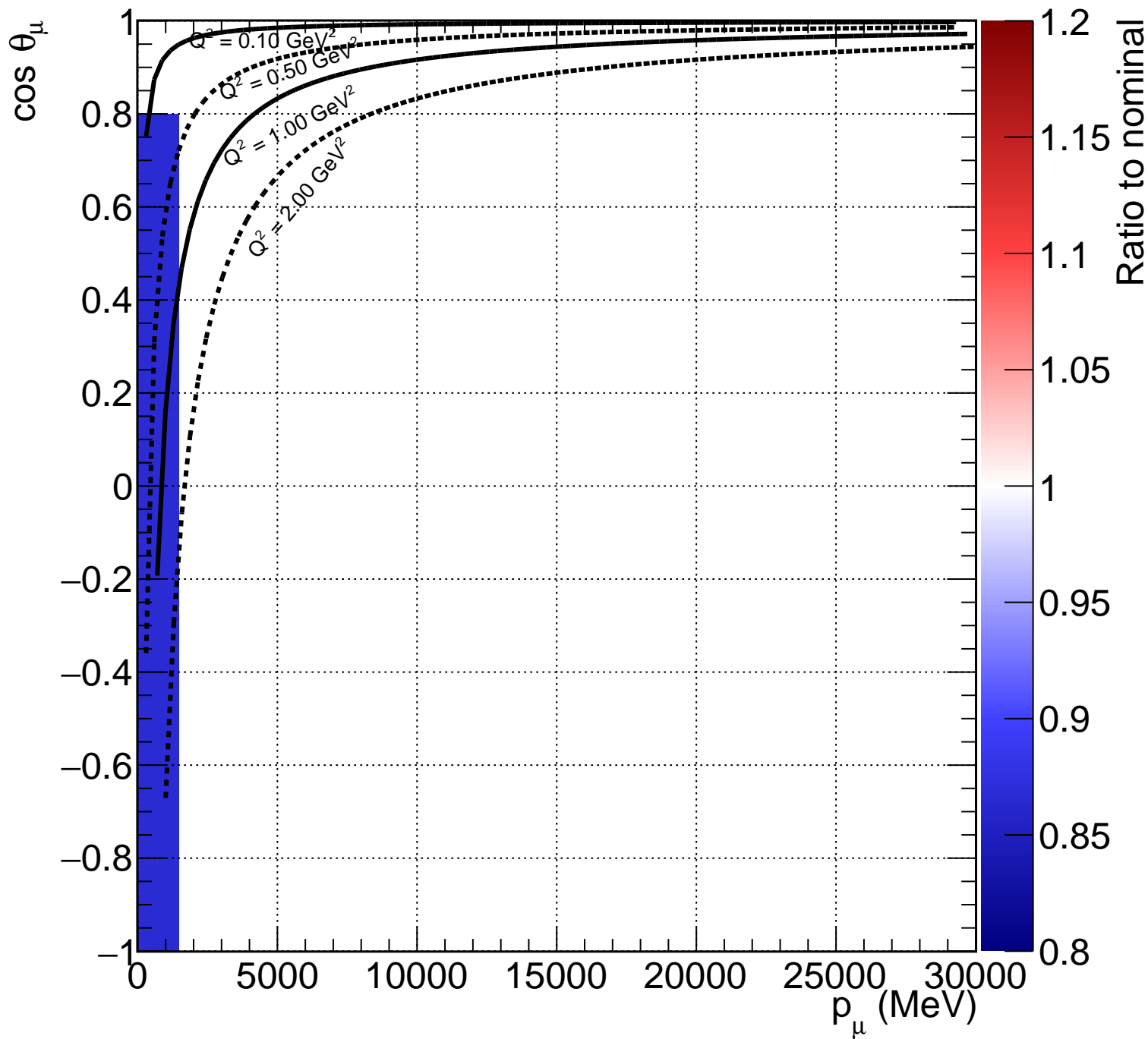
# FGD2\_numuCC\_1pi\_ddd\_328\_0.912028



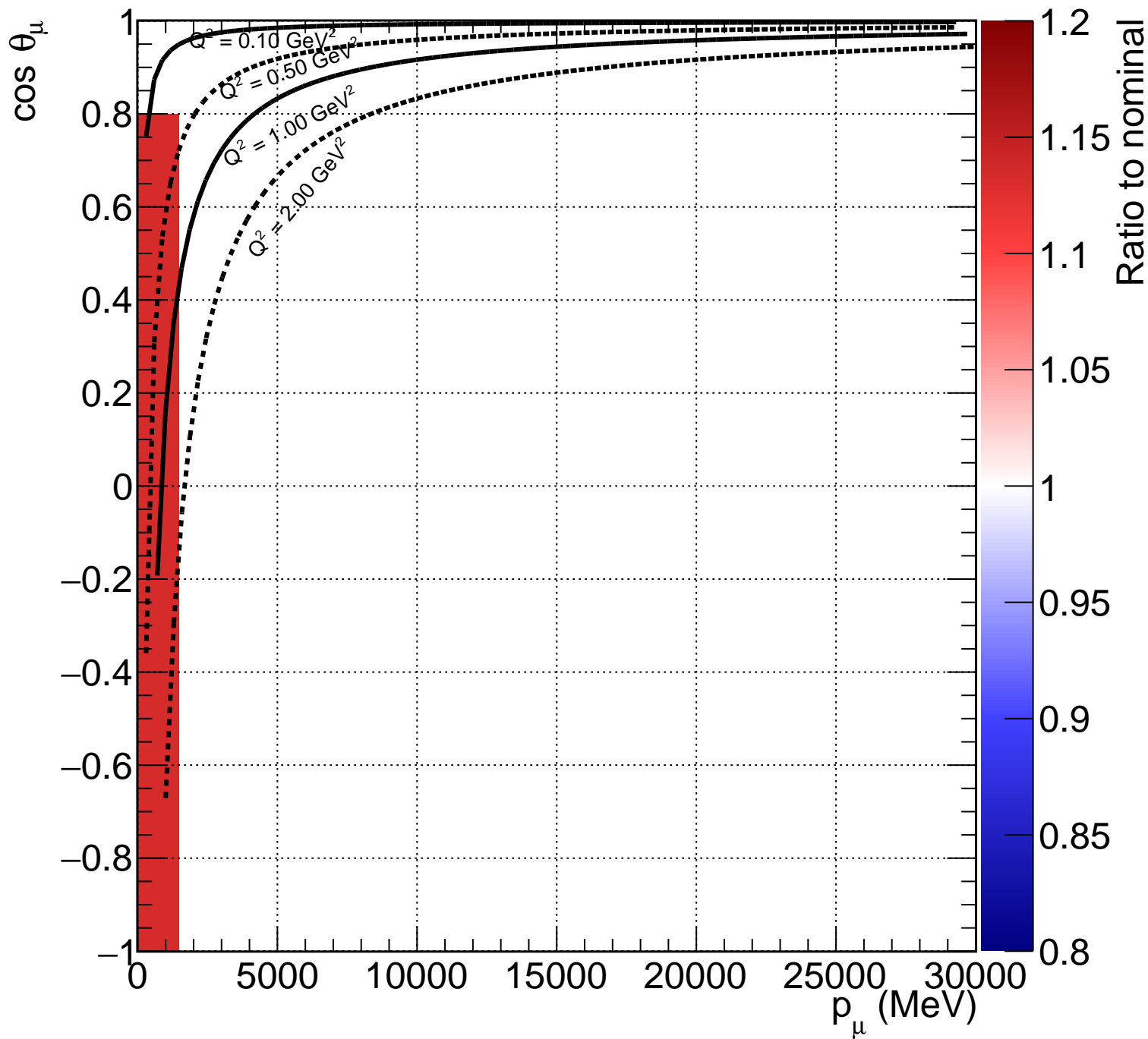
# FGD2\_numuCC\_1pi\_ndd\_328\_1.04986



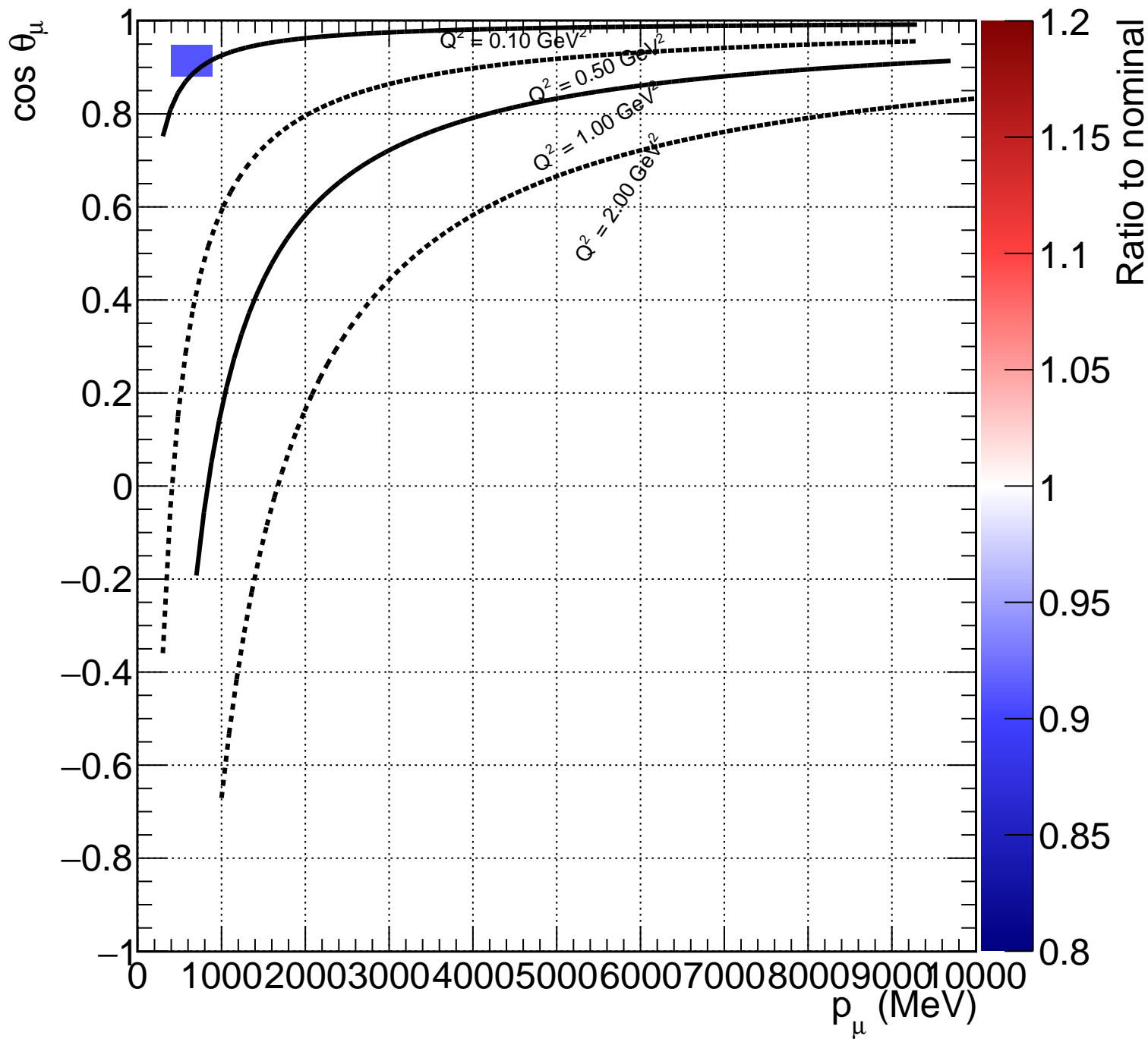
# FGD2\_numuCC\_other\_ndd\_360\_0.880869



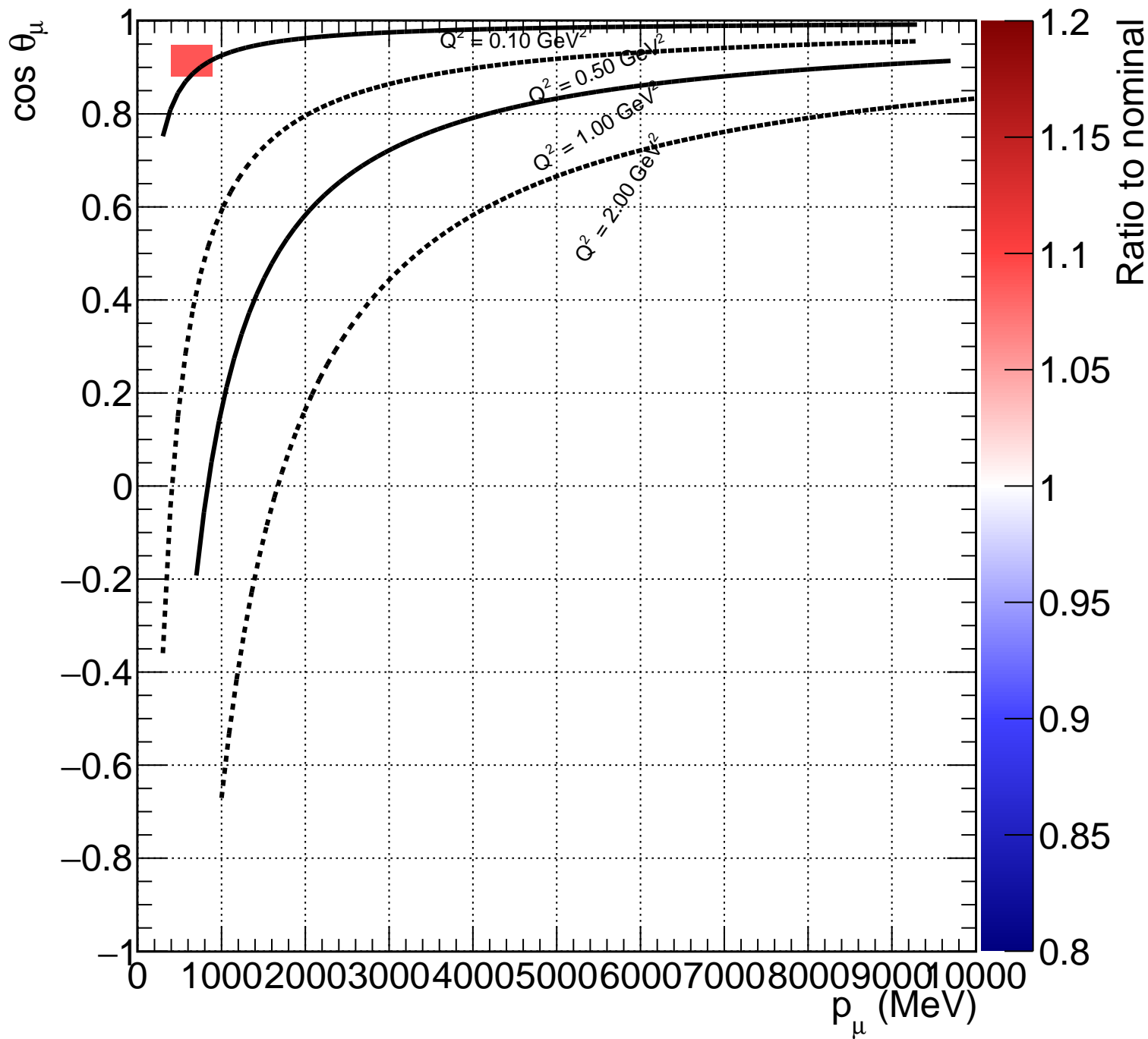
# FGD2\_numuCC\_other\_ndd\_360\_1.15083



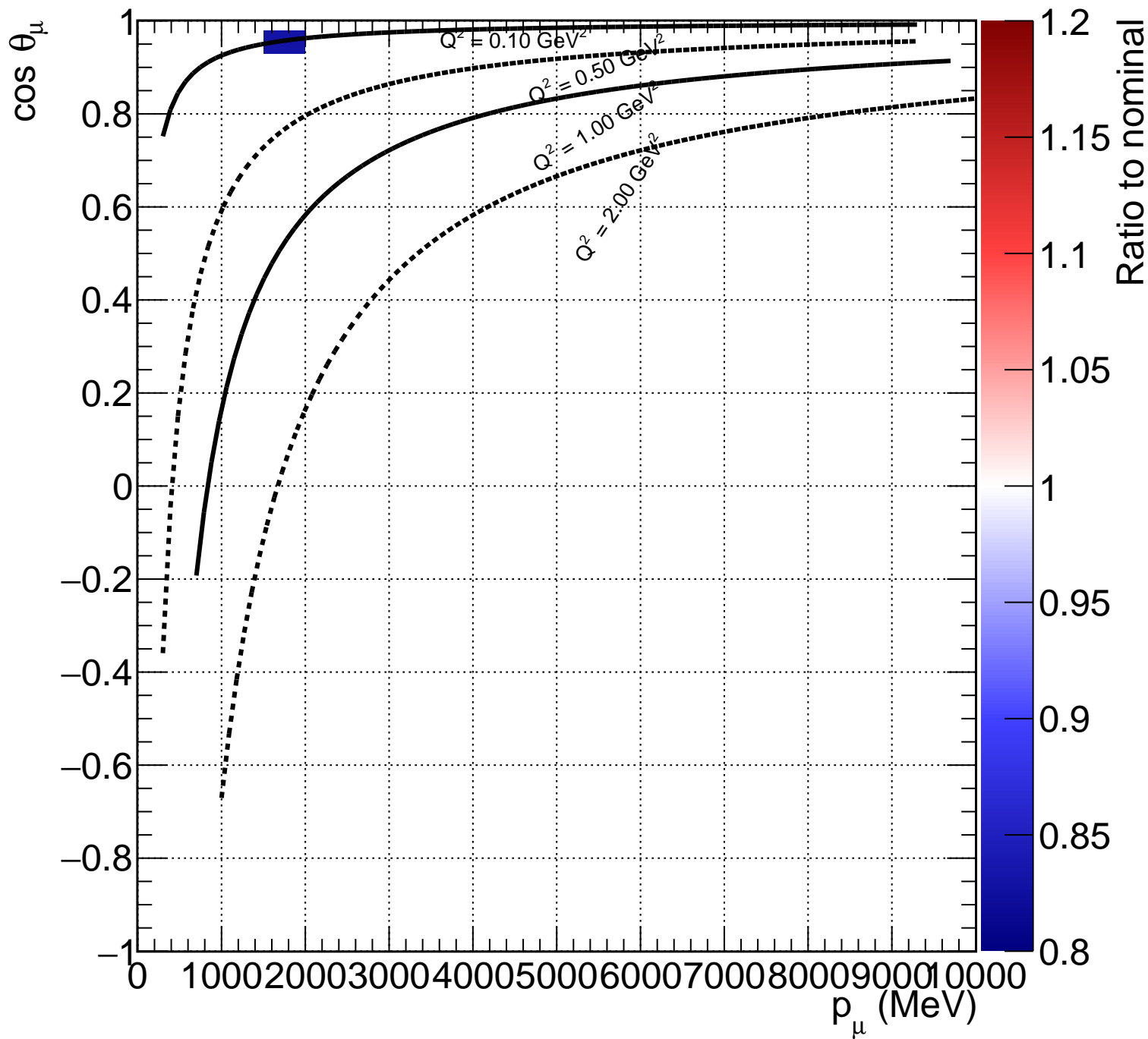
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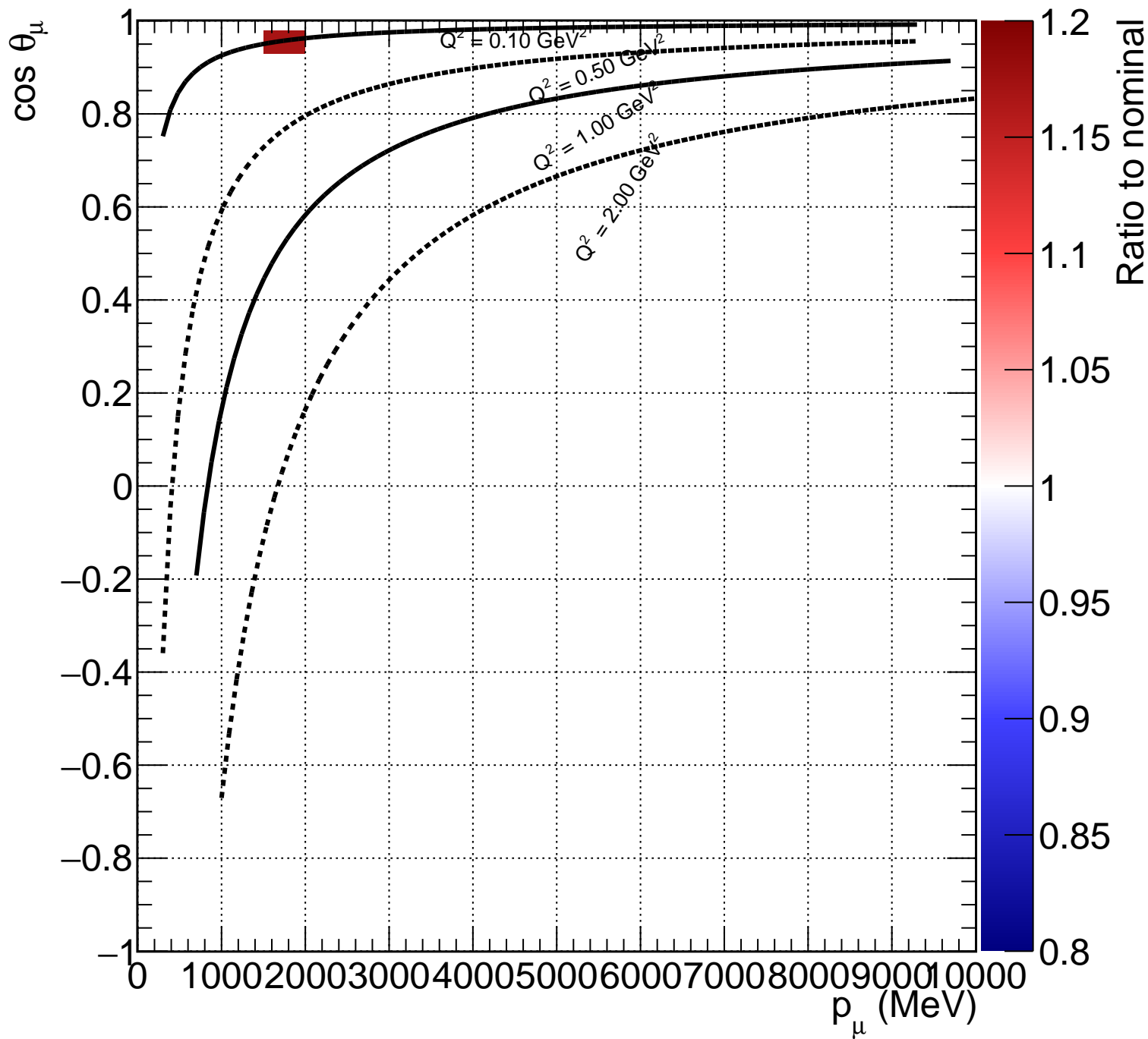
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# FGD1\_anti-numuCC\_nQE\_ndd\_183\_0.904391

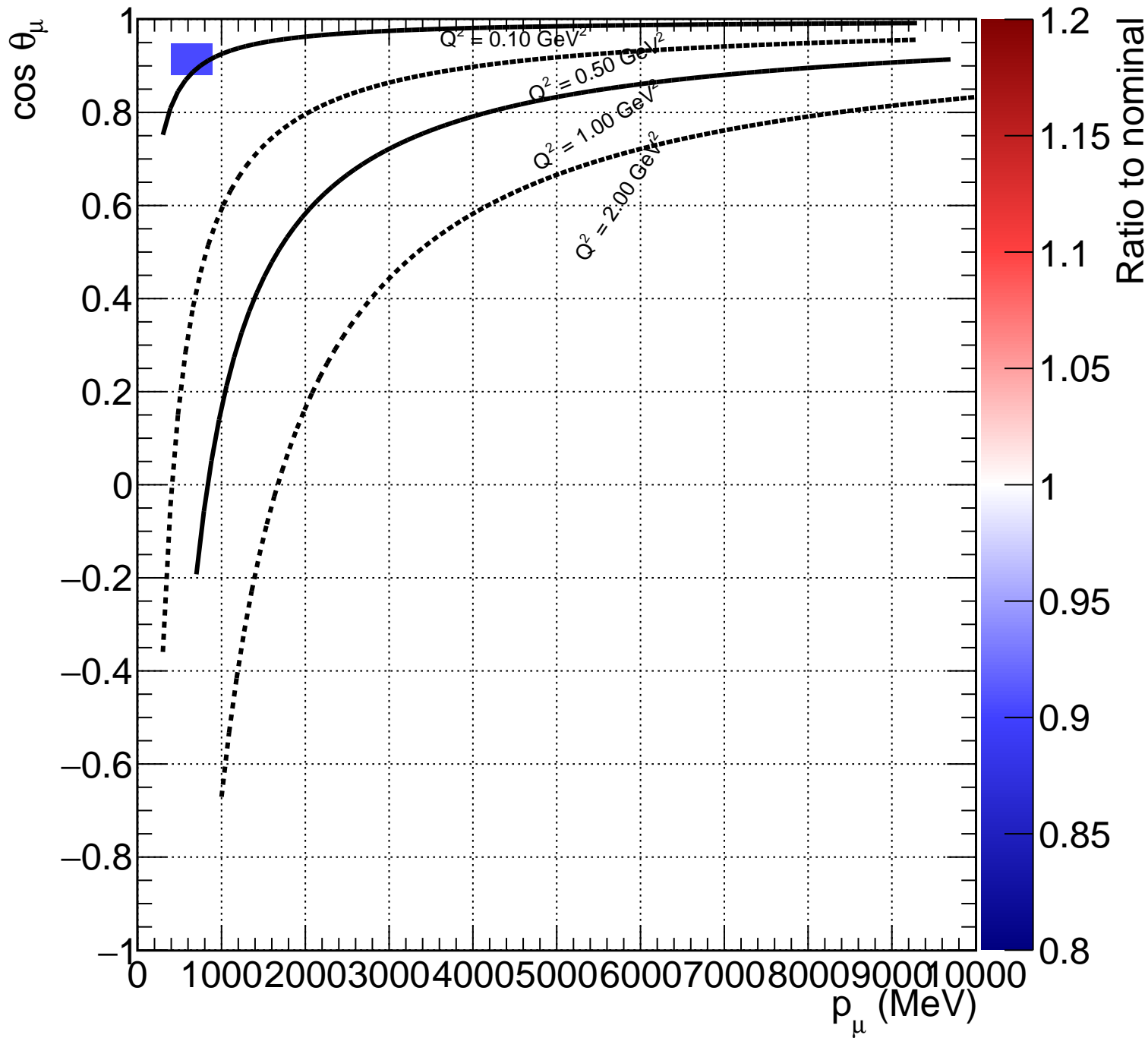


# FGD1\_anti-numuCC\_nQE\_ddd\_183\_1.27258

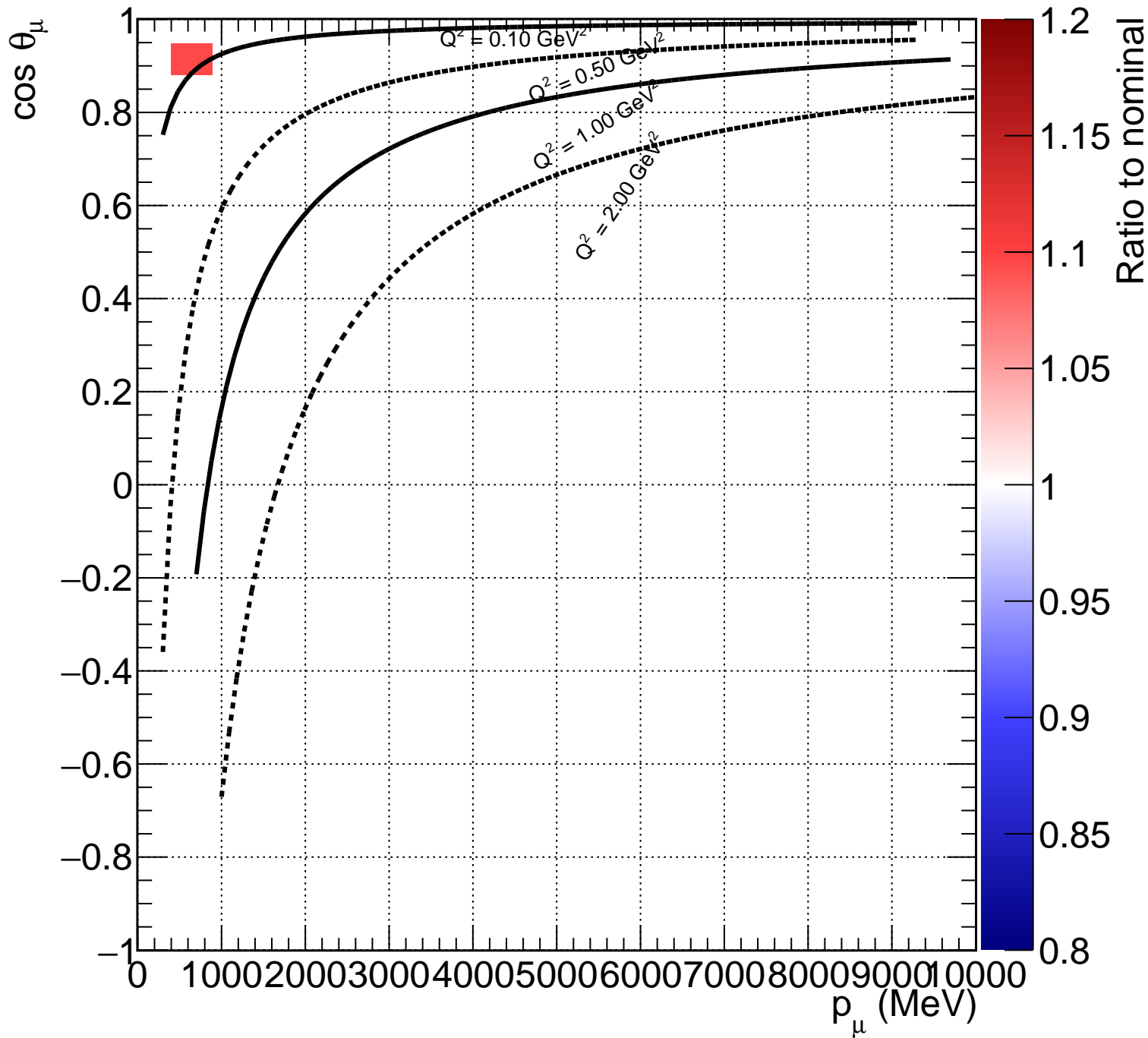




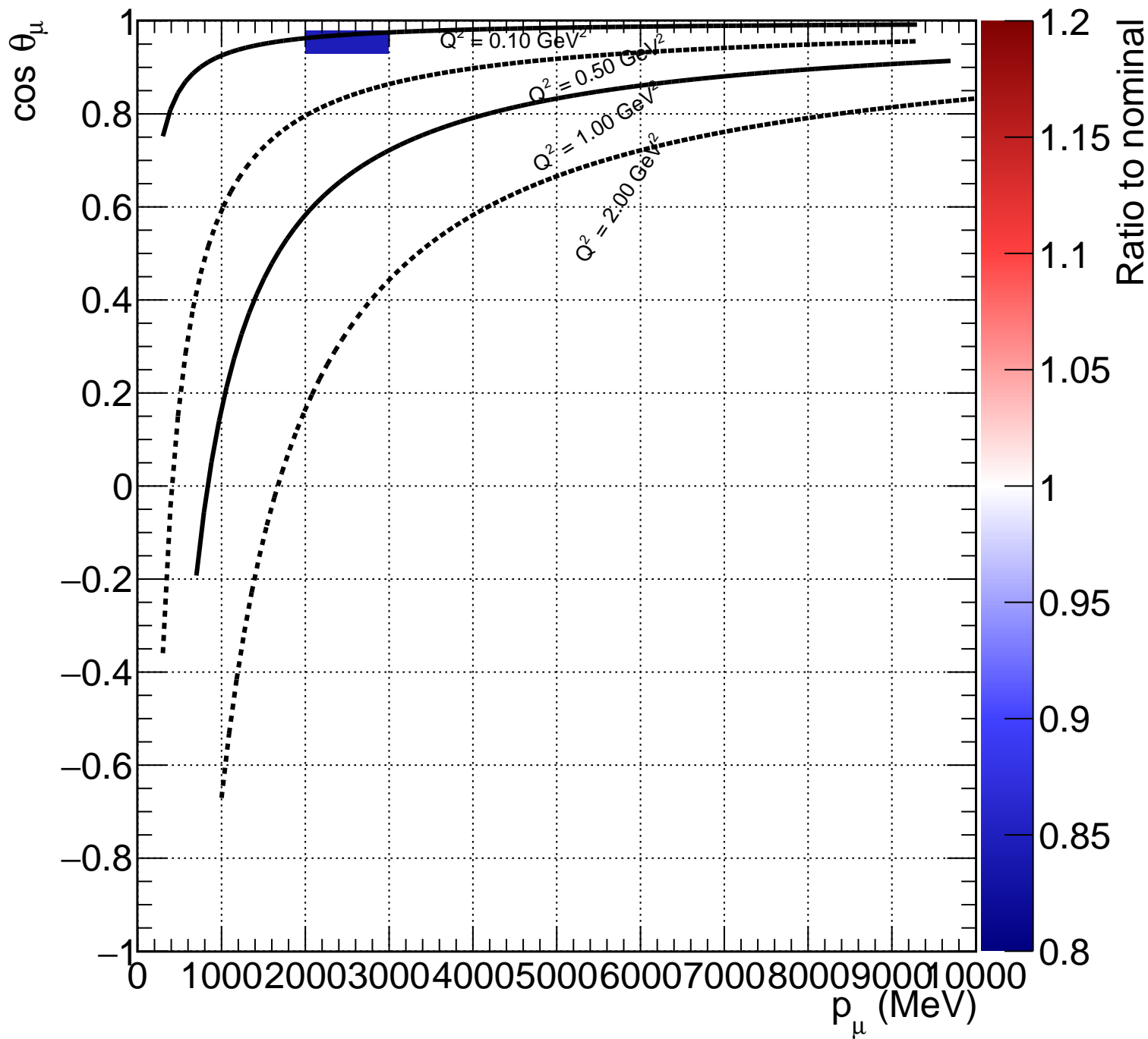
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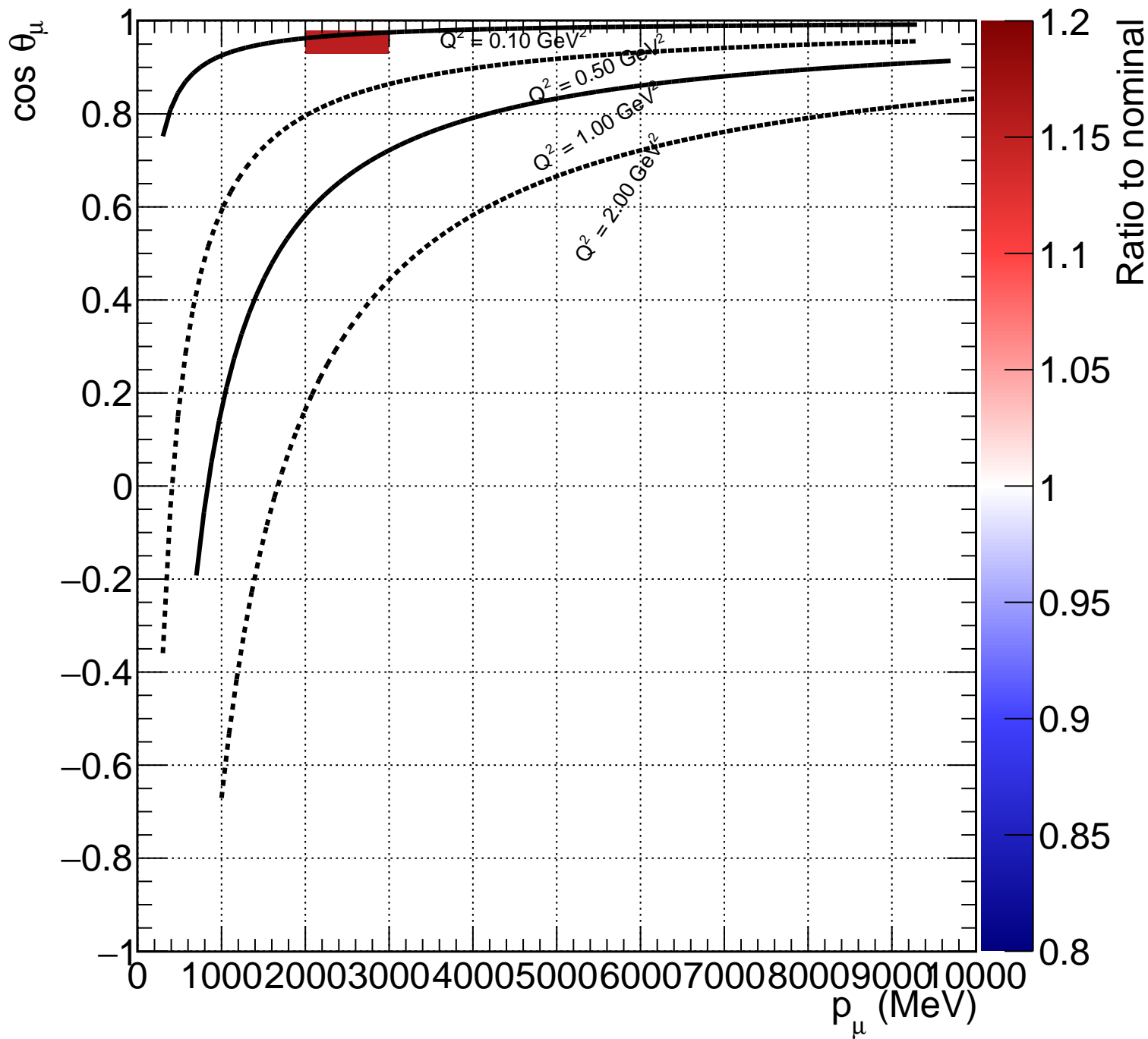
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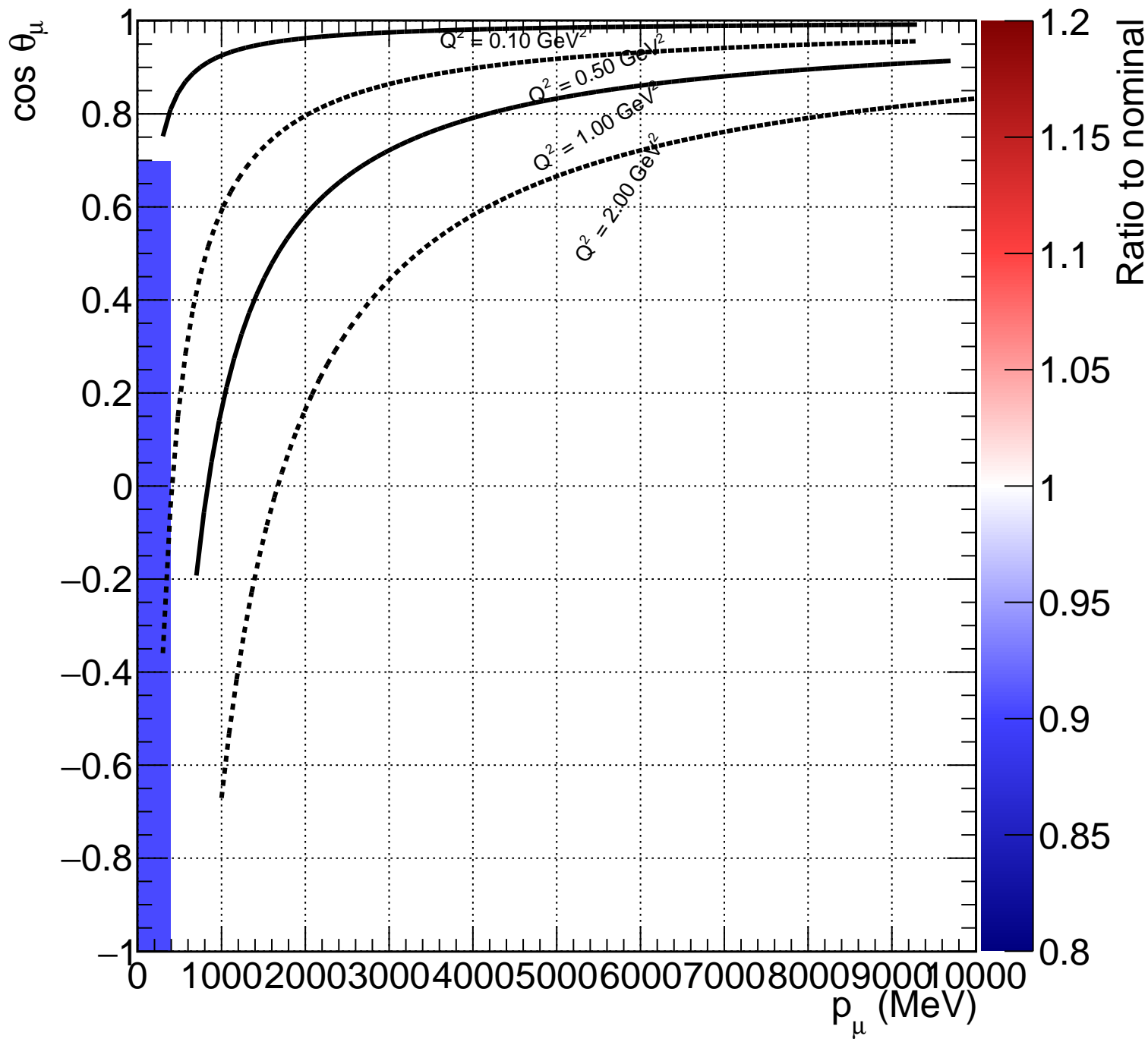


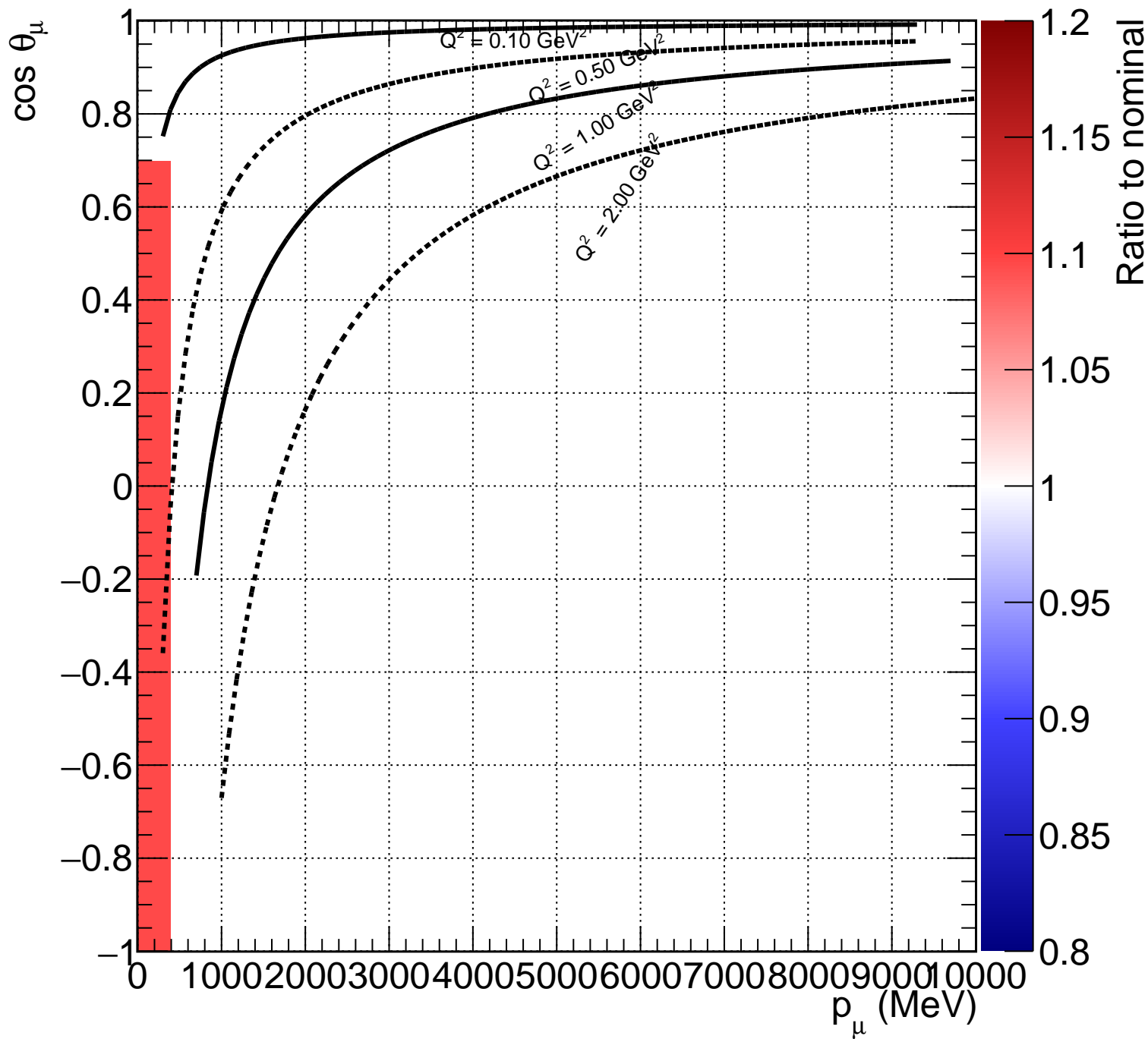
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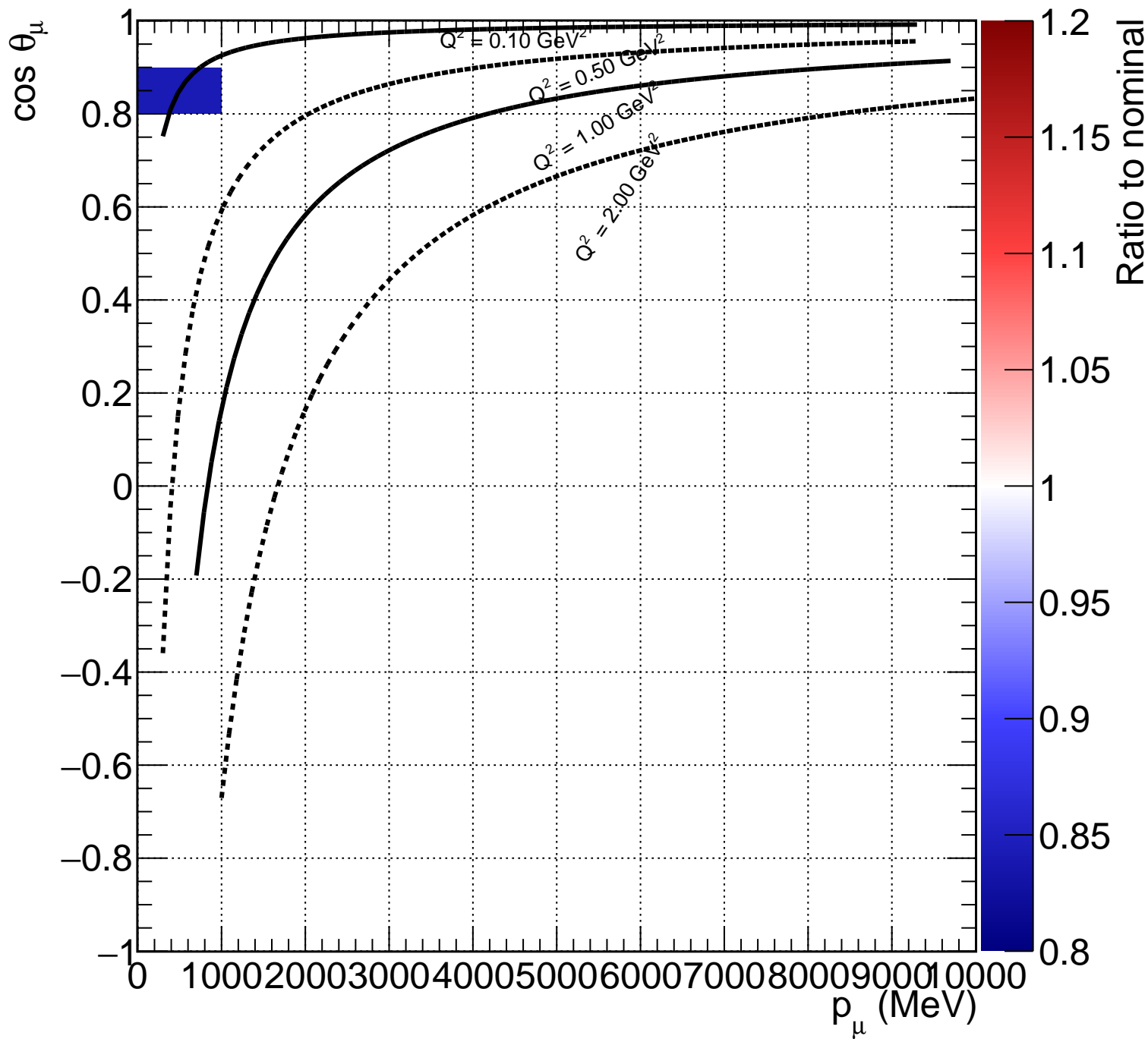


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FGD1\_NuMuBkg\_CCnQE\_in\_AntiNu\_Mode\_ndd\_239\_1.335

