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	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
F	i0	i1	i2	i3	i4	i5	i6	i6	i7	i8	i8	i8	i9	i9	i9	i9	i9	i9	i9	i9	i9
D		i0	i1	i2	i3	i4	i5	i5	i5	i6	i7	i7	i7	i8	i9	i9	i9	i9	i9	i9	i9
E			i0	i1	i2	i3	i4	nop	nop	i5	i6	nop	nop	i7	i8	nop	nop	i9	i9	i9	i9
M				i0	i1	i2	i3	i4	nop	nop	i5	i6	nop	nop	i7	i8	nop	nop	i9	i9	i9
W					i0	i1	i2	i3	i4	nop	nop	i5	i6	nop	nop	i7	i8	nop	nop	i9	i9

i0: lbi r0, 0
 i1: lbi r5, 43
 i2: lbi r6, 43
 i3: lbi r7, 43
 i4: ld r1, r0, 0 ← i0
 i5: st r5, r1, 0 ← i1, i4
 i6: ld r1, r0, 2
 i7: st r6, r1, 1 ← i6
 i8: ld r1, r0, 4
 i9: st r7, r1, 1 ← i8
 i10: halt

cycle	instruction retired	Reason
5	i0: lbi r0, 0	
6	i1: lbi r5, 43	
7	i2: lbi r6, 43	
8	i3: lbi r7, 43	
9	i4: ld r1, r0, 0	
10	nop (stall bubble)	i5 has a RAW dependency on i4, no forwarding is enabled
11	— — —	
12	i5: st r5, r1, 0	
13	i6: ld r1, r0, 2	
14	nop (stall bubble)	i7 has a RAW dependency on i6, no forwarding is enabled
15	— — —	
16	i7: st r6, r1, 1	
17	i8: ld r1, r0, 4	
18	nop (stall bubble)	i9 has a RAW dependency on i7, no forwarding is enabled
19	— — —	
20	i9: st r7, r1, 1	
21	i10: halt	